

Intel

Xeon SoC Pre-silicon Verification Intern

May 2022 – August 2022

Remote

- Designed the first uploaders for new internal database to standardize platform, compress log files, and decrease regression time
- Converted Perl post-processing checker to Python and migrated large log files to new database to increase checker performance
- Automated System Verilog test bench creation for SoC in different PCI-E configurations; a process which was being done by hand