Tesla   **January 2021 – August 2021**

Silicon Development Intern – Autopilot Hardware Group   *Palo Alto, CA*

- Delivered temperature and voltage heartbeat via RTOS task for bringup of newly-received compute die to ensure silicon's stability
- Developed SPI, temperature, voltage interrupts/drivers for third-party IPs, accounted for bug in SPI silicon with SW solution
- Implemented, integrated, and rigorously tested SW architecture designed to send temperature and voltage over the buggy SPI
- Automated sending protobuf requests via .JSON files and synced script to hardware reading protobuf messages from UART
- Created Python script with linux-like commands to build LittleFS QSPI images, unblocking coworkers without sudo