

**Intel****May 2022 – August 2022***Xeon SoC Pre-silicon Verification Intern (Full-time)**Remote*

- Completed working voltage sensor and SPI drivers despite a complicated bug in Tesla's SPI silicon
- Implemented and rigorously tested a software architecture designed to funnel temperature and voltage data over SPI
- Created a python script able to generate QSPI images working around the LittleFS library, which used linux-like commands