Intel   **May 2022 – August 2022**
Xeon SoC Pre-silicon Verification Intern (Full-time)   *Remote*

• Spearheaded the adoption of an internal database API that leveraged python by constructing

• Enhanced the test bench that was used to verify the SoC and processor together

• Automated the generation for the SoC's PCI-E System Verilog test bench generation for the SoC's PCI-E

• Converted a perl post-processing checker to python in order to leverage the new database and increase its performance

• Optimized reading from the uploader scripts for large file size log files