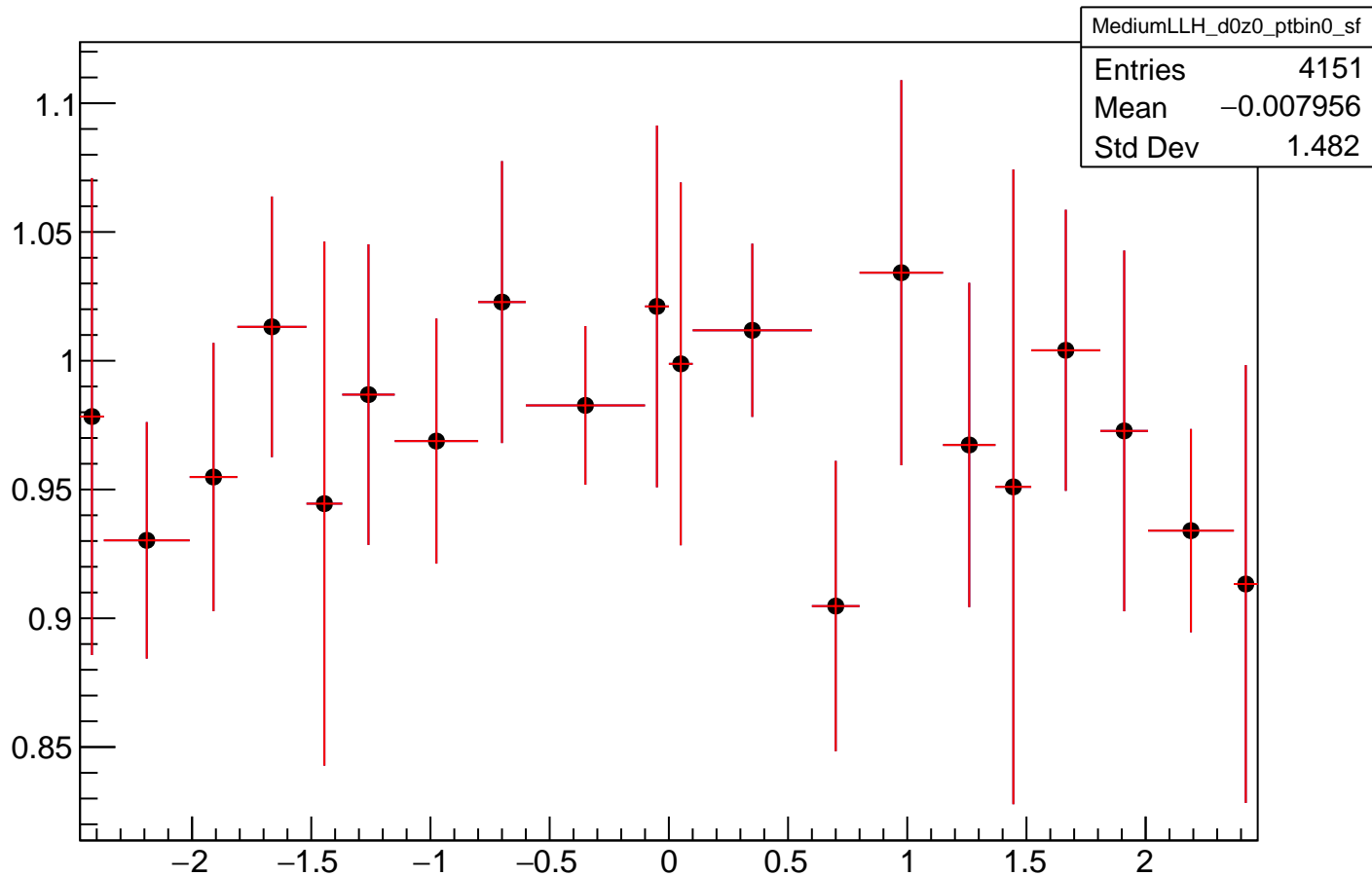
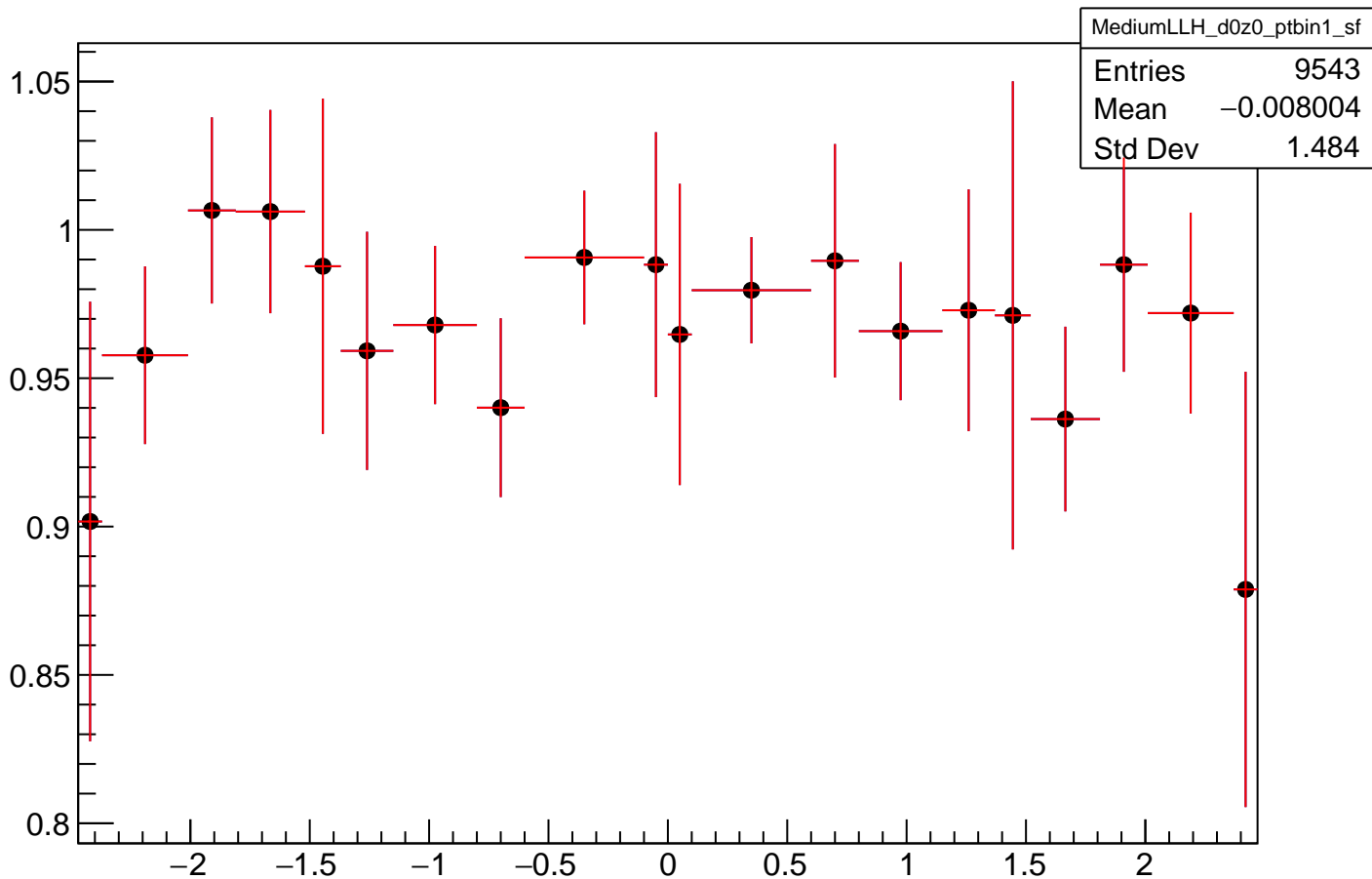


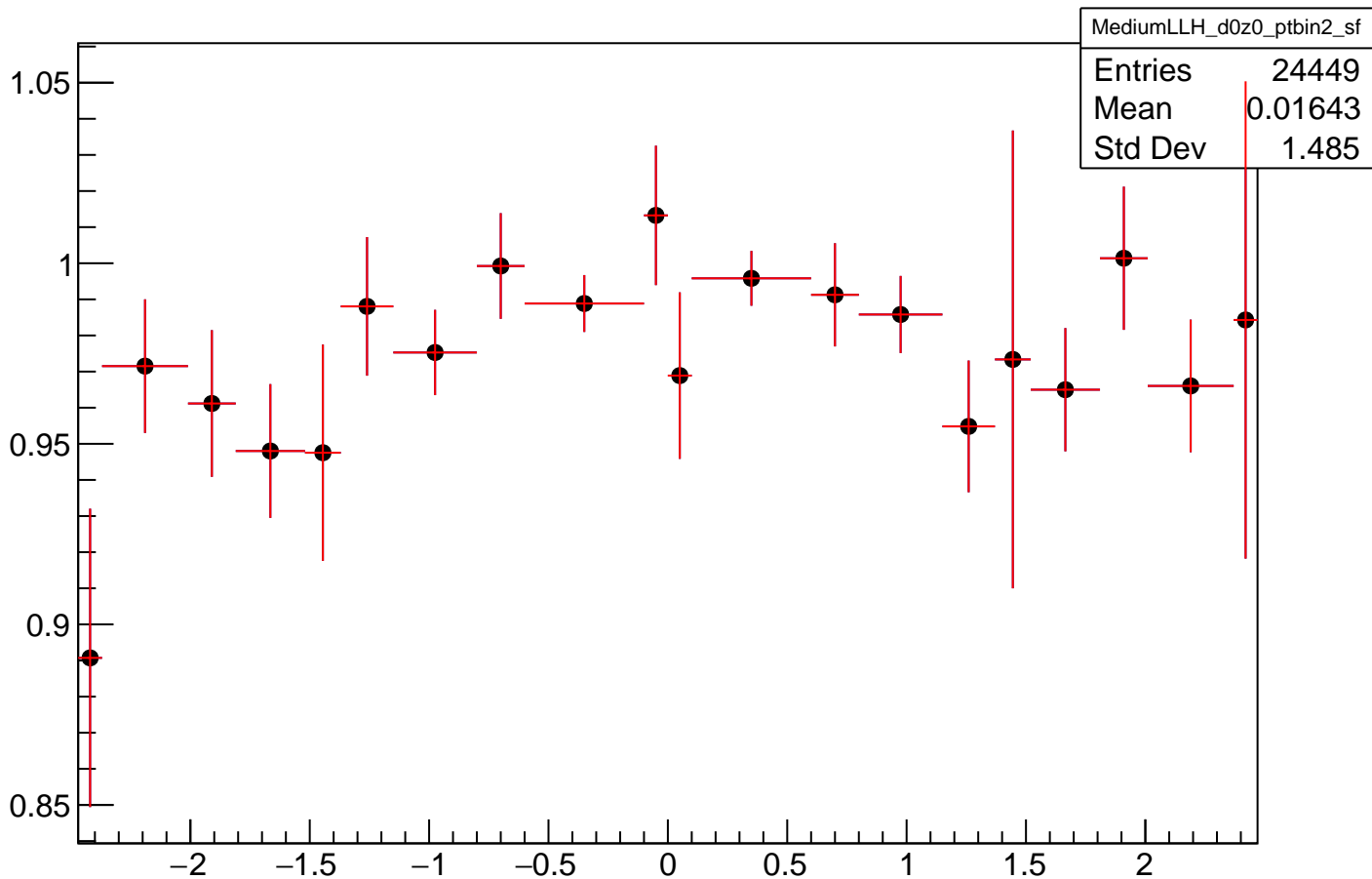
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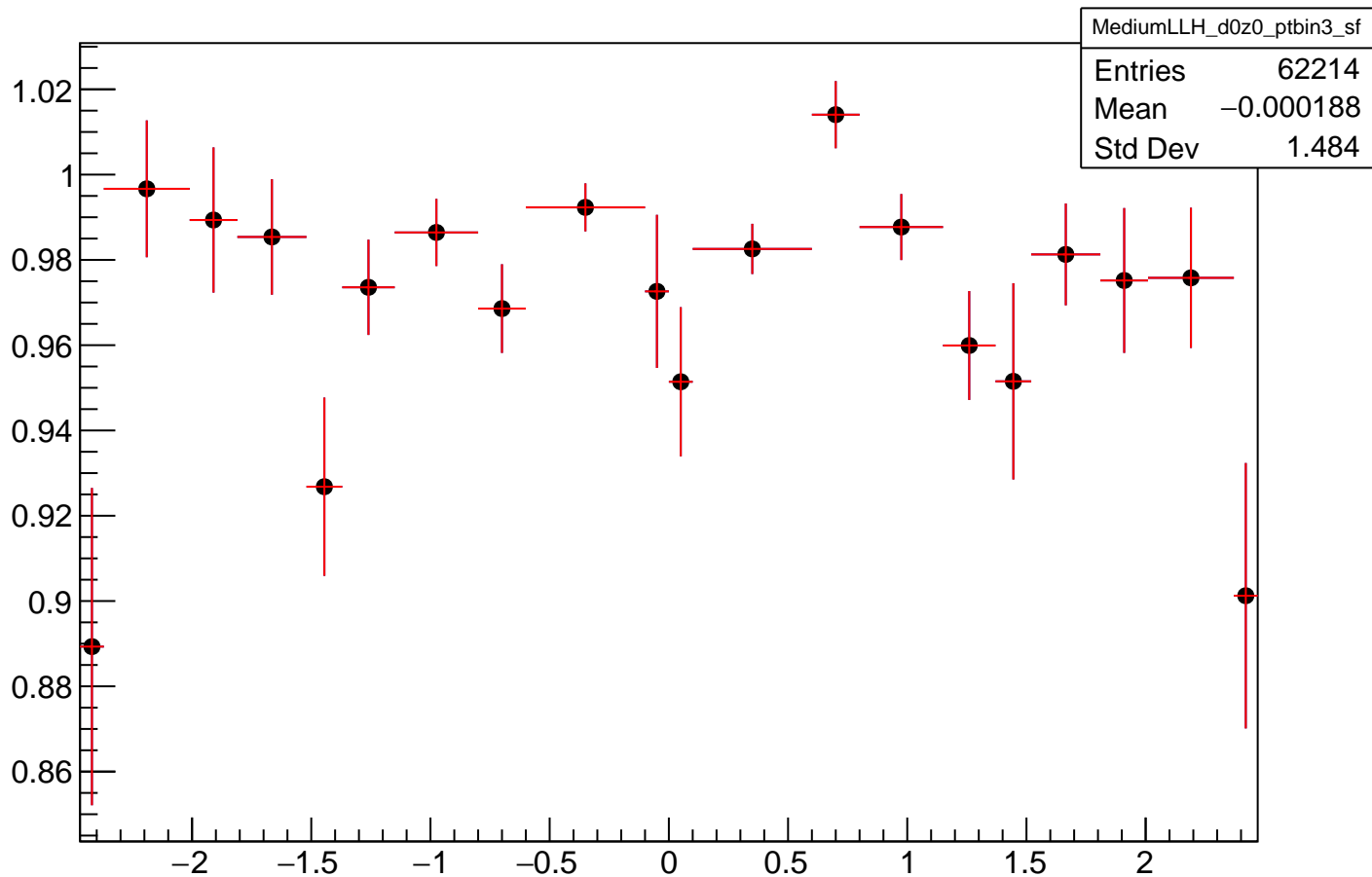
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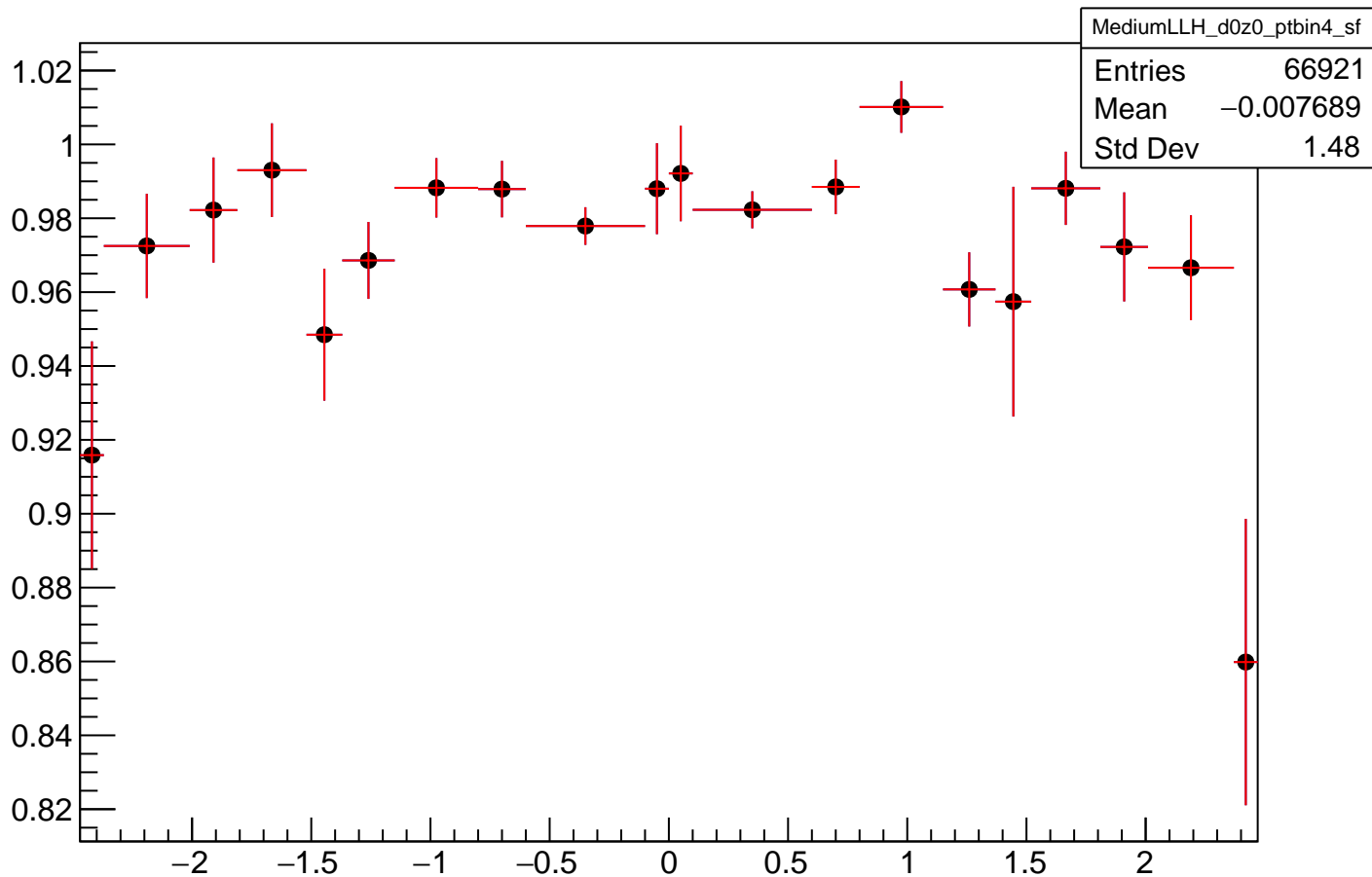
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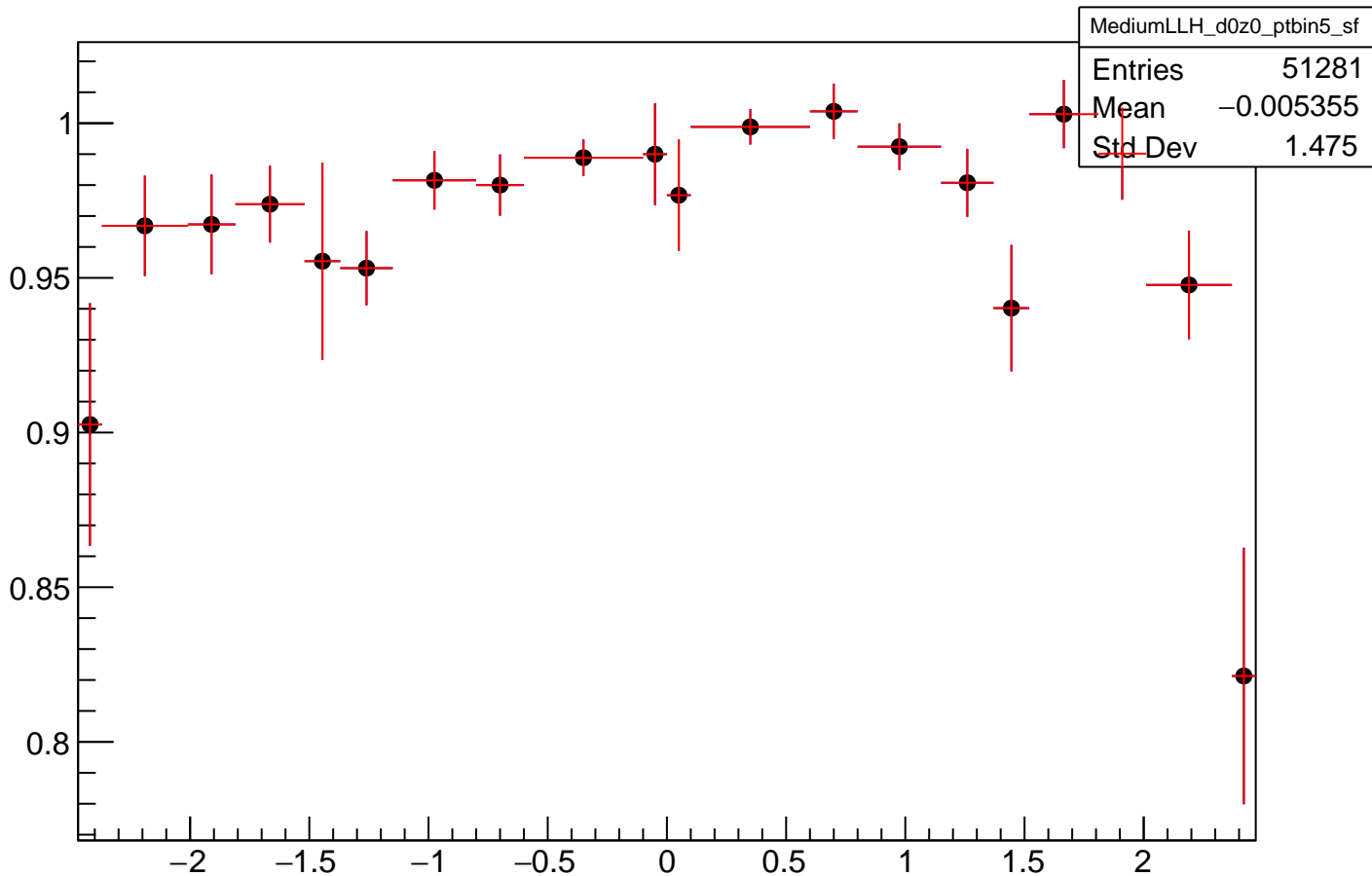
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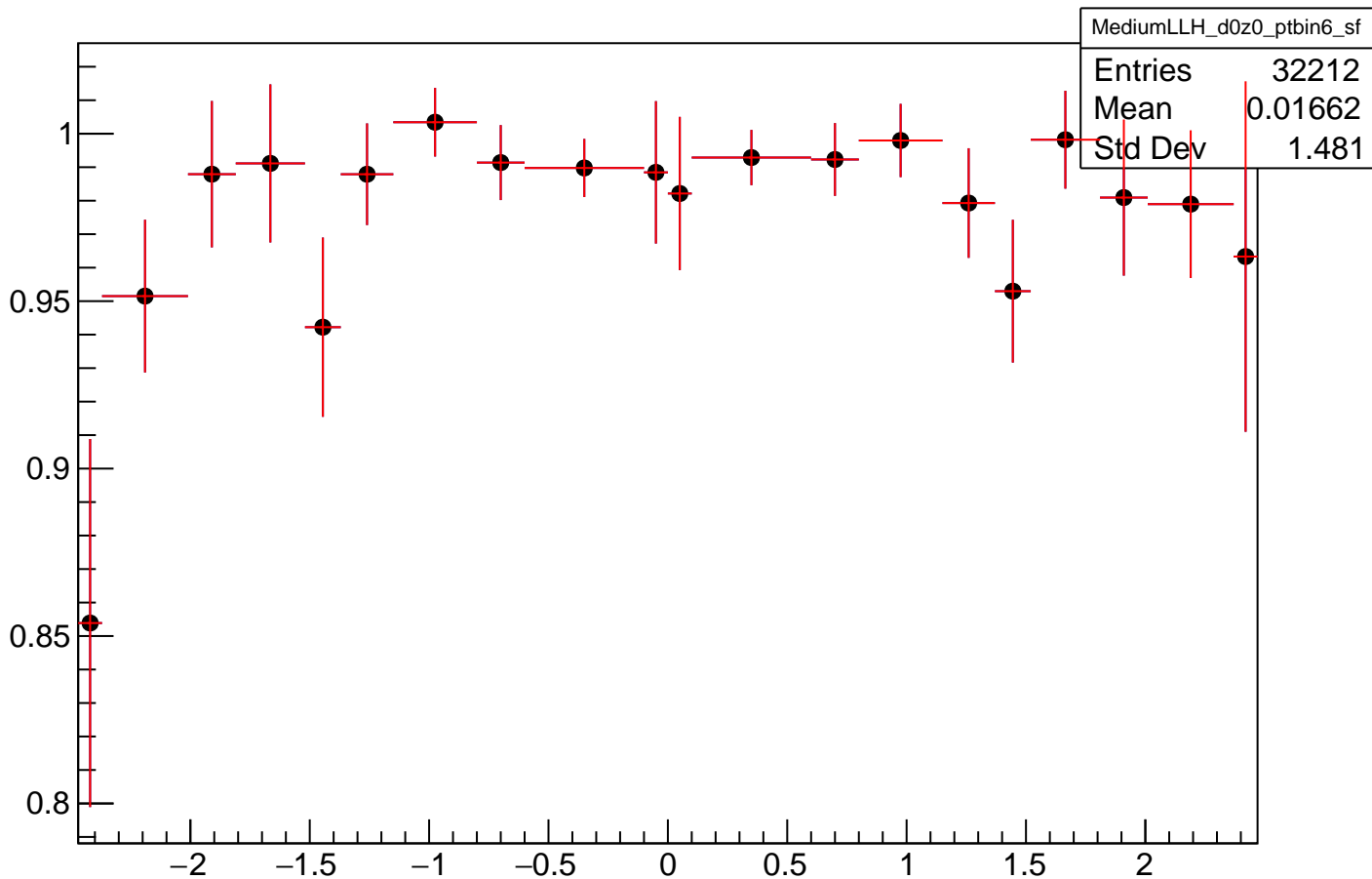
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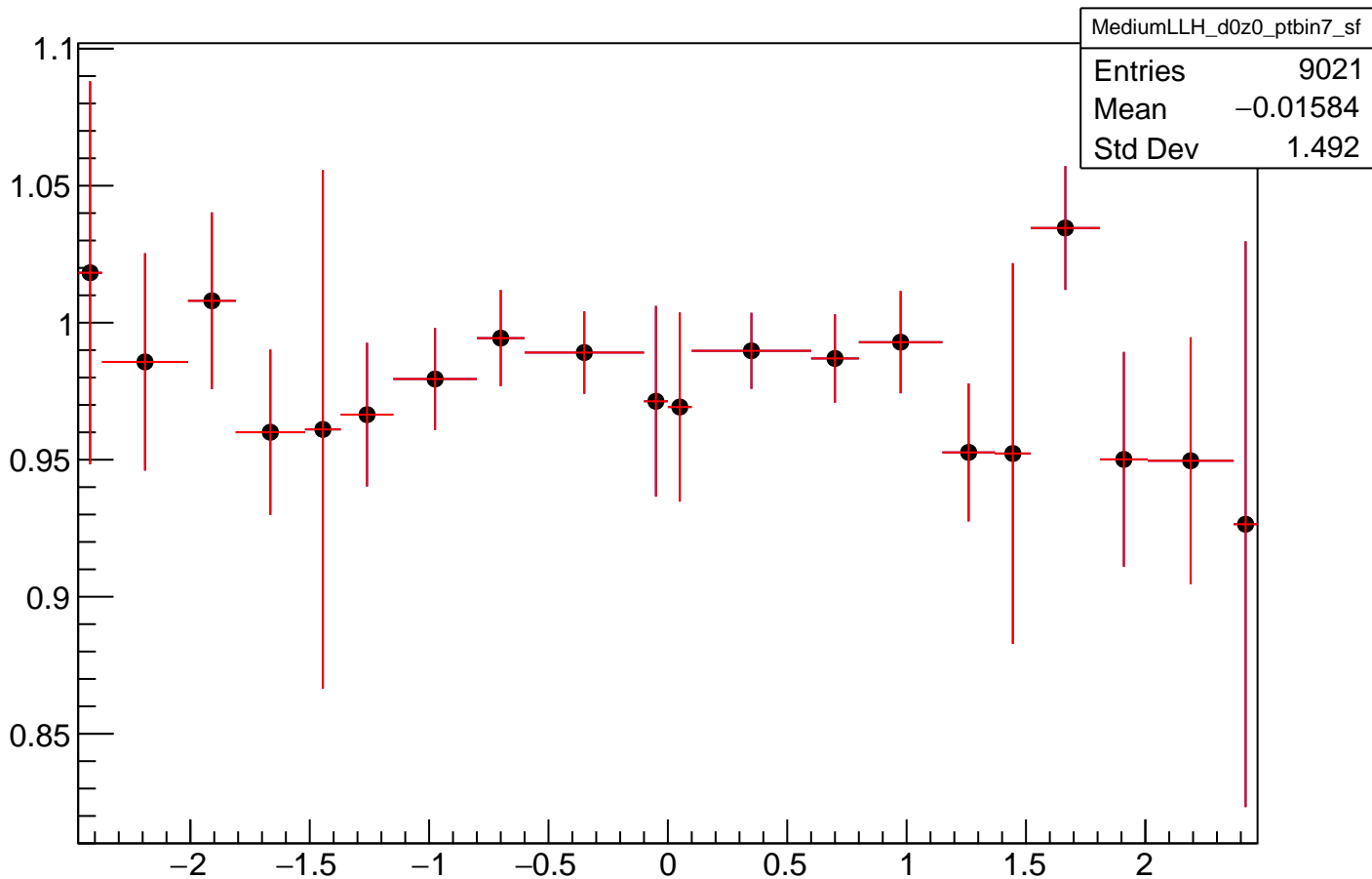
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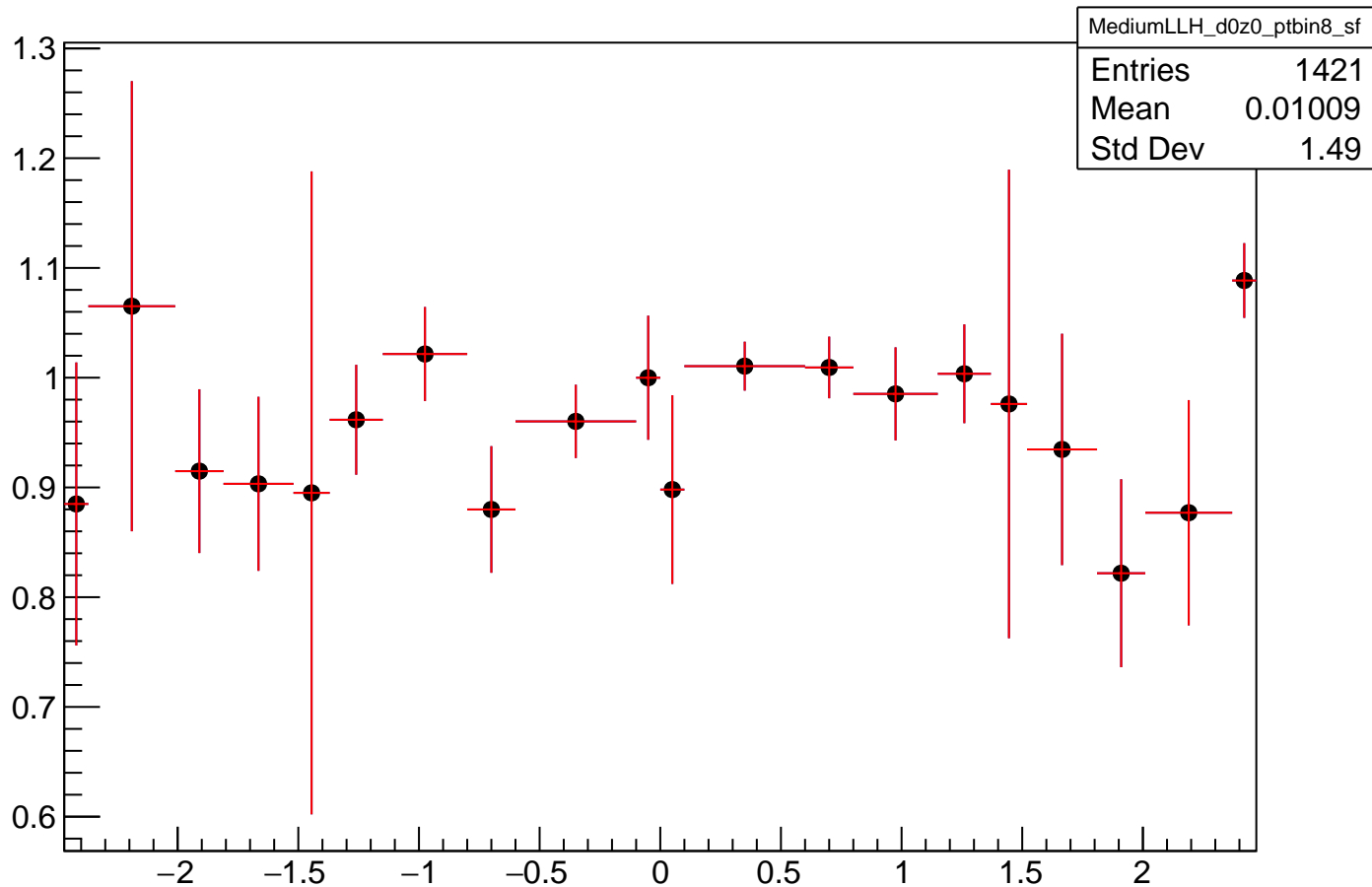
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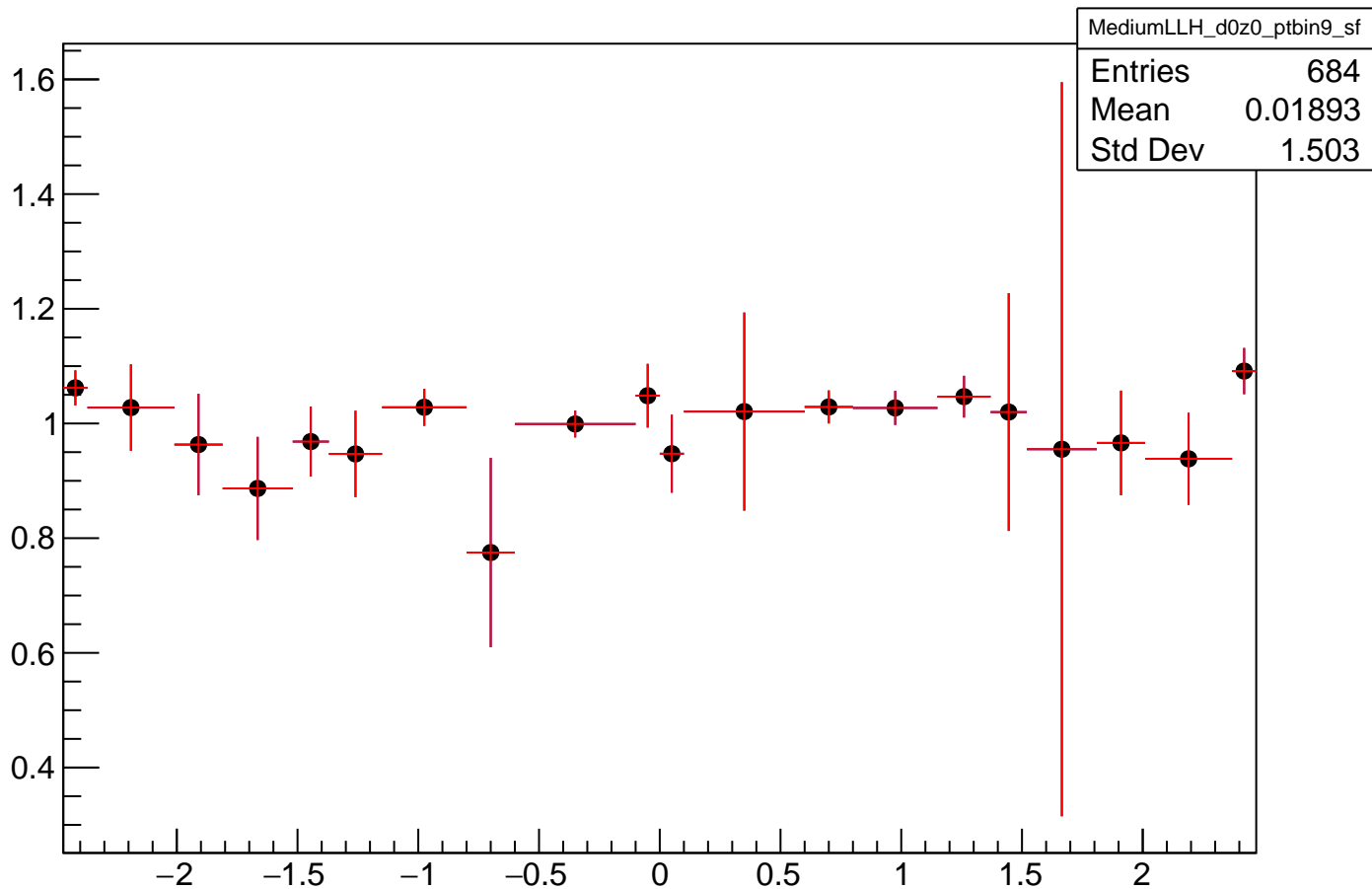
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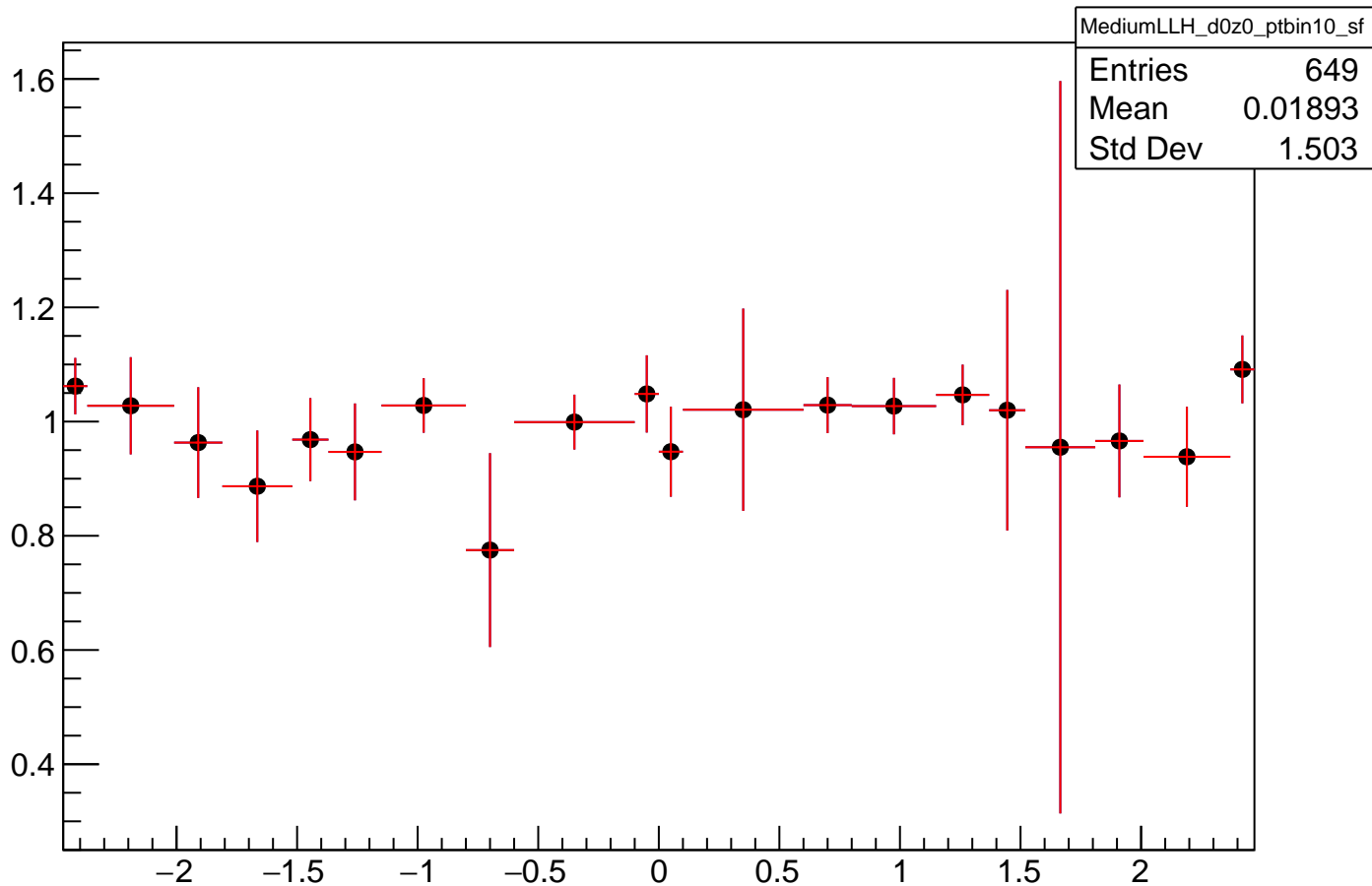
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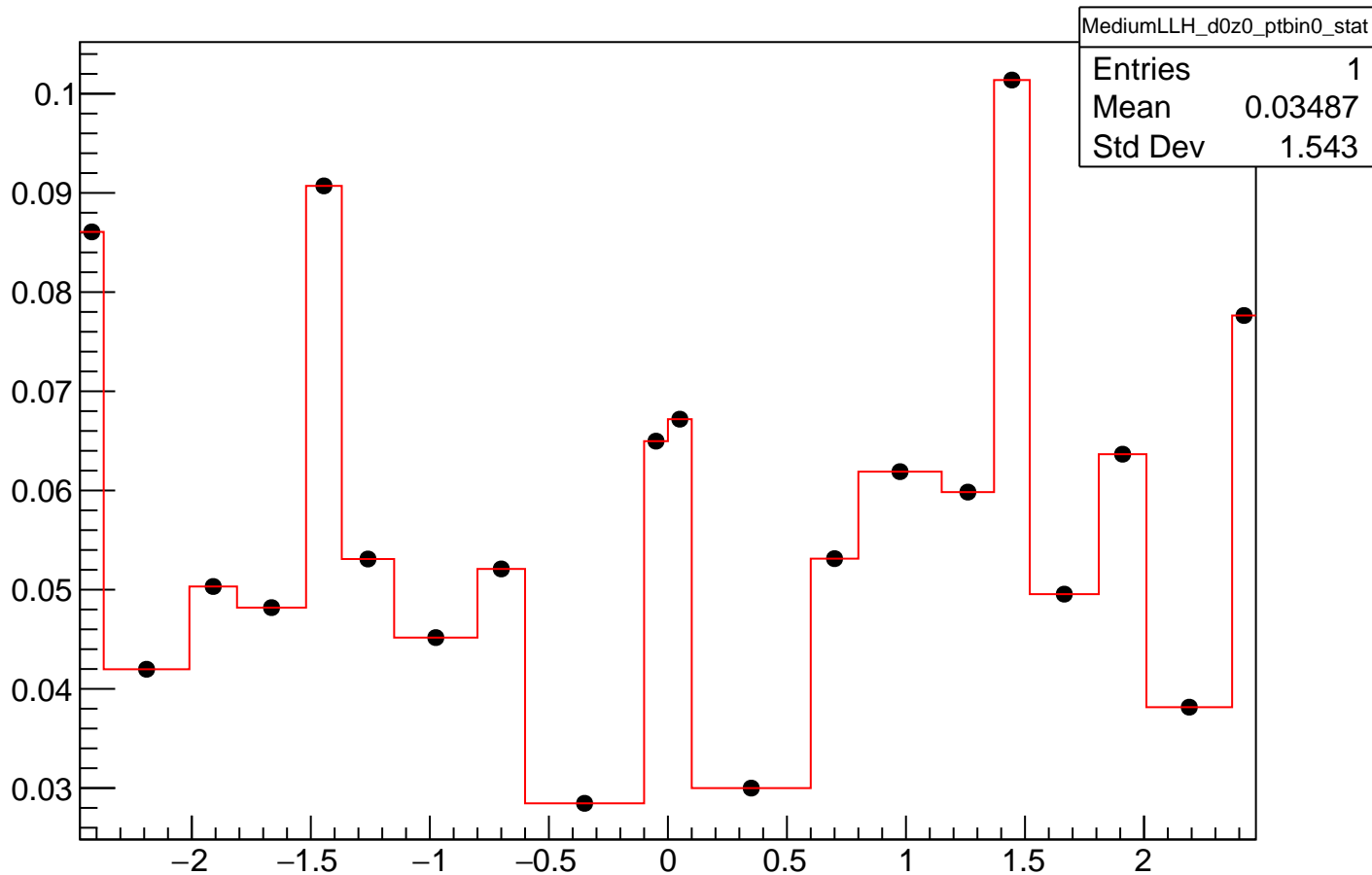
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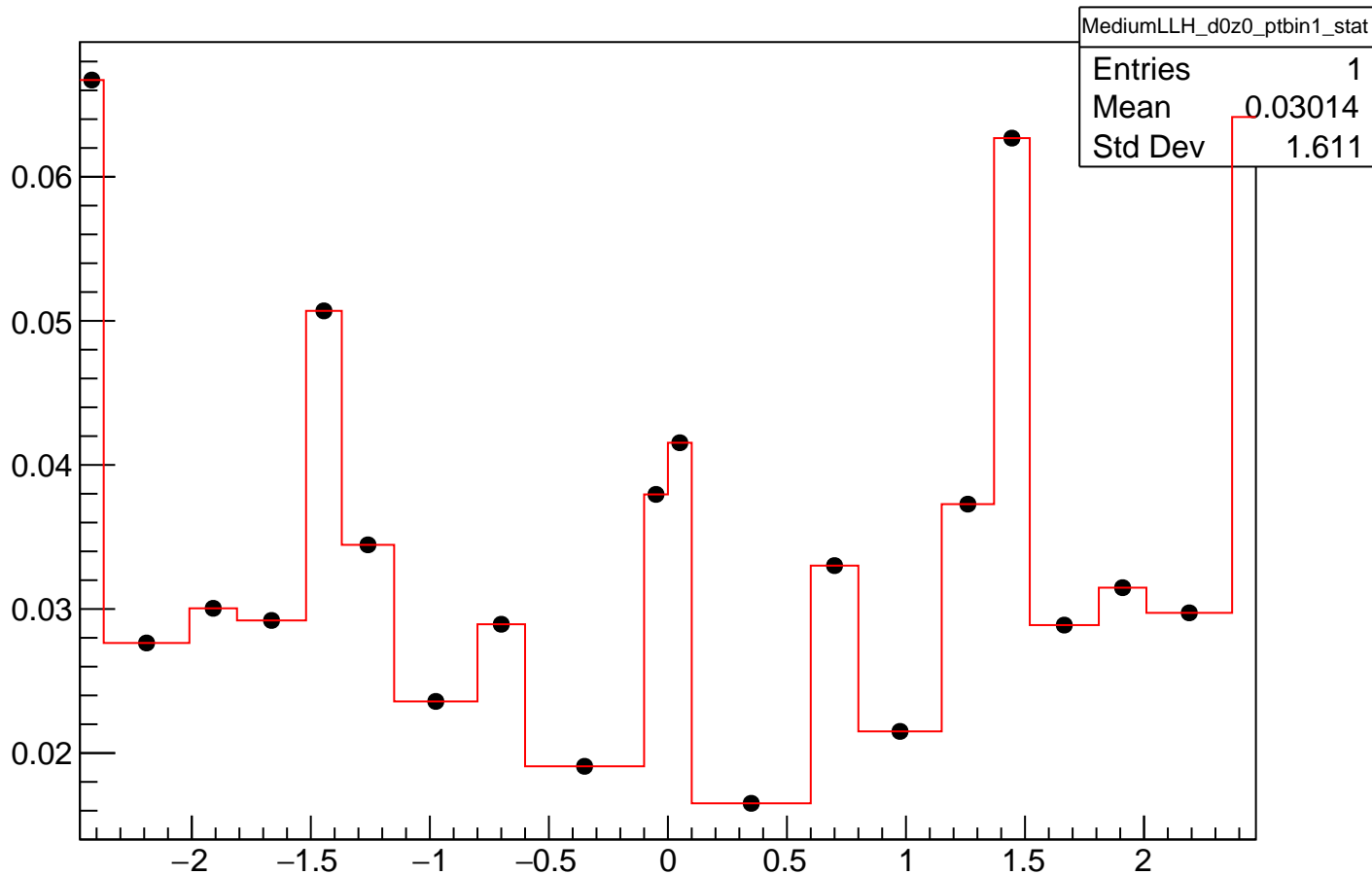
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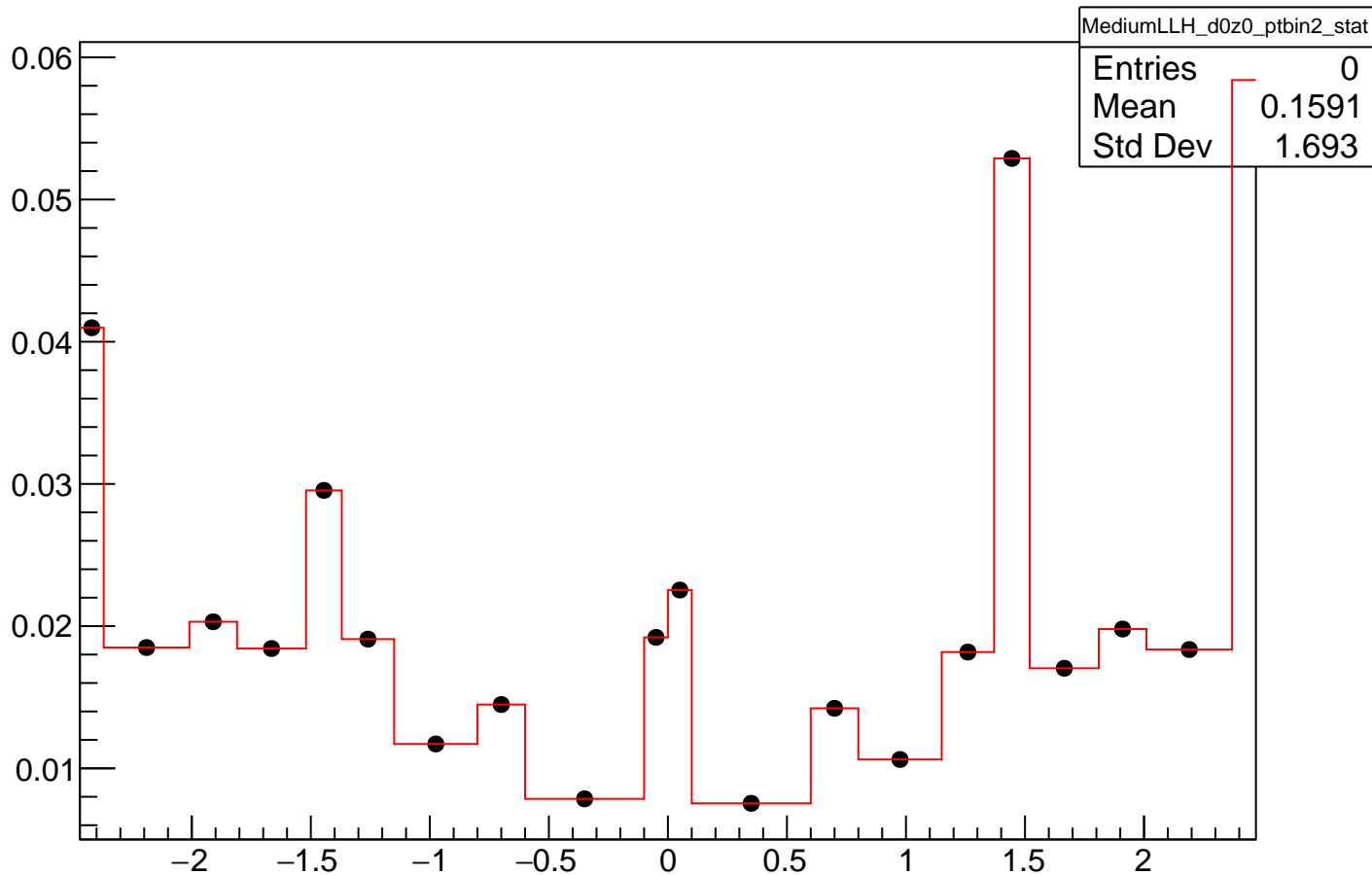
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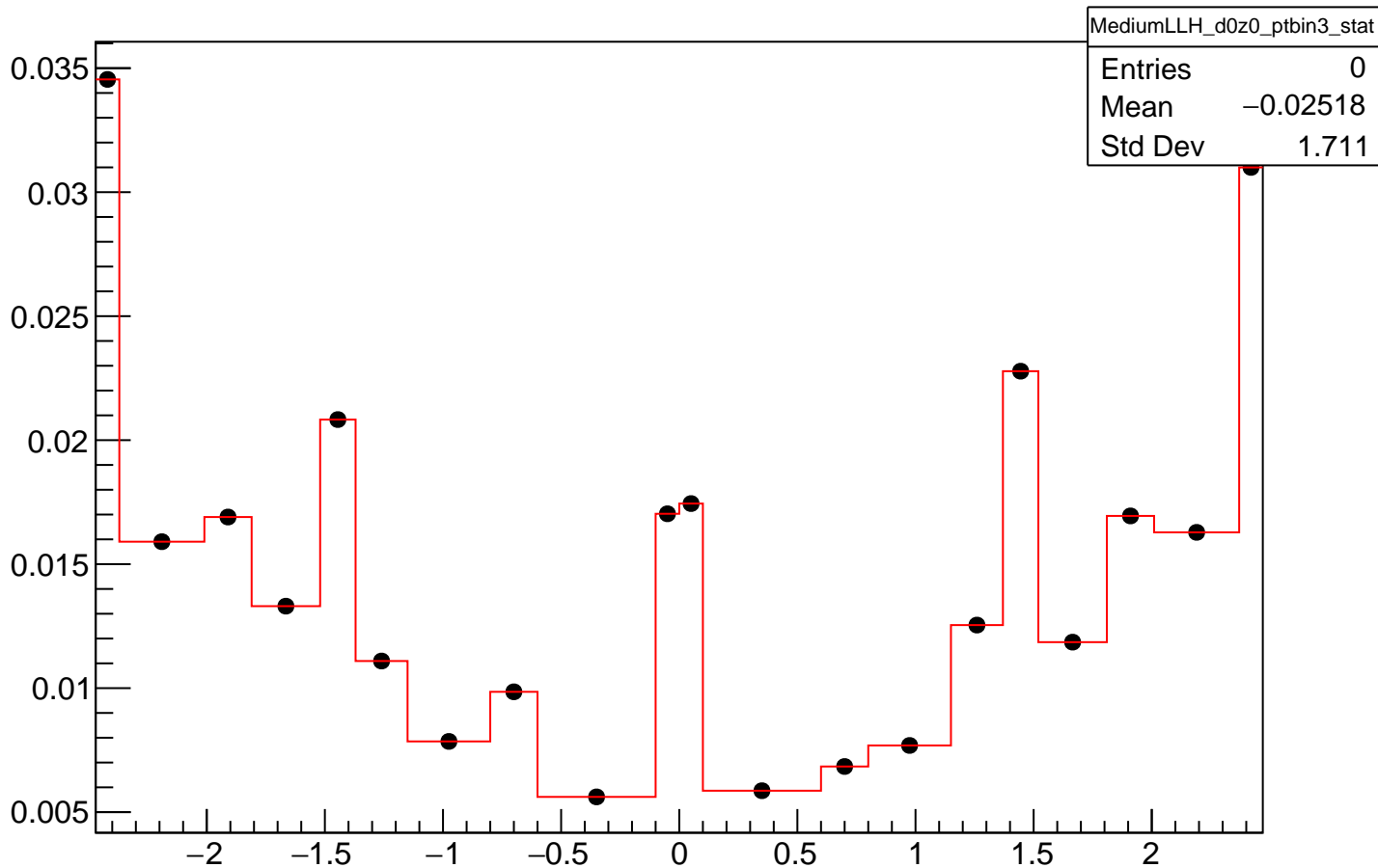
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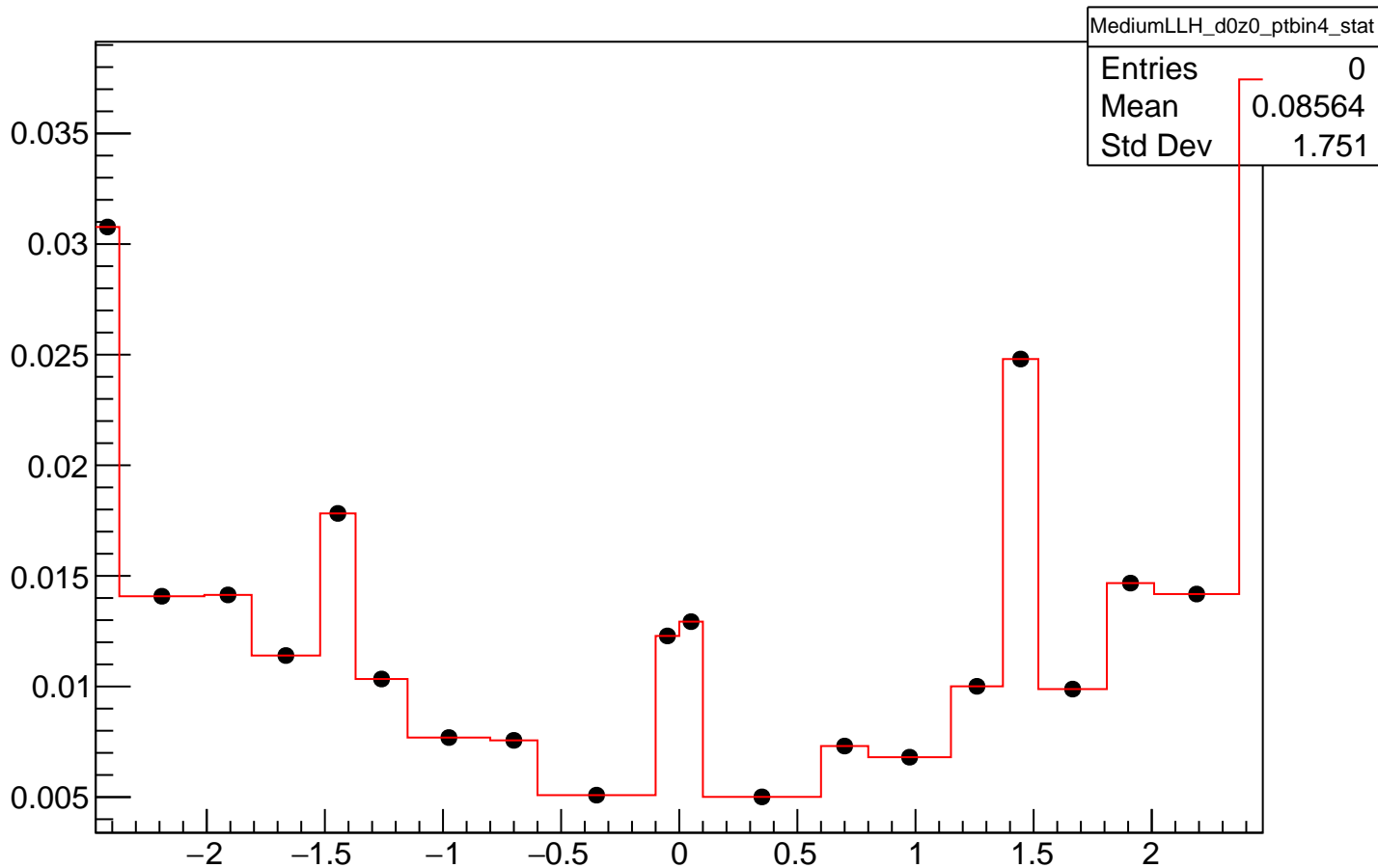
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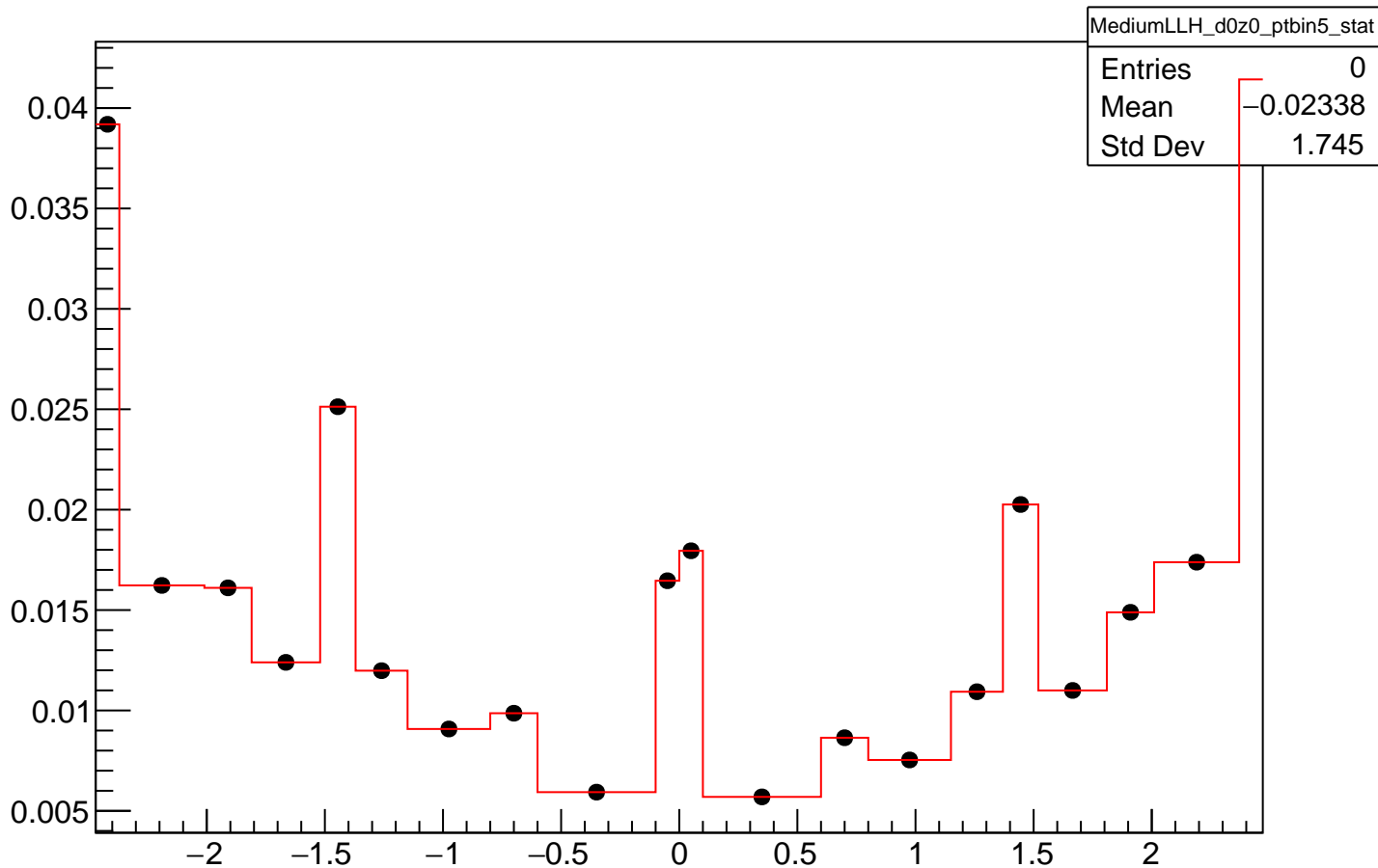
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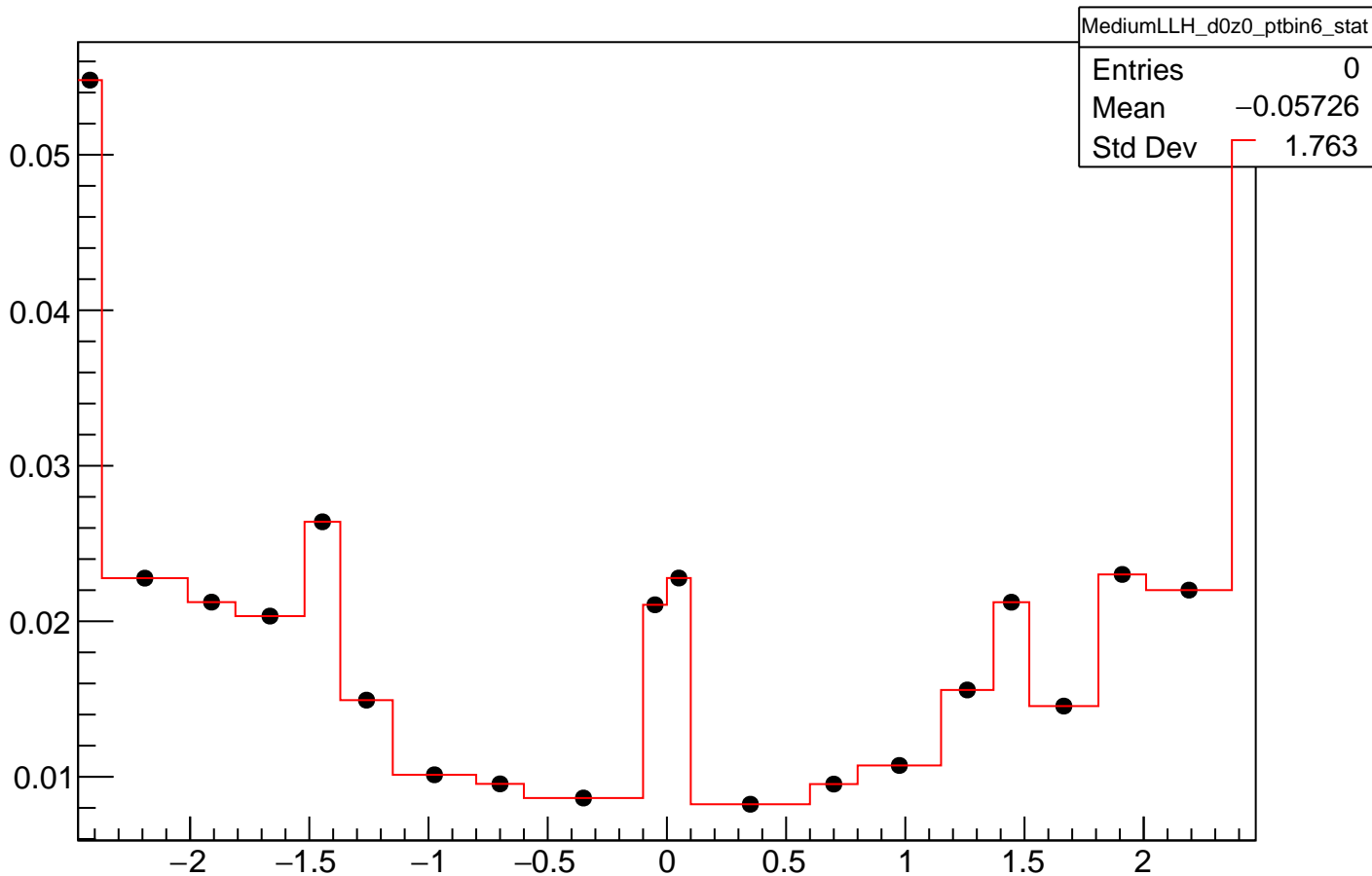
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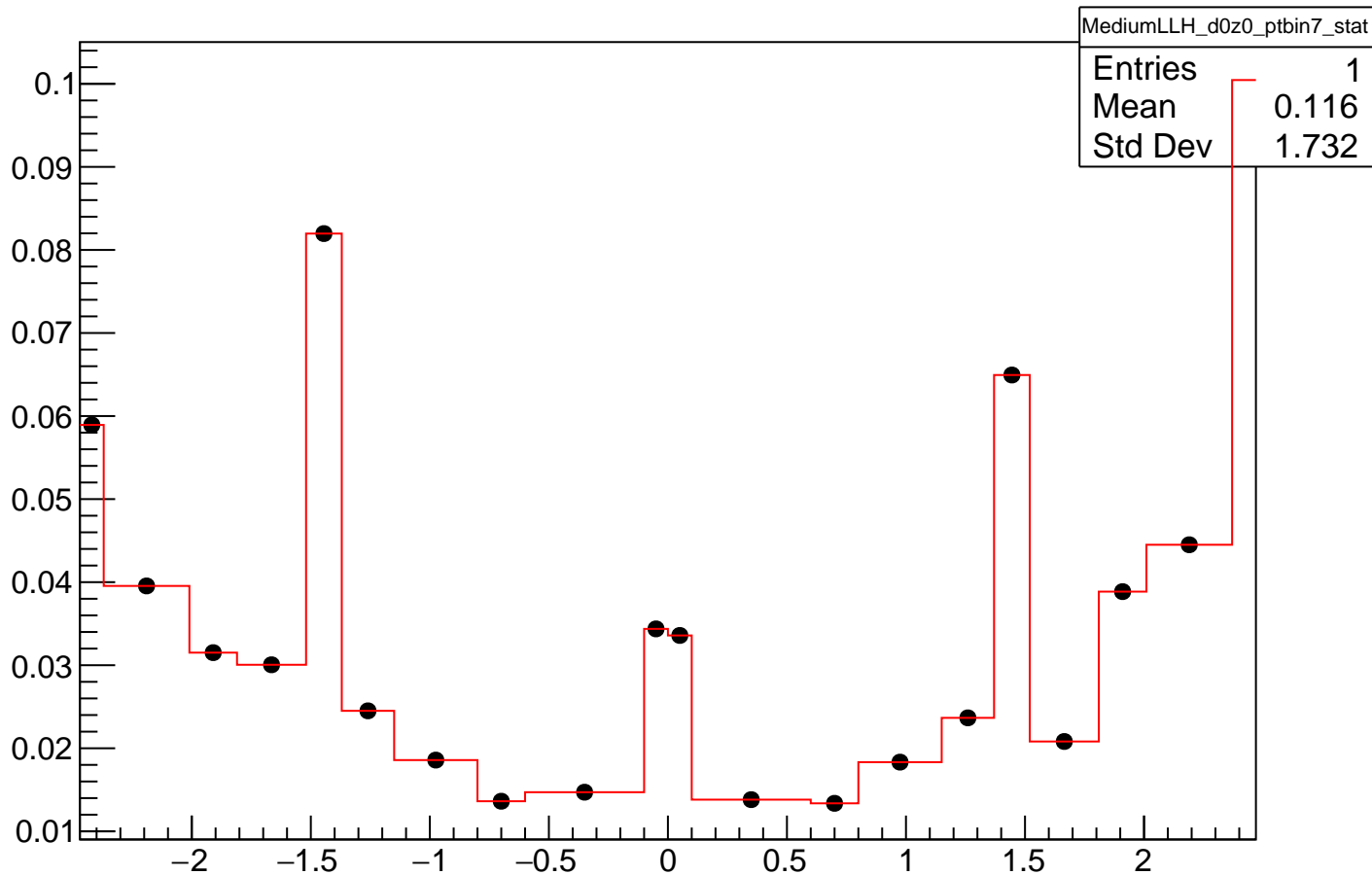
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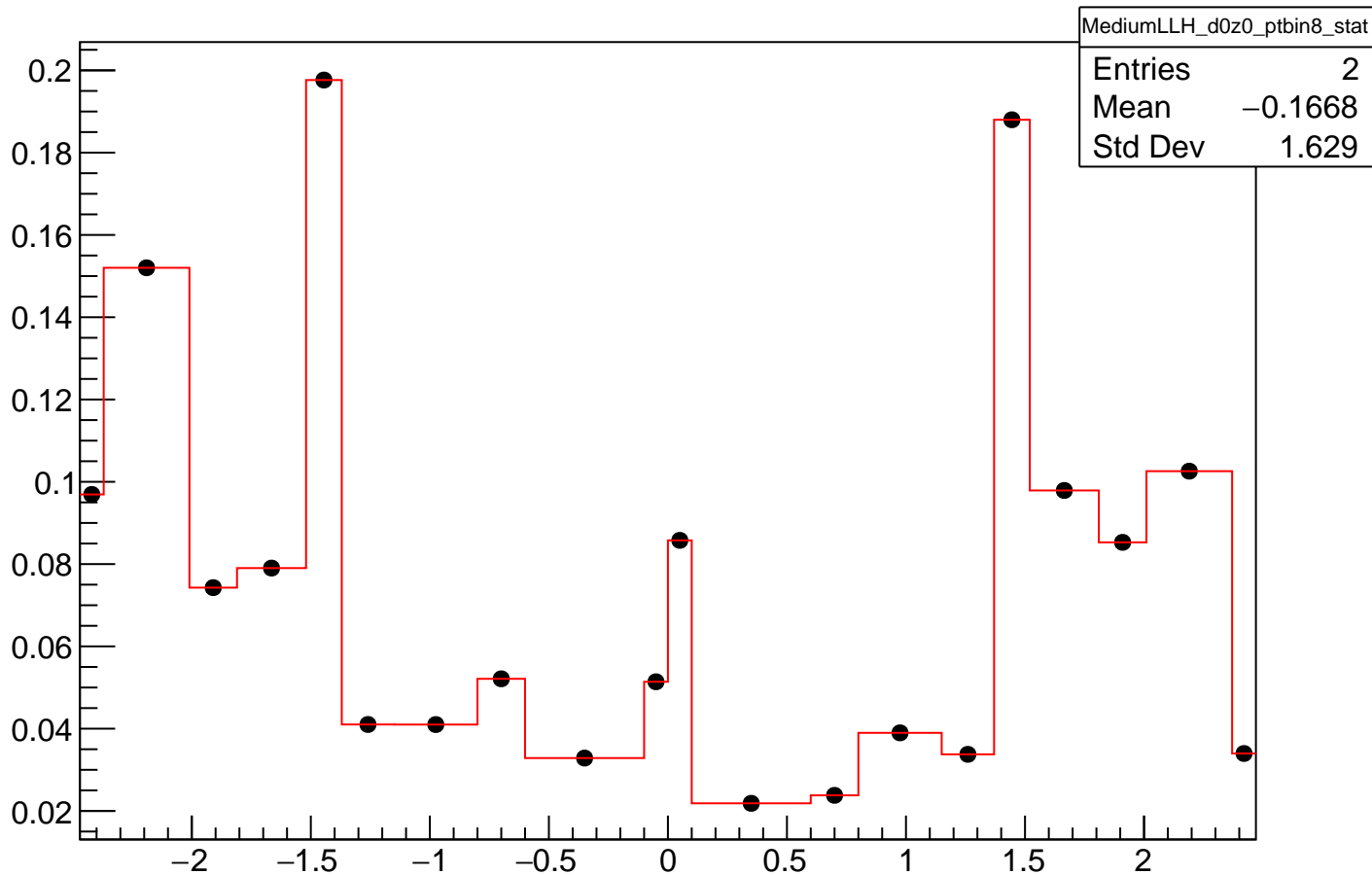
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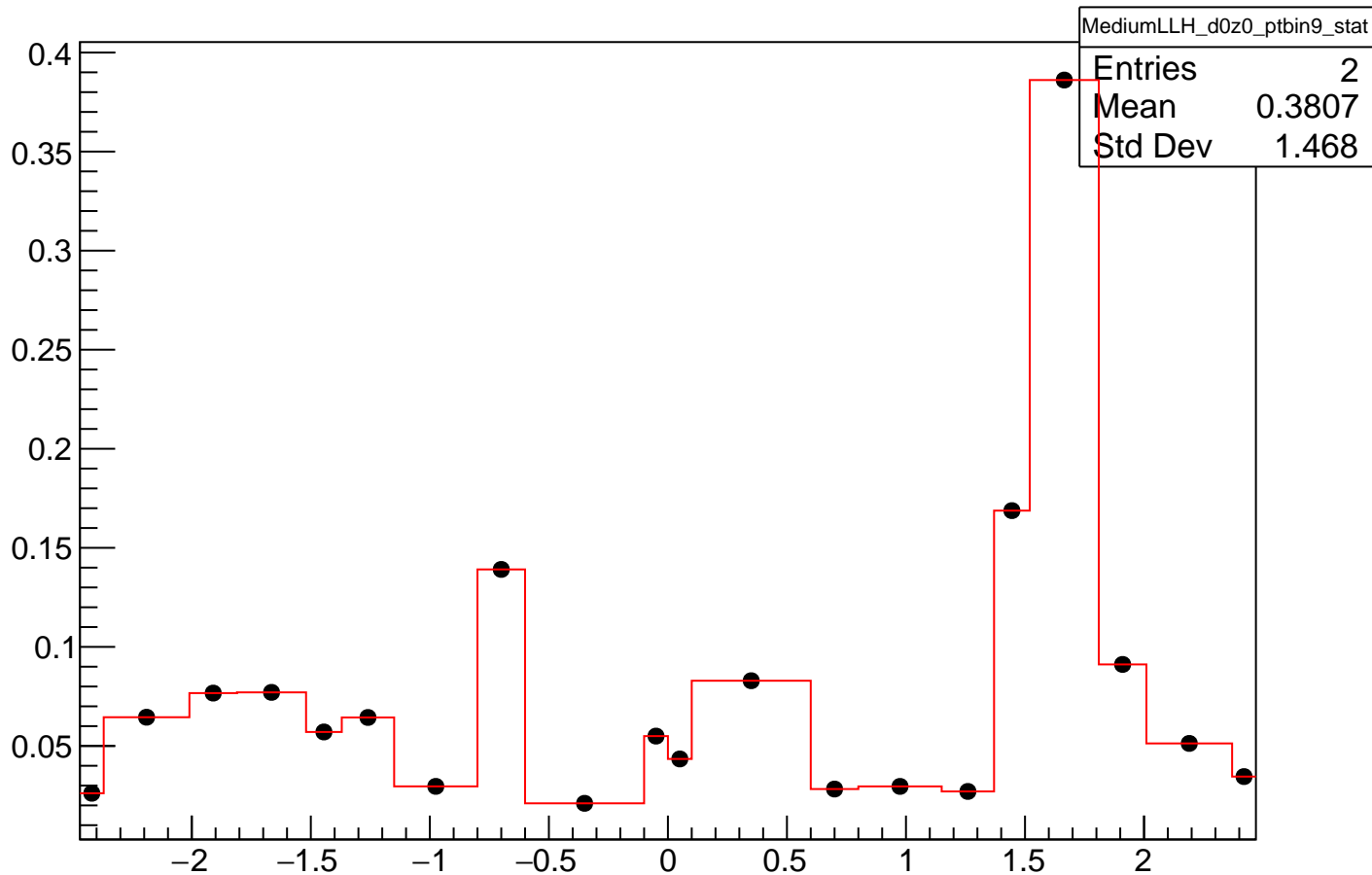
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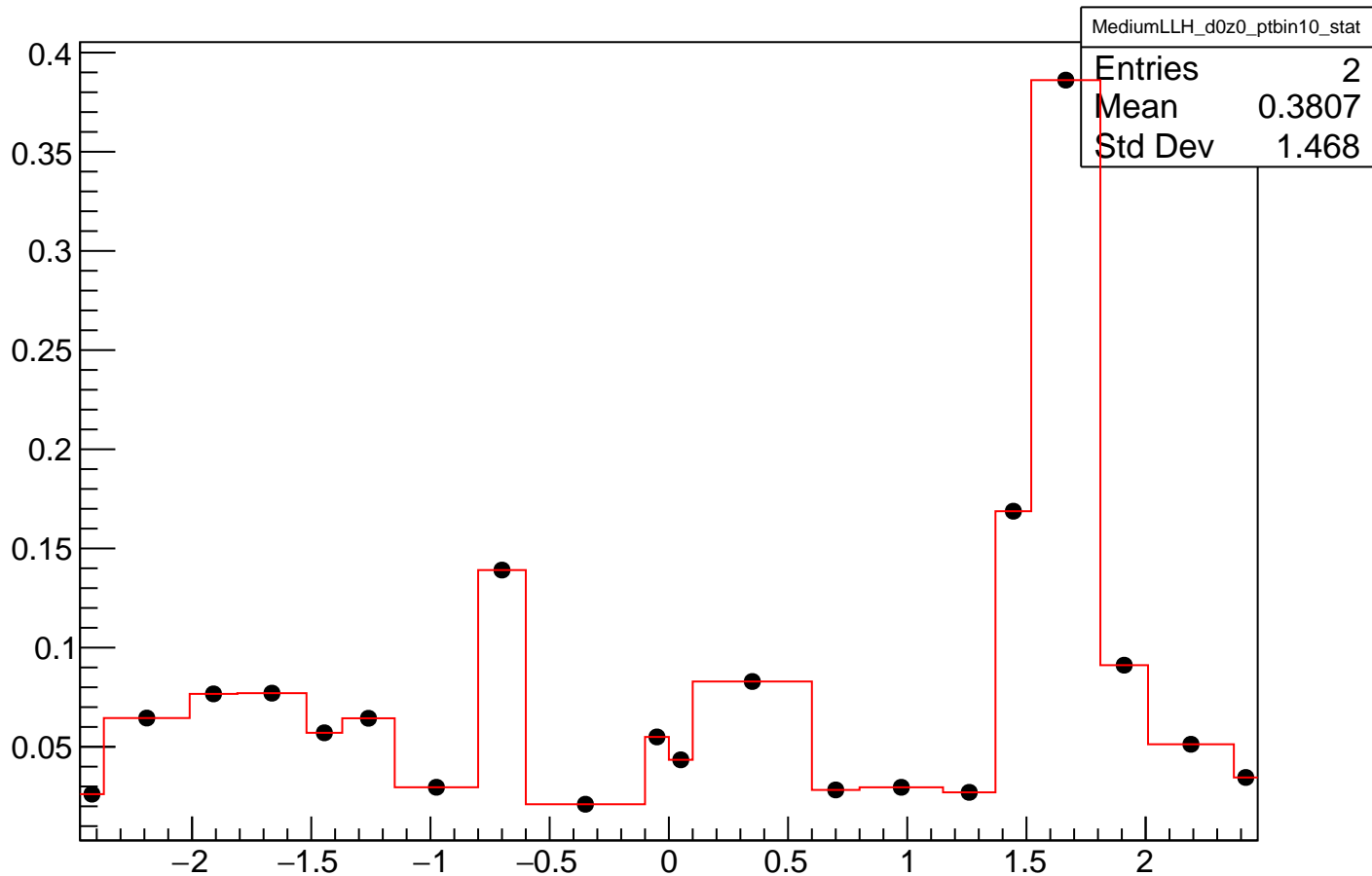
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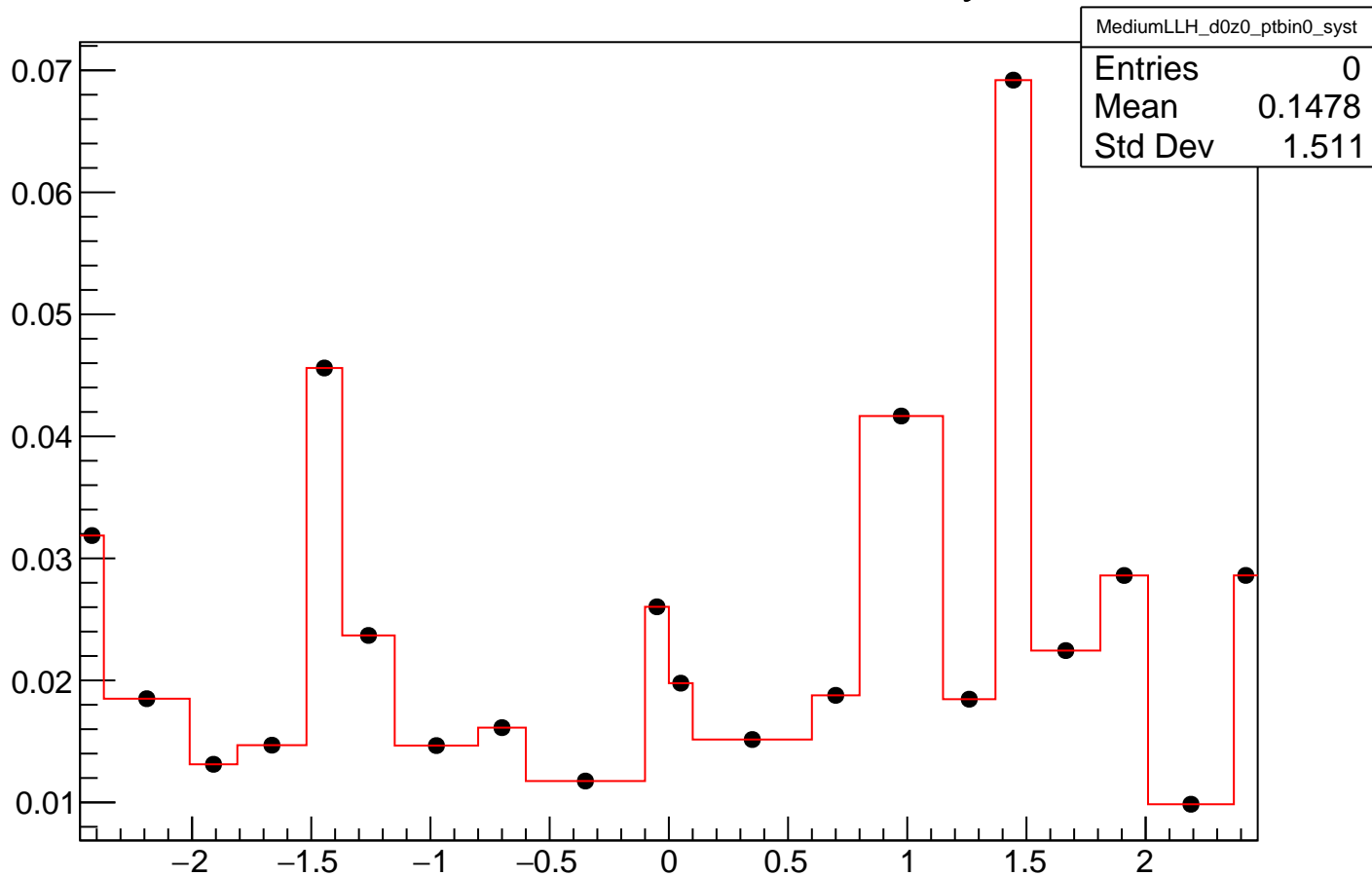
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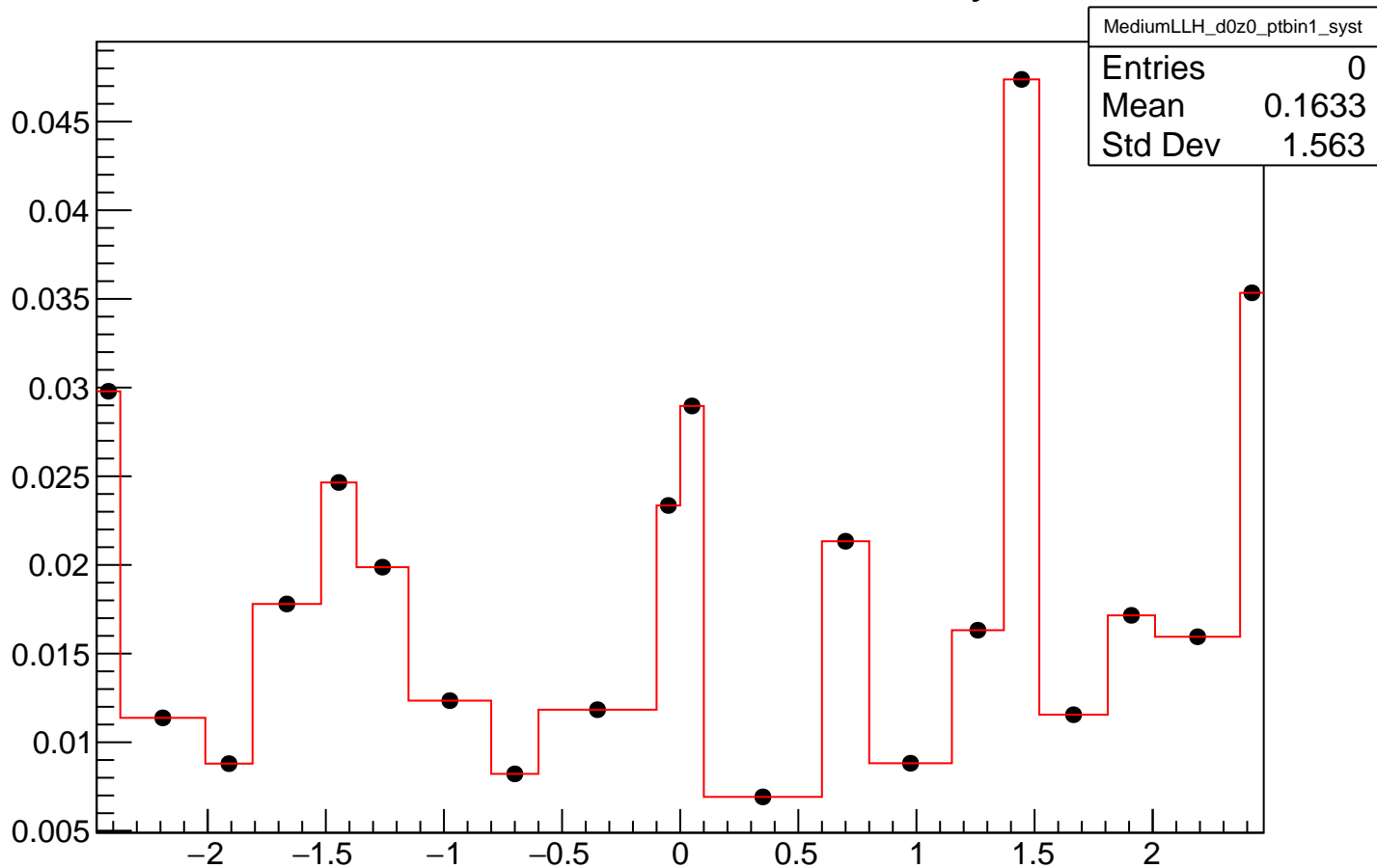
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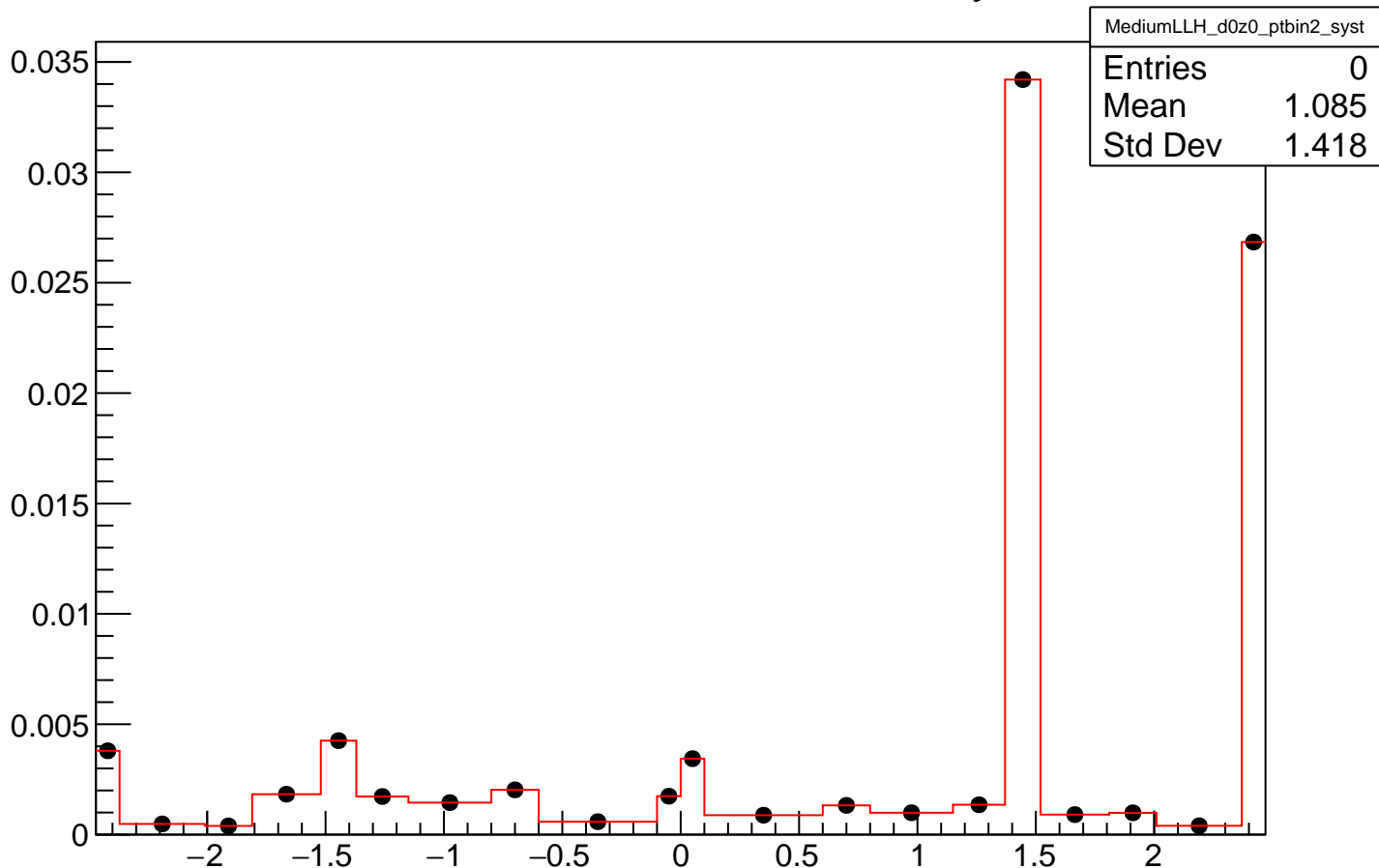
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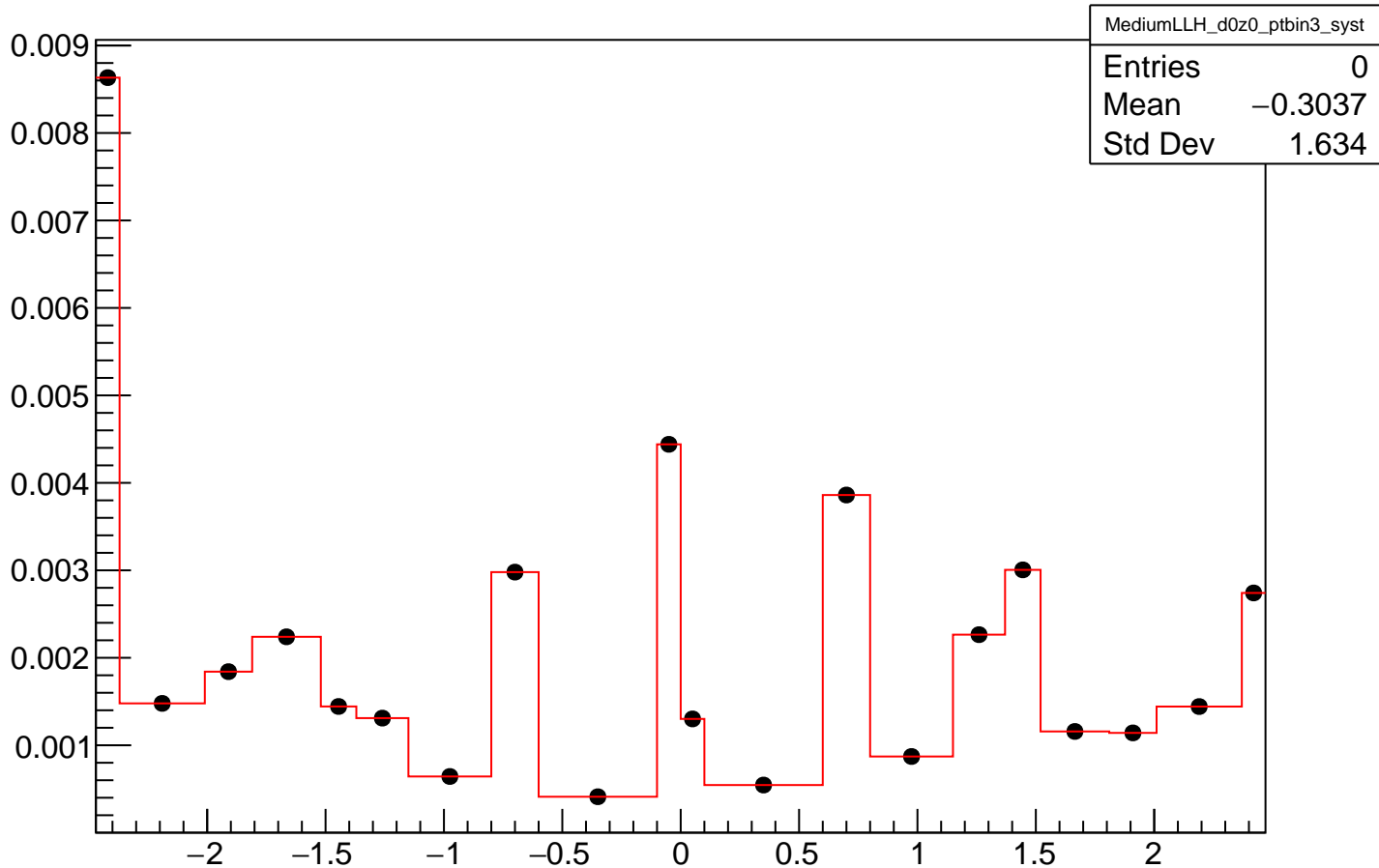
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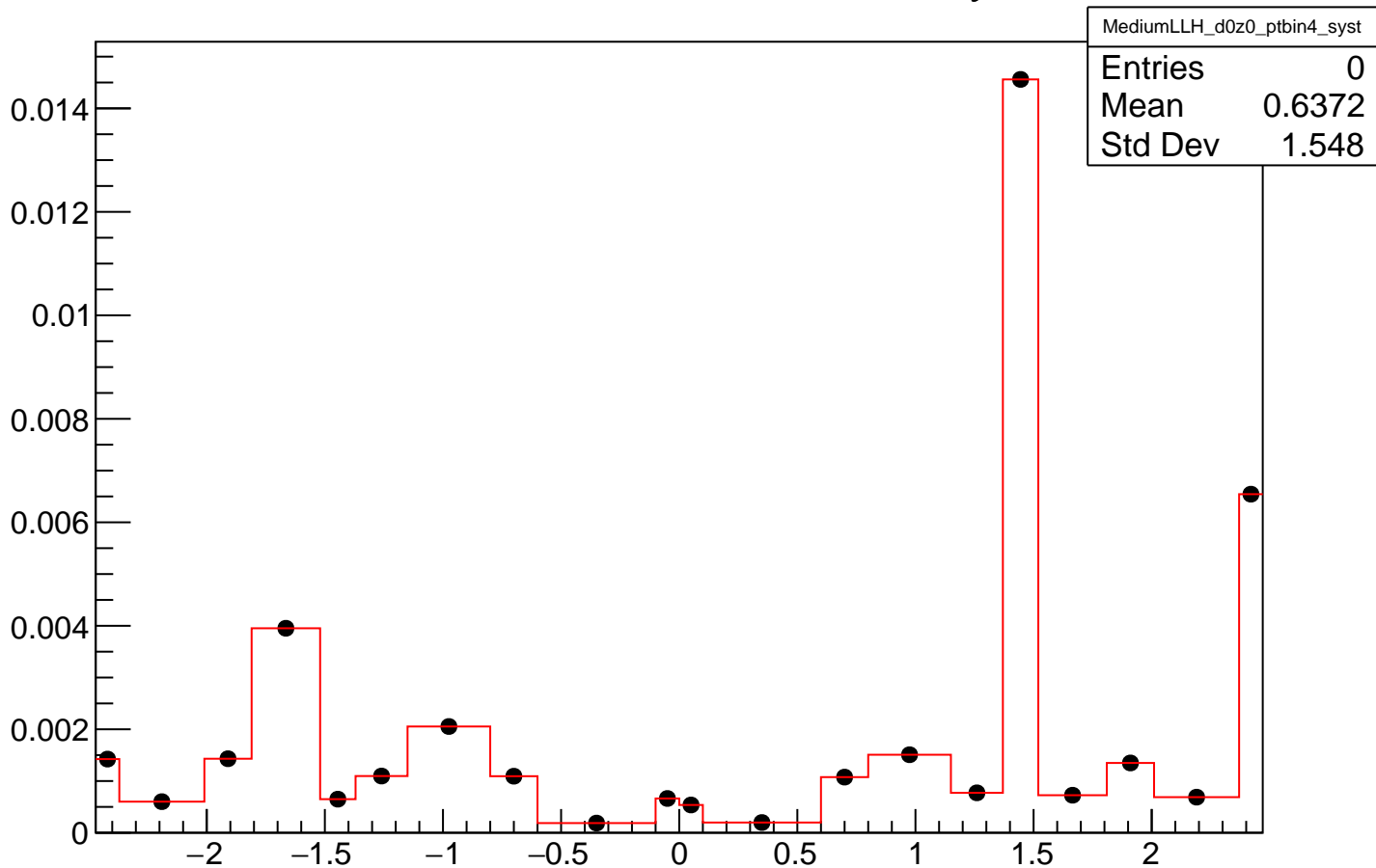
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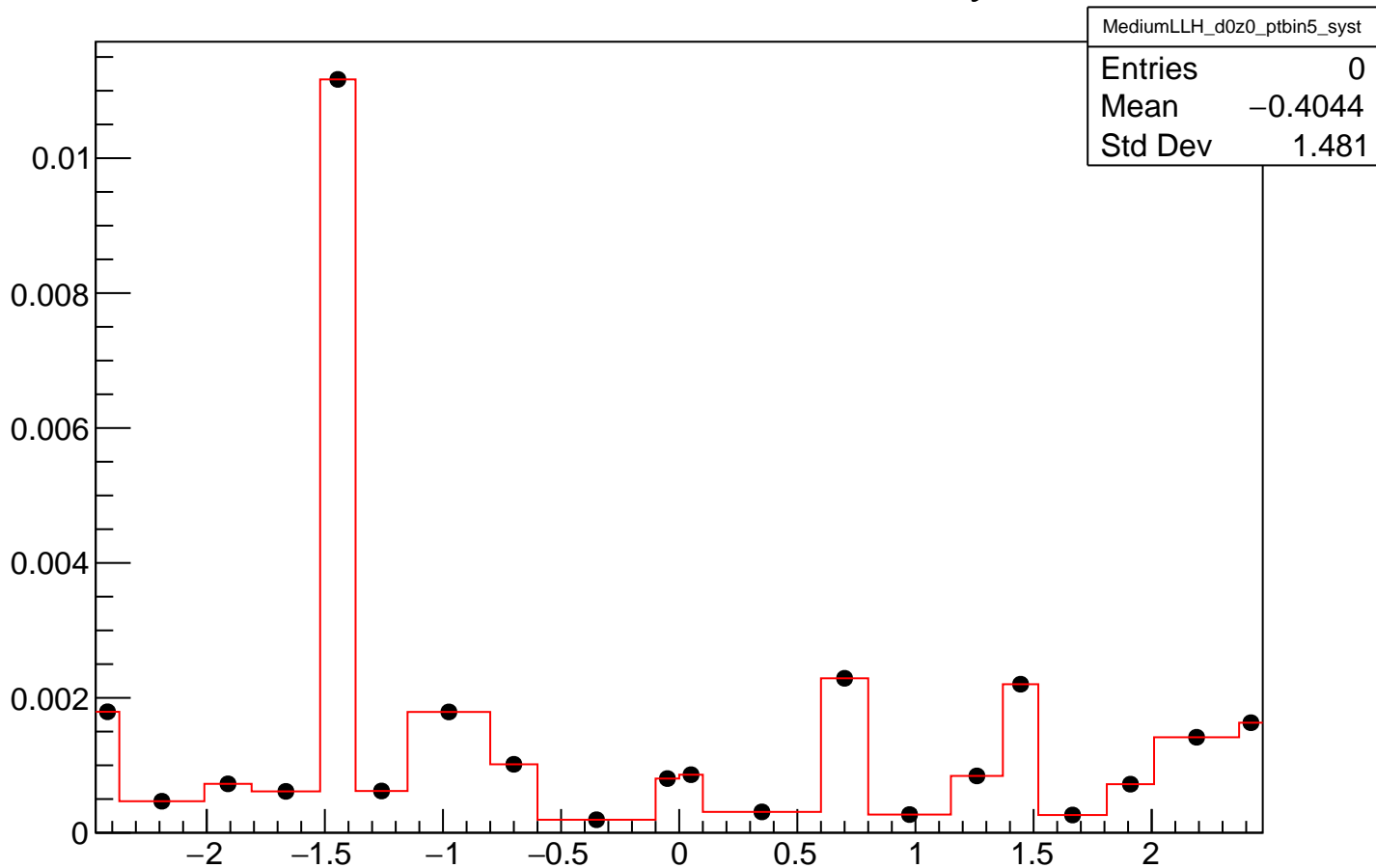
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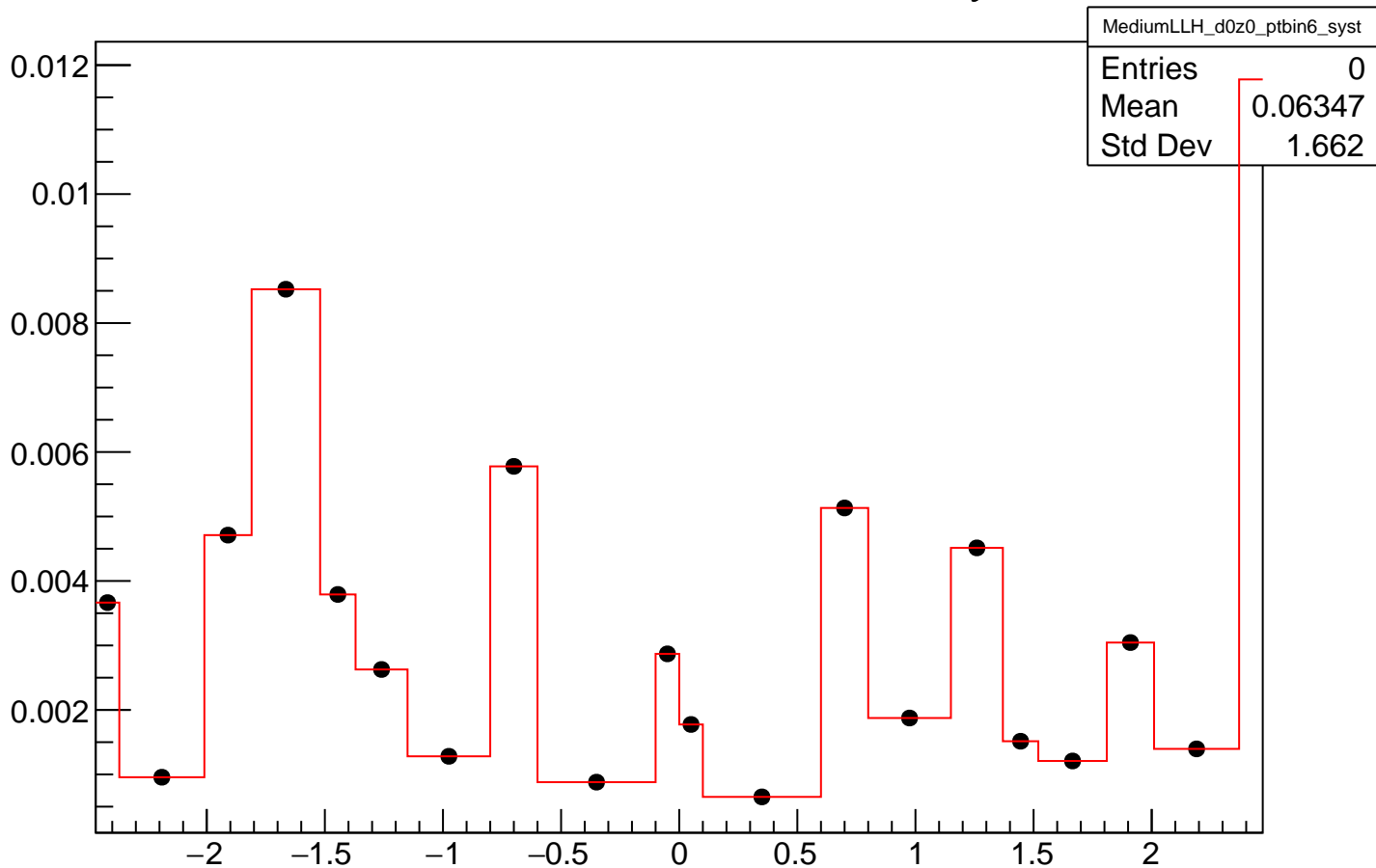
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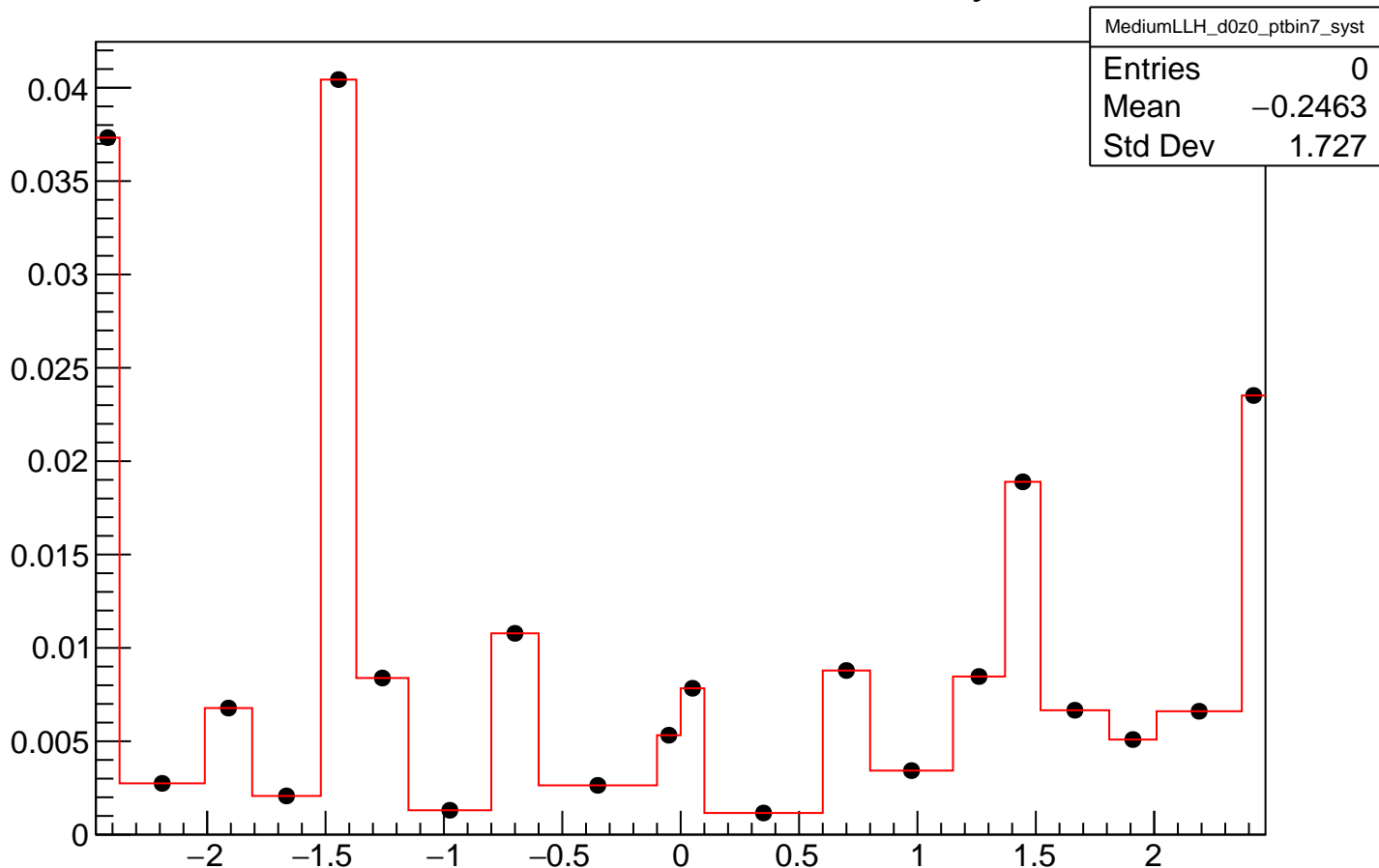
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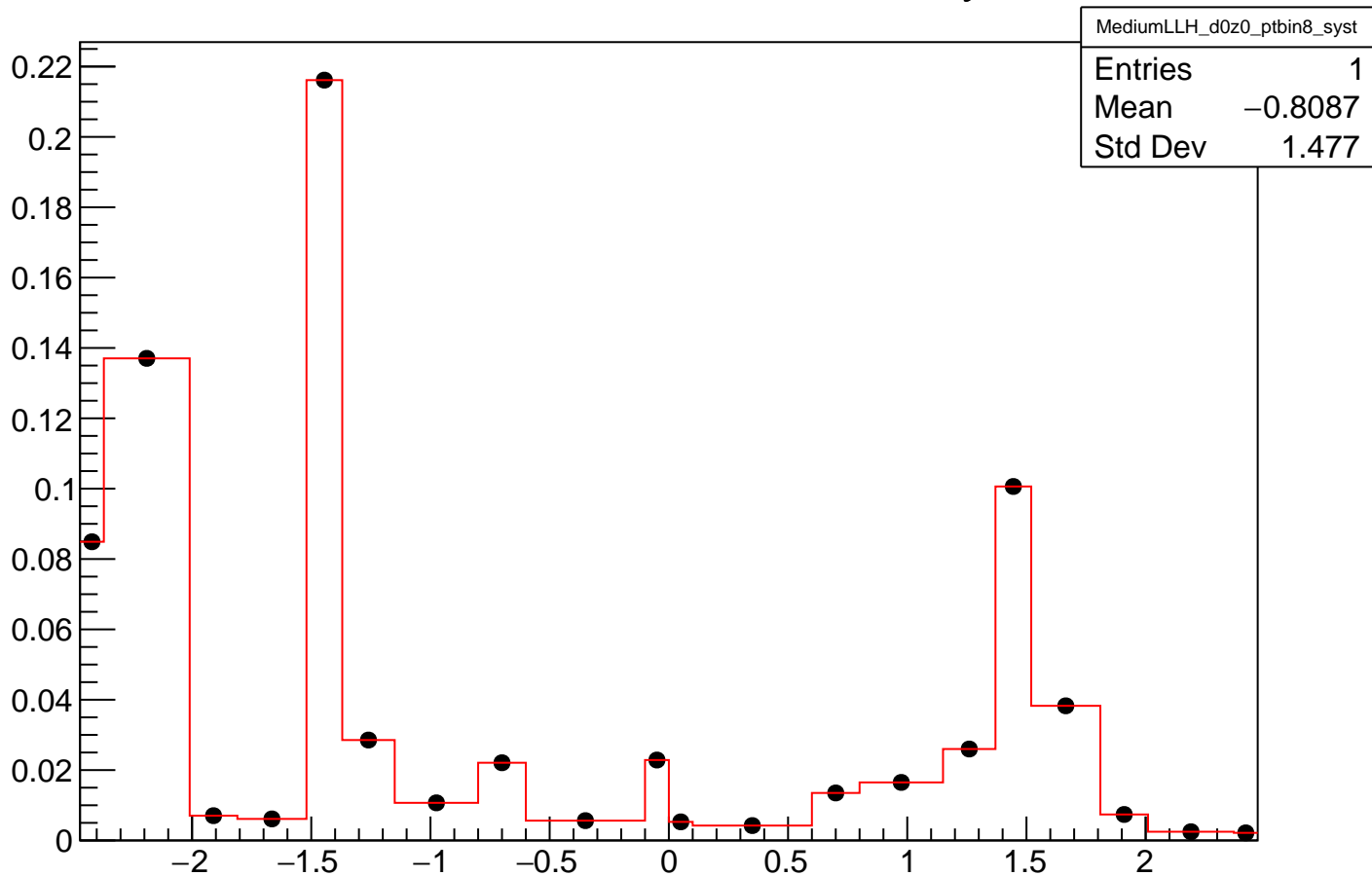
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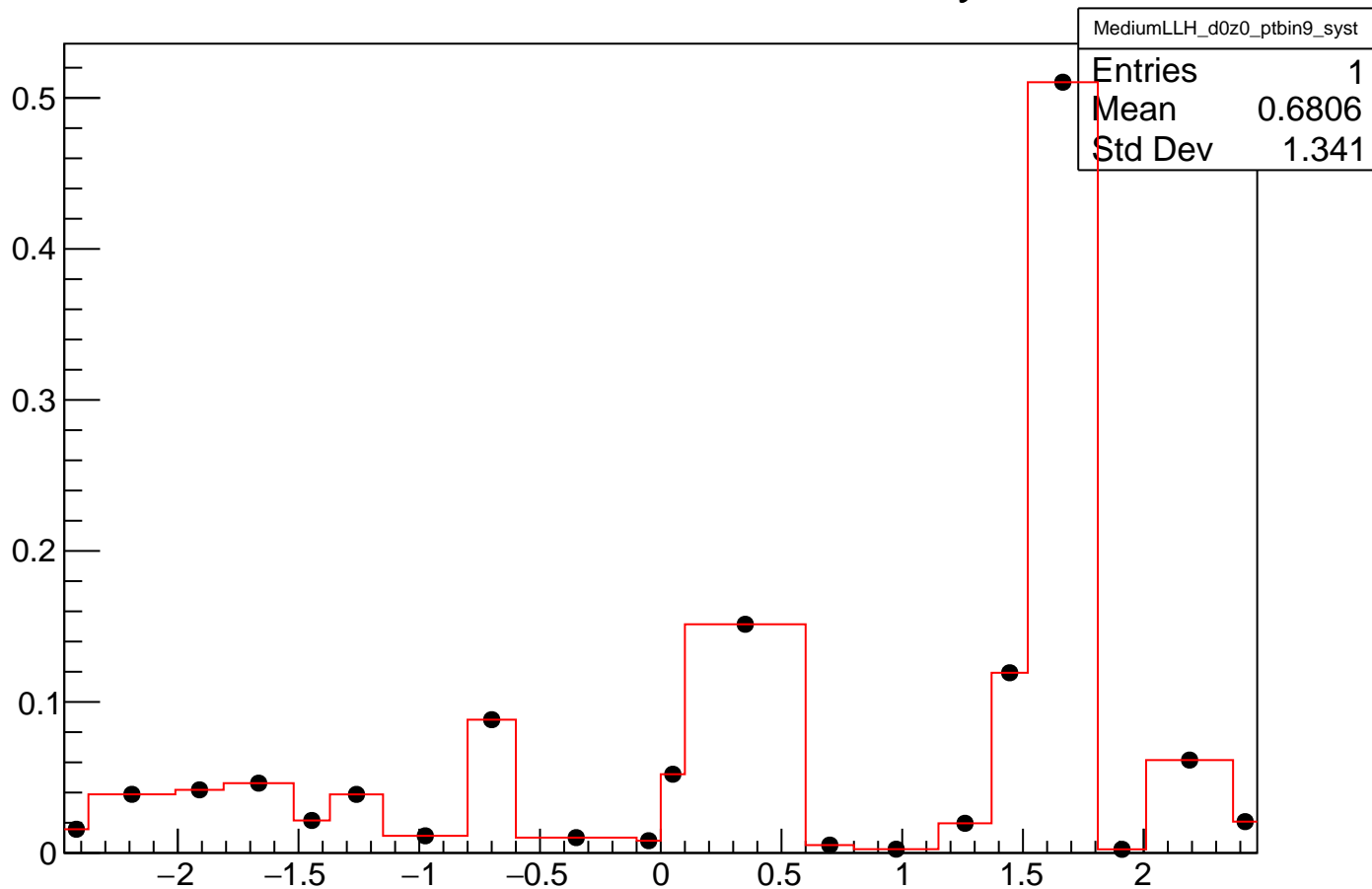
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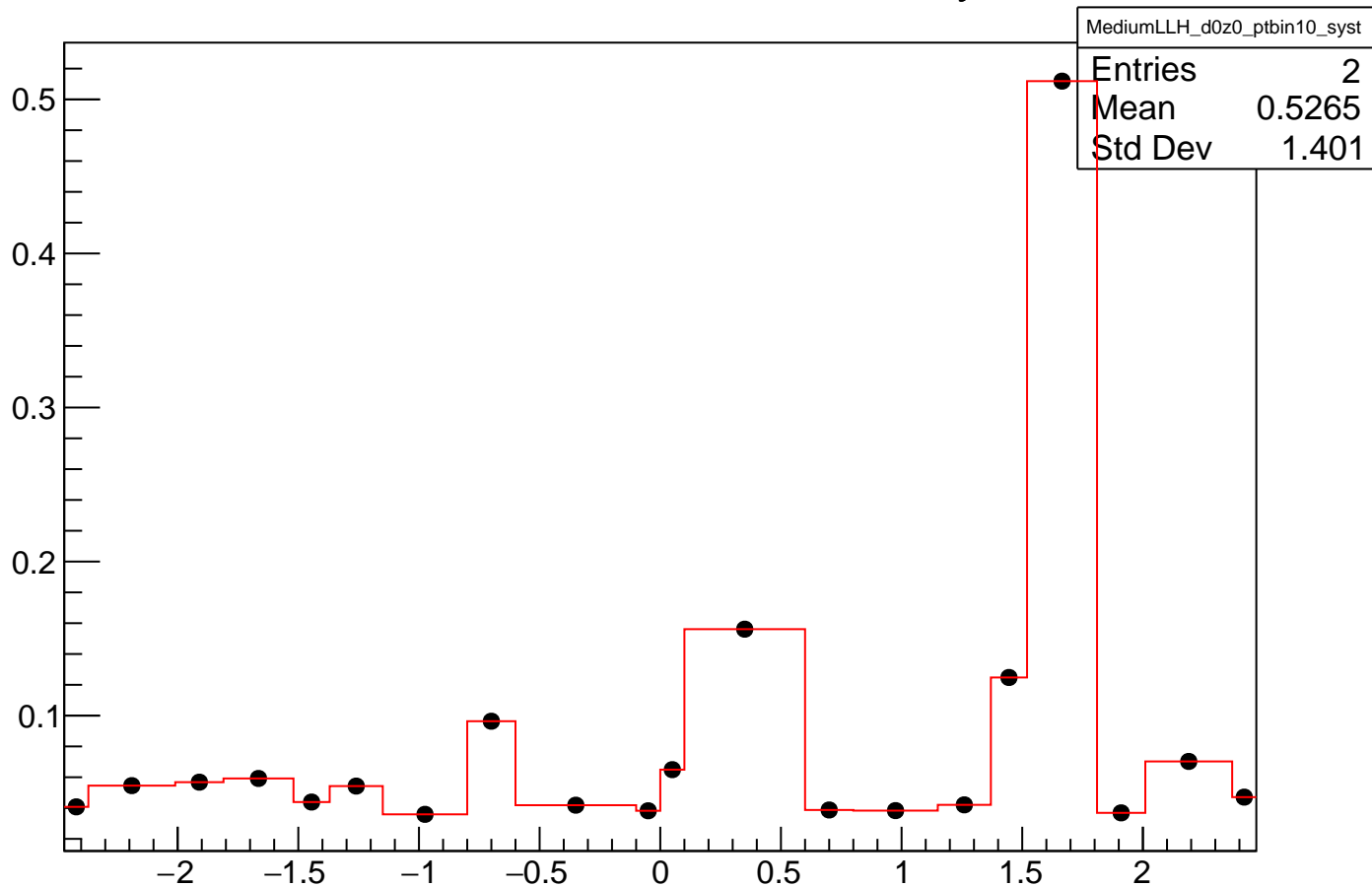
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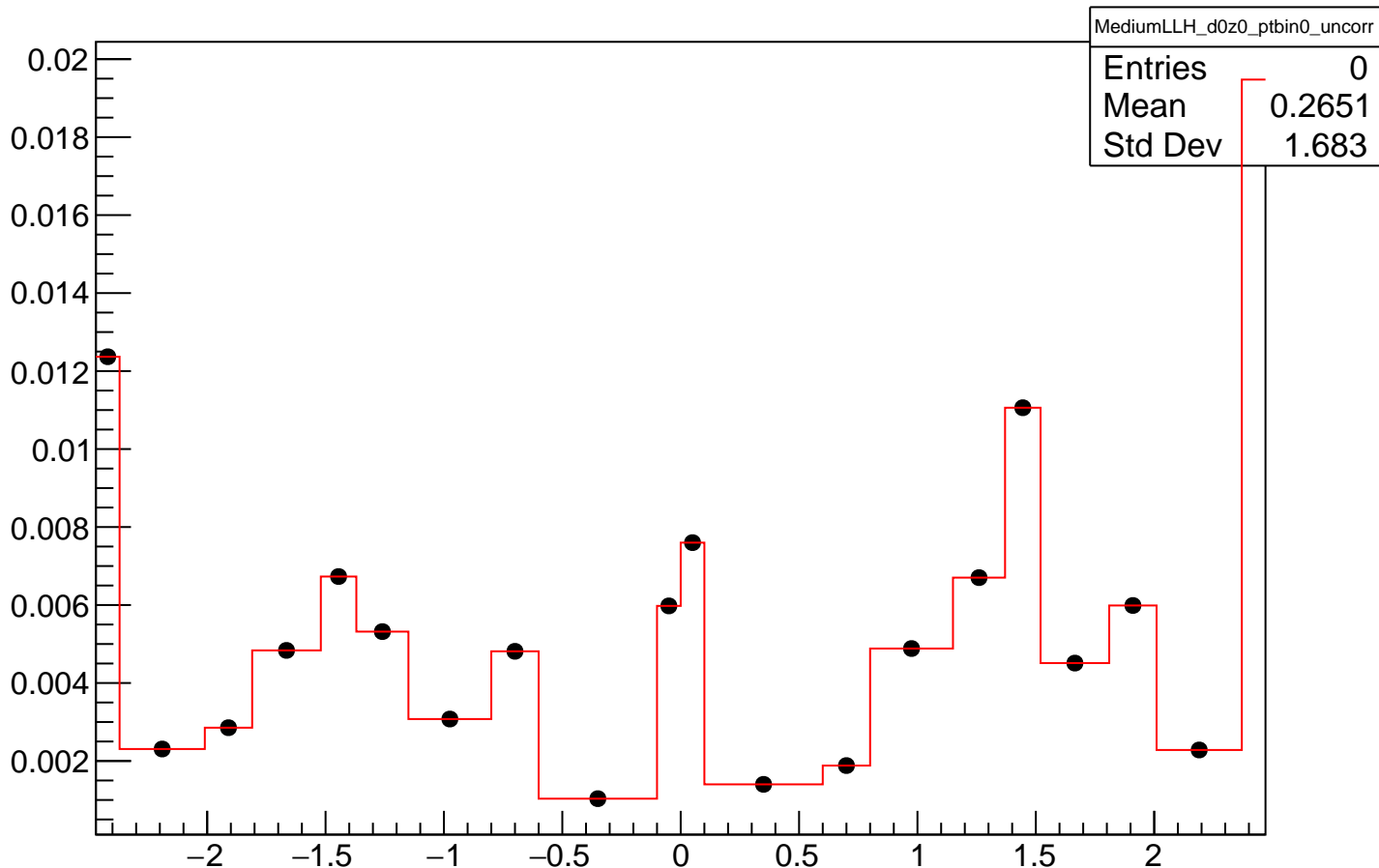
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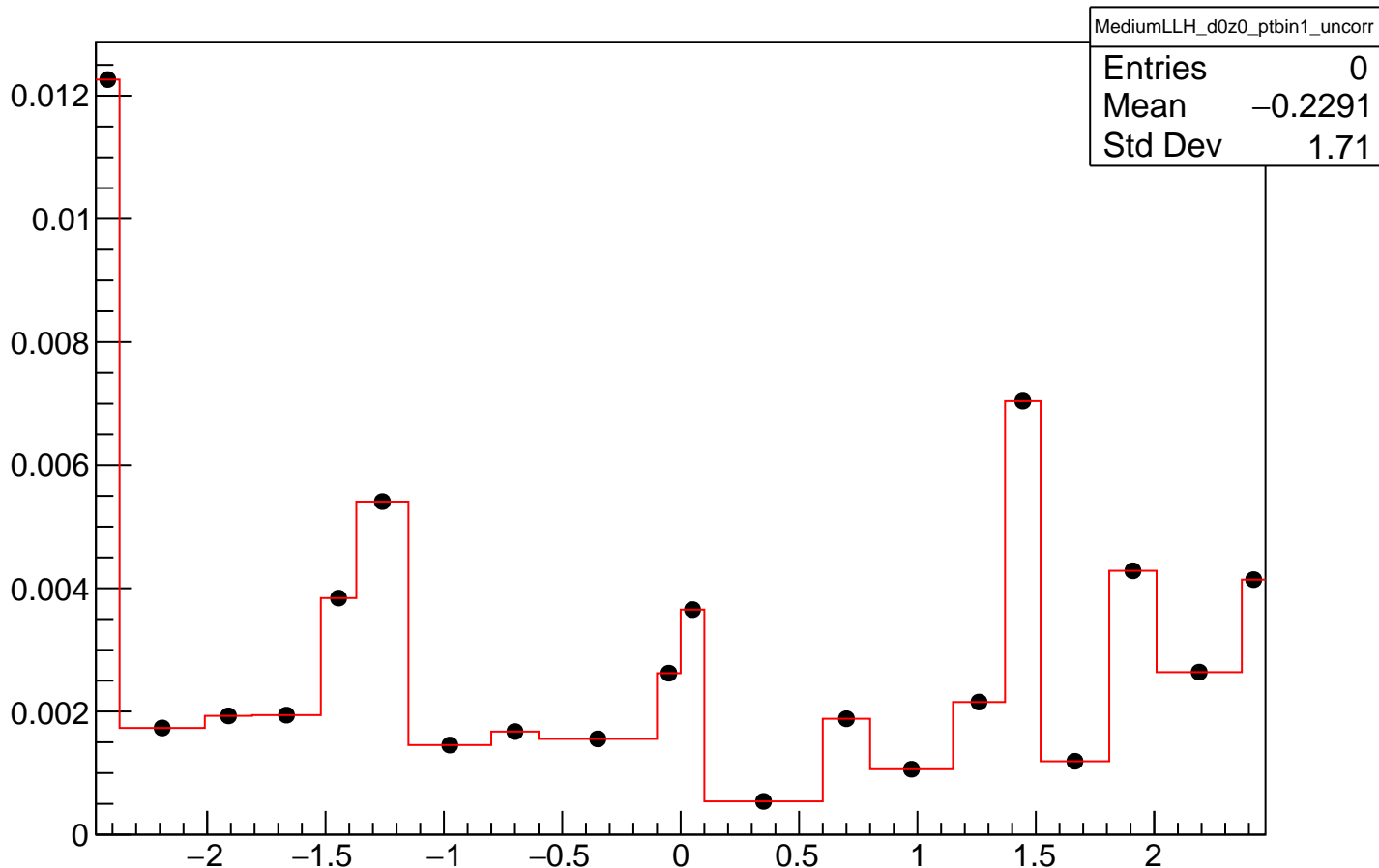
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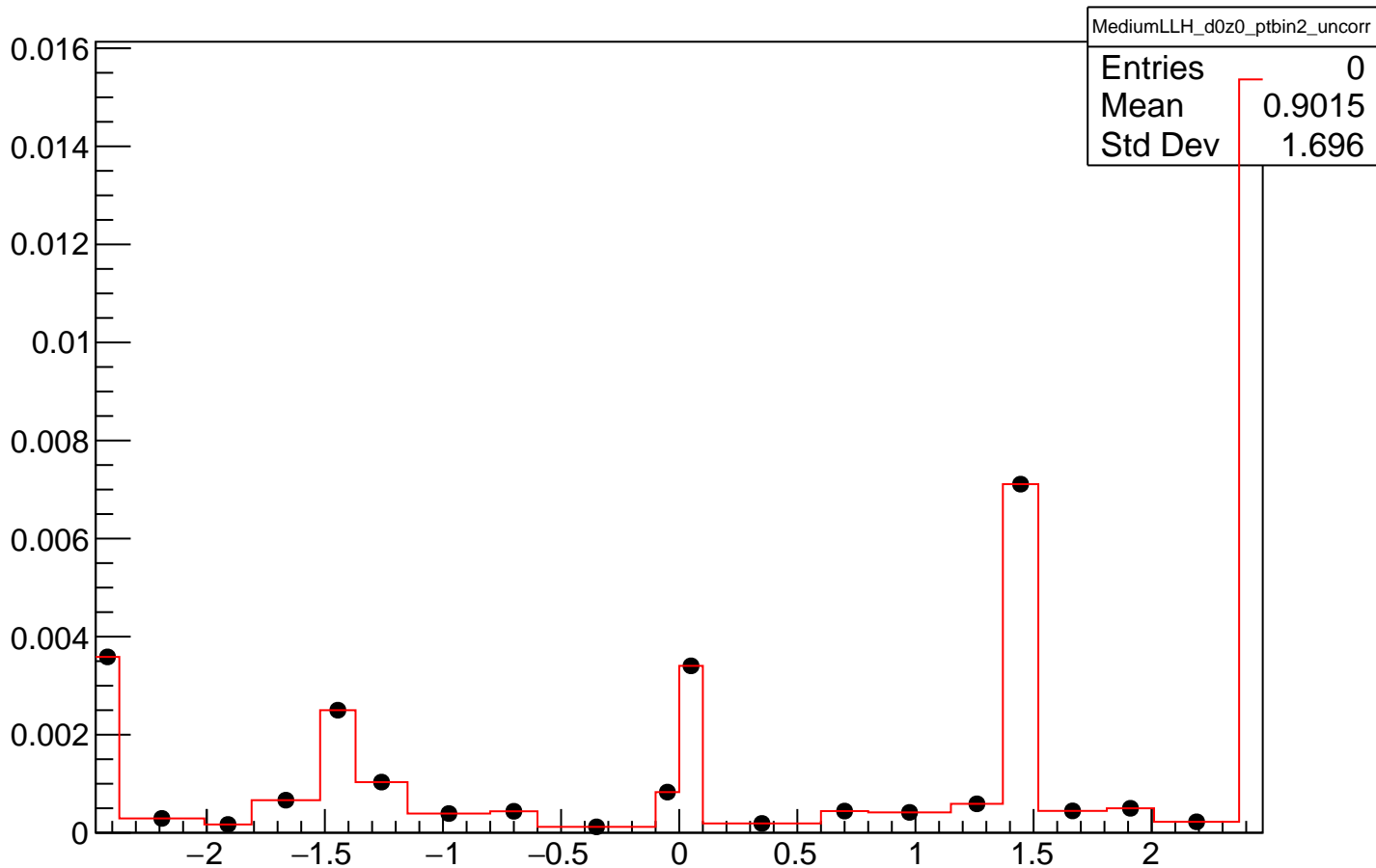
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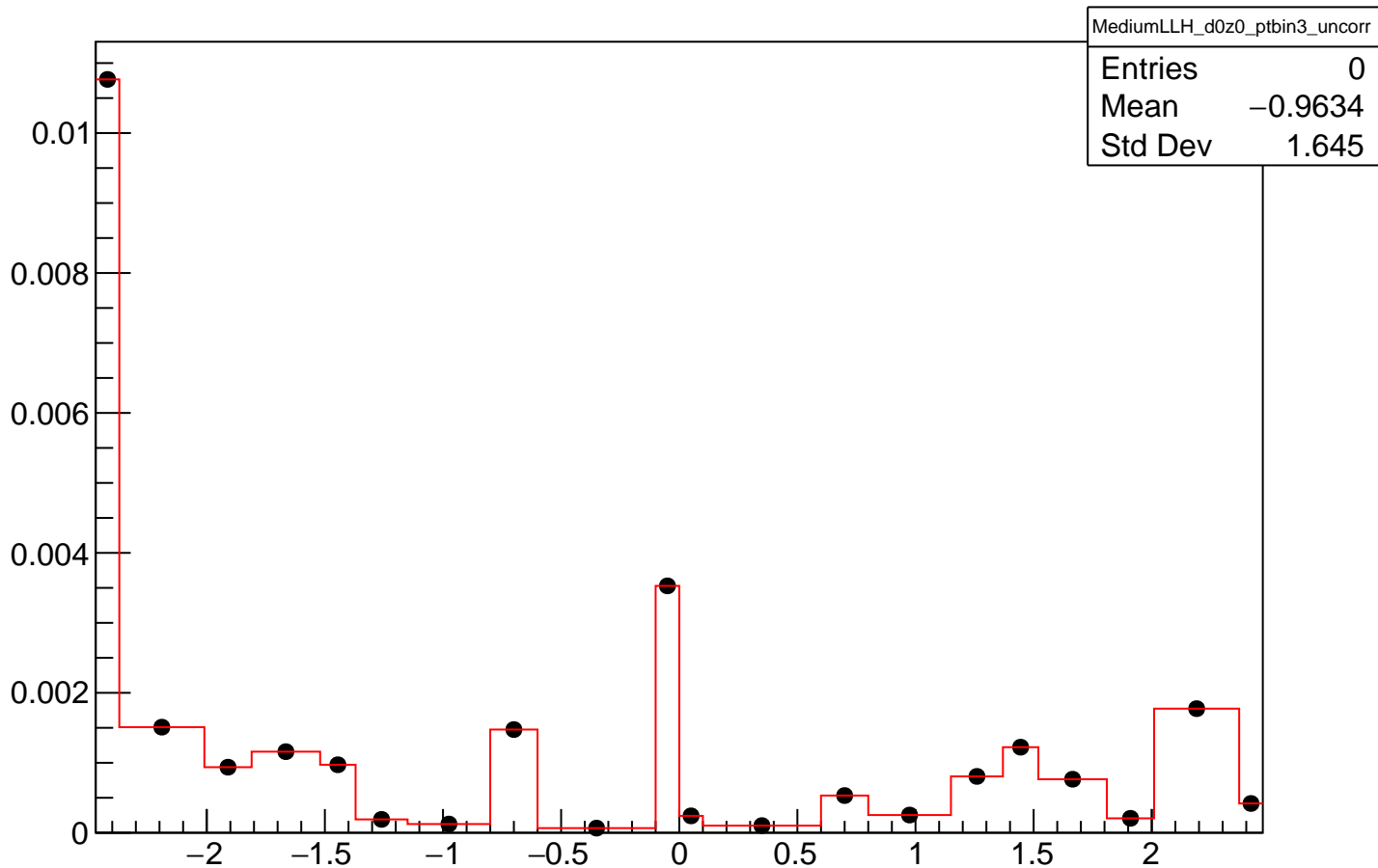
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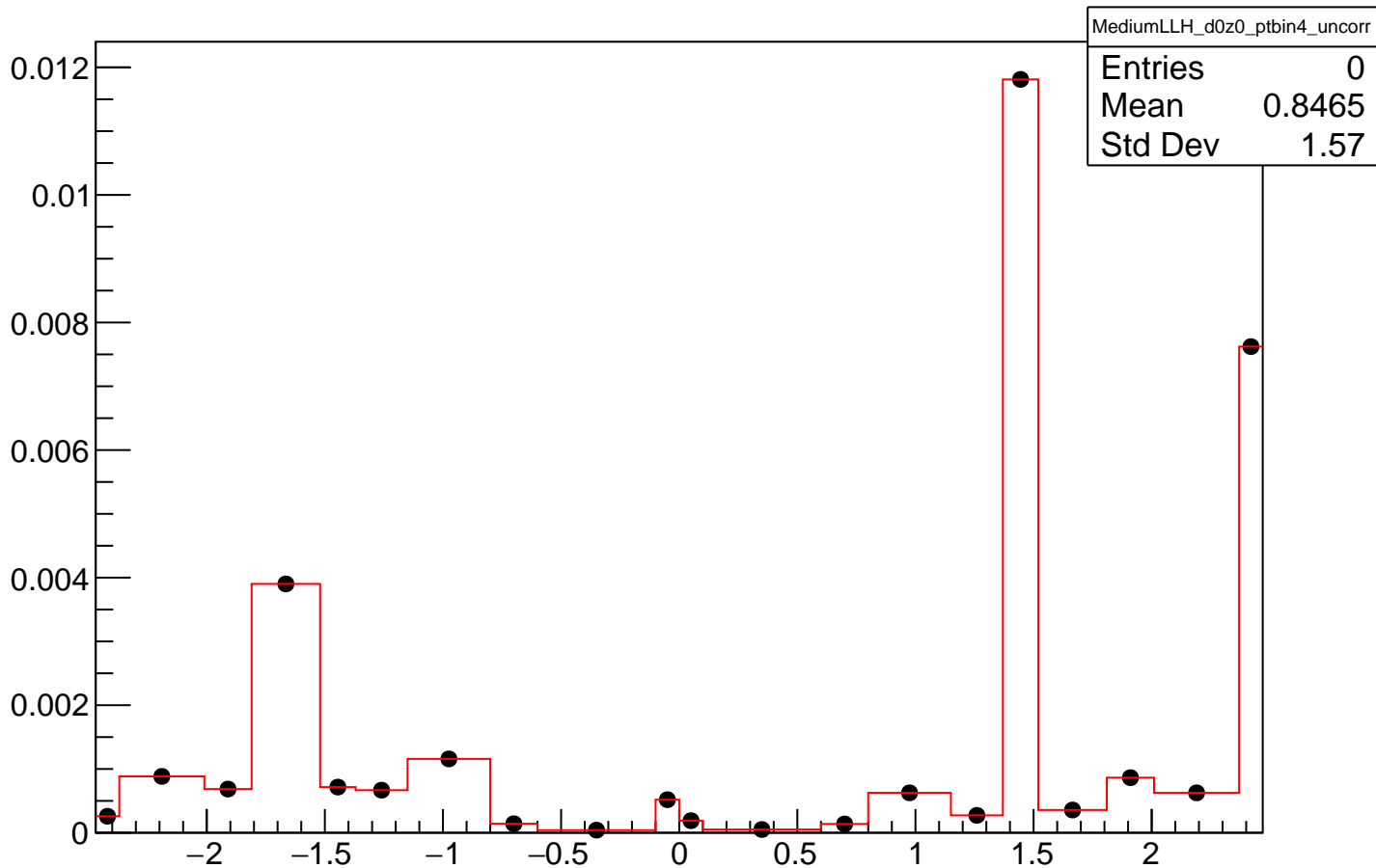
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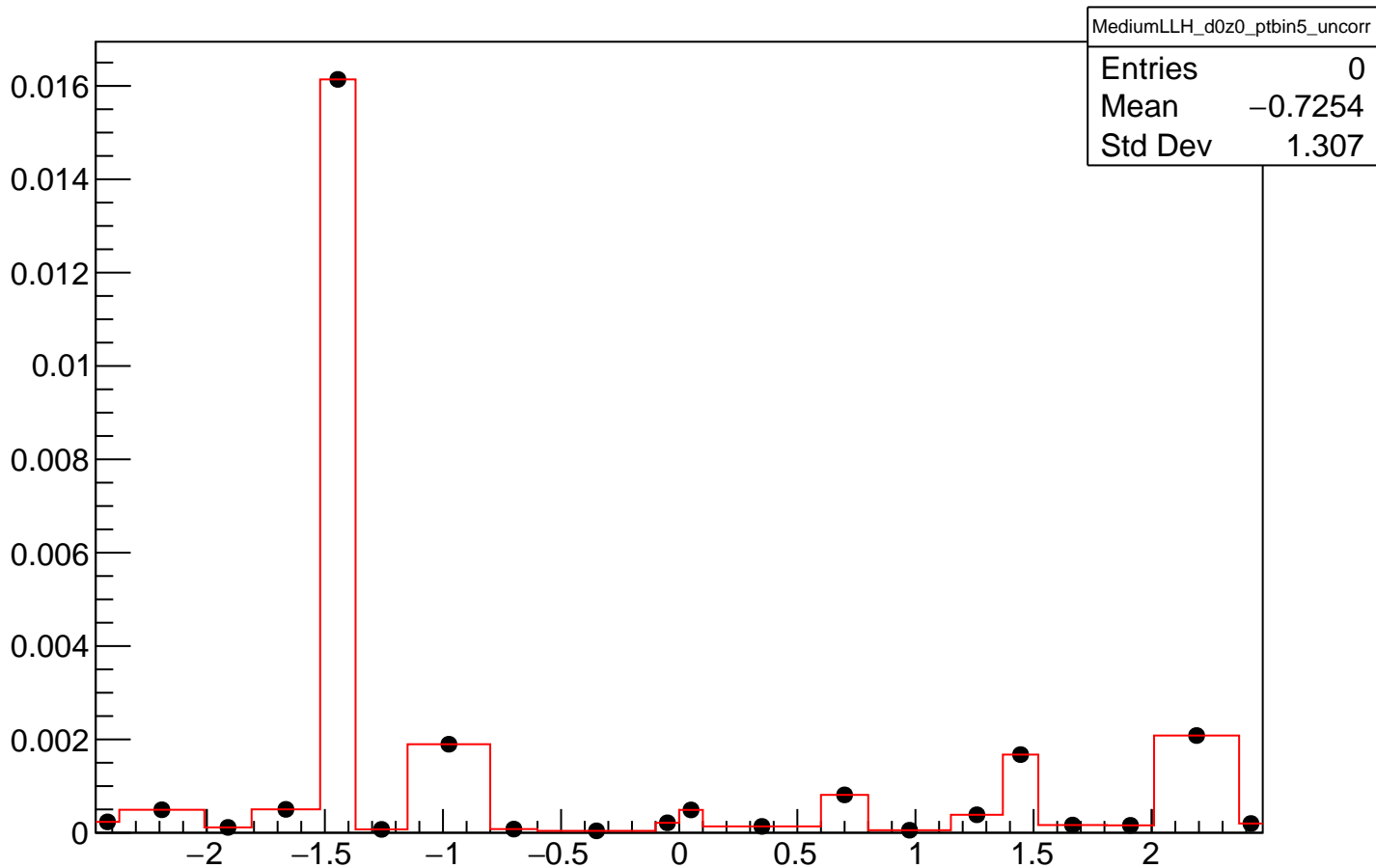
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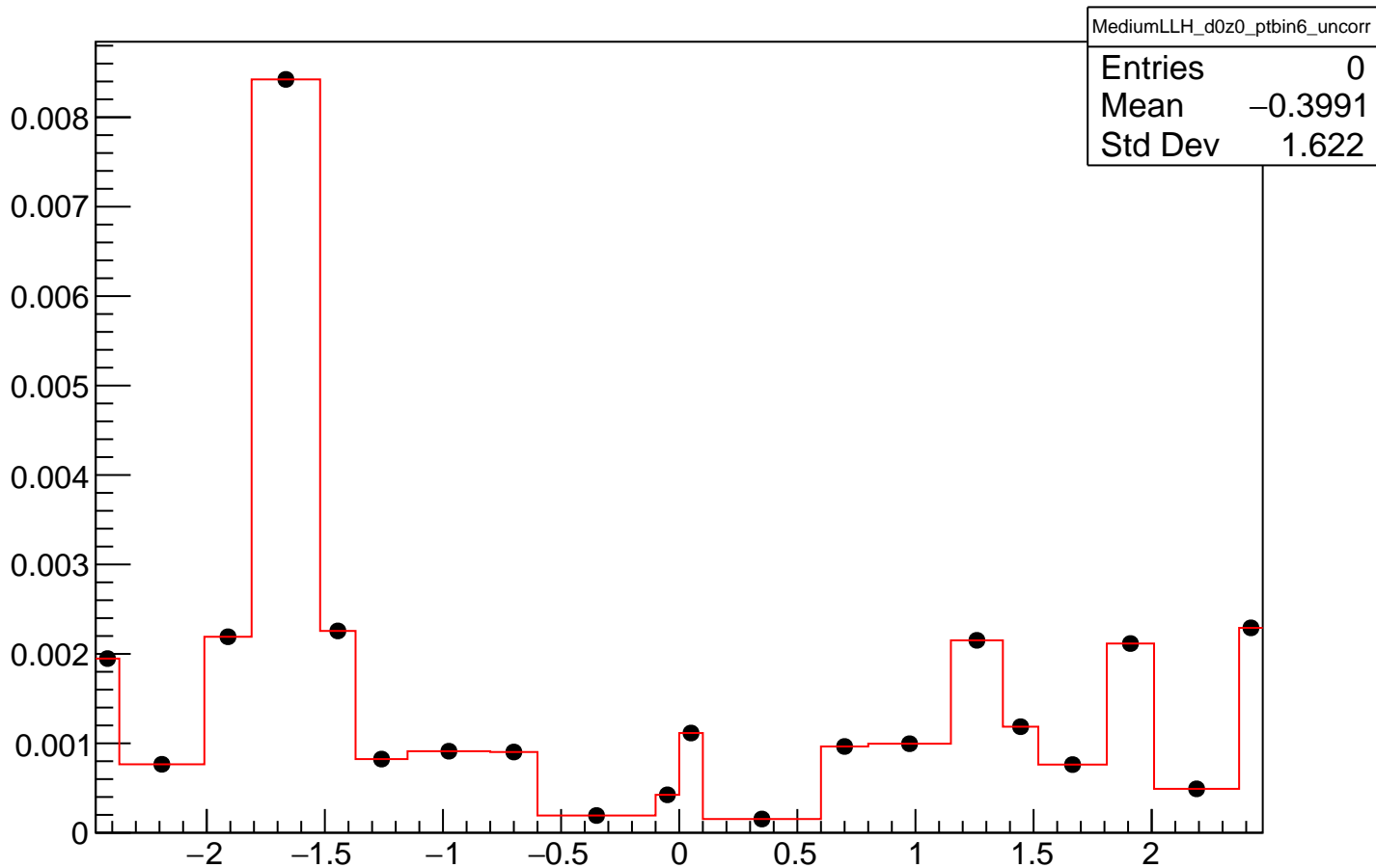
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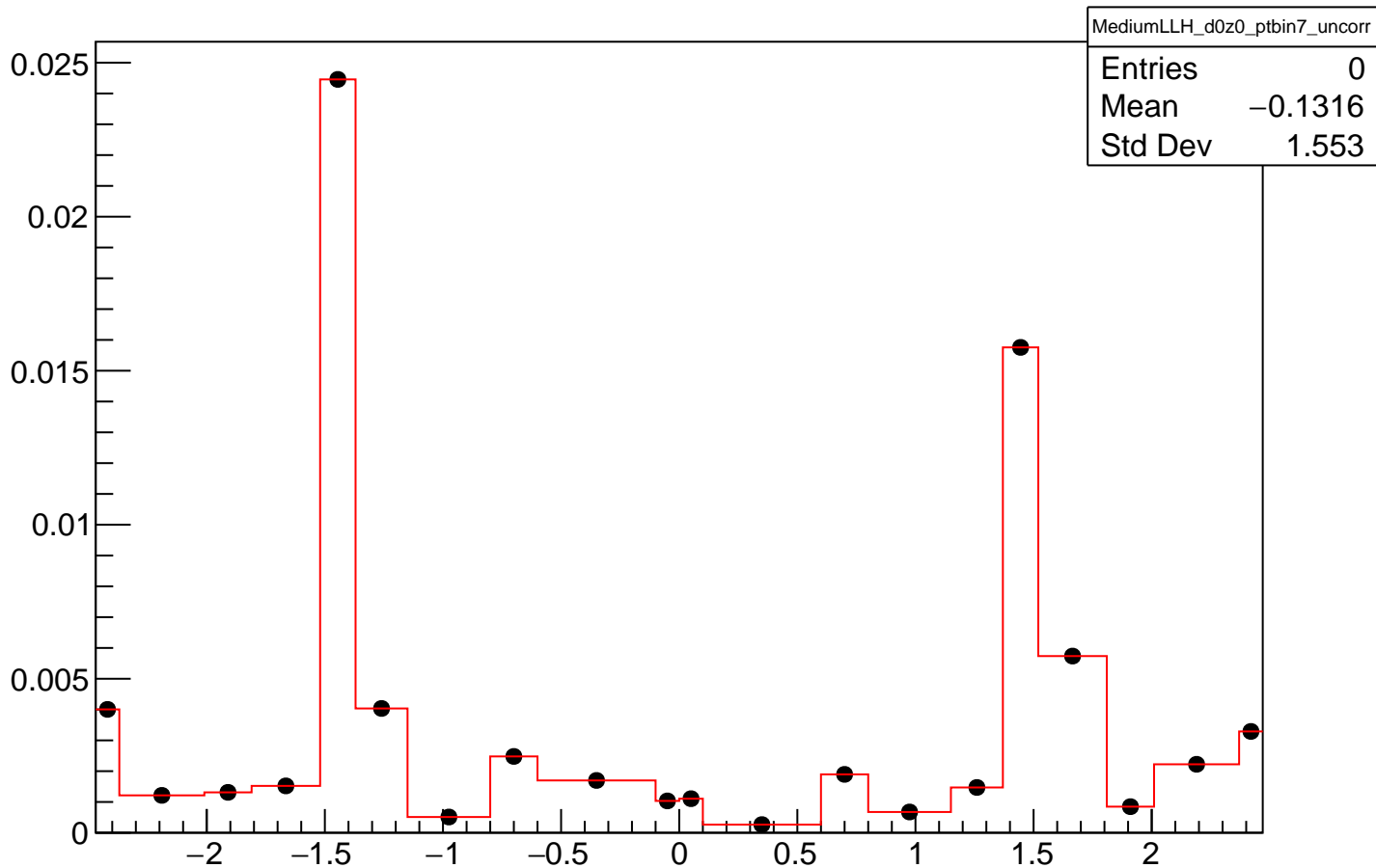
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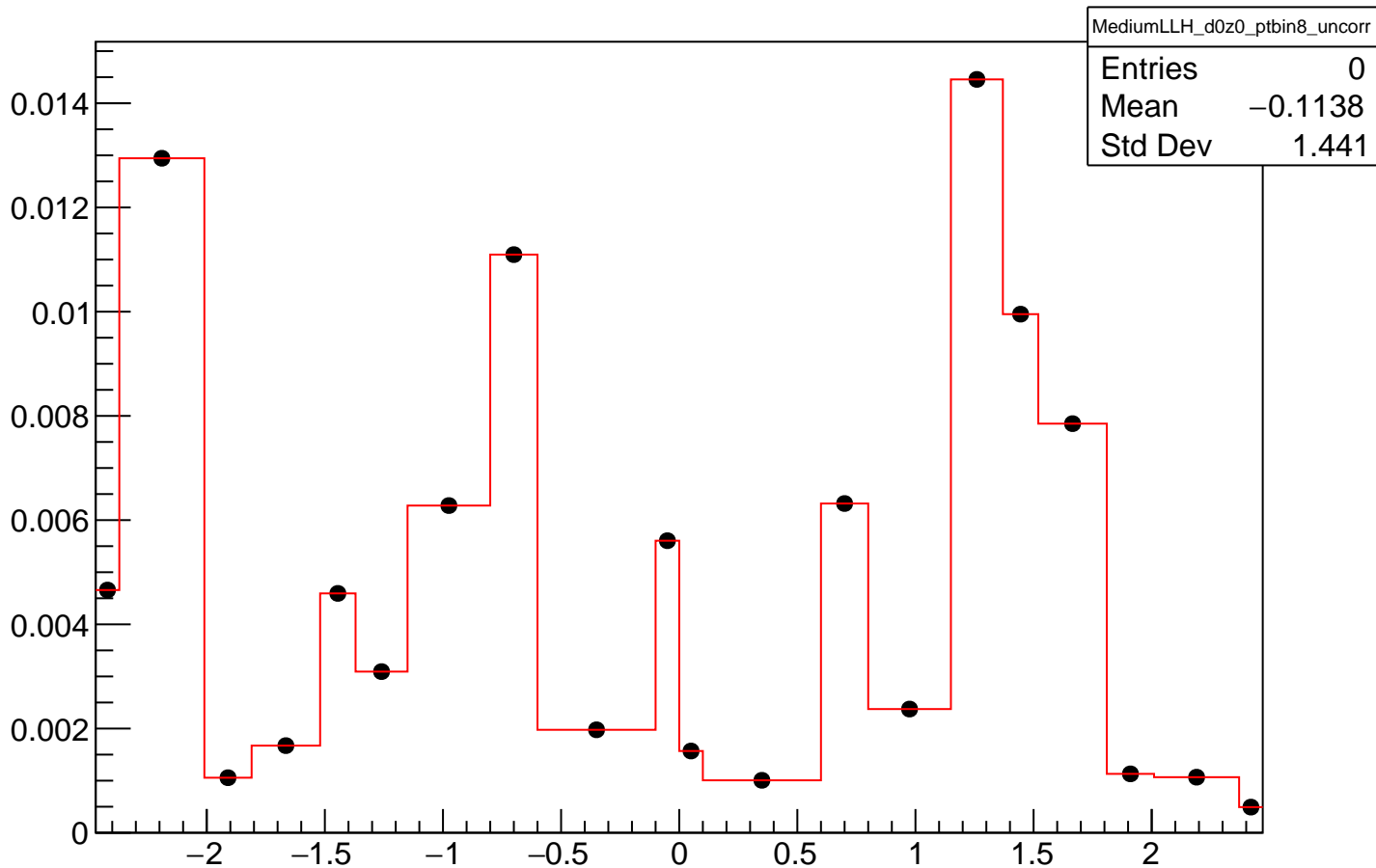
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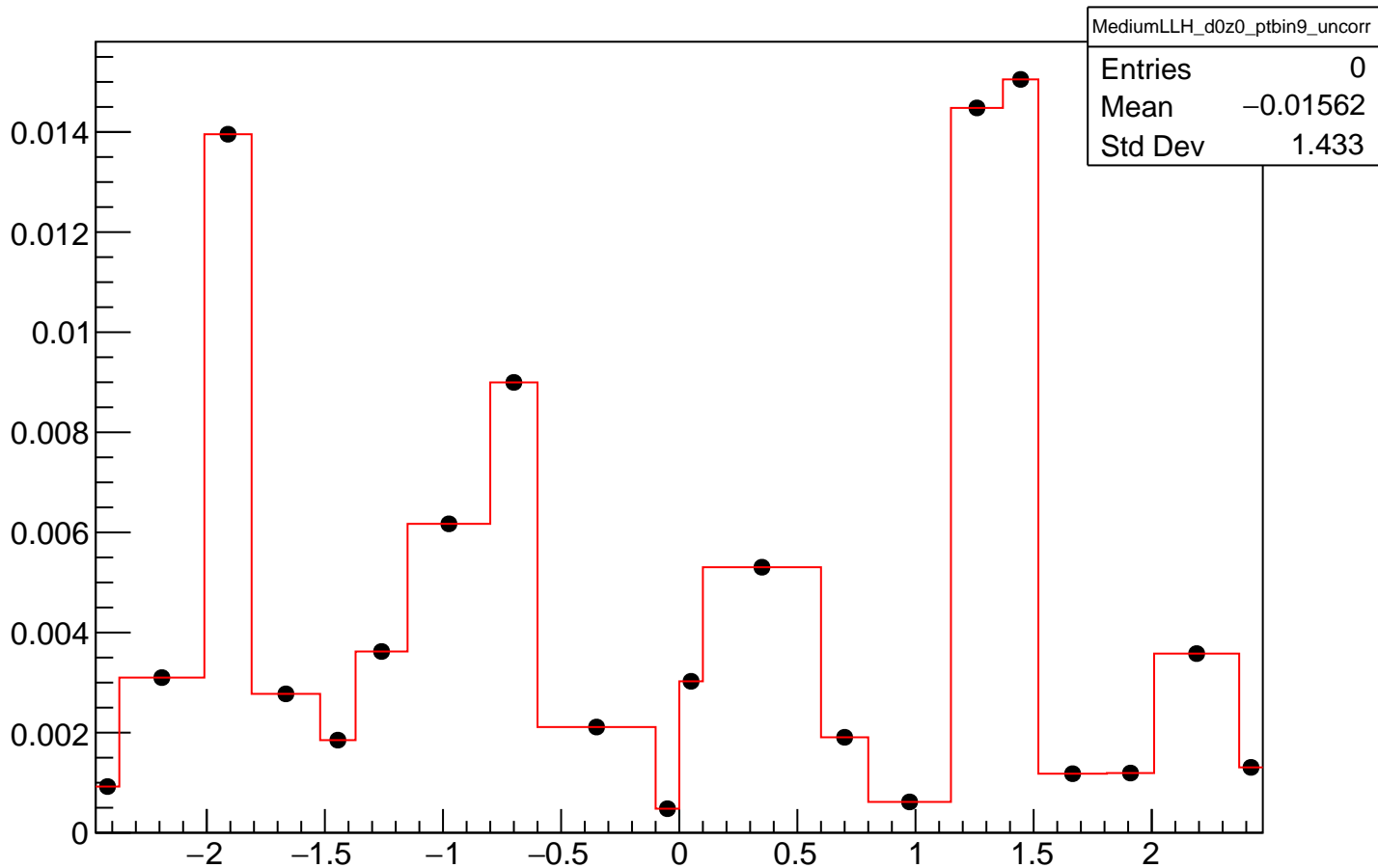
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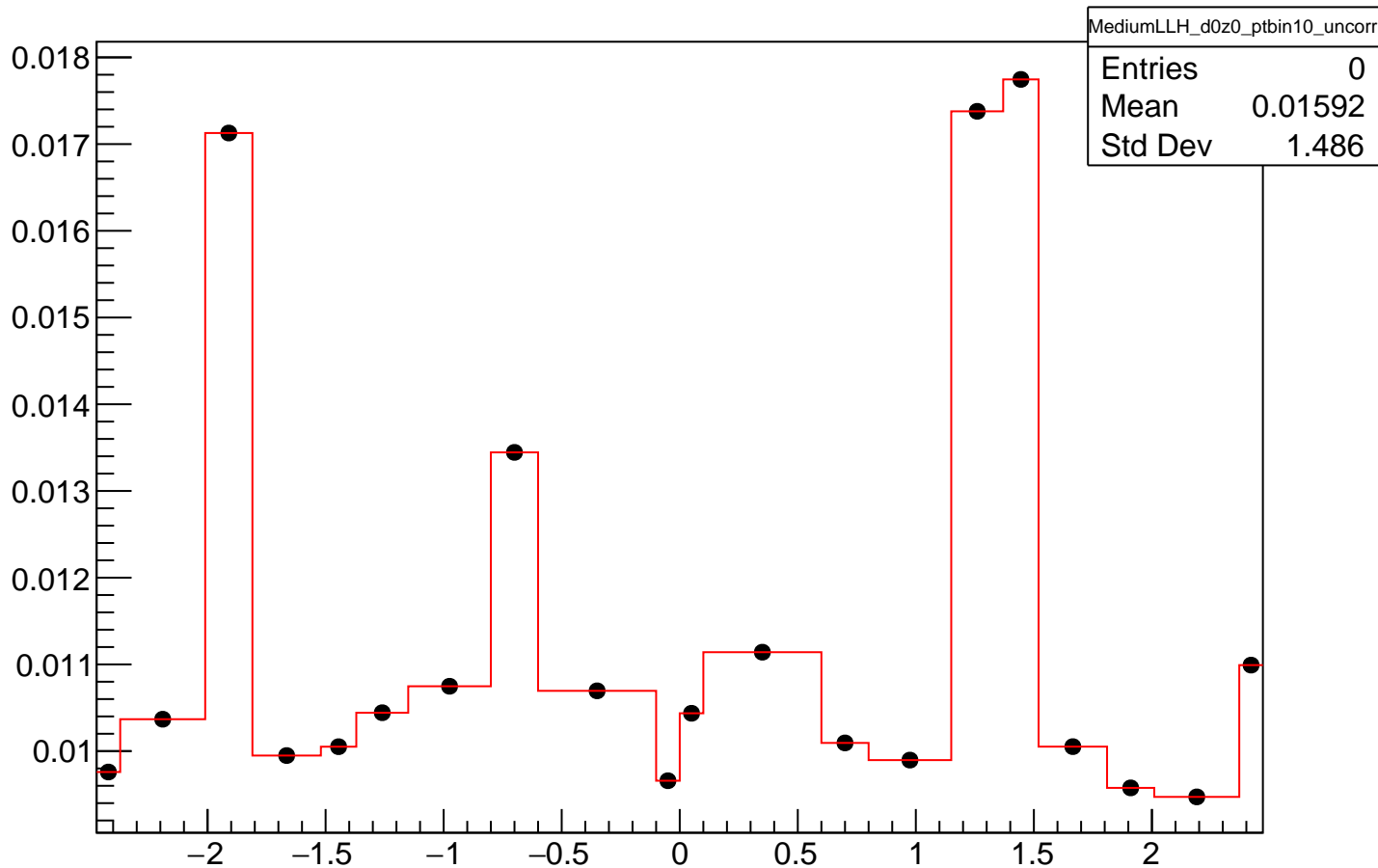
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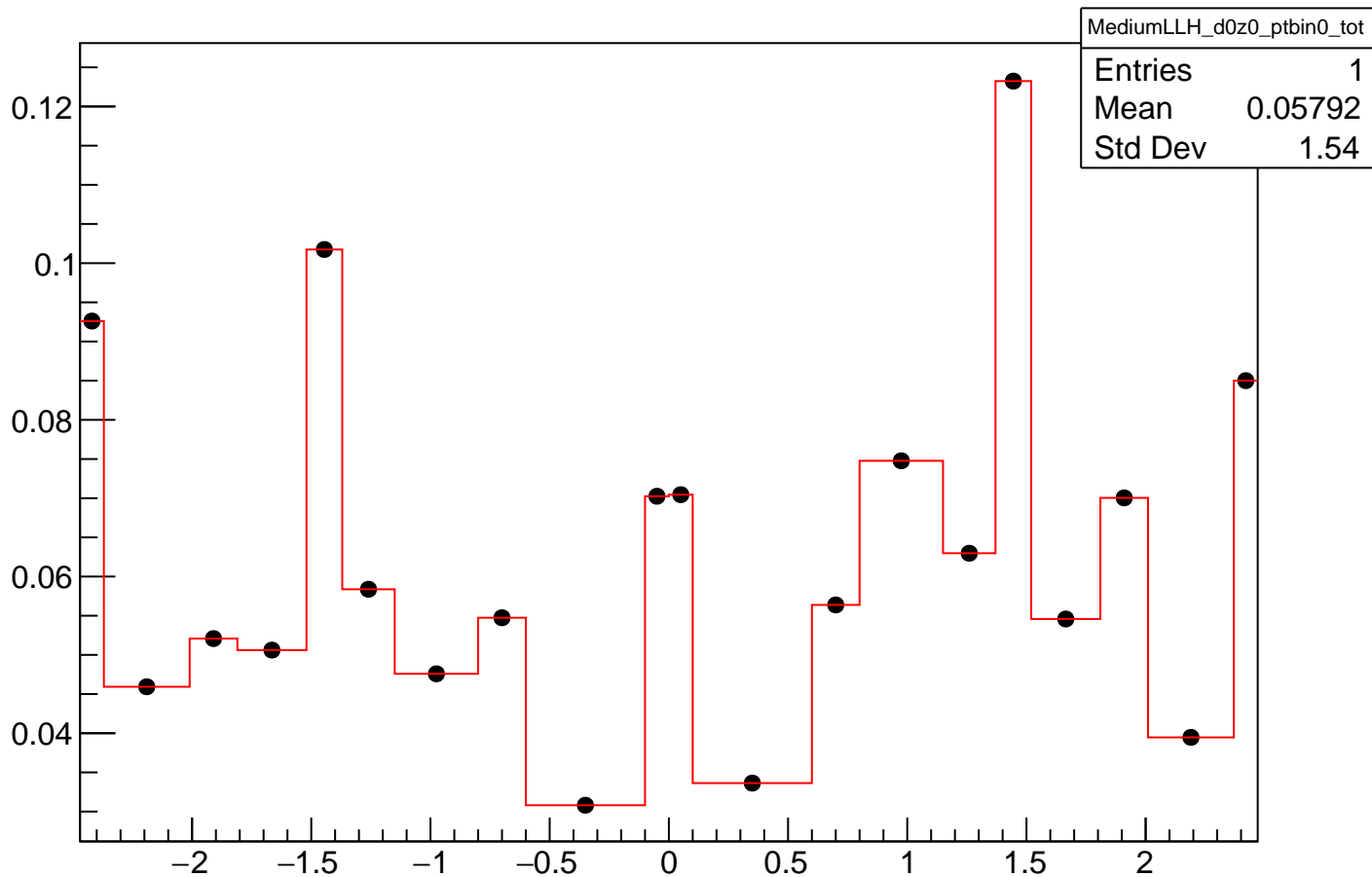
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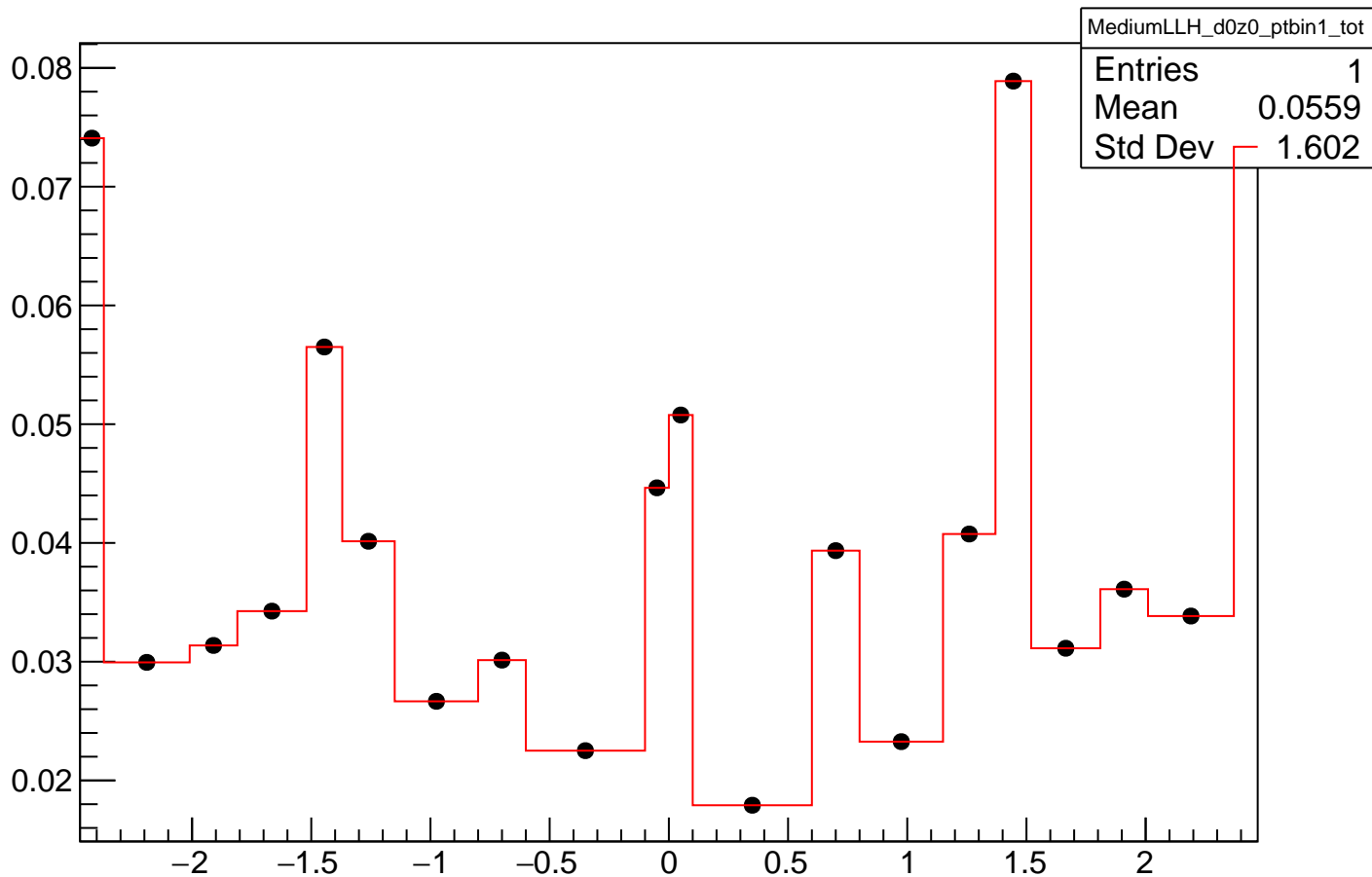
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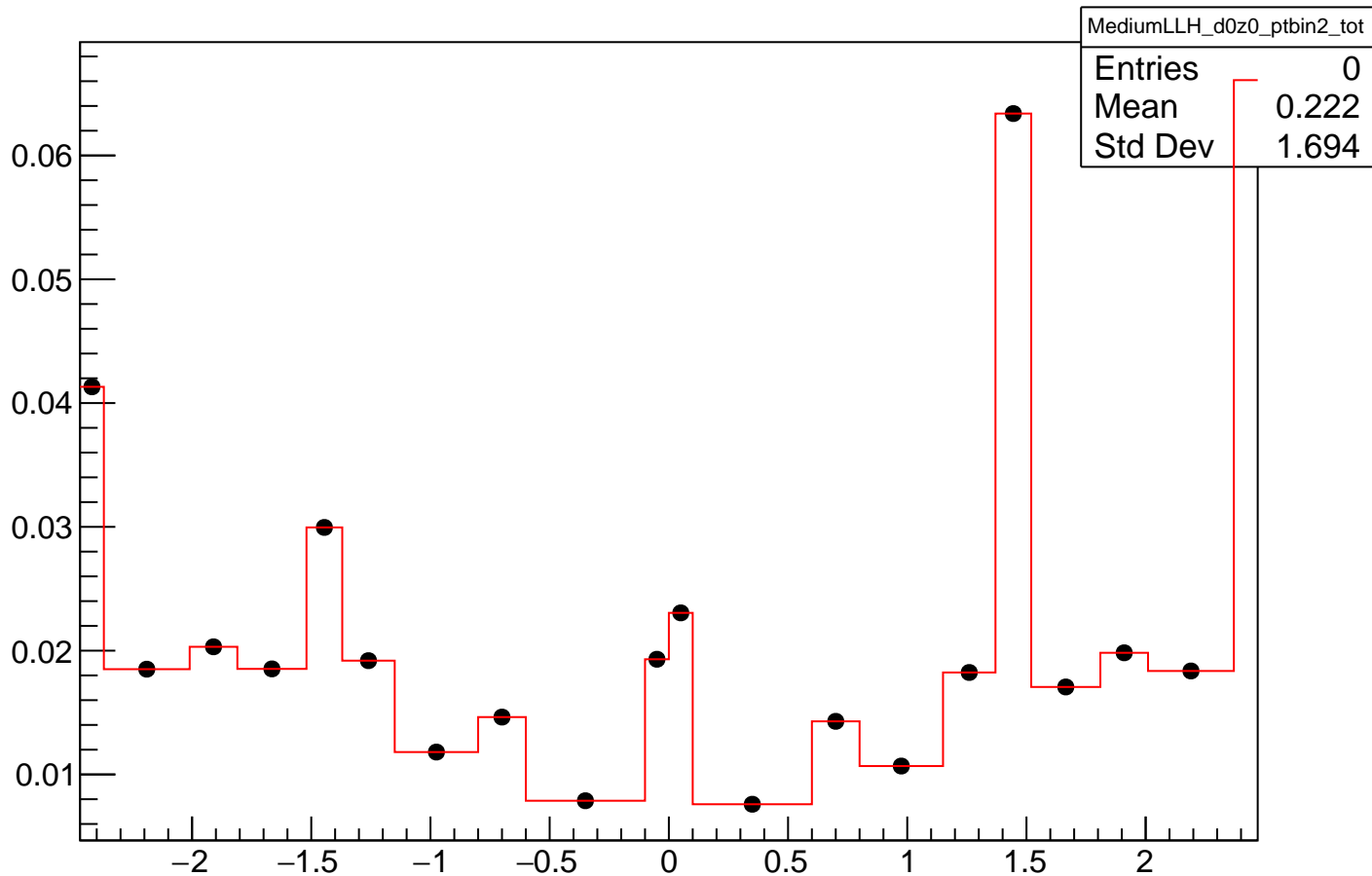
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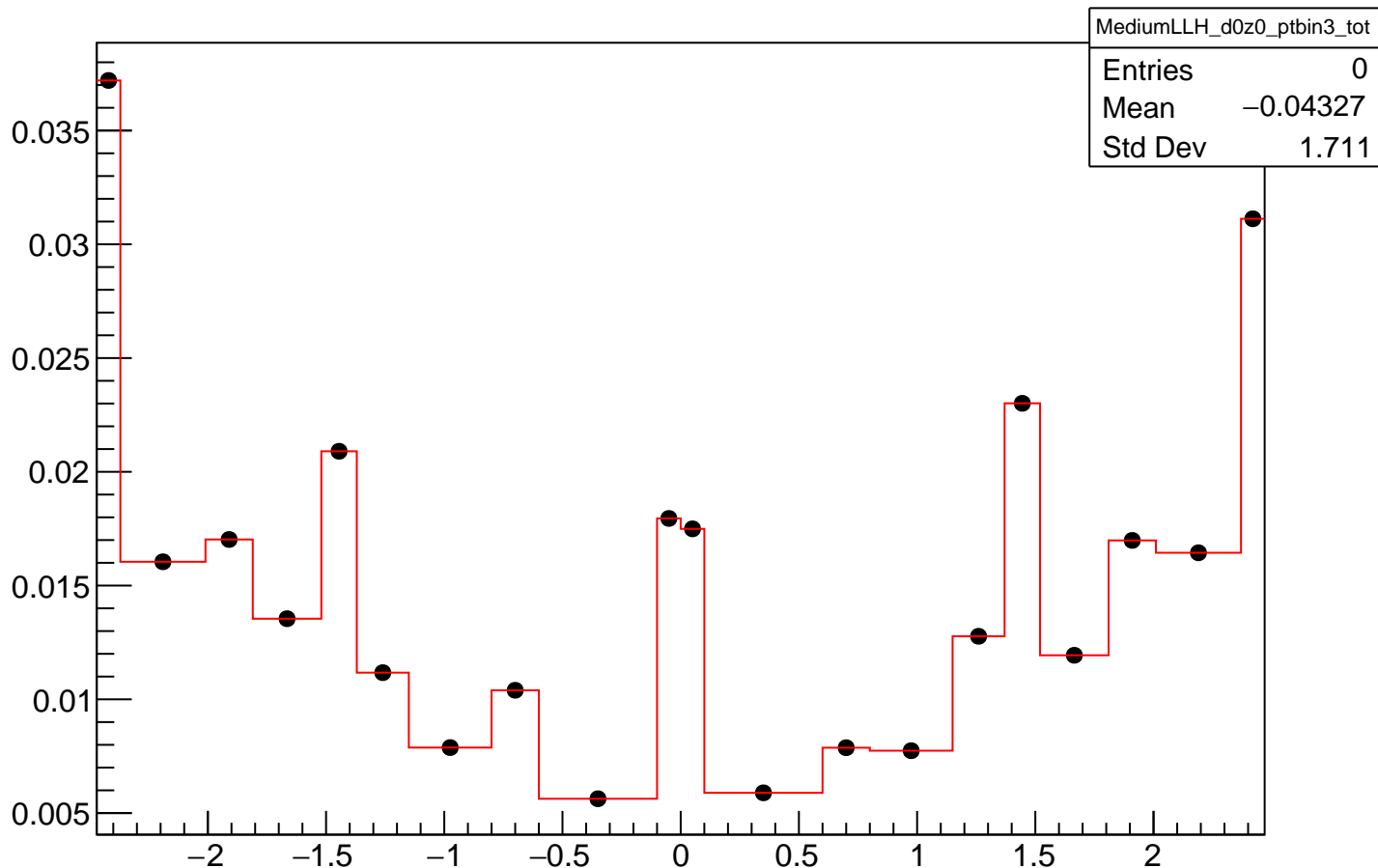
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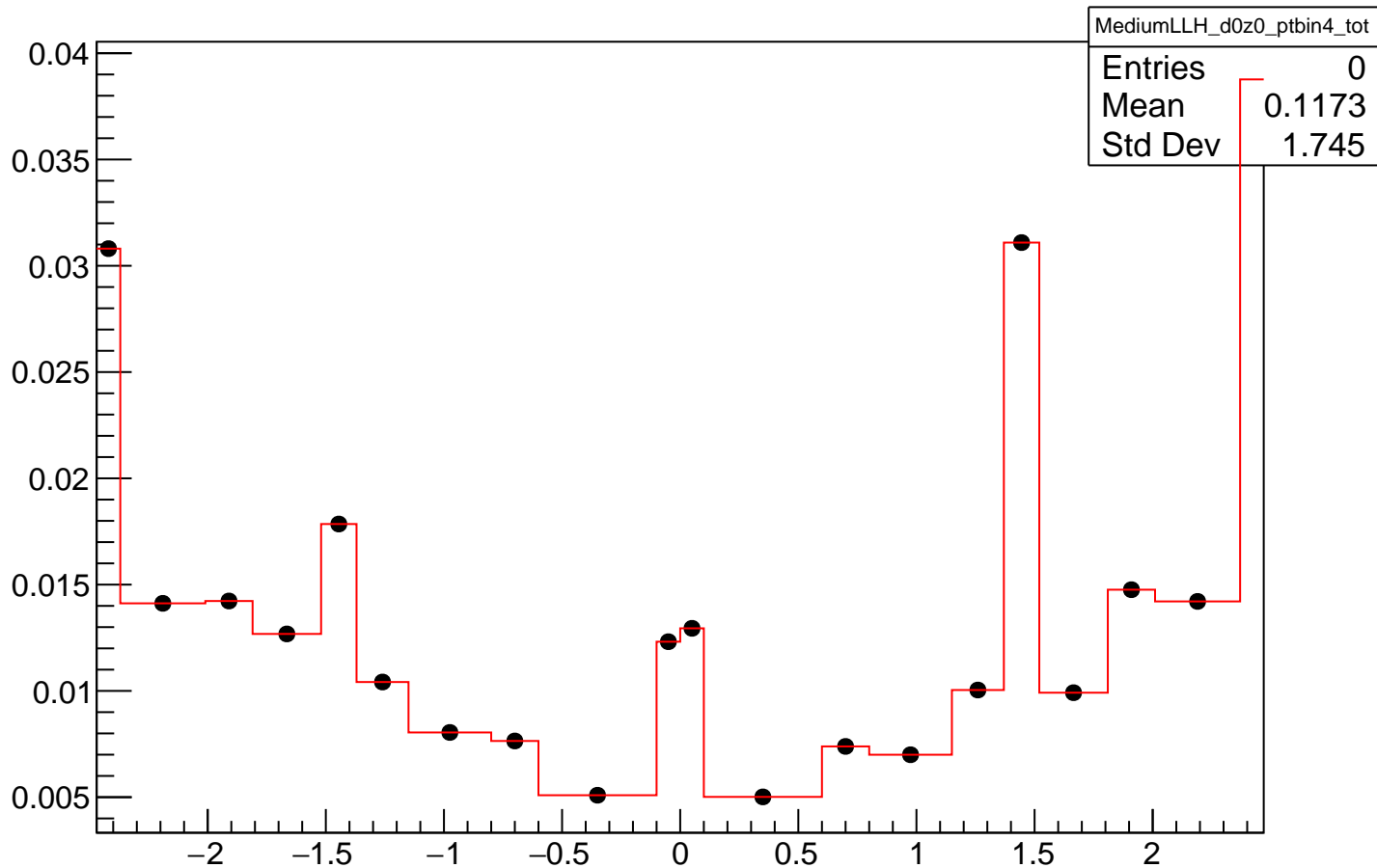
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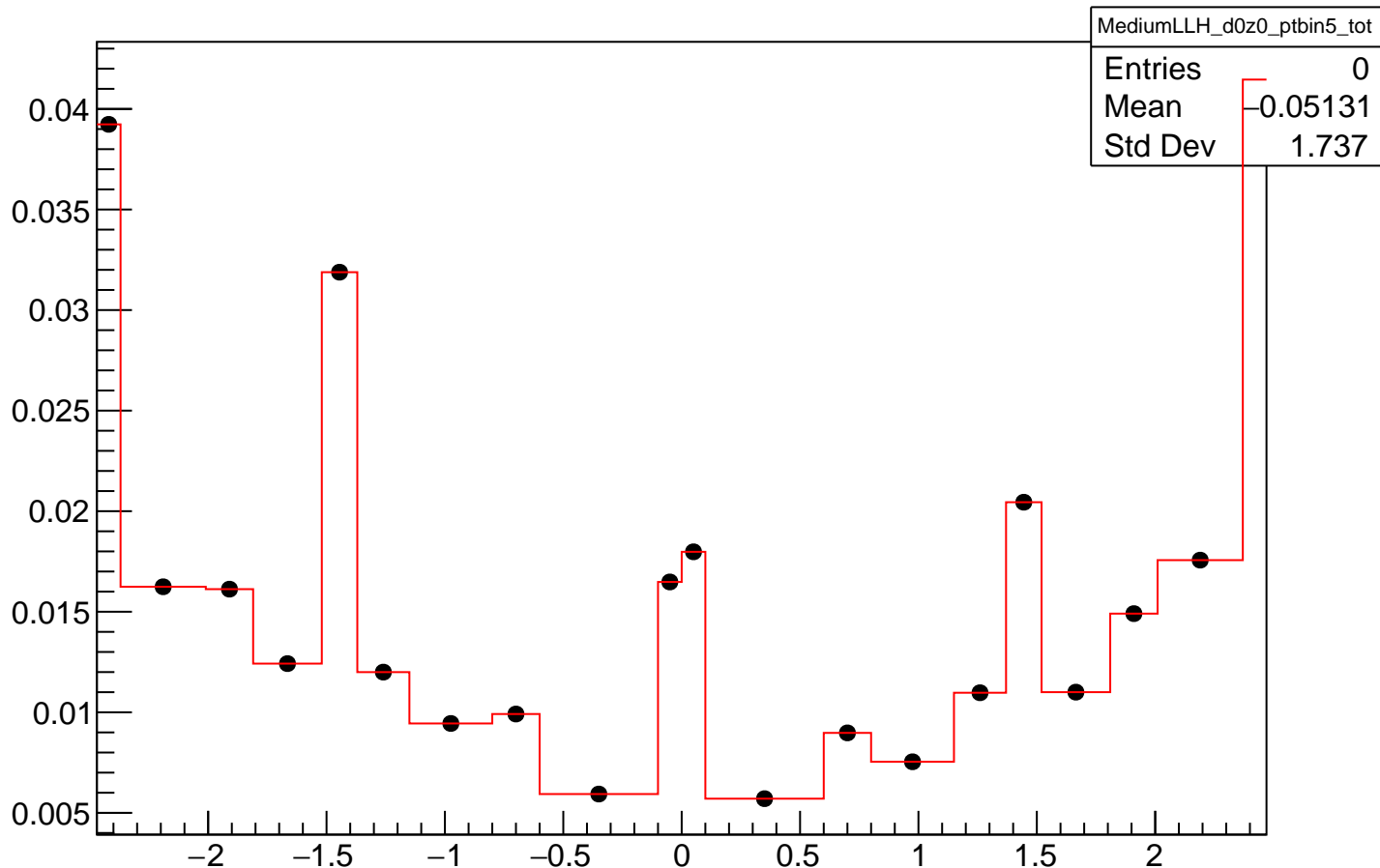
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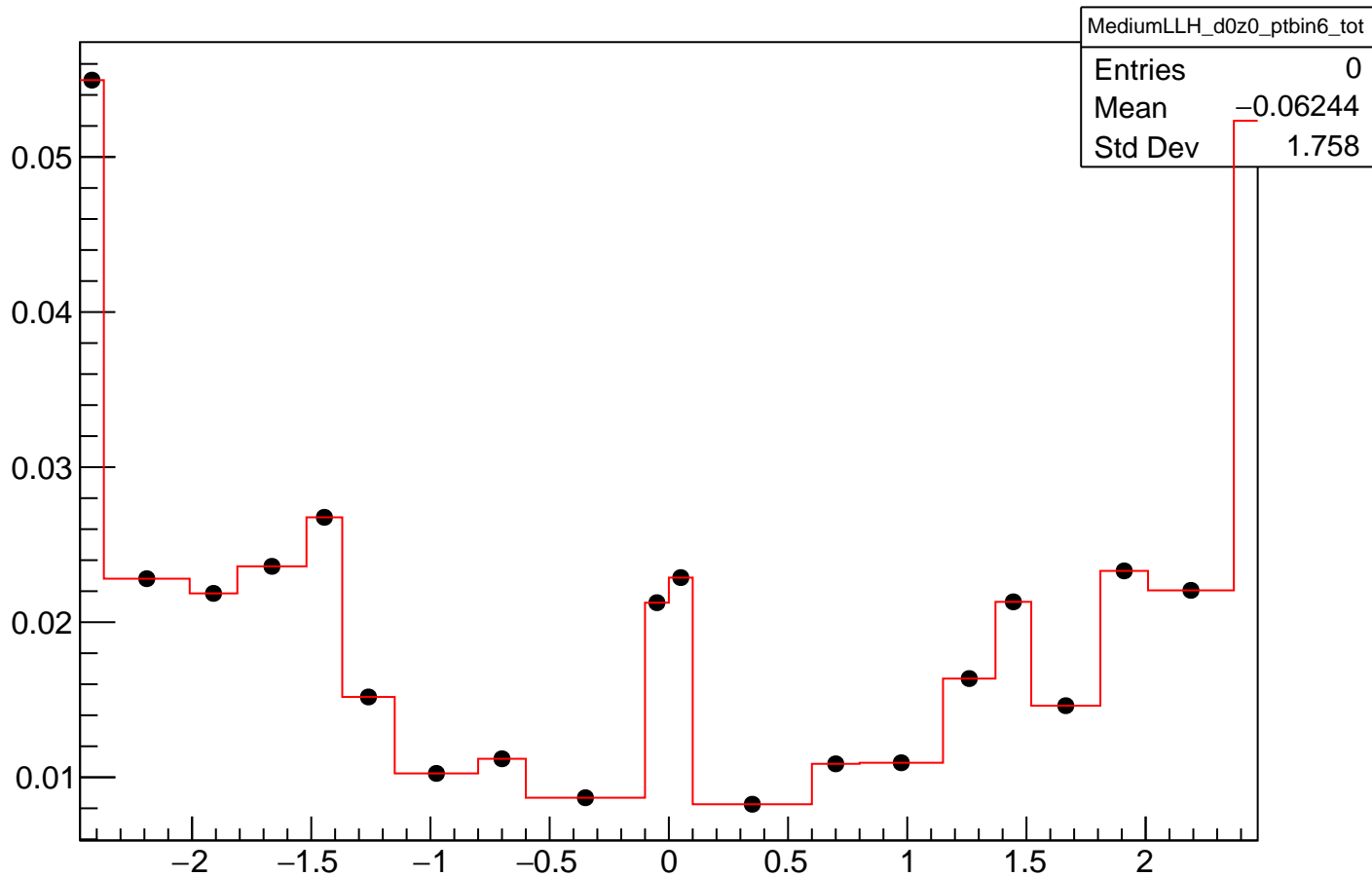
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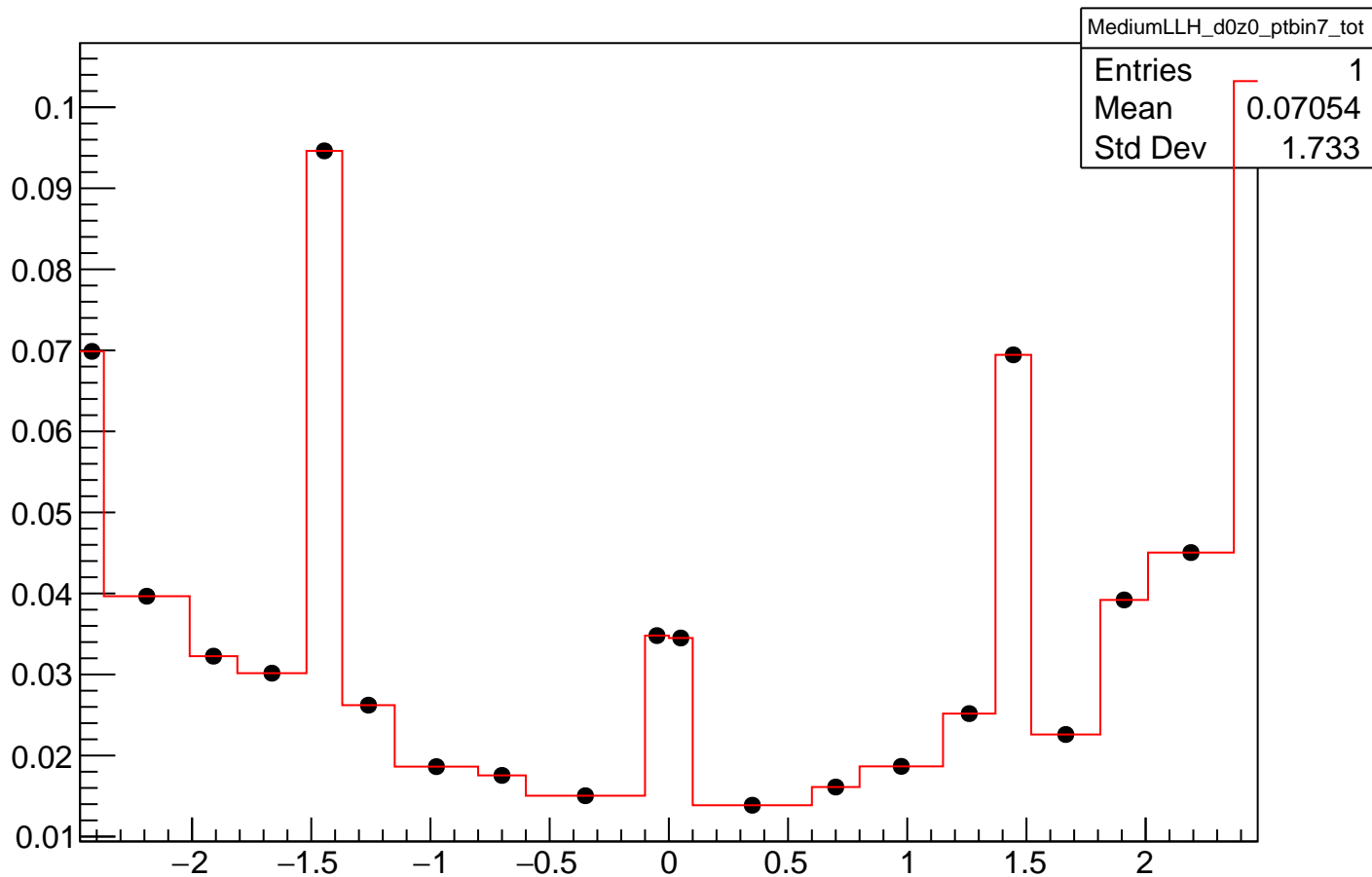
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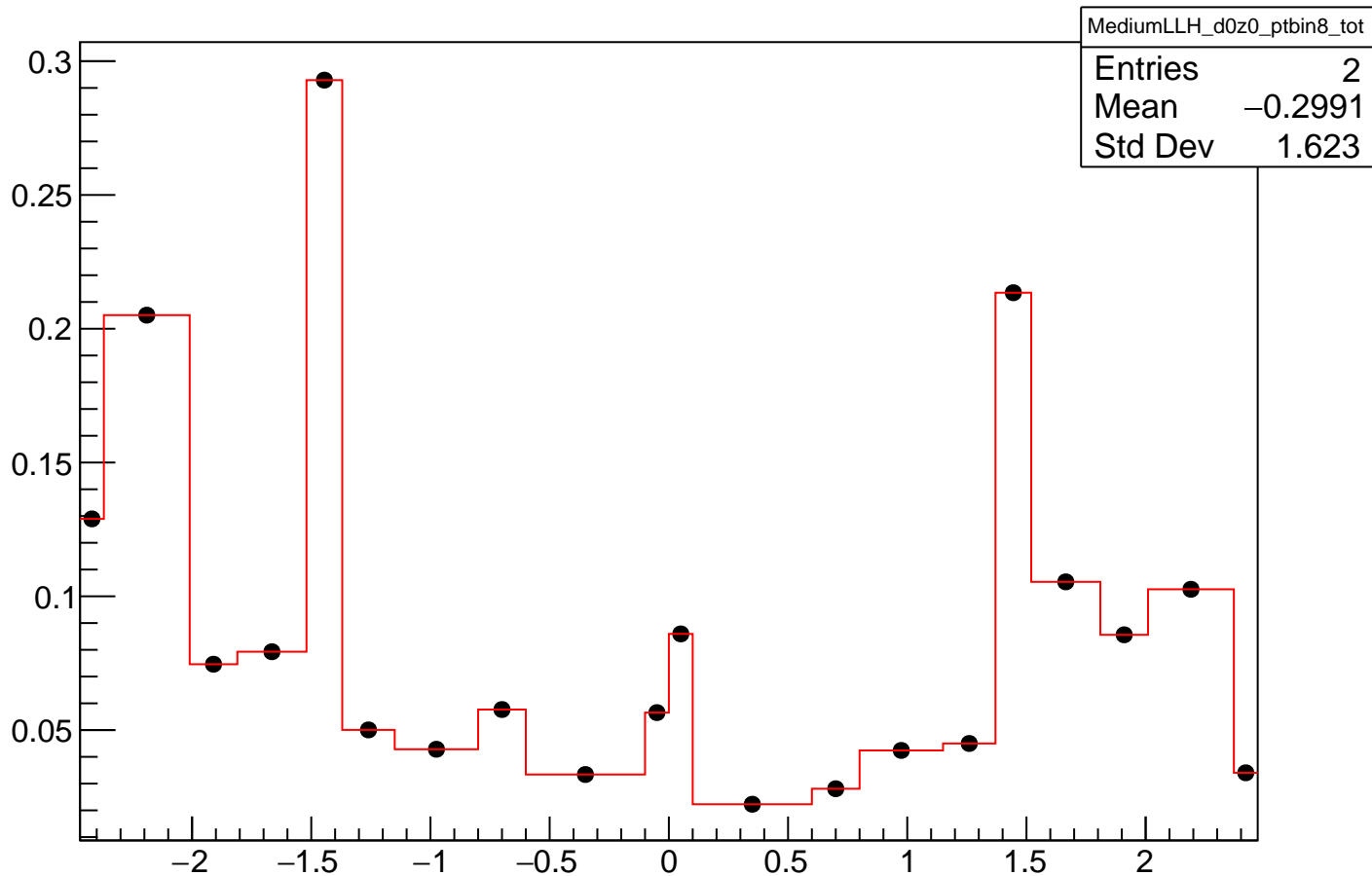
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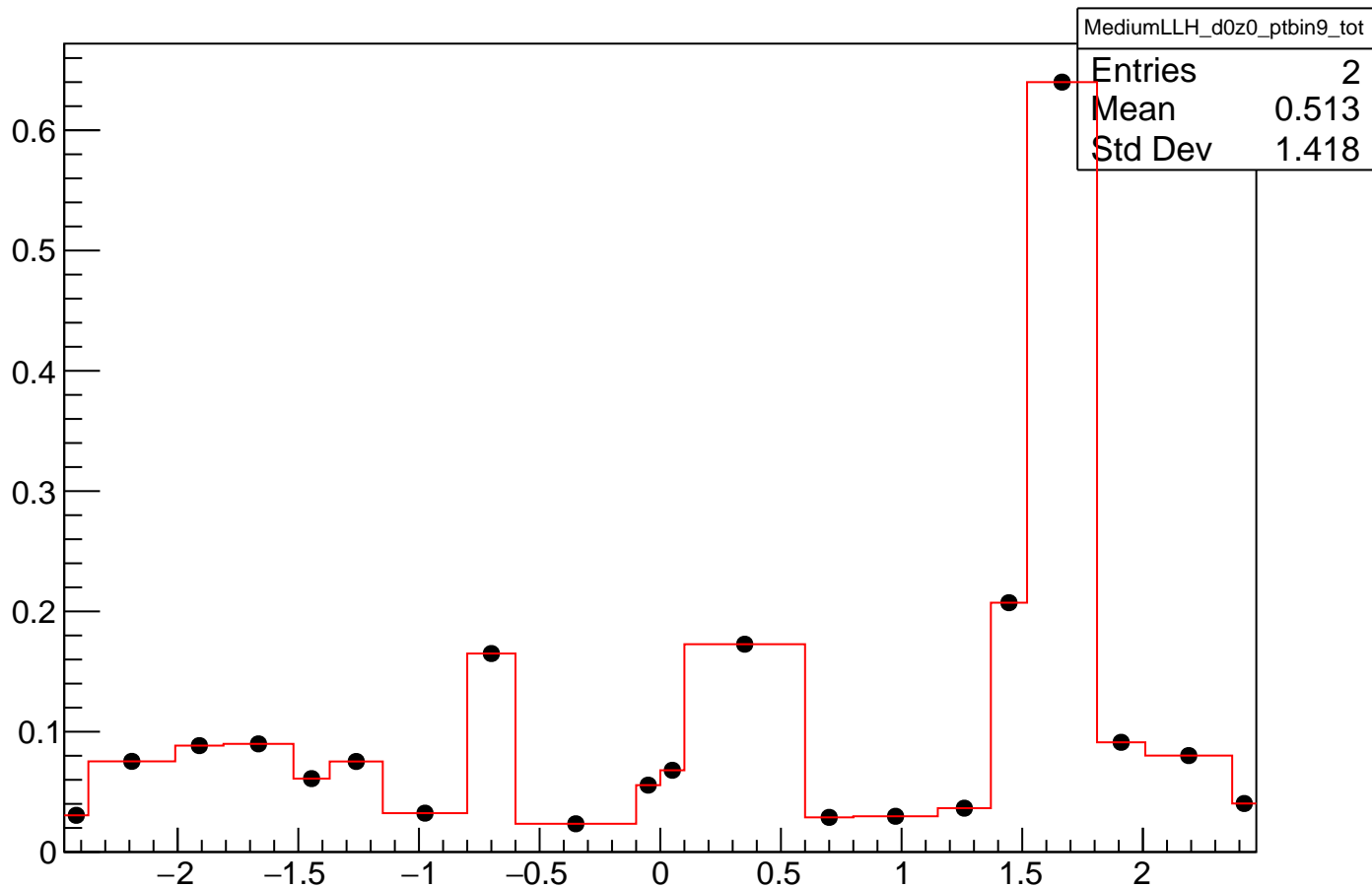
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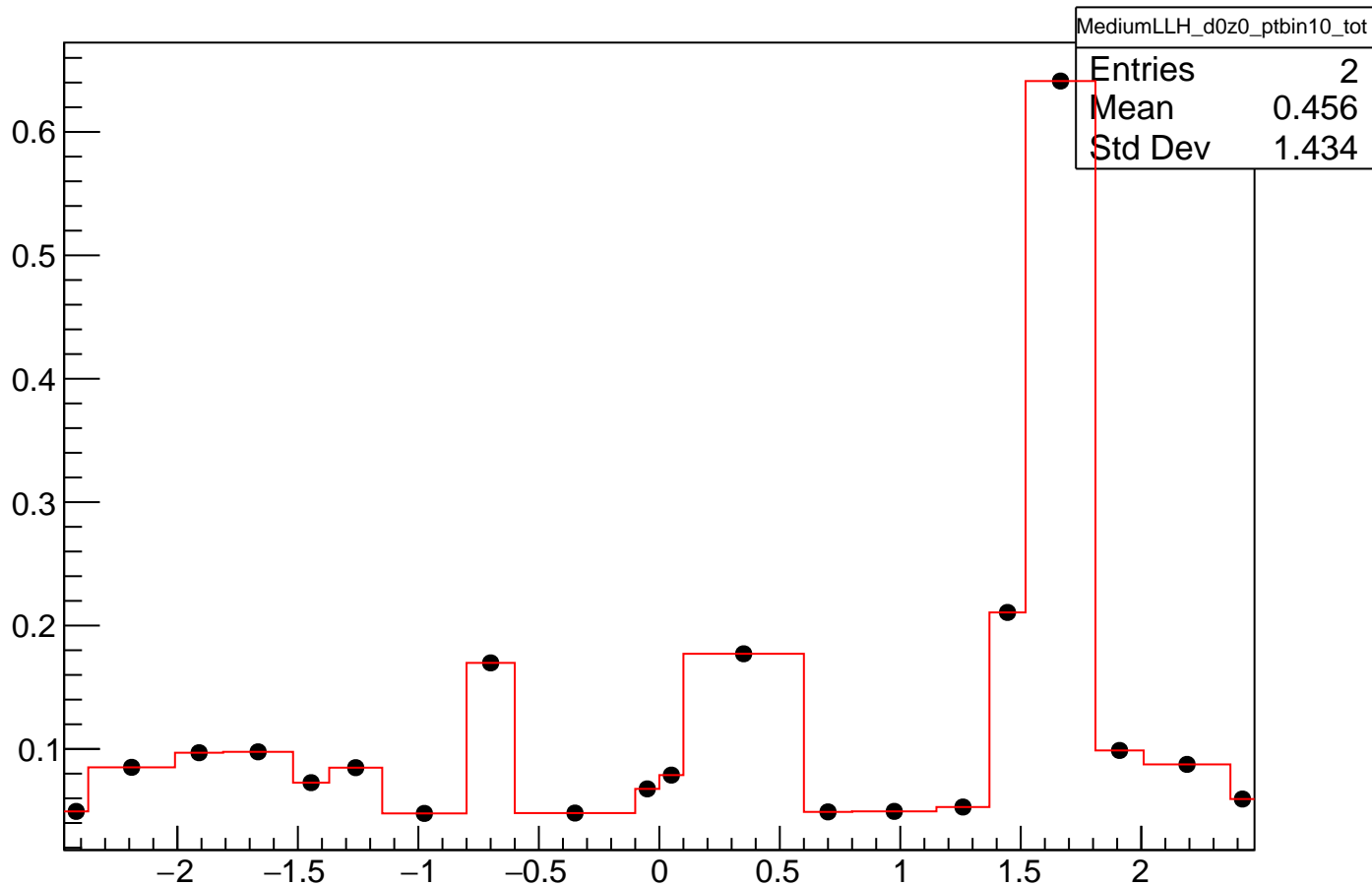
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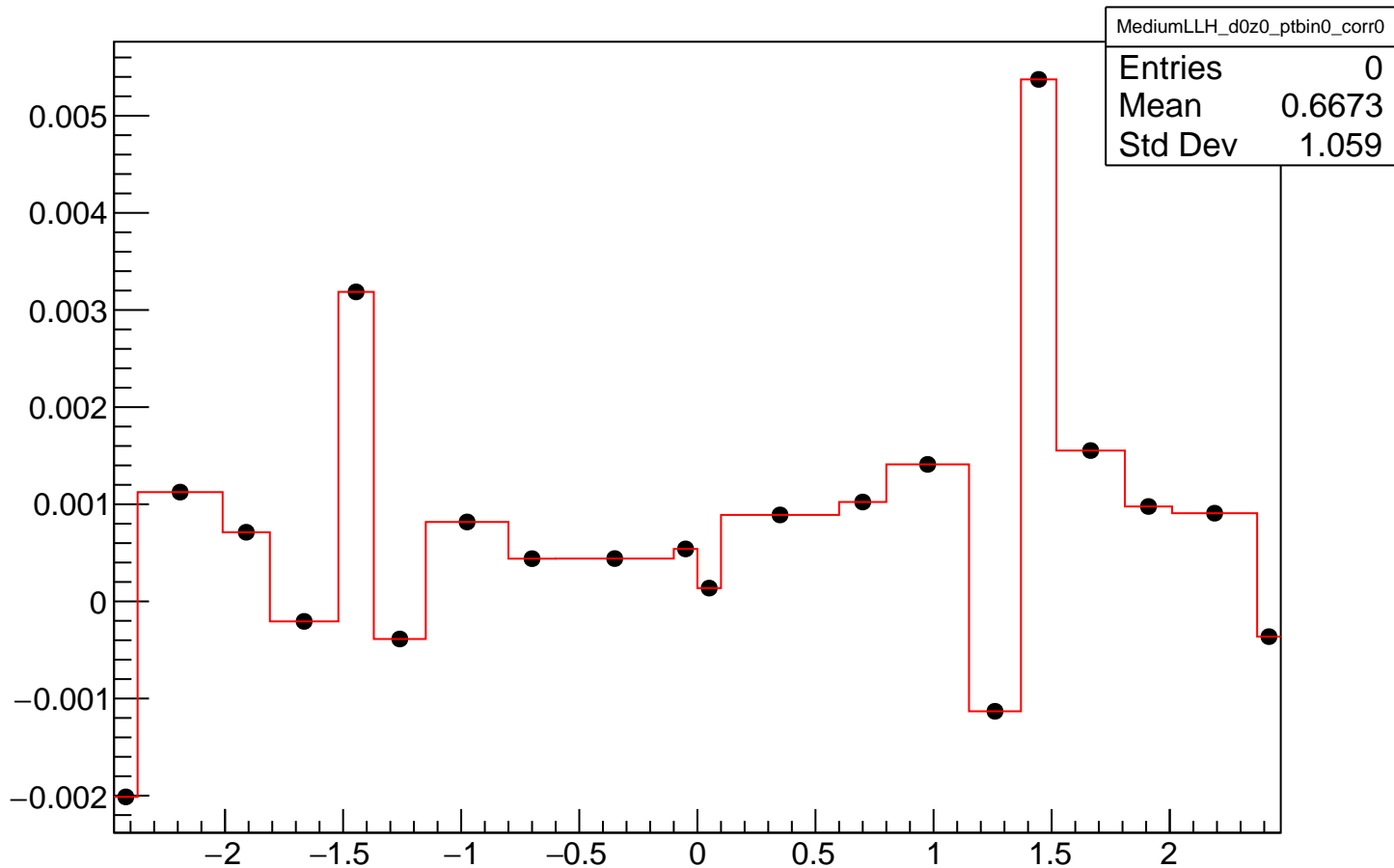
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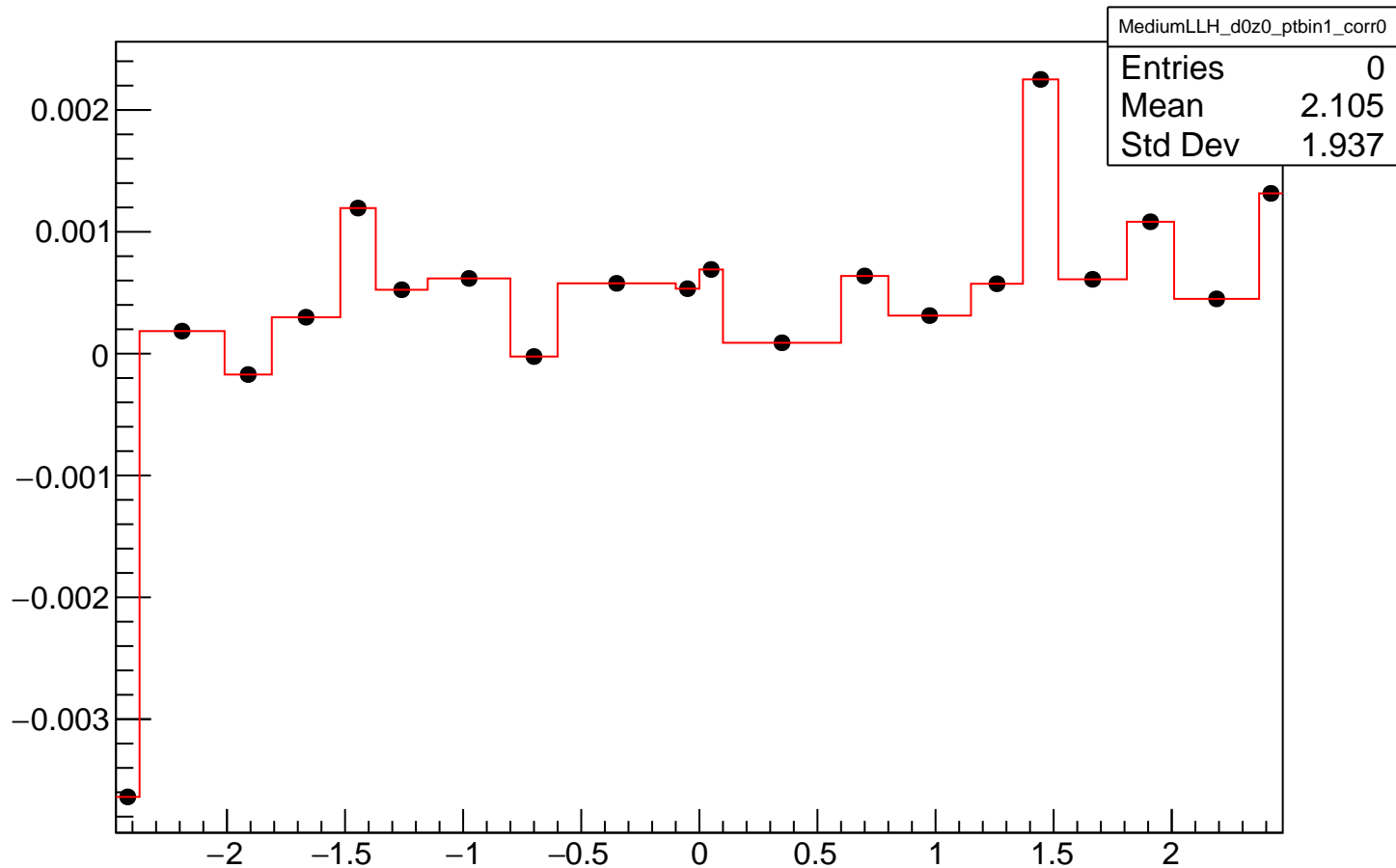
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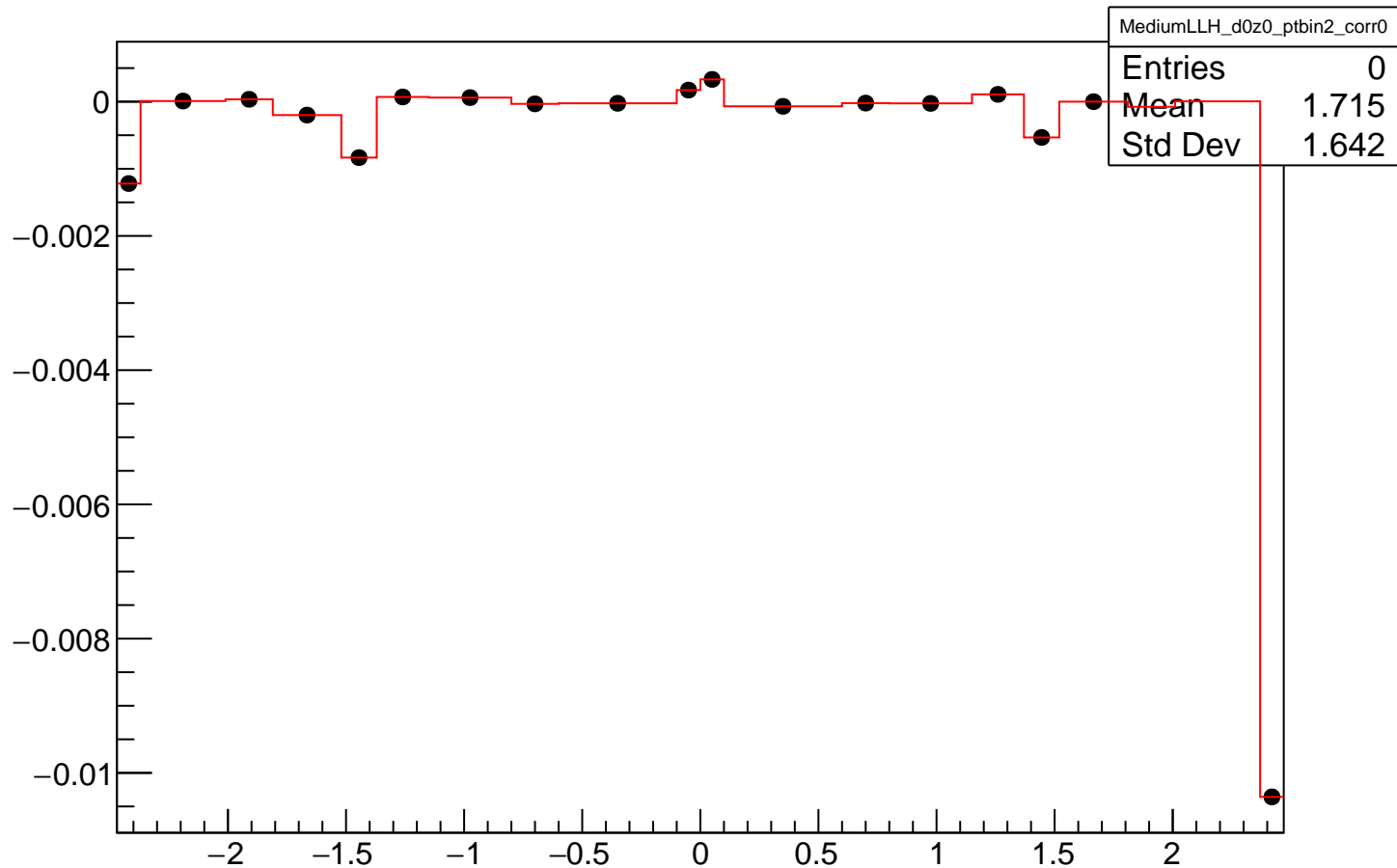
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FullSim_MediumLLH_d0z0_corr0



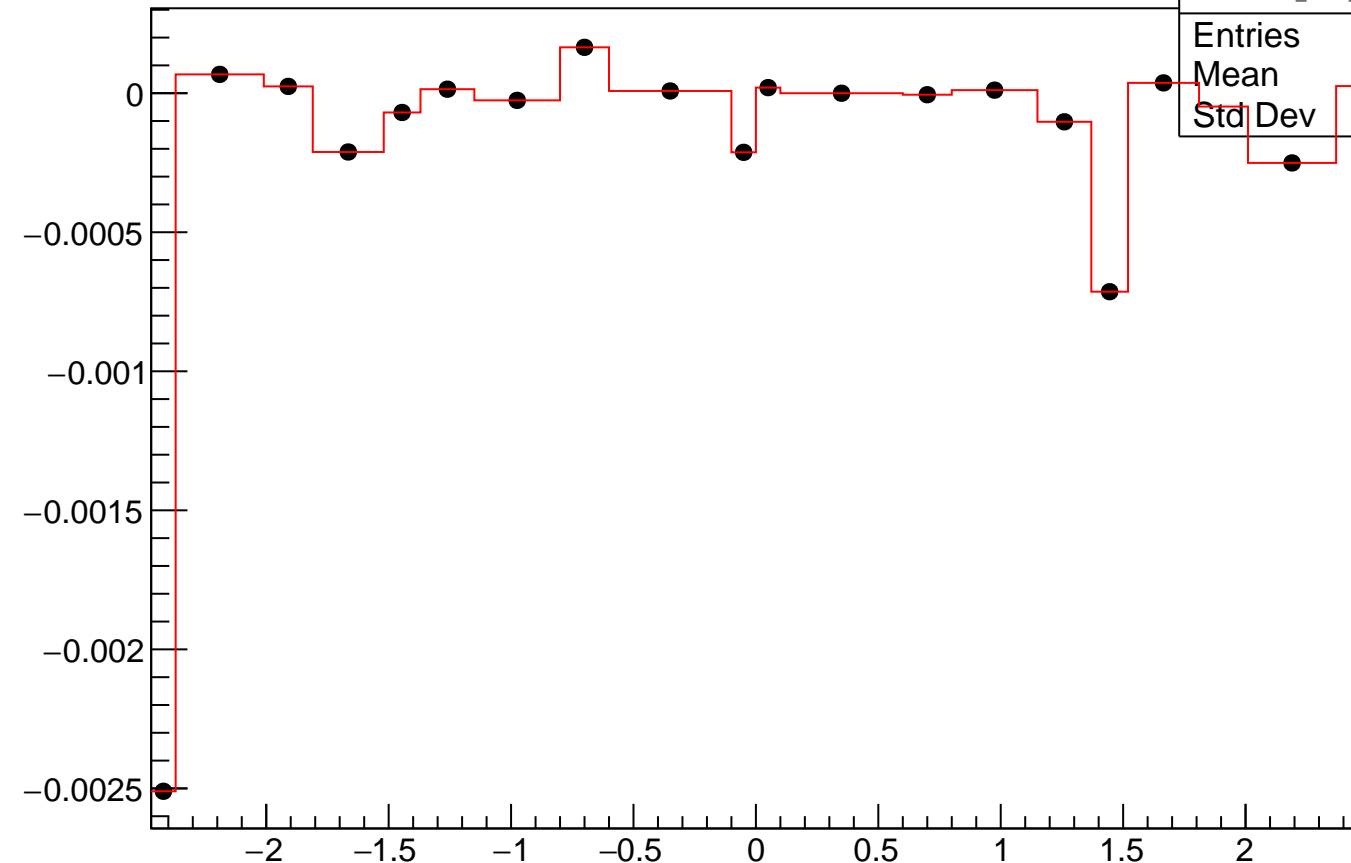
FullSim_MediumLLH_d0z0_corr0



FullSim_MediumLLH_d0z0_corr0

MediumLLH_d0z0_ptbin3_corr0

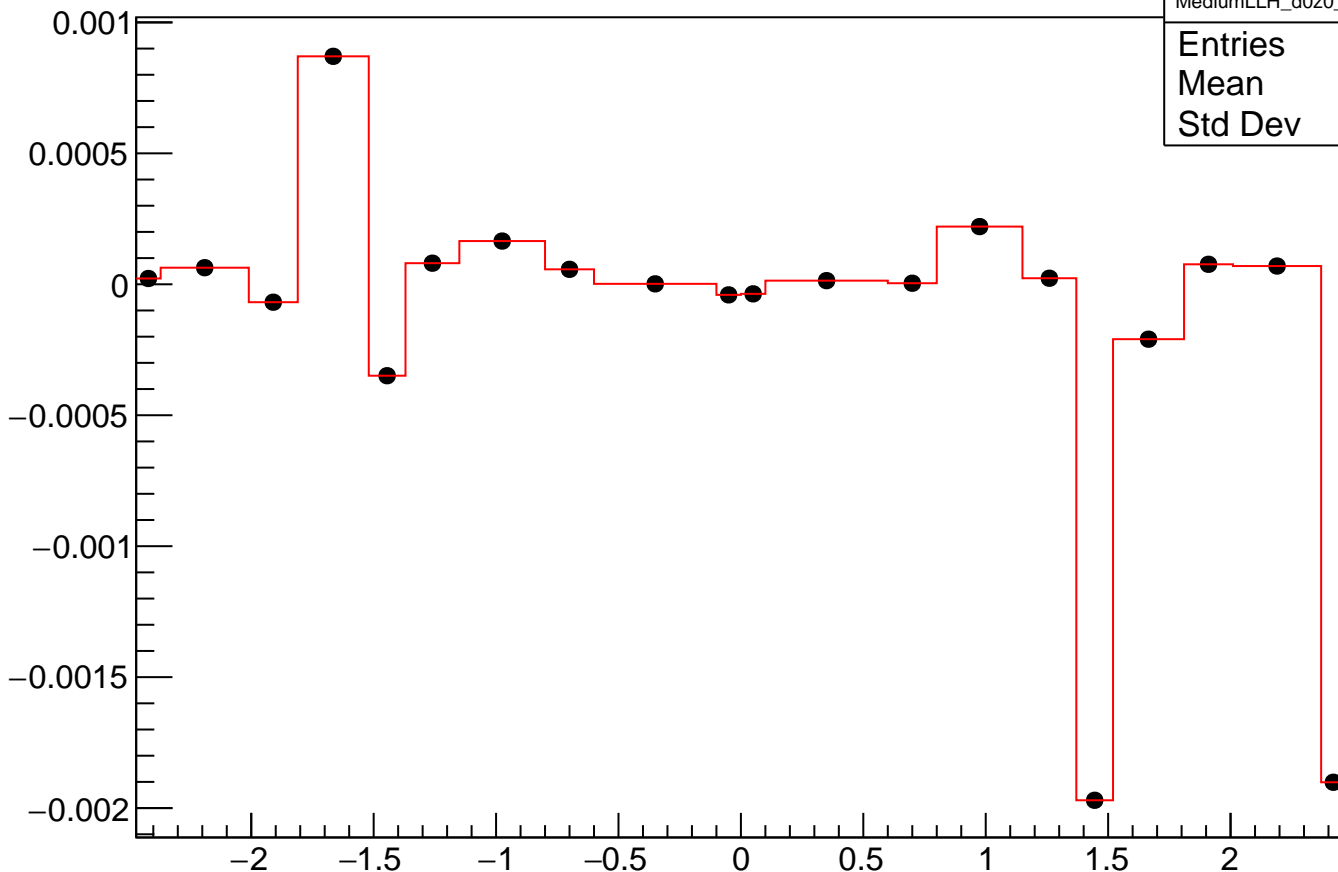
Entries	0
Mean	-1.208
Std Dev	1.797



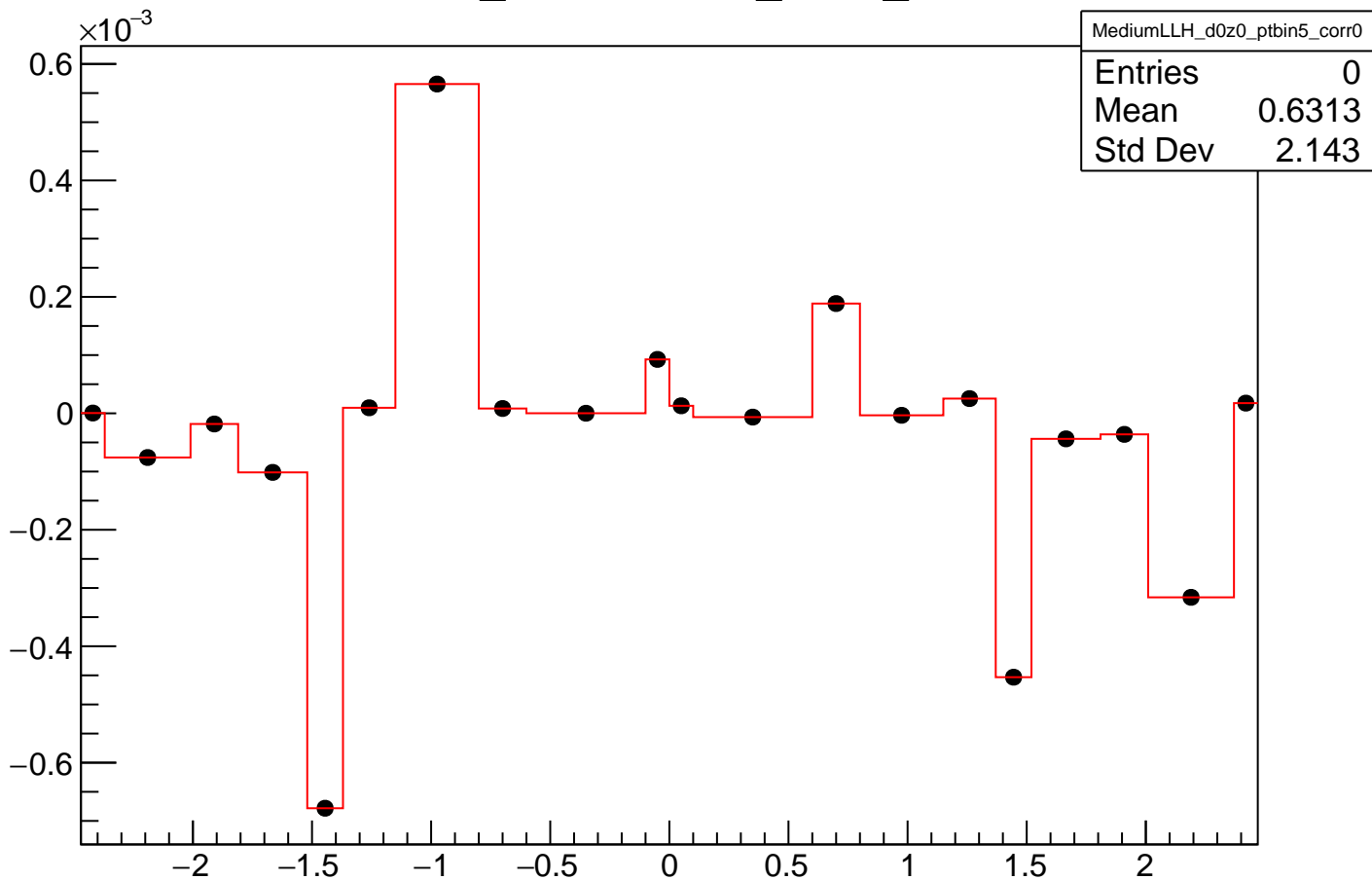
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MediumLLH_d0z0_ptbin4_corr0

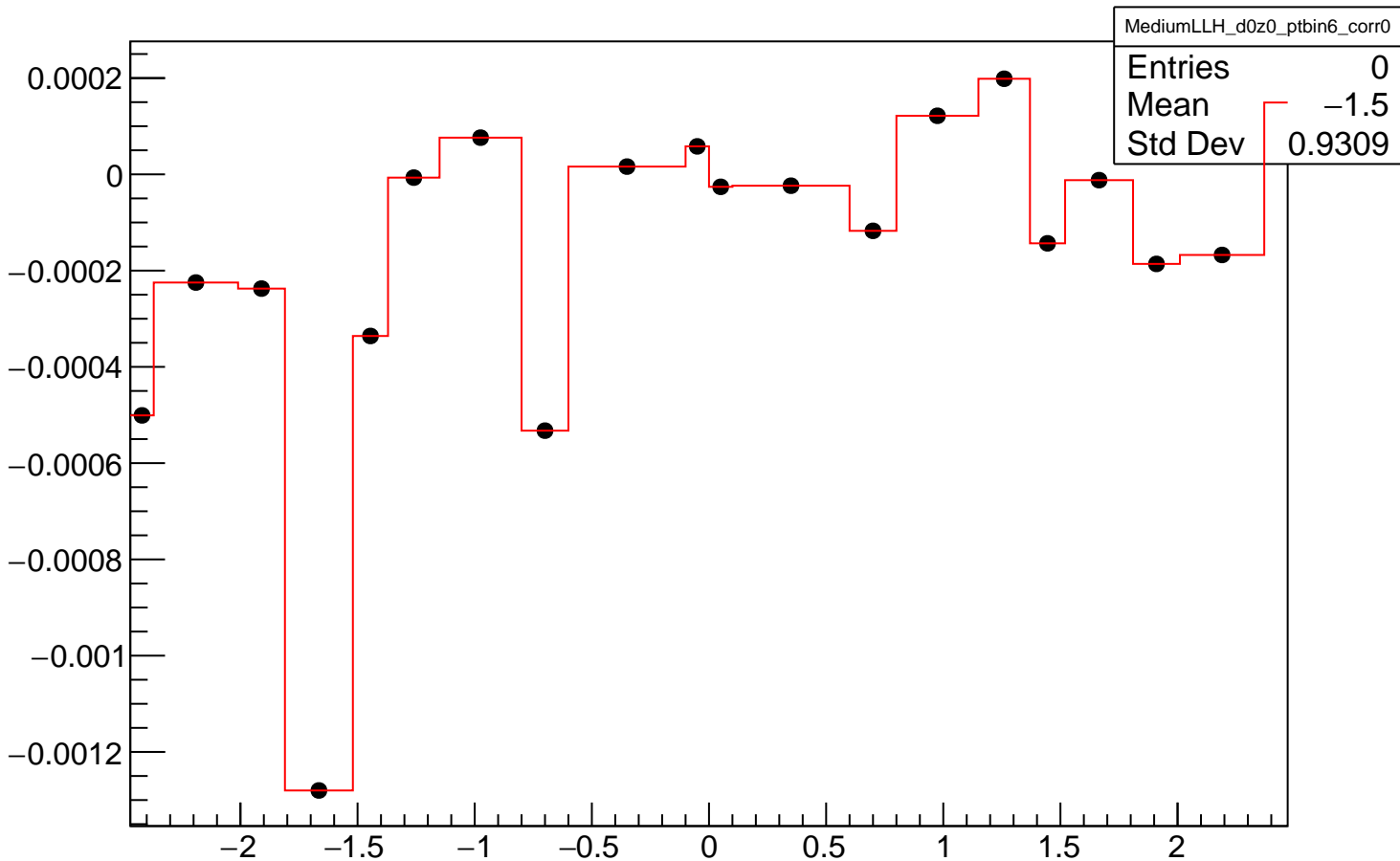
Entries	0
Mean	2.943
Std Dev	2.064



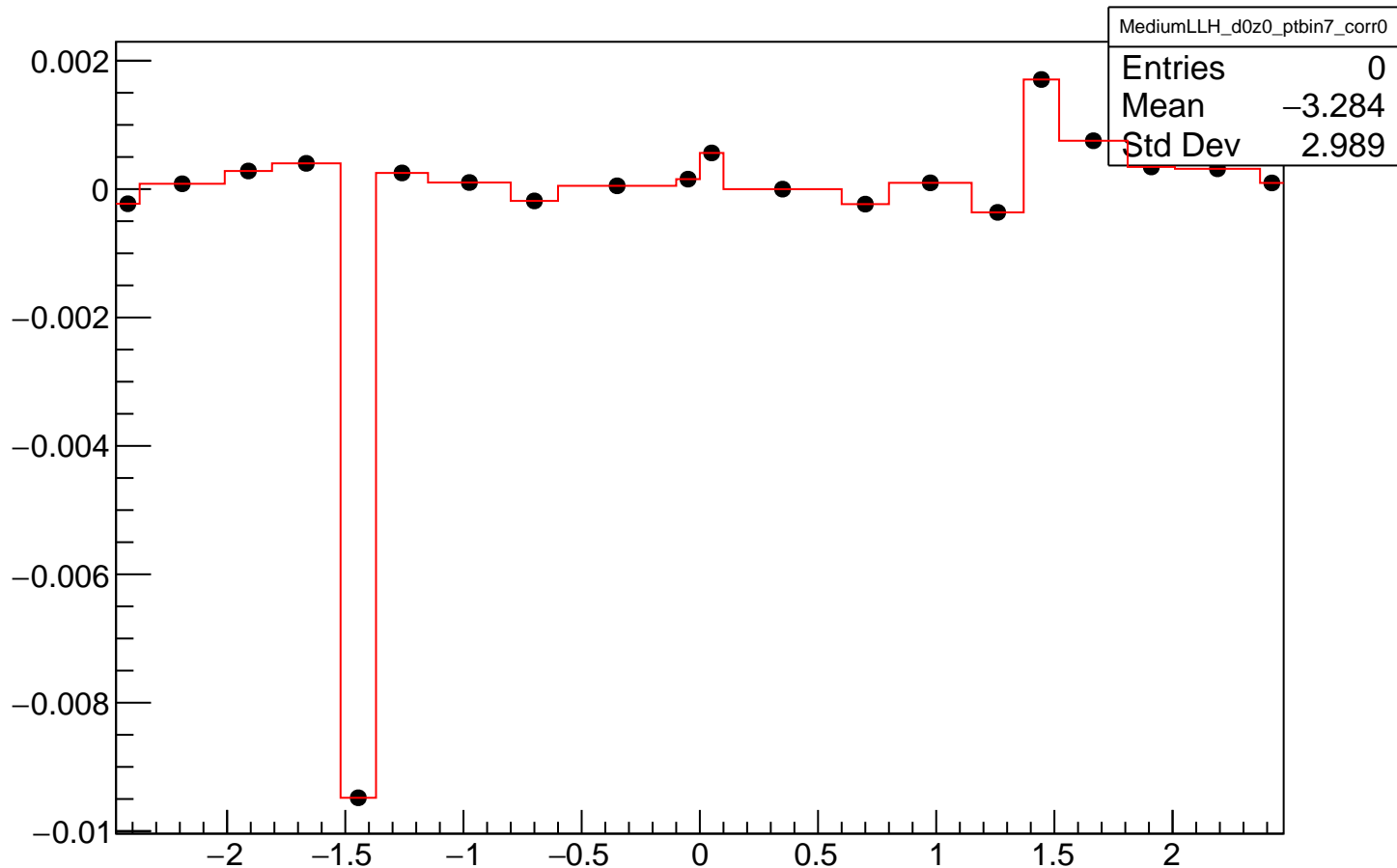
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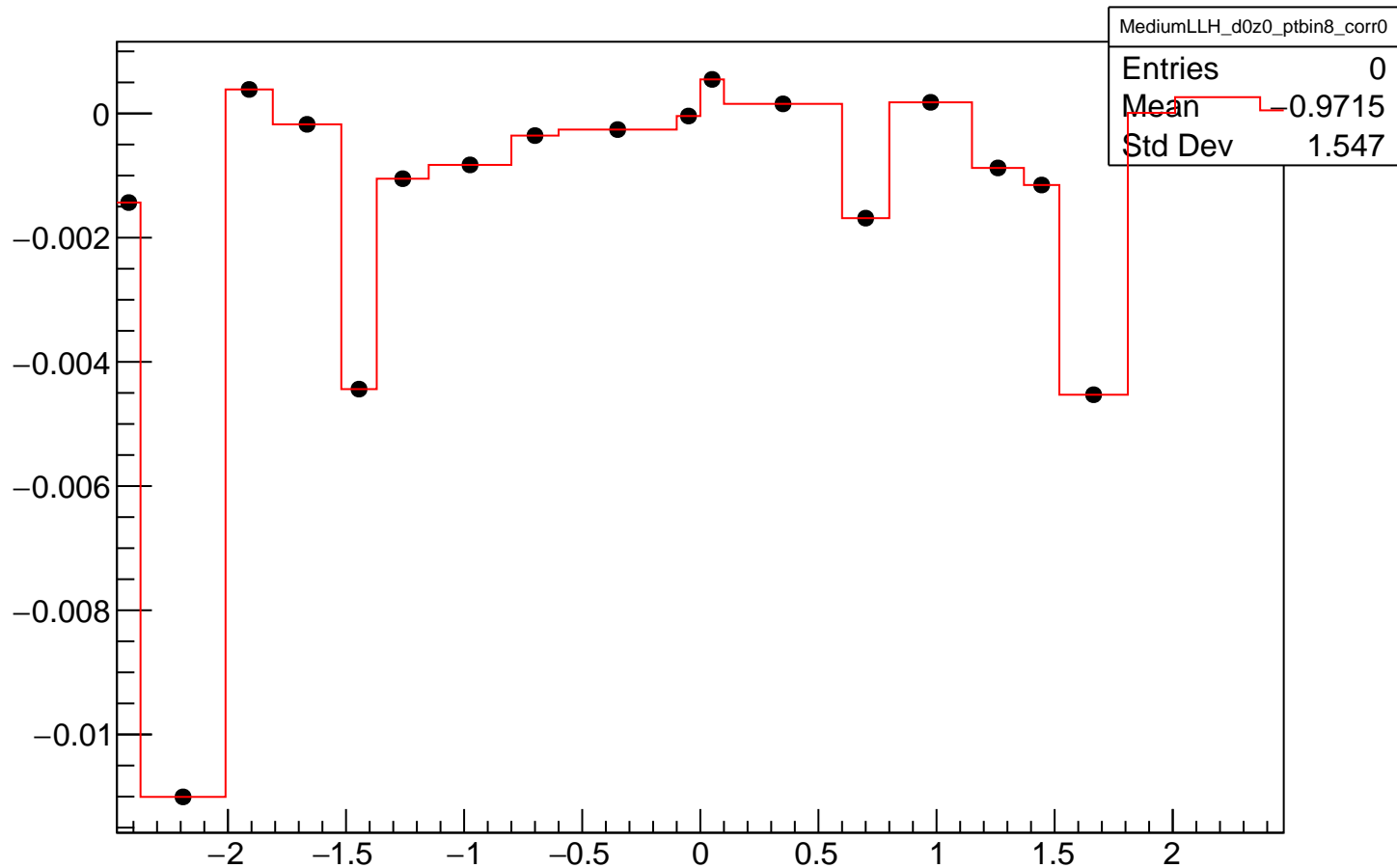
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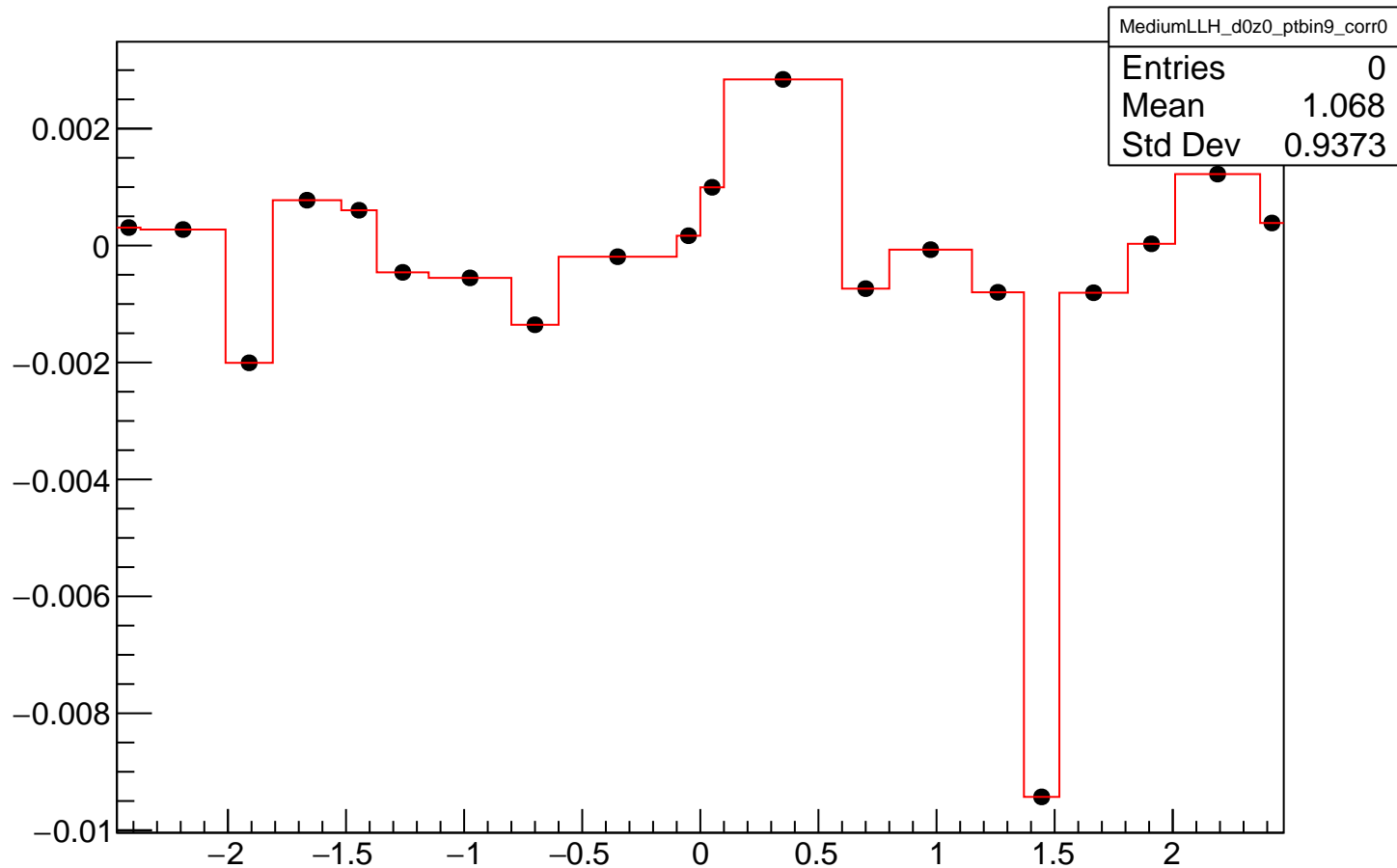
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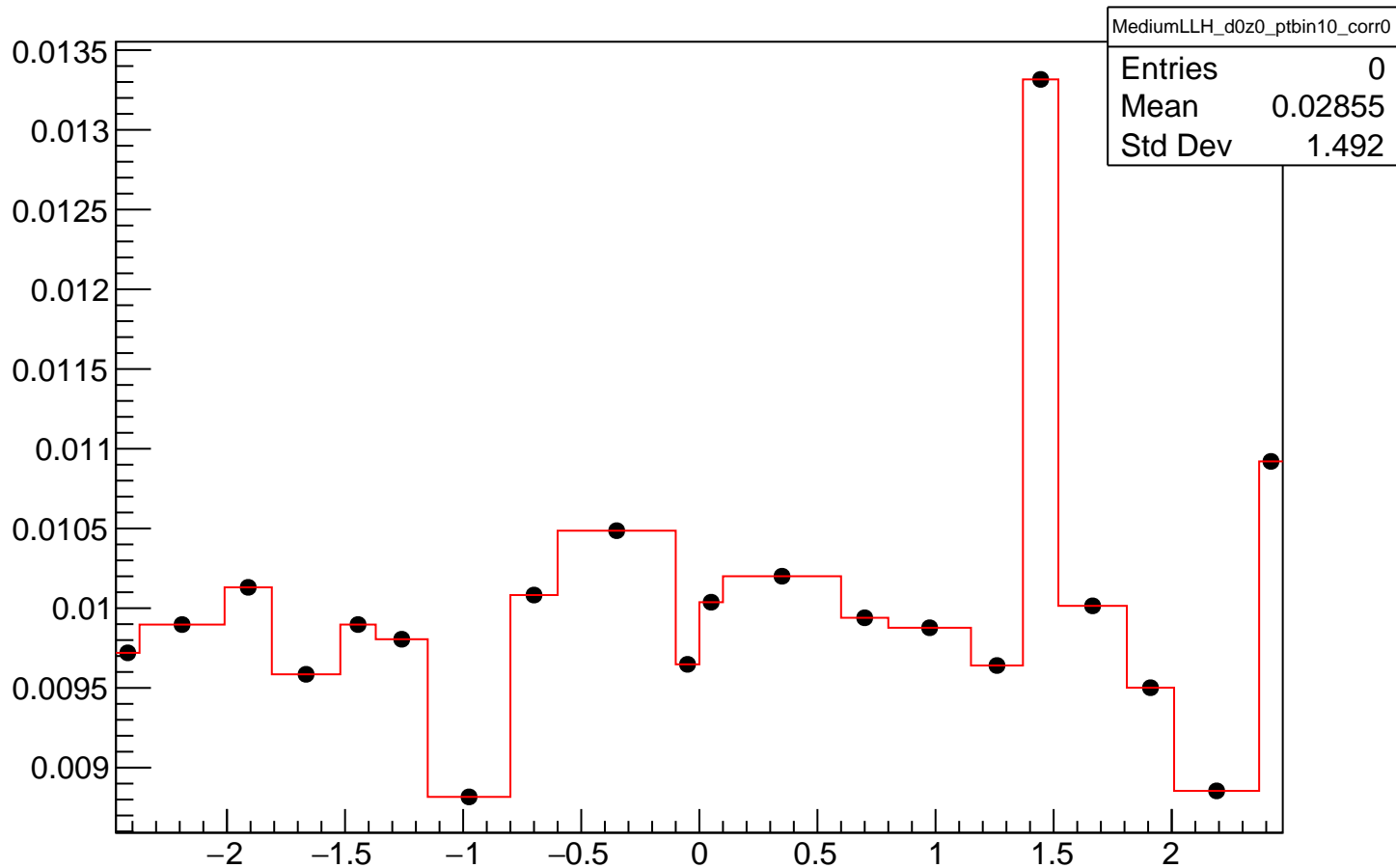
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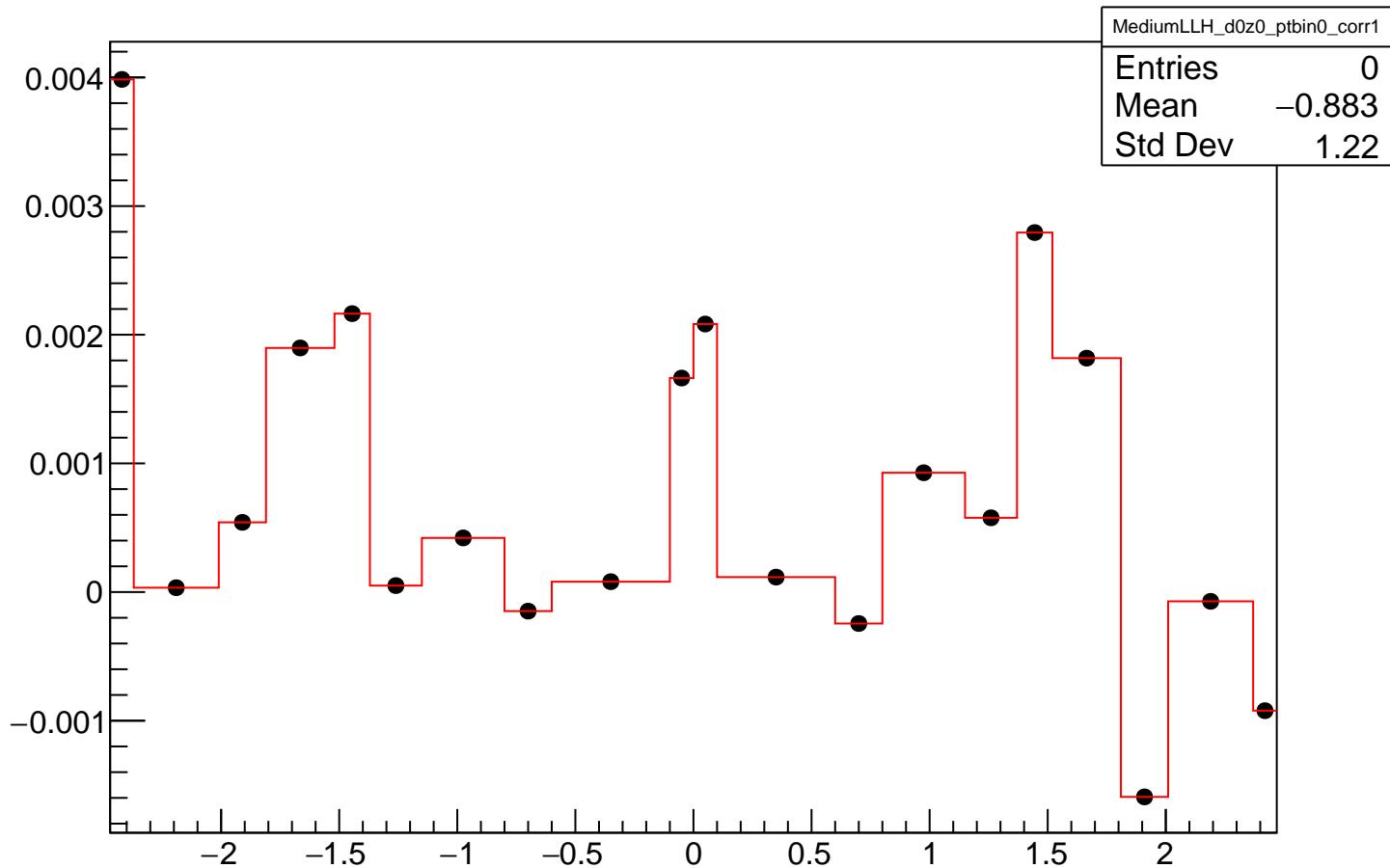
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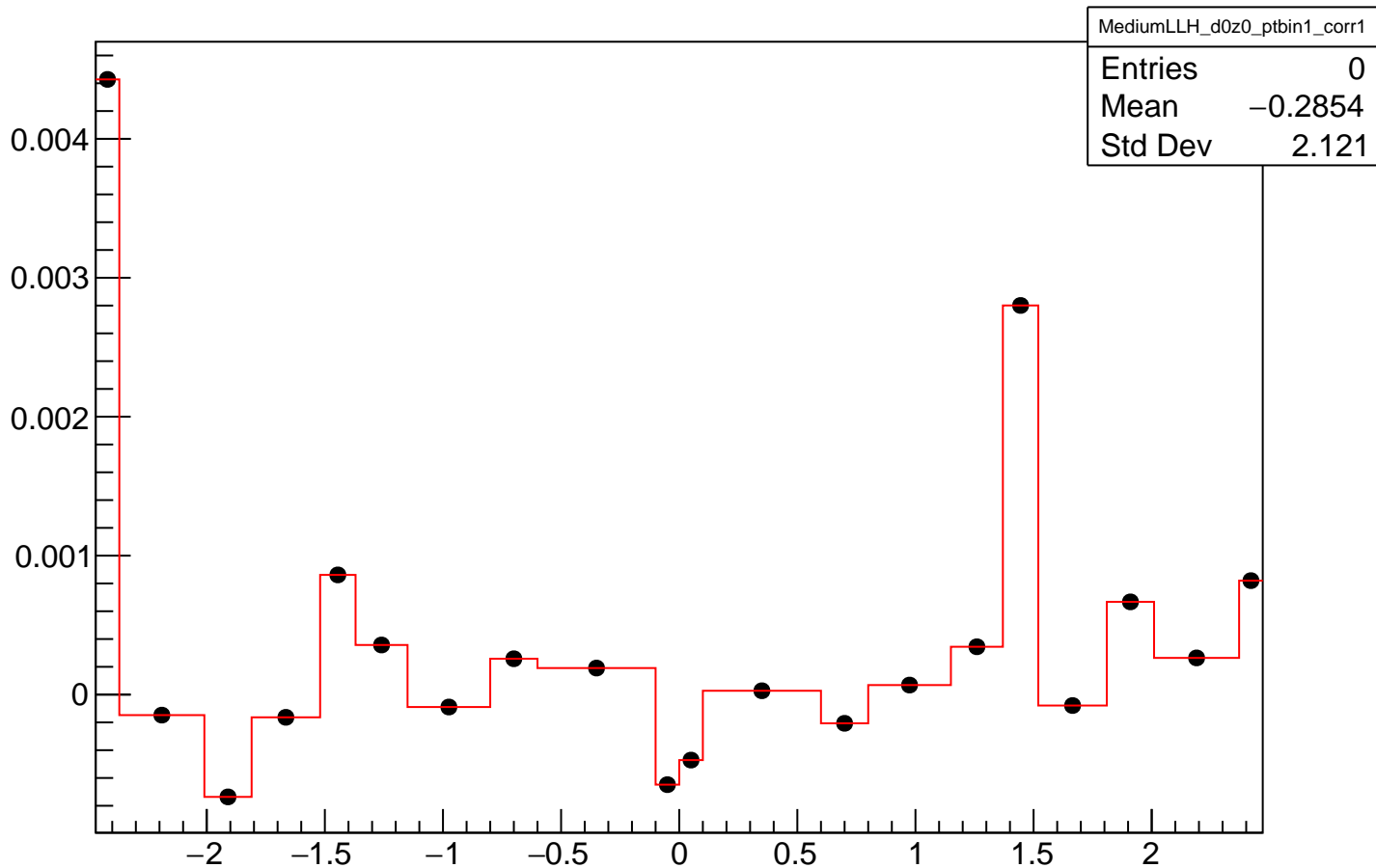
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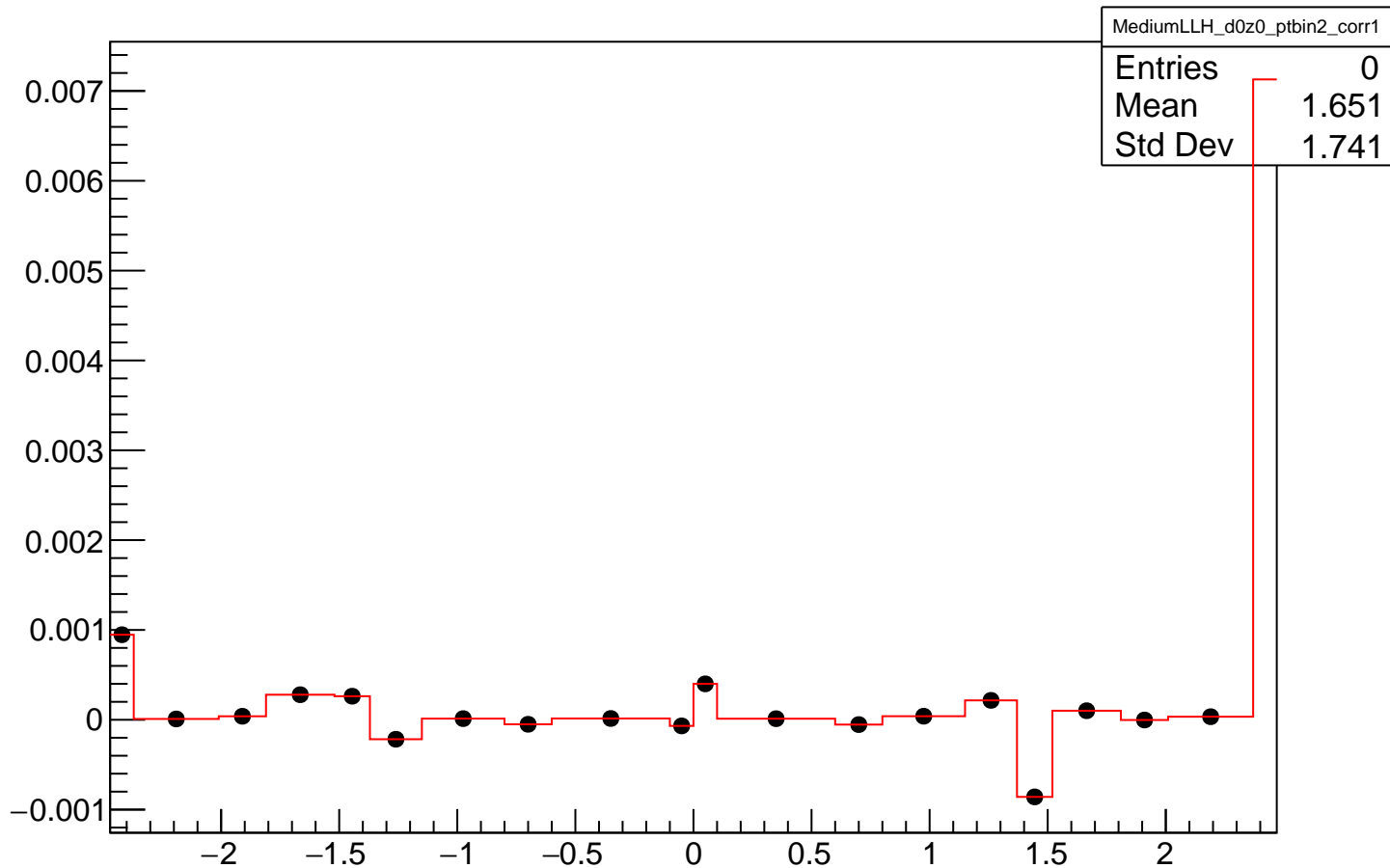
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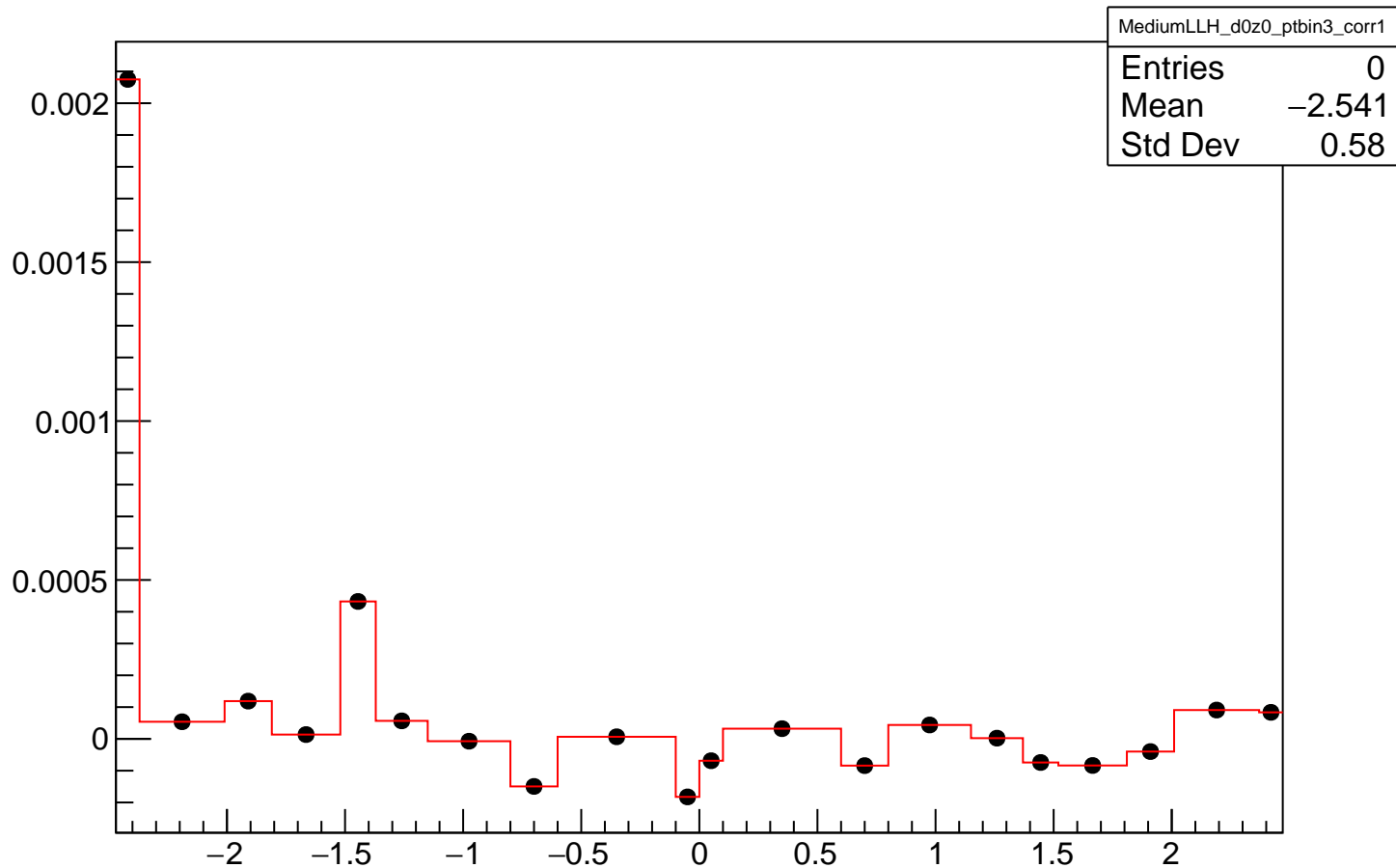
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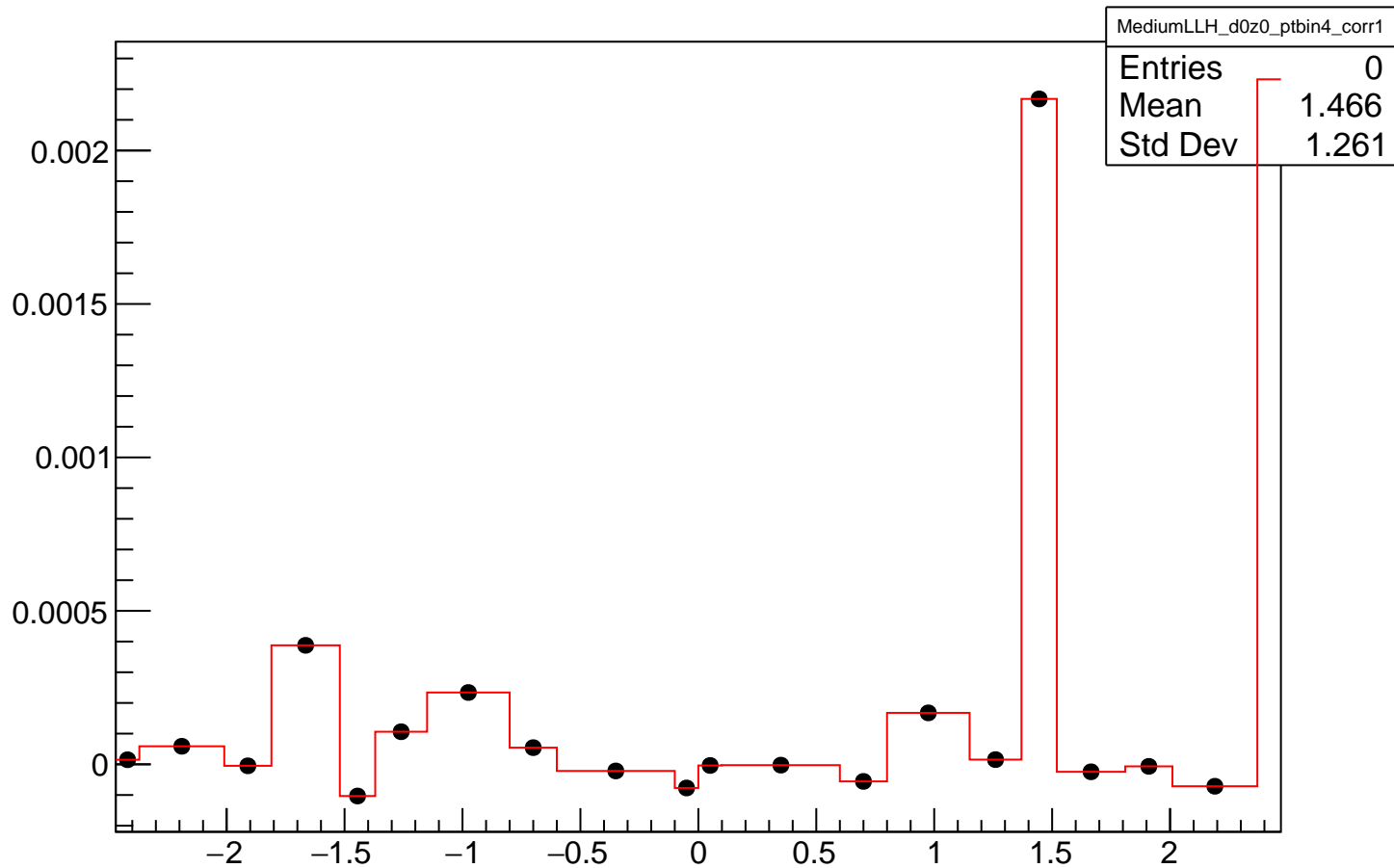
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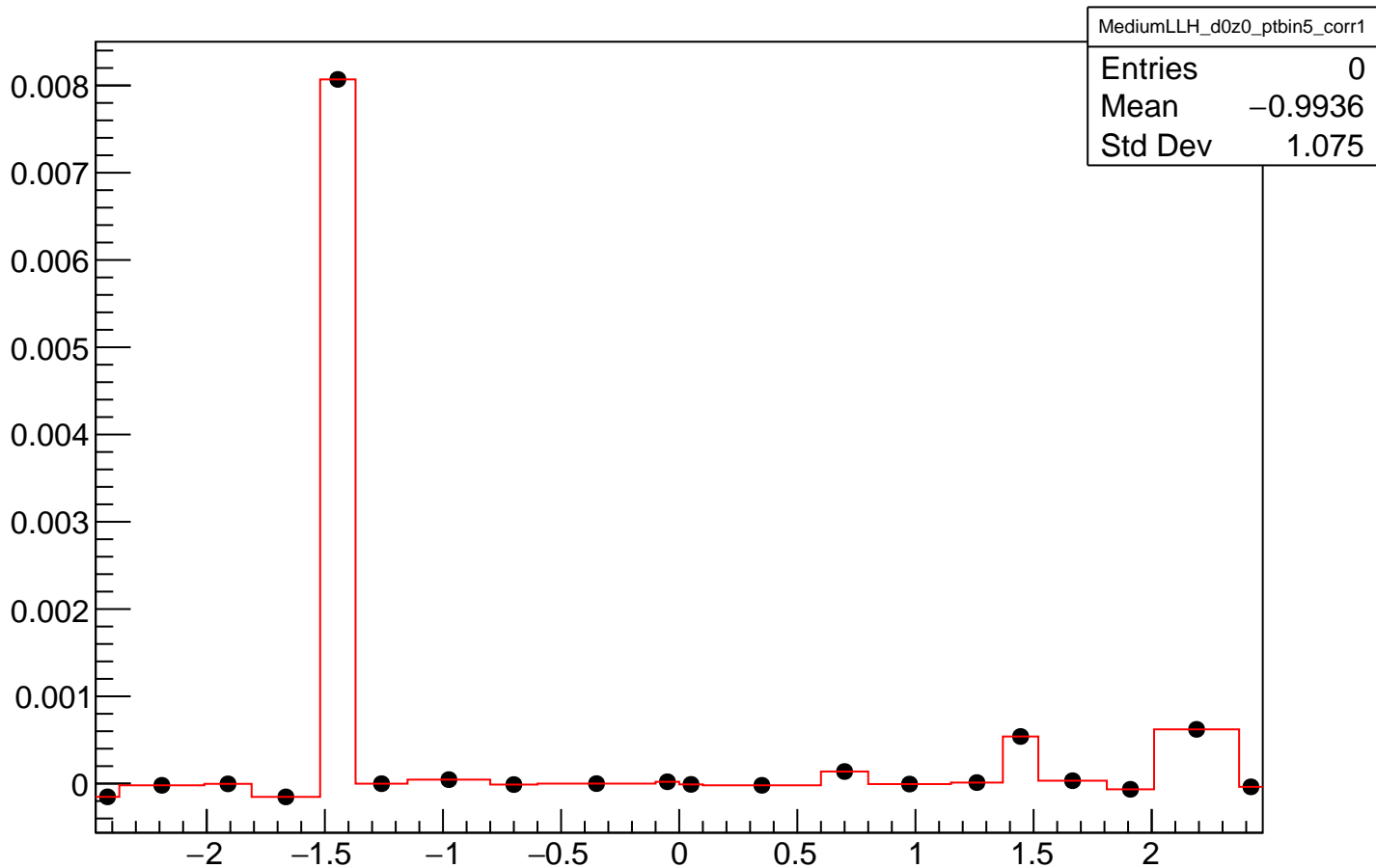
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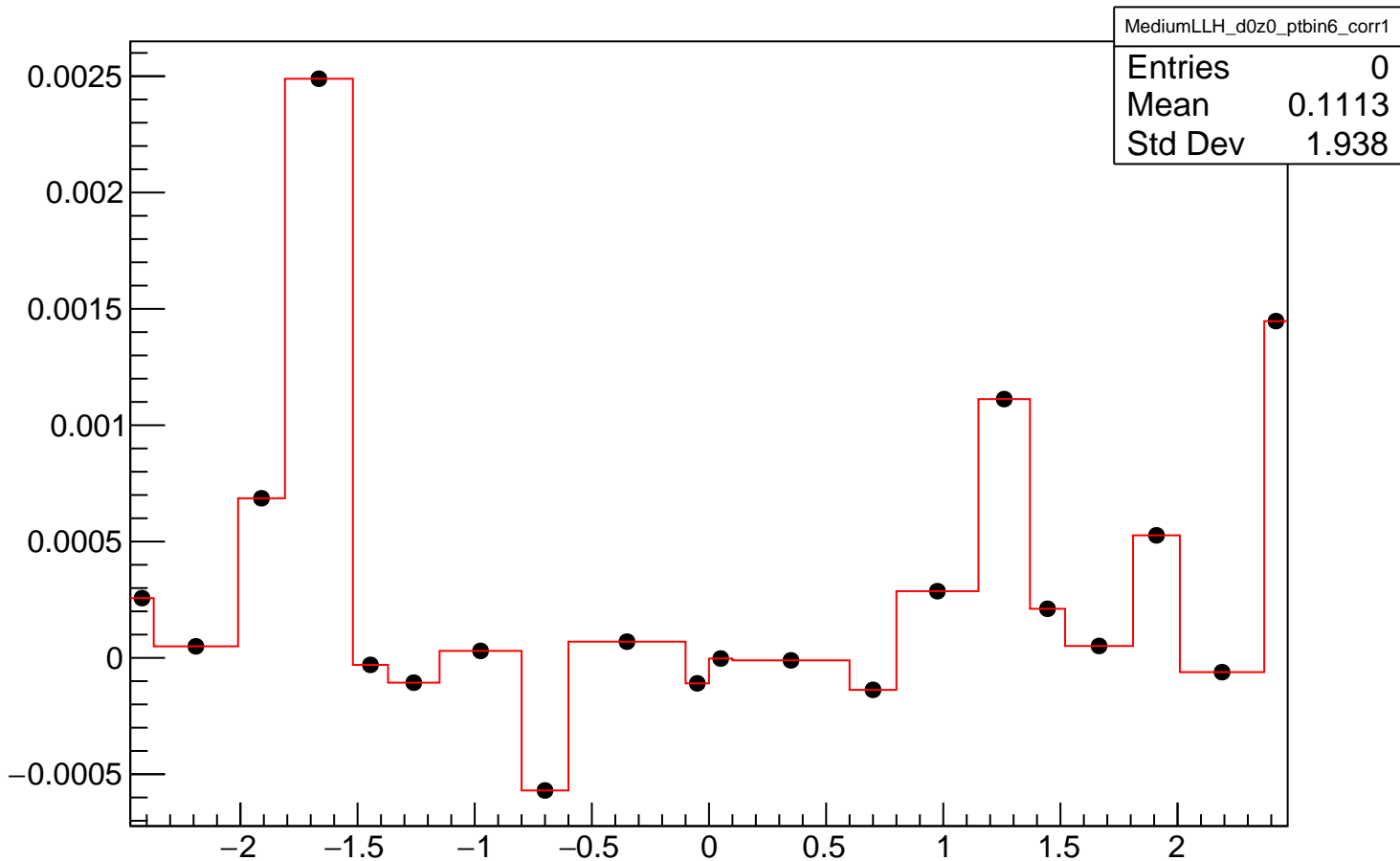
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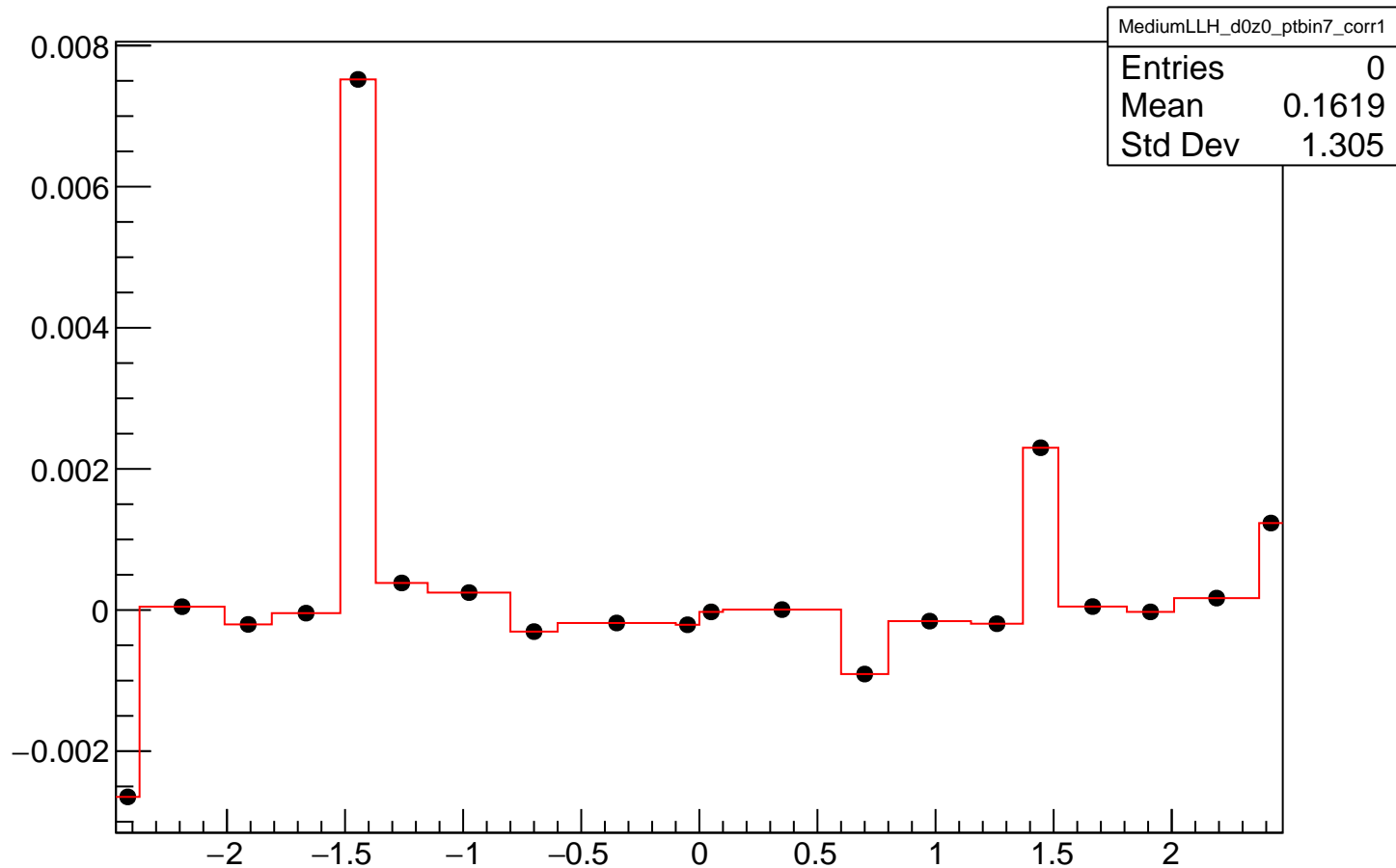
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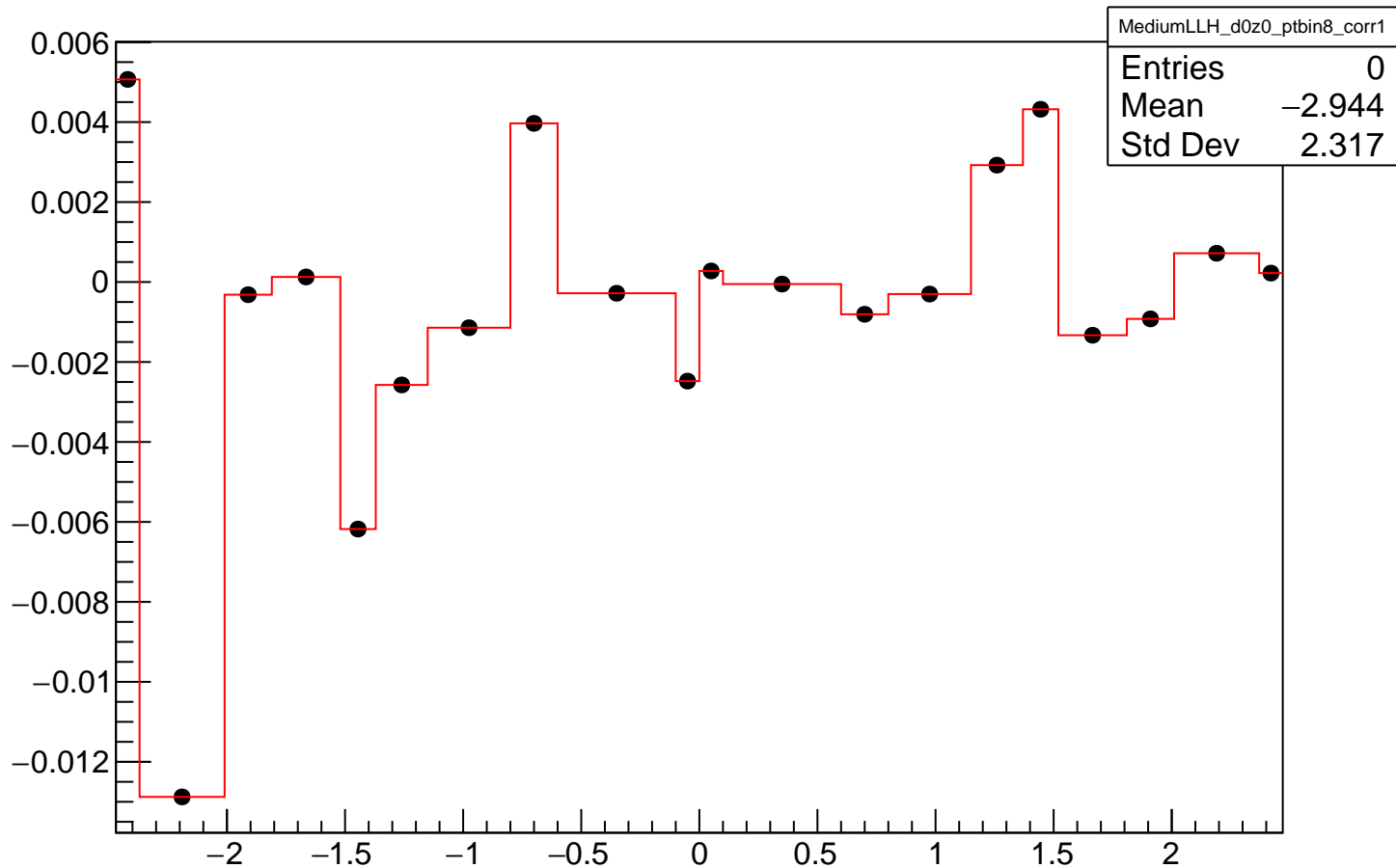
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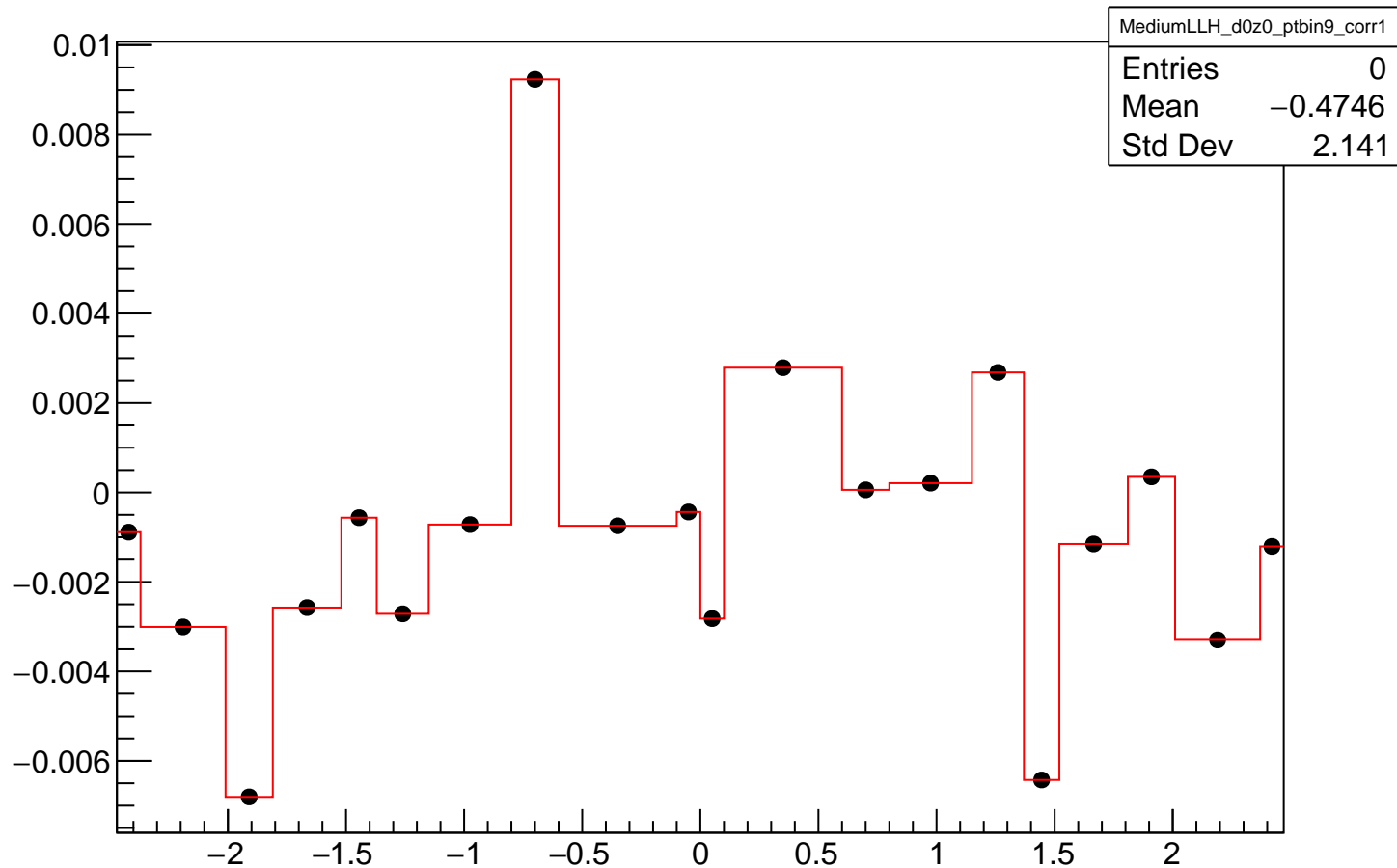
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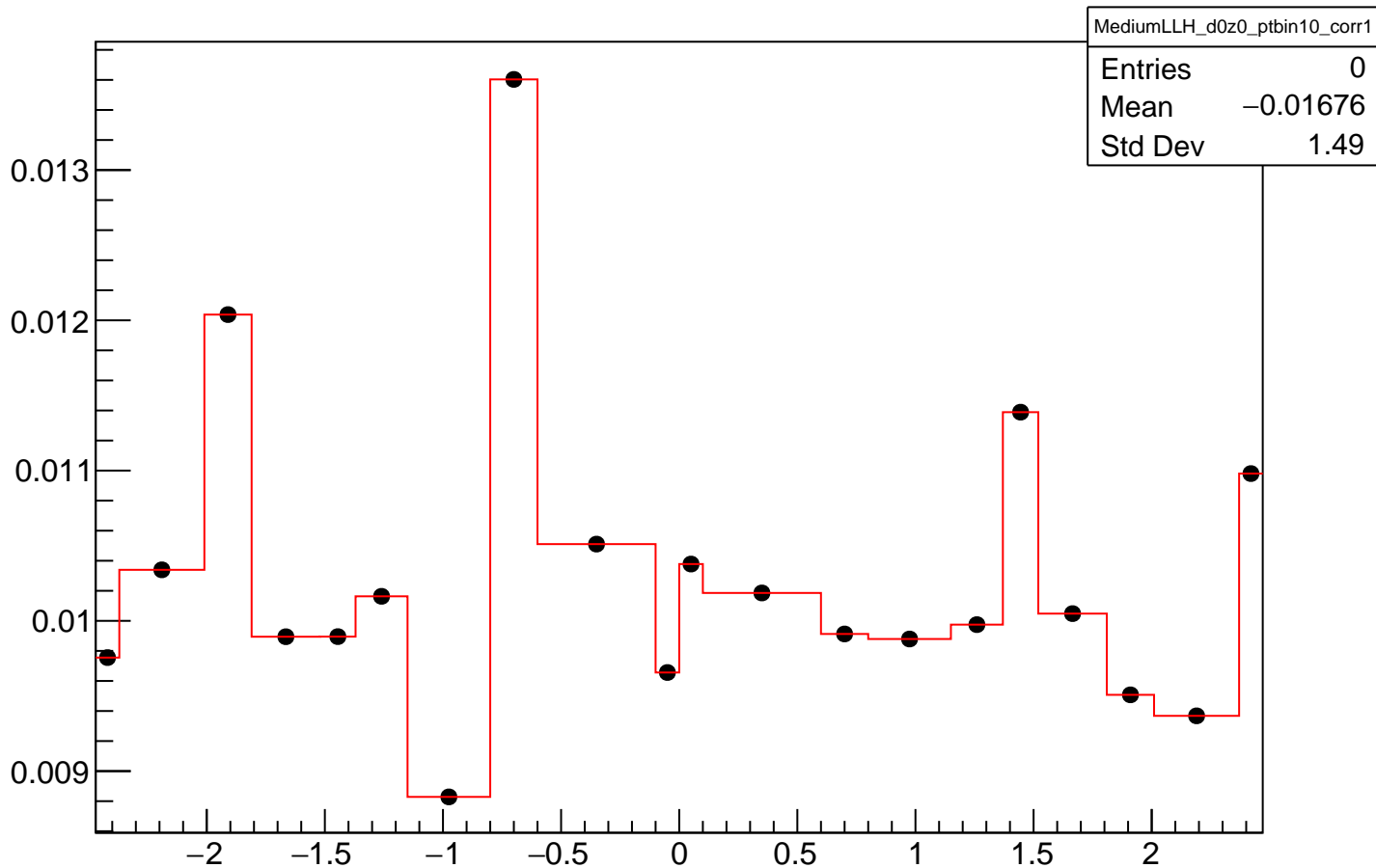
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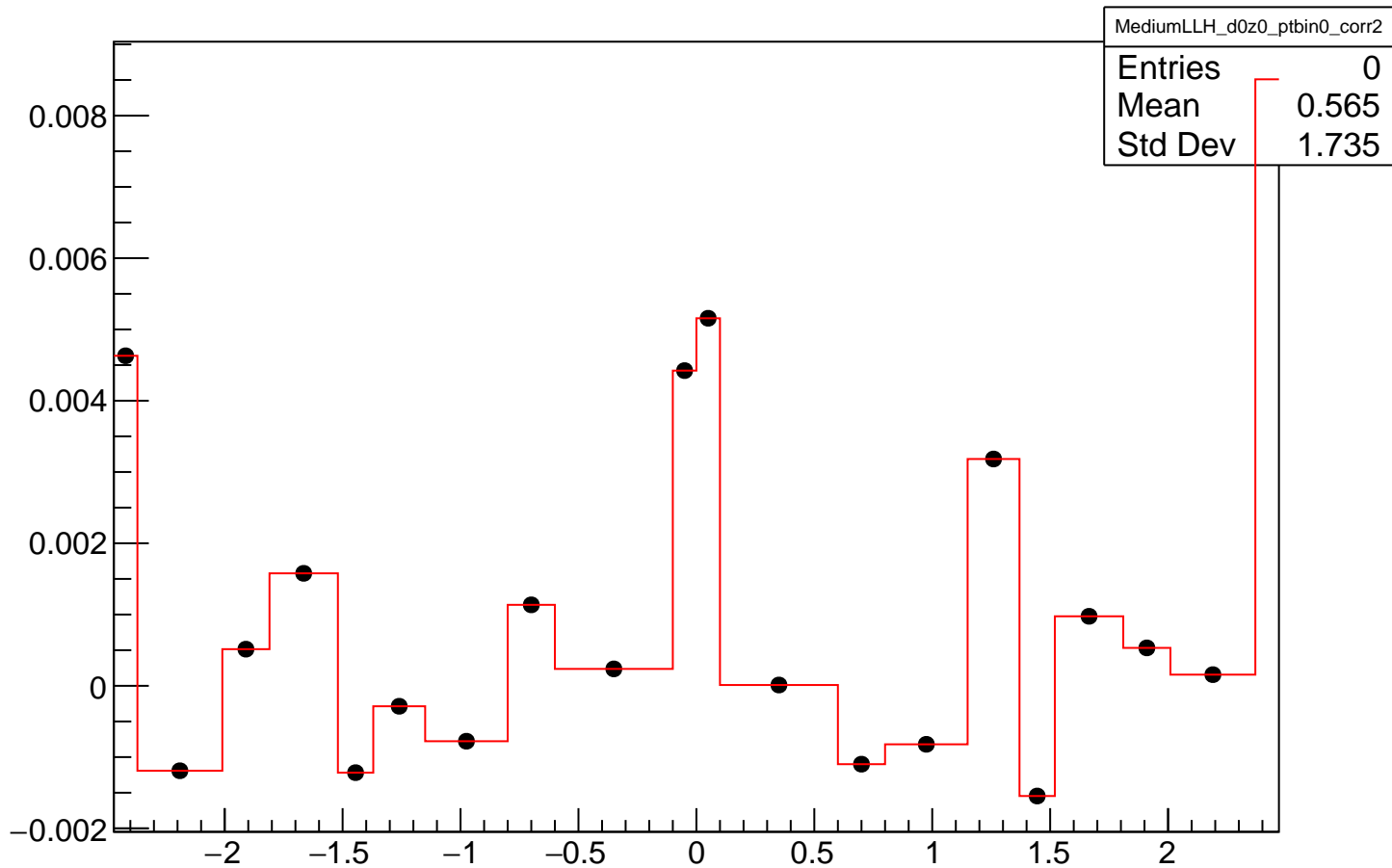
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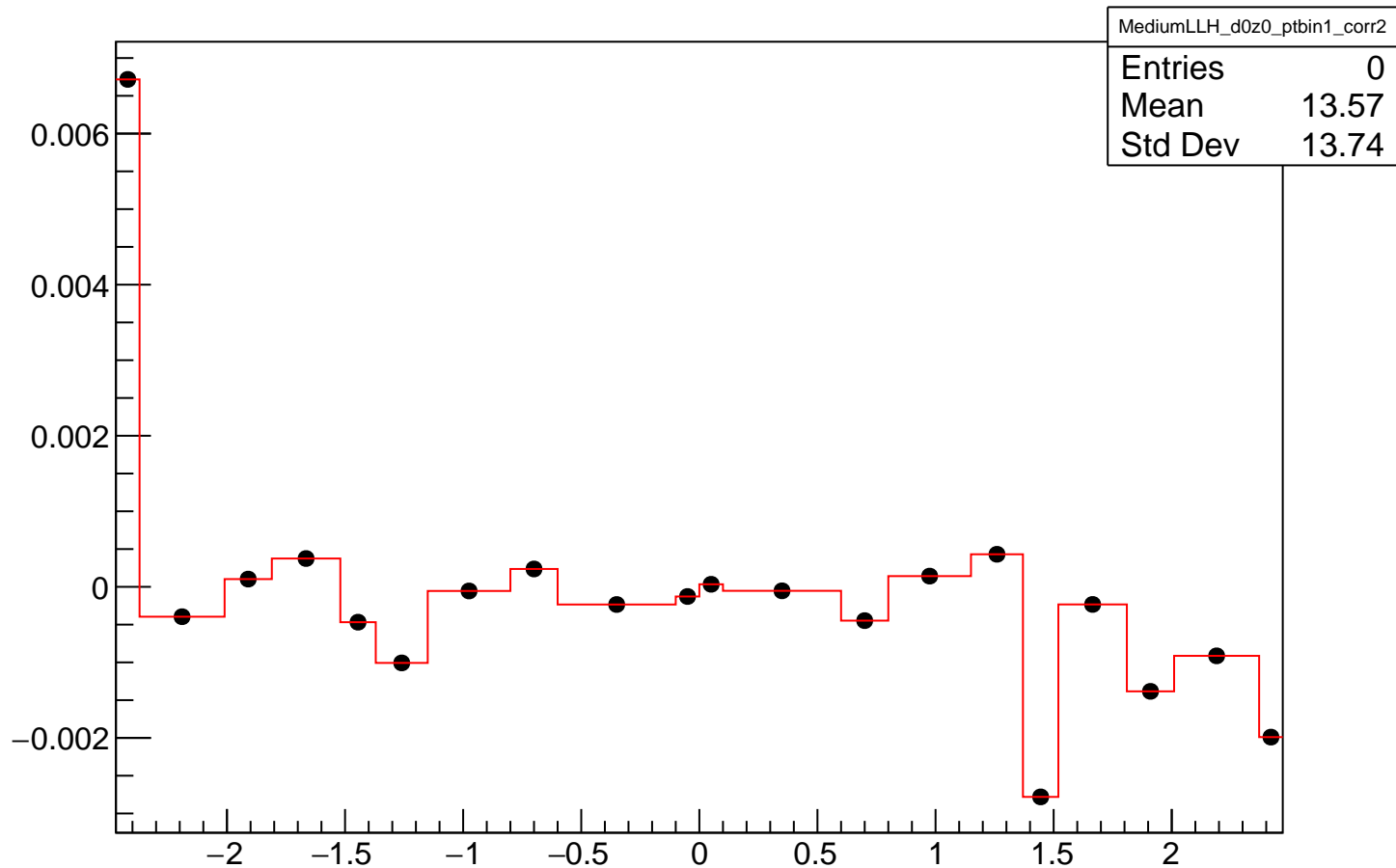
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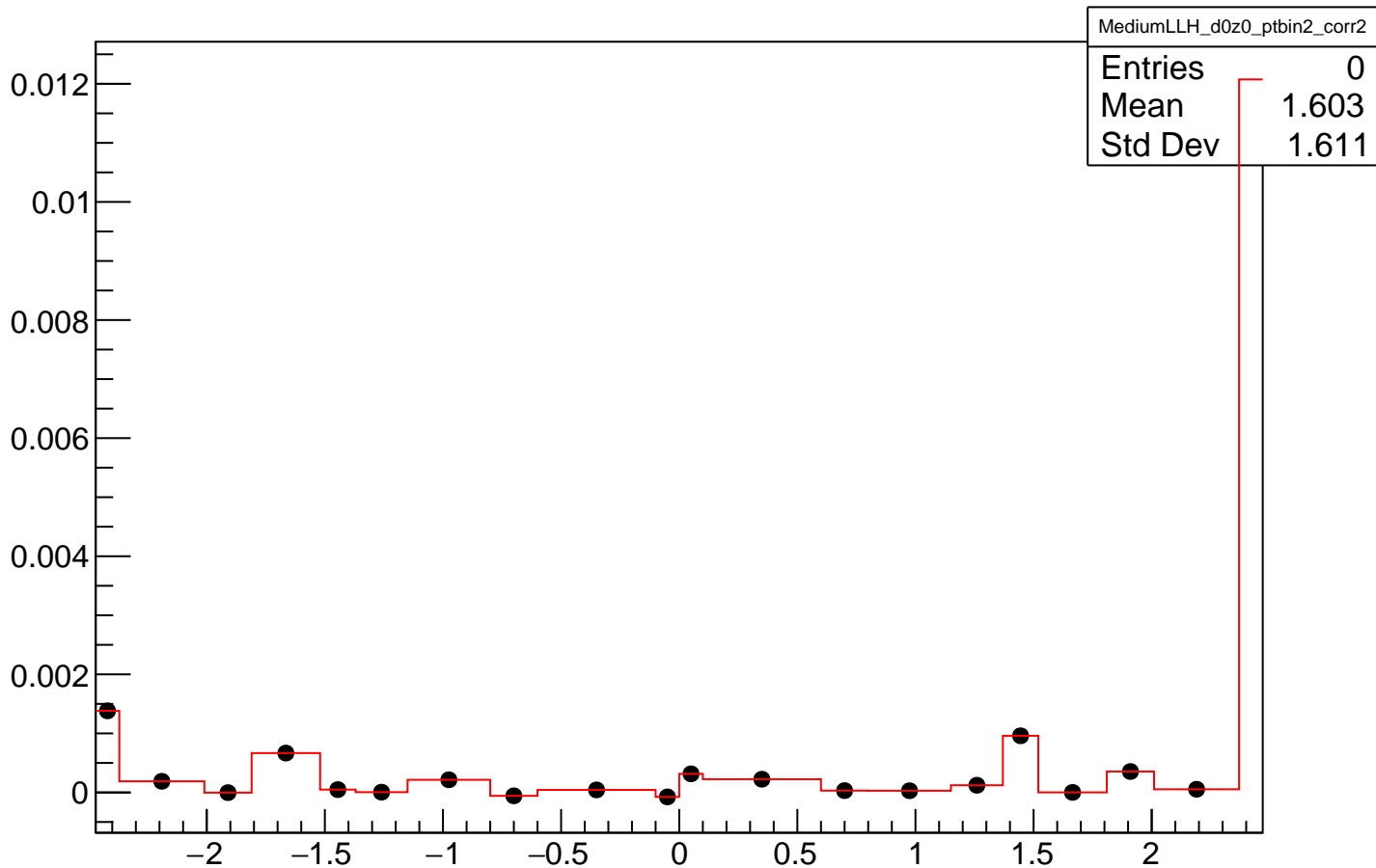
FullSim_MediumLLH_d0z0_corr2



FullSim_MediumLLH_d0z0_corr2



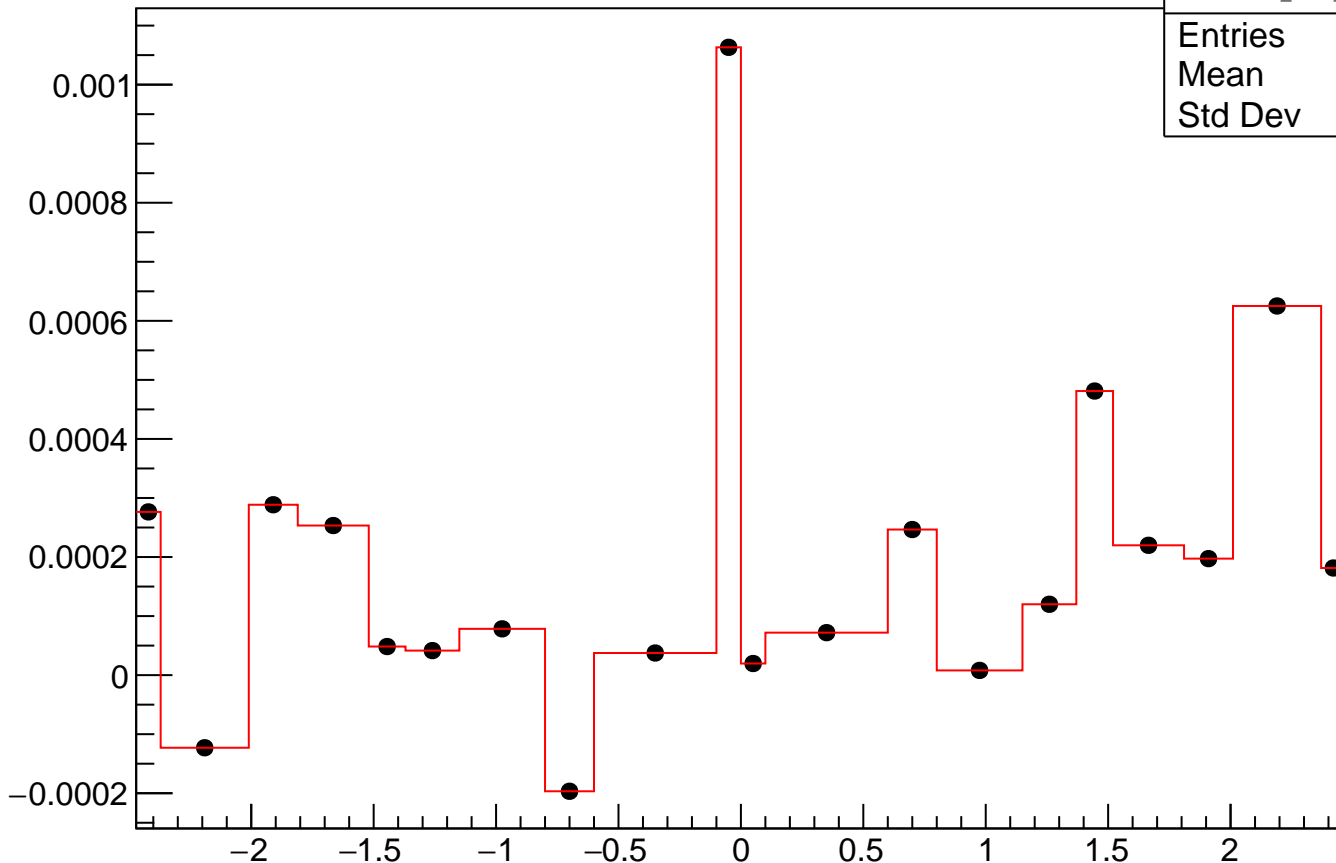
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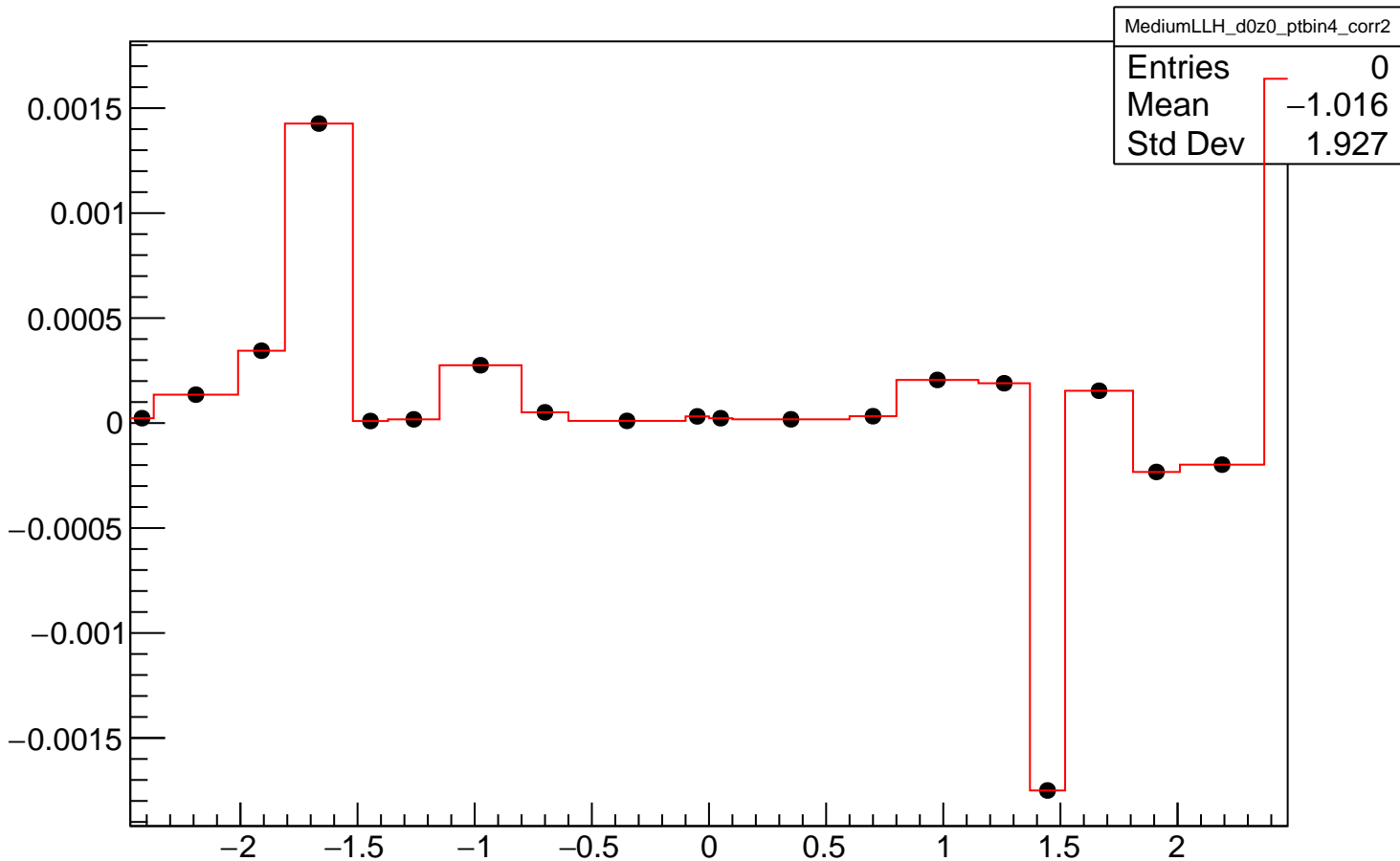
FullSim_MediumLLH_d0z0_corr2

MediumLLH_d0z0_ptbin3_corr2

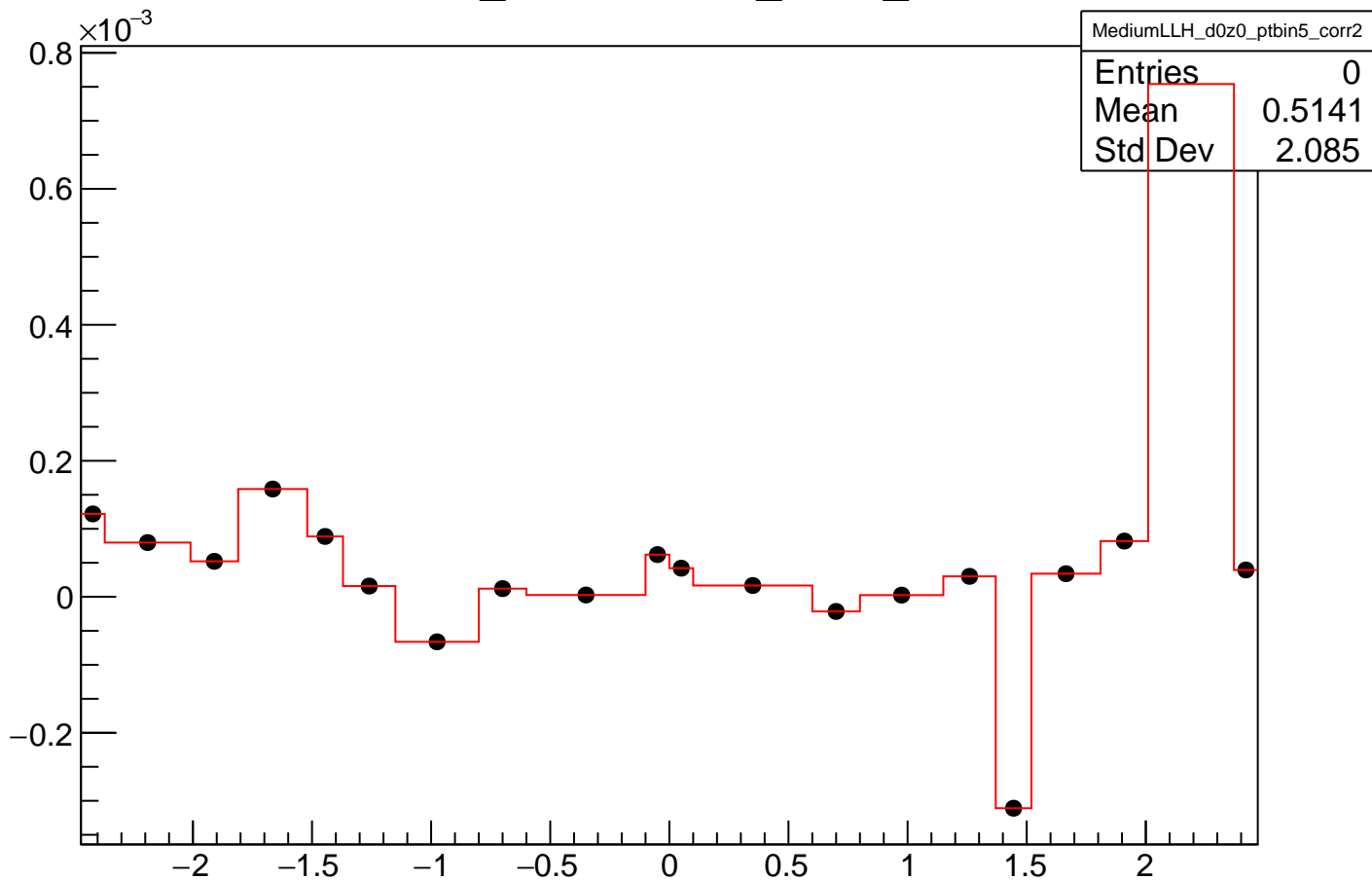
Entries	0
Mean	0.5342
Std Dev	1.472



FullSim_MediumLLH_d0z0_corr2



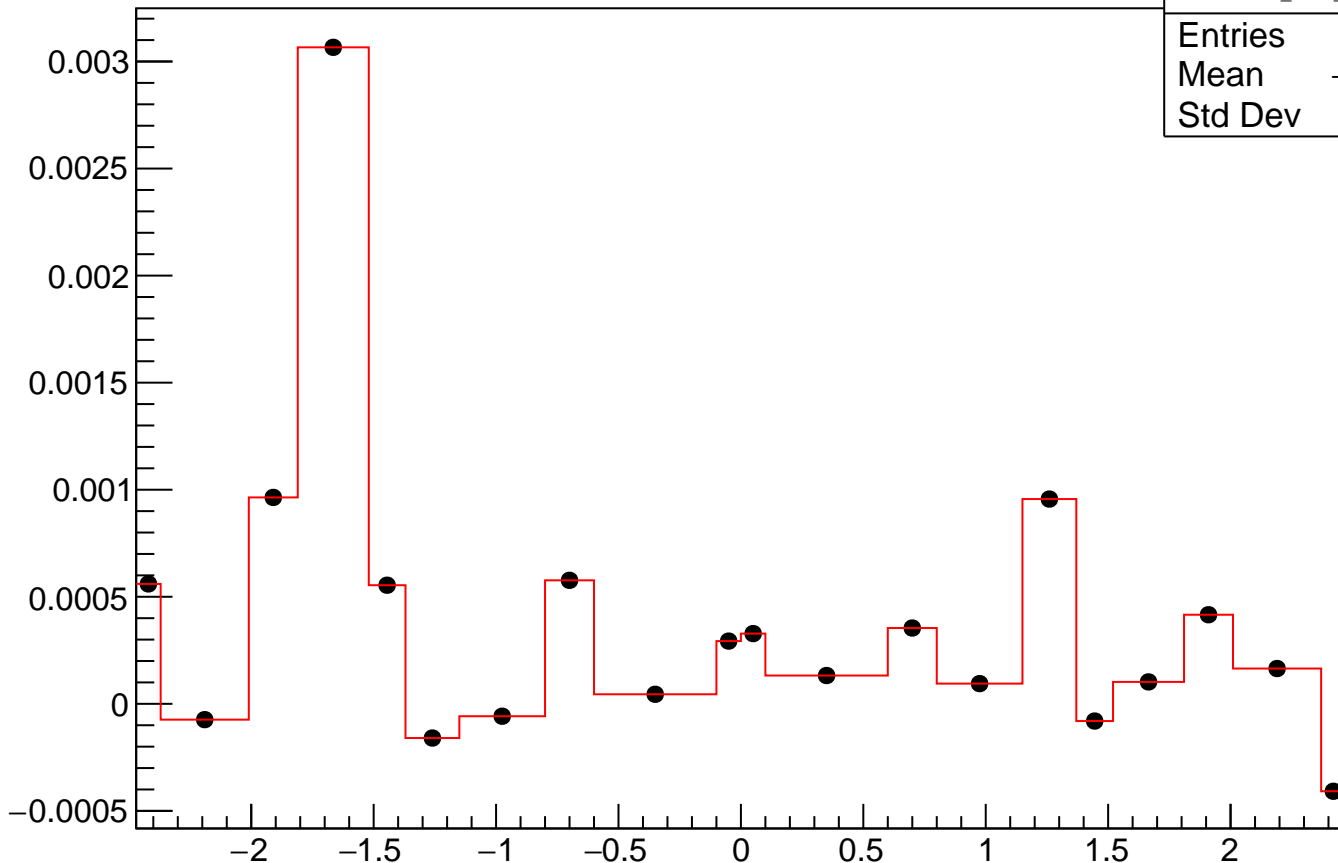
FullSim_MediumLLH_d0z0_corr2



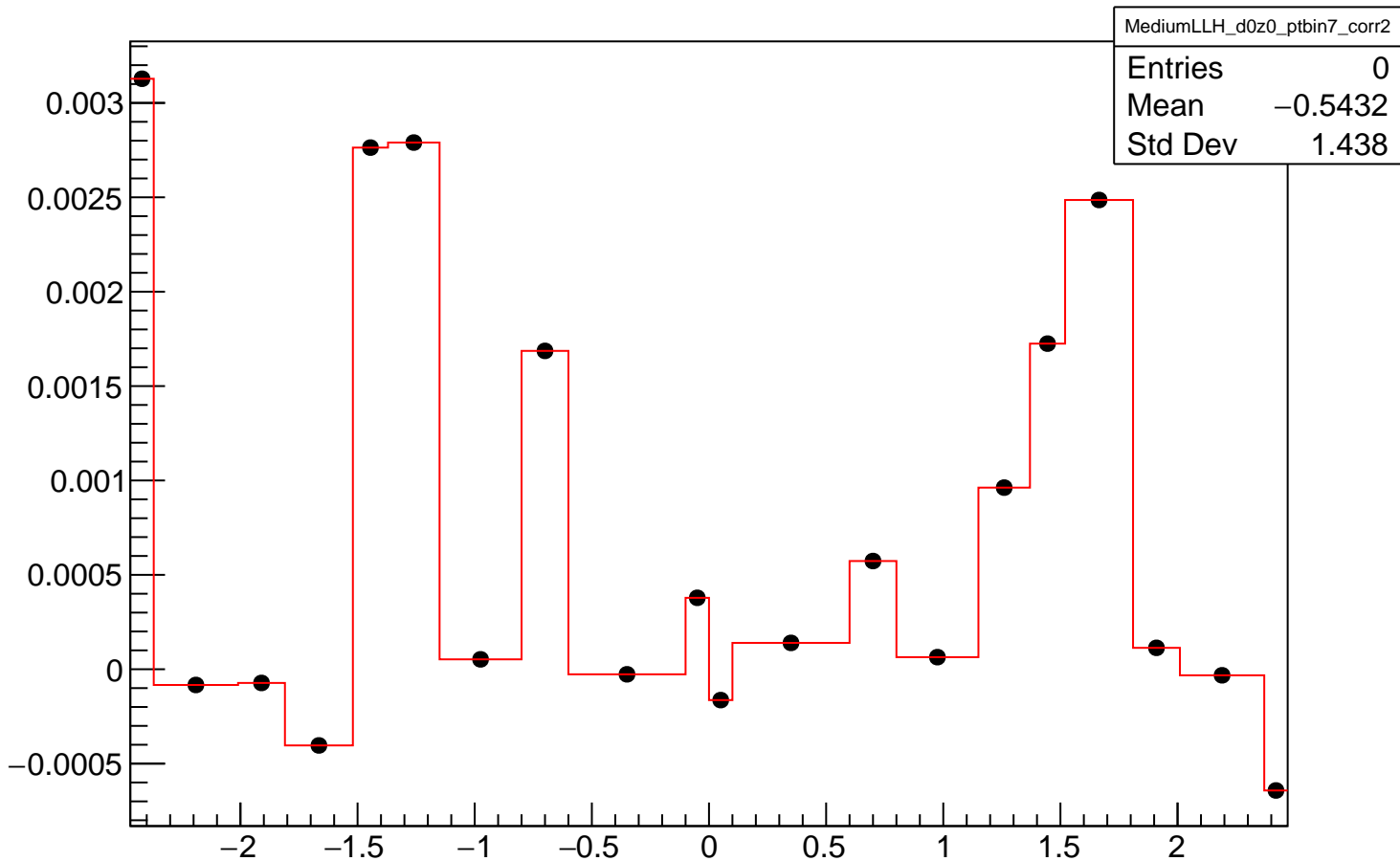
FullSim_MediumLLH_d0z0_corr2

MediumLLH_d0z0_ptbin6_corr2

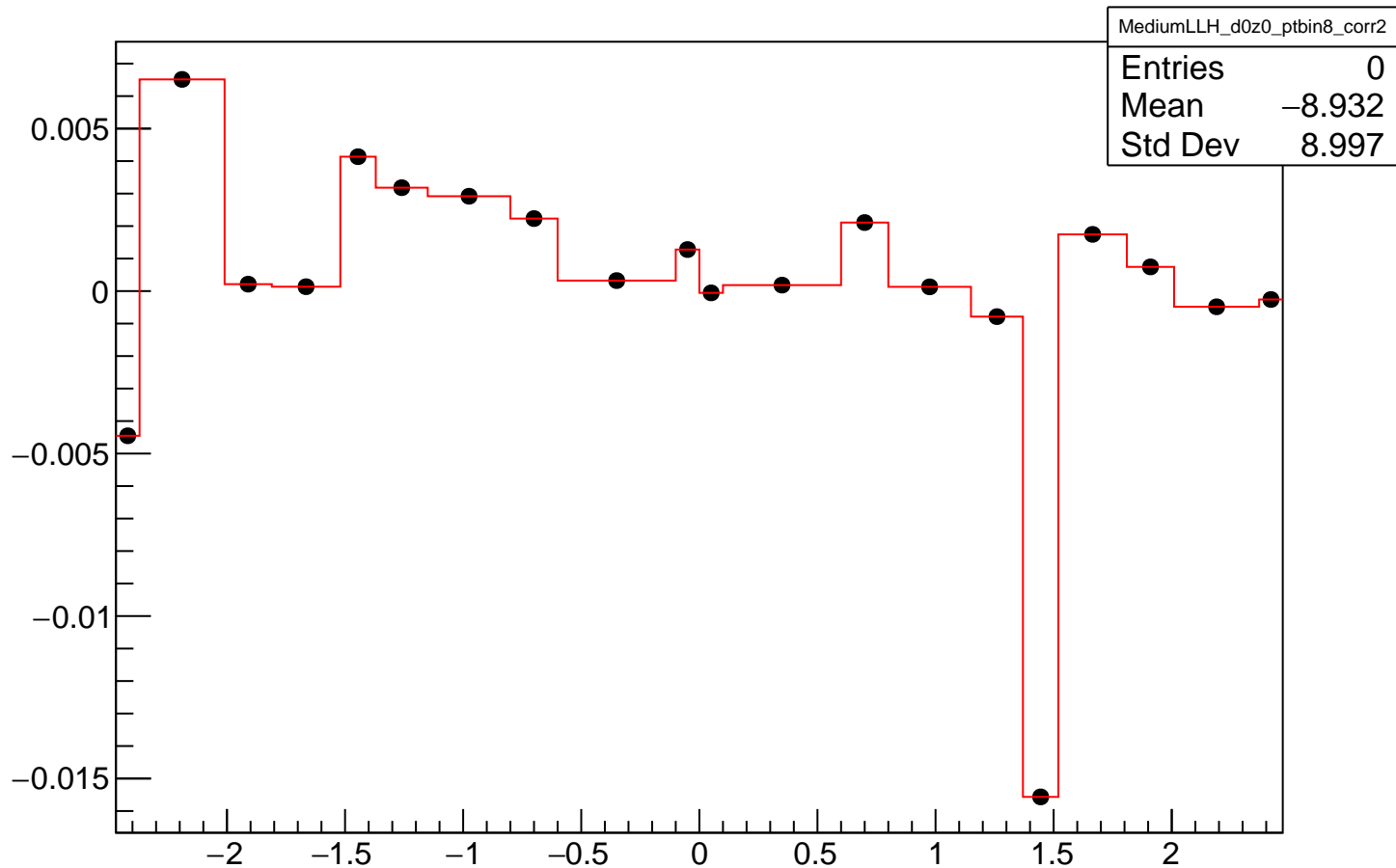
Entries	0
Mean	-0.9311
Std Dev	1.192



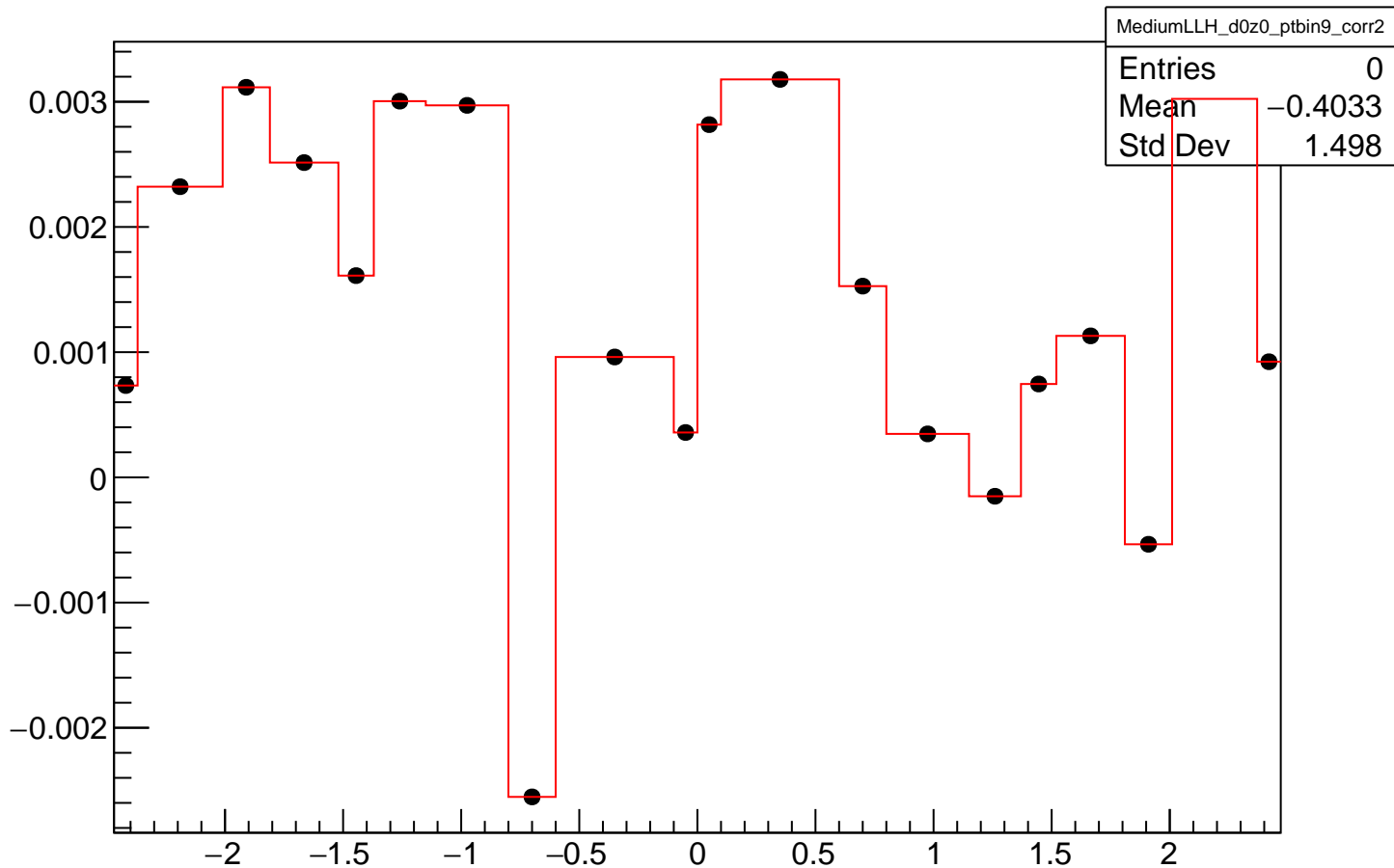
FullSim_MediumLLH_d0z0_corr2



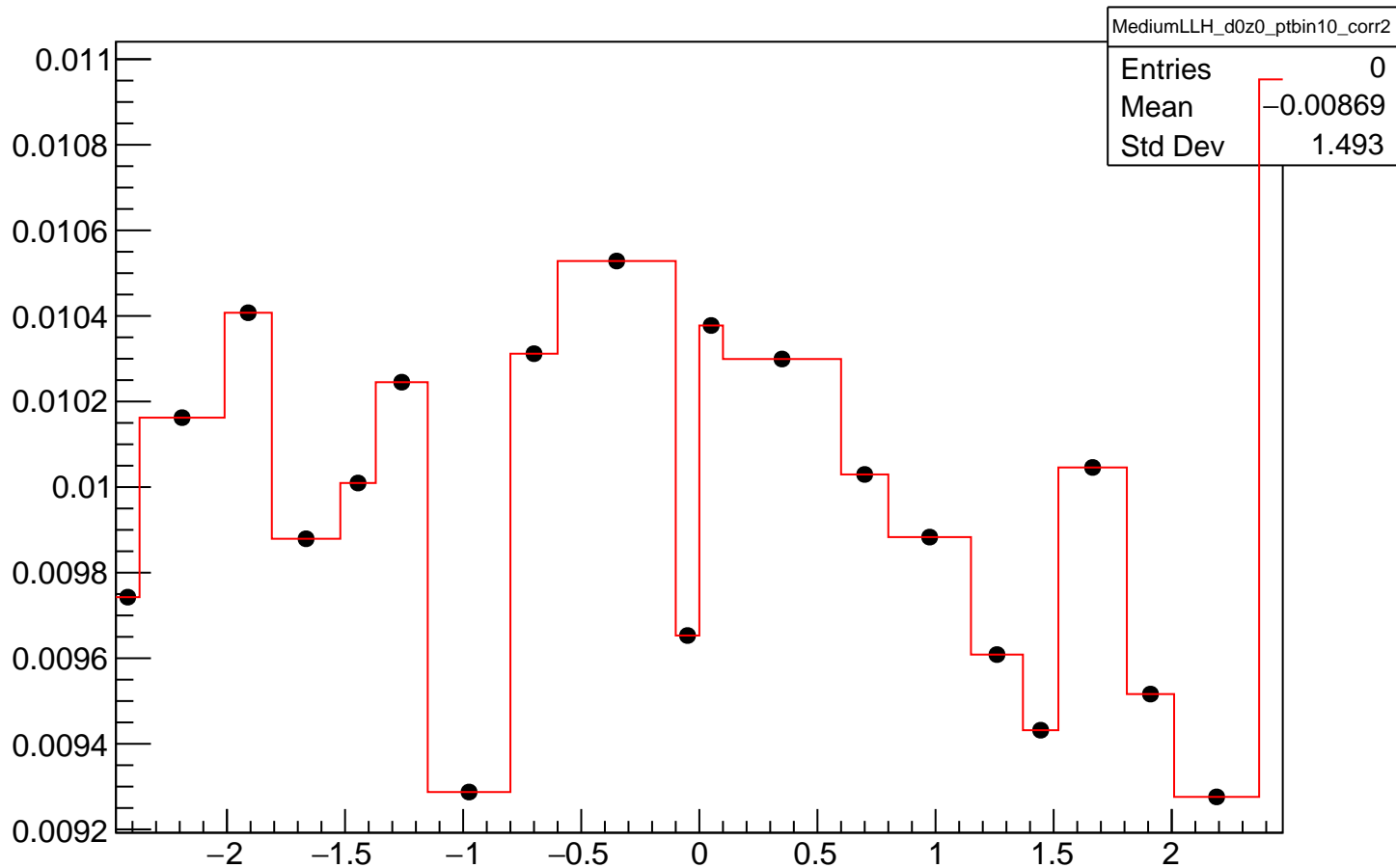
FullSim_MediumLLH_d0z0_corr2



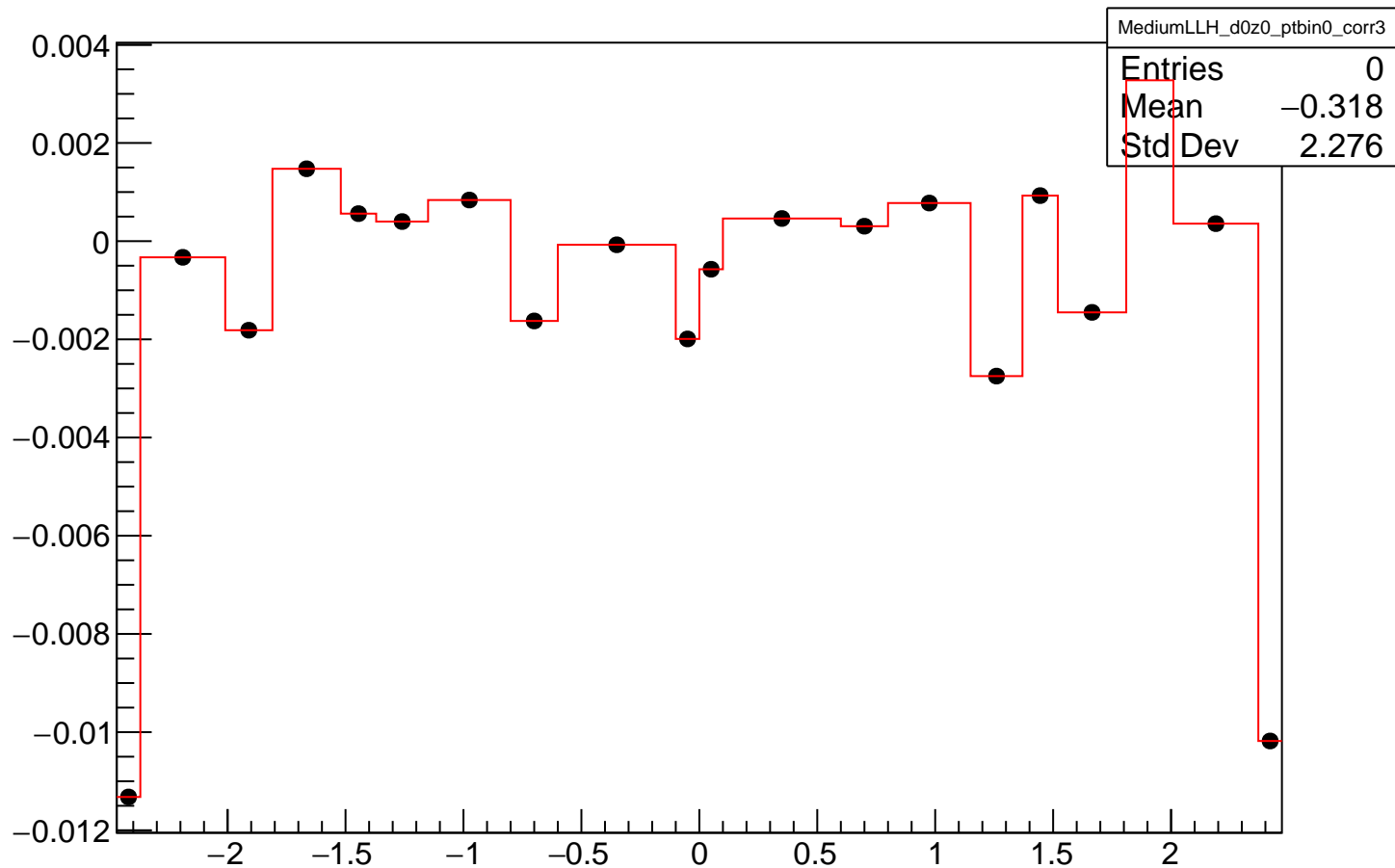
FullSim_MediumLLH_d0z0_corr2



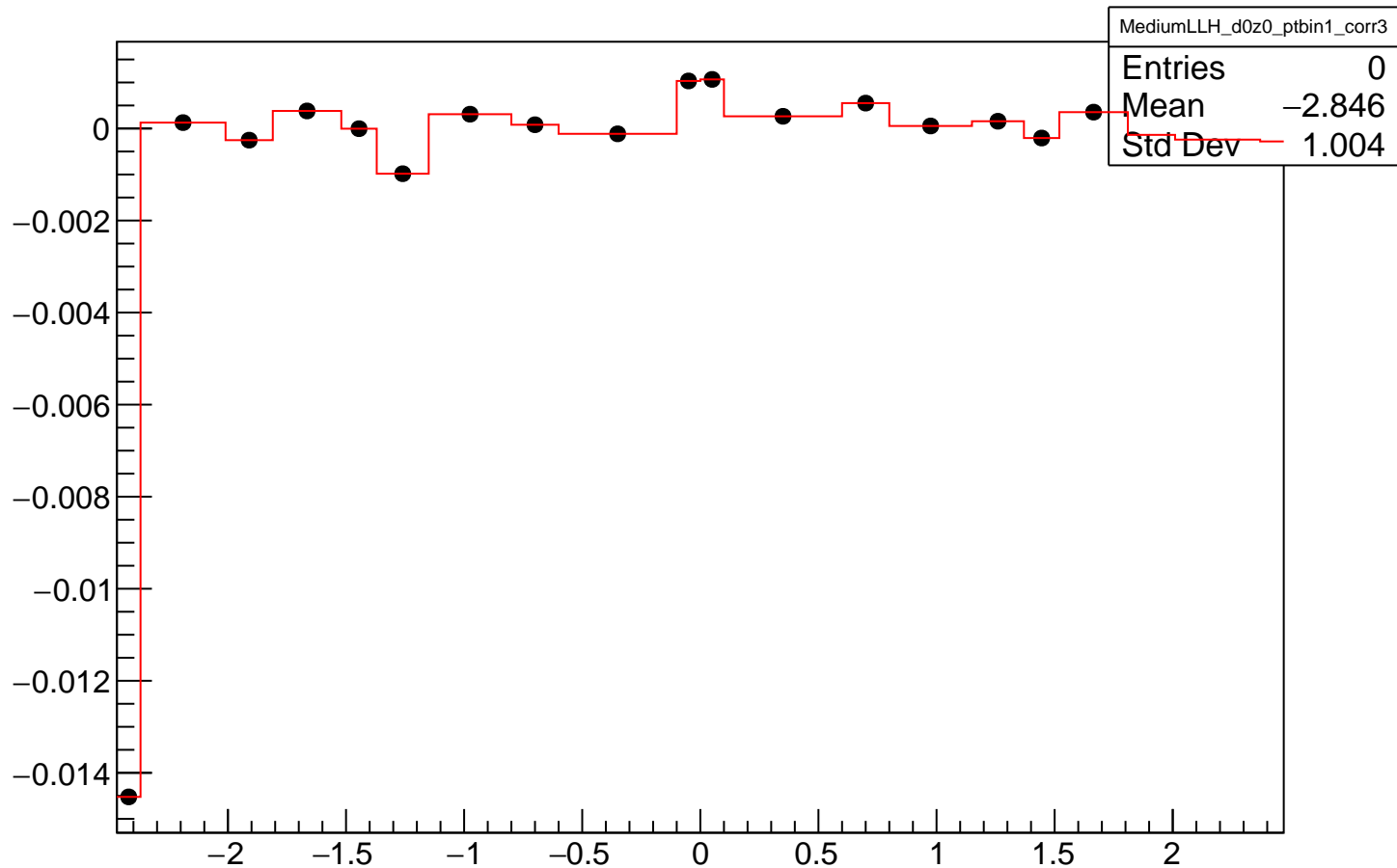
FullSim_MediumLLH_d0z0_corr2



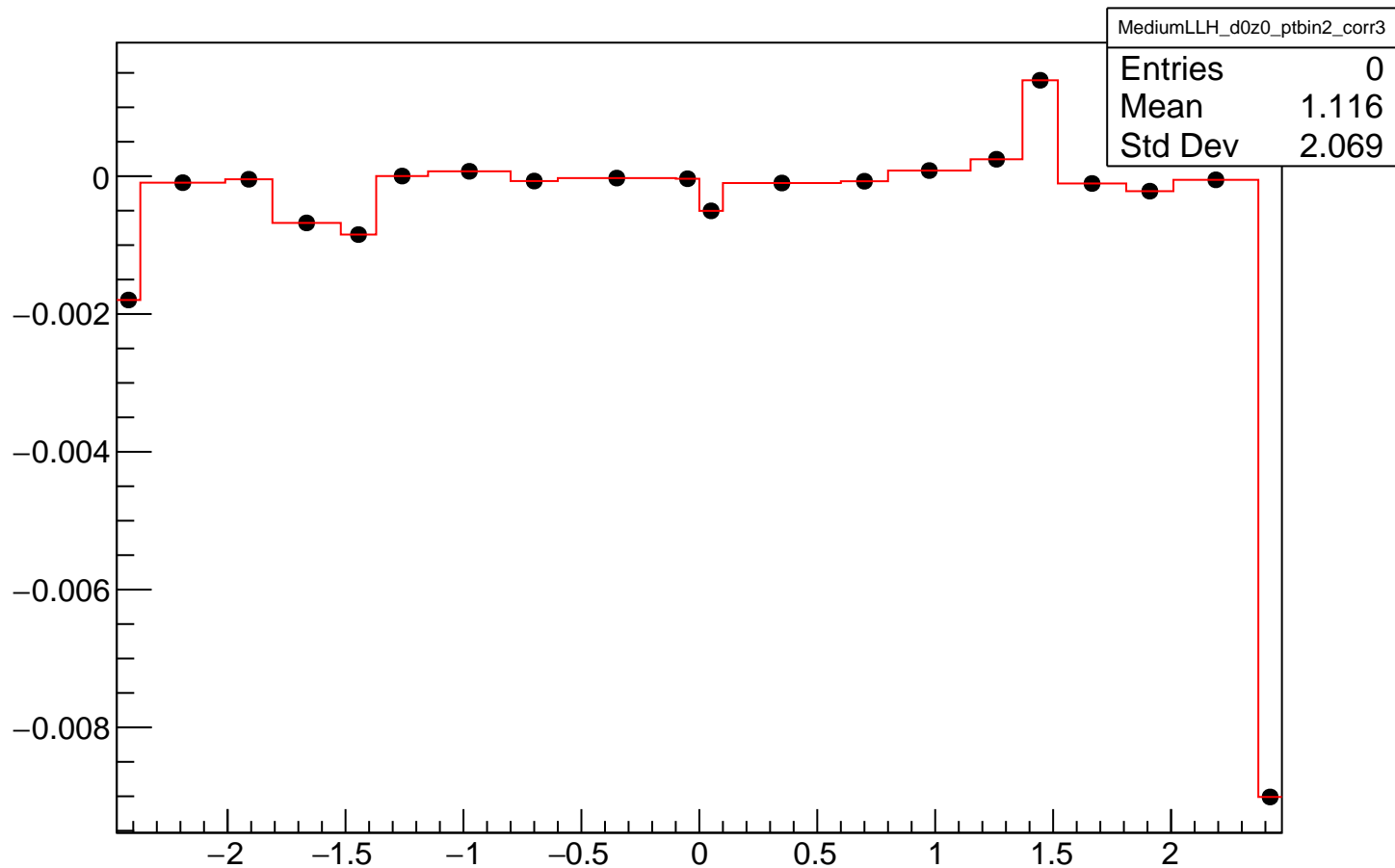
FullSim_MediumLLH_d0z0_corr3



FullSim_MediumLLH_d0z0_corr3



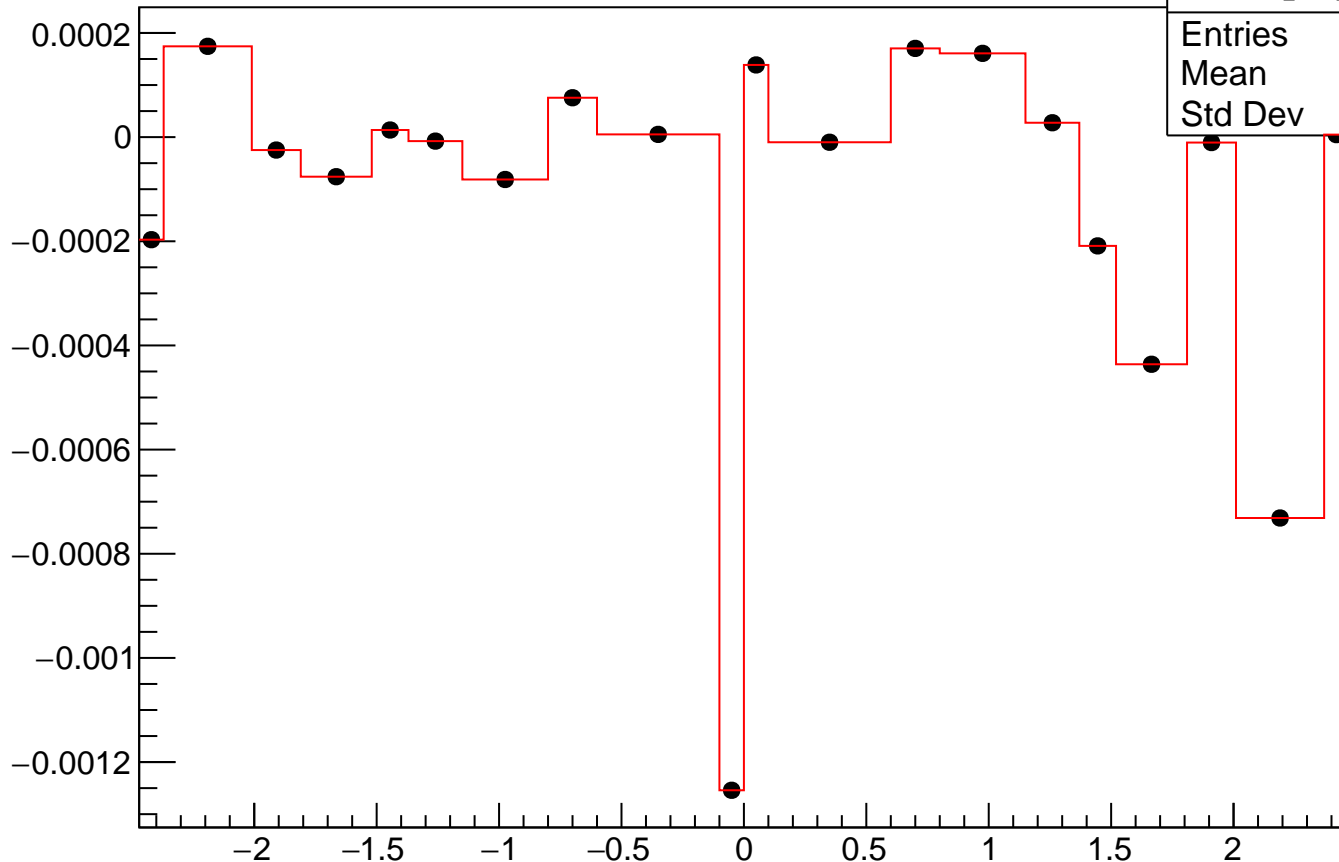
FullSim_MediumLLH_d0z0_corr3



FullSim_MediumLLH_d0z0_corr3

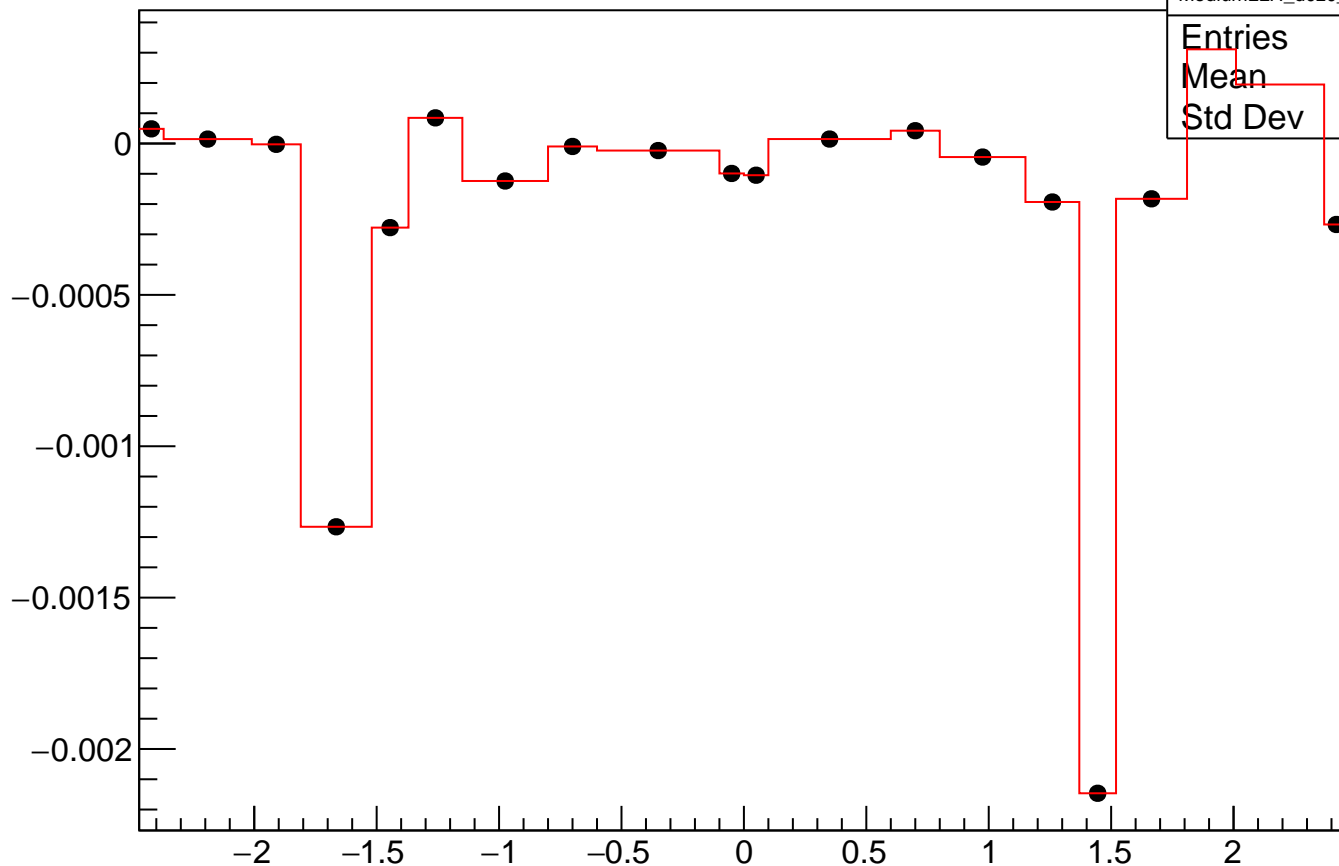
MediumLLH_d0z0_ptbin3_corr3

Entries	0
Mean	0.8725
Std Dev	1.295

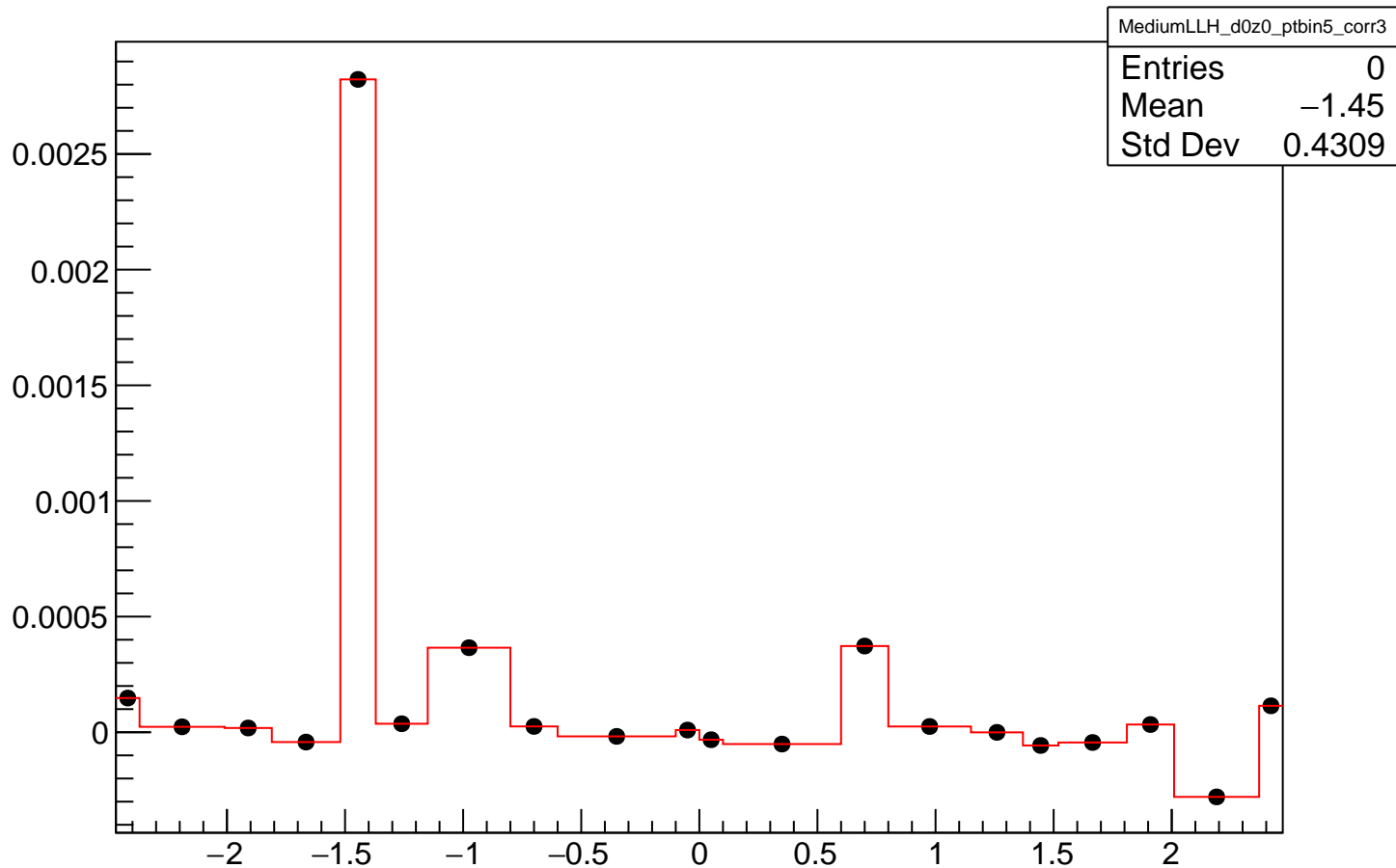


FullSim_MediumLLH_d0z0_corr3

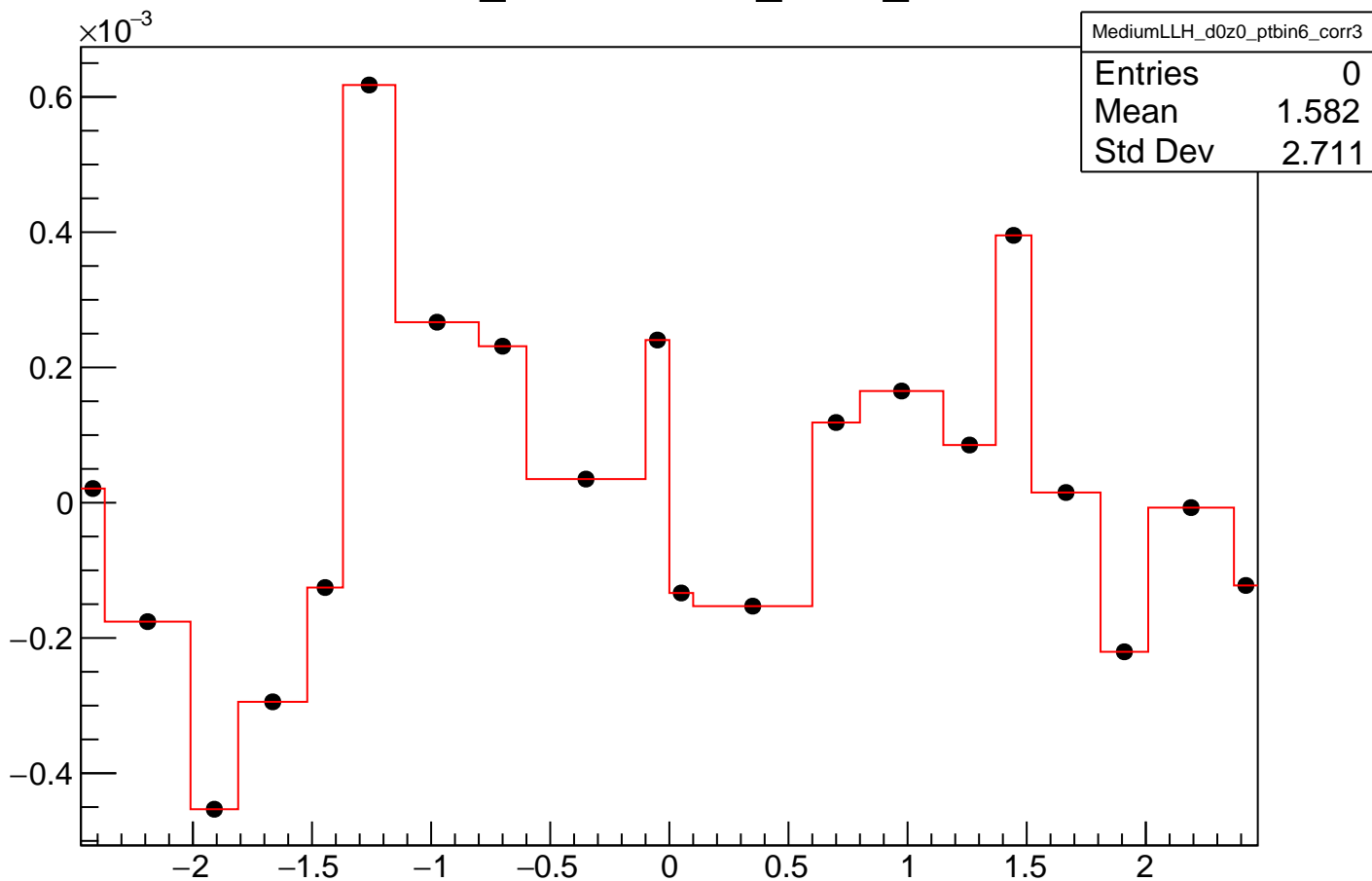
MediumLLH_d0z0_ptbin4_corr3	
Entries	0
Mean	0.2208
Std Dev	1.44



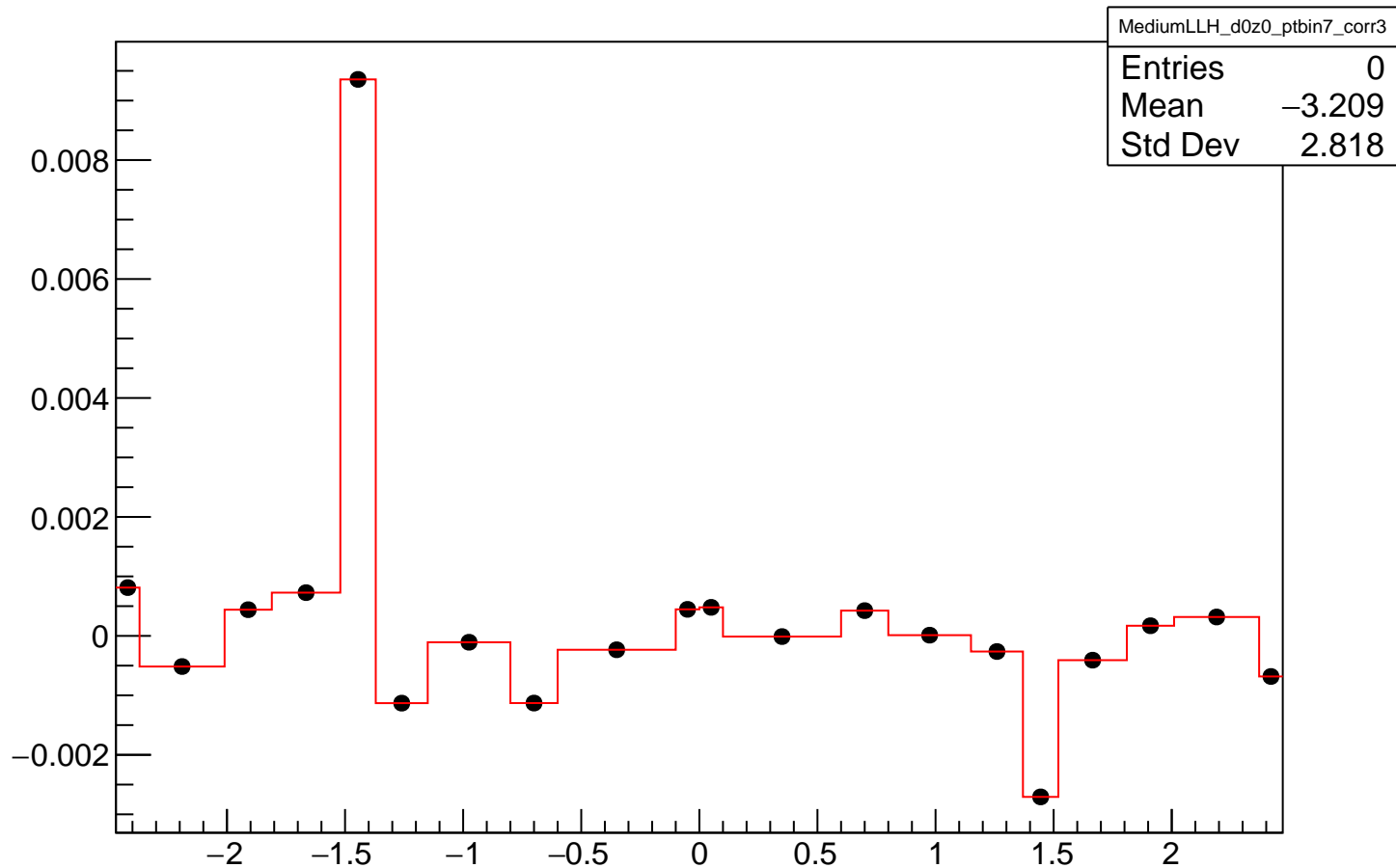
FullSim_MediumLLH_d0z0_corr3



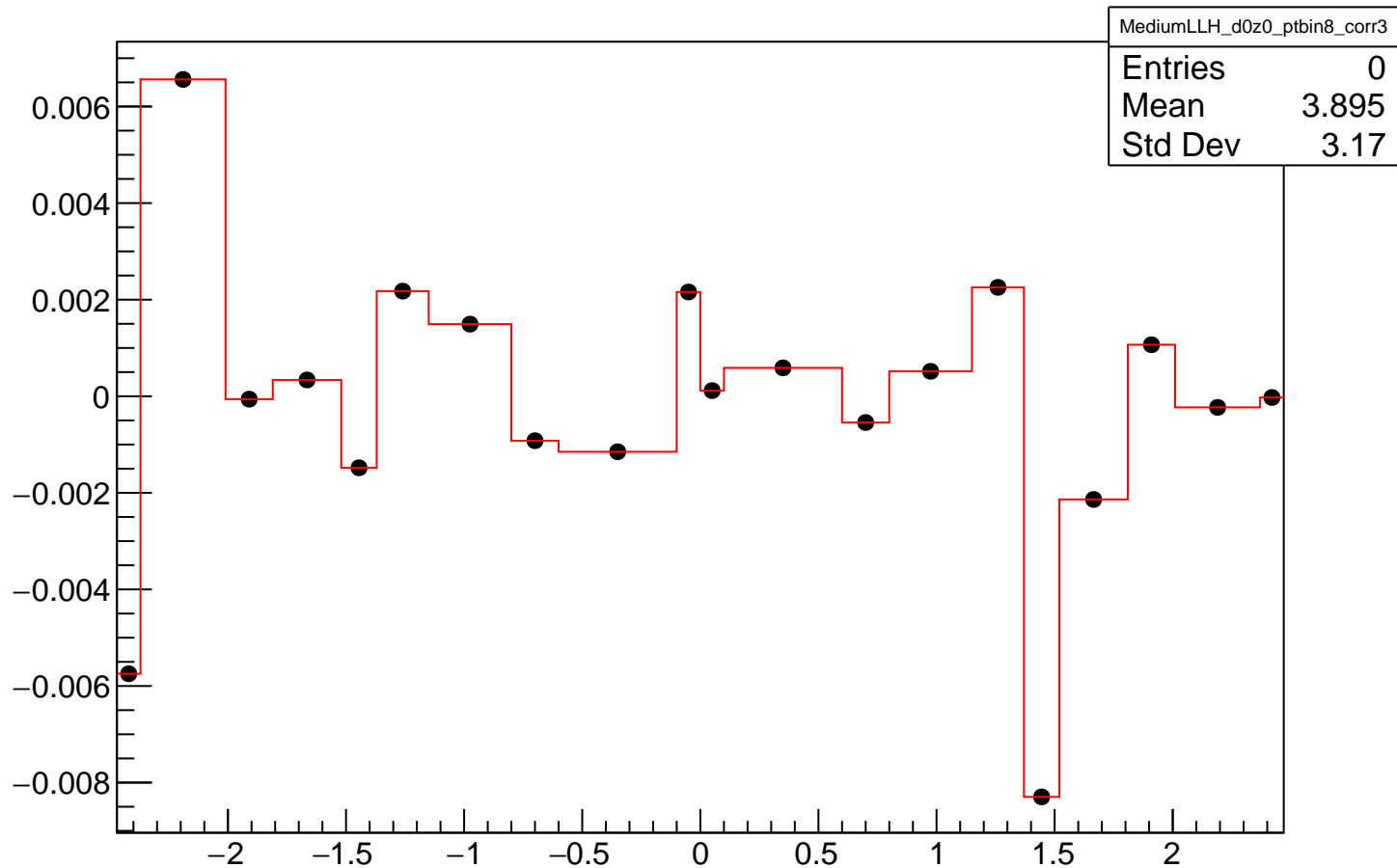
FullSim_MediumLLH_d0z0_corr3



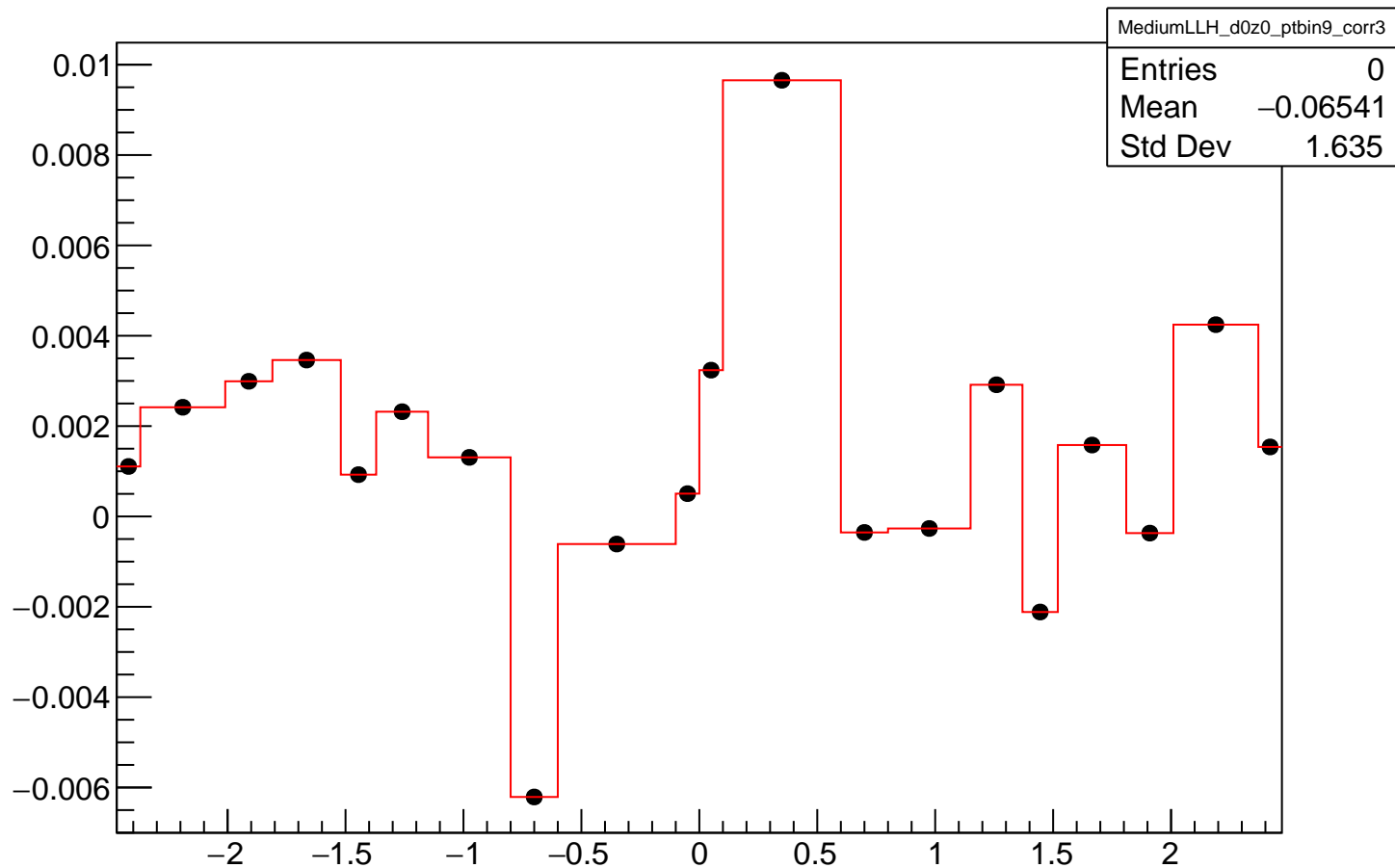
FullSim_MediumLLH_d0z0_corr3



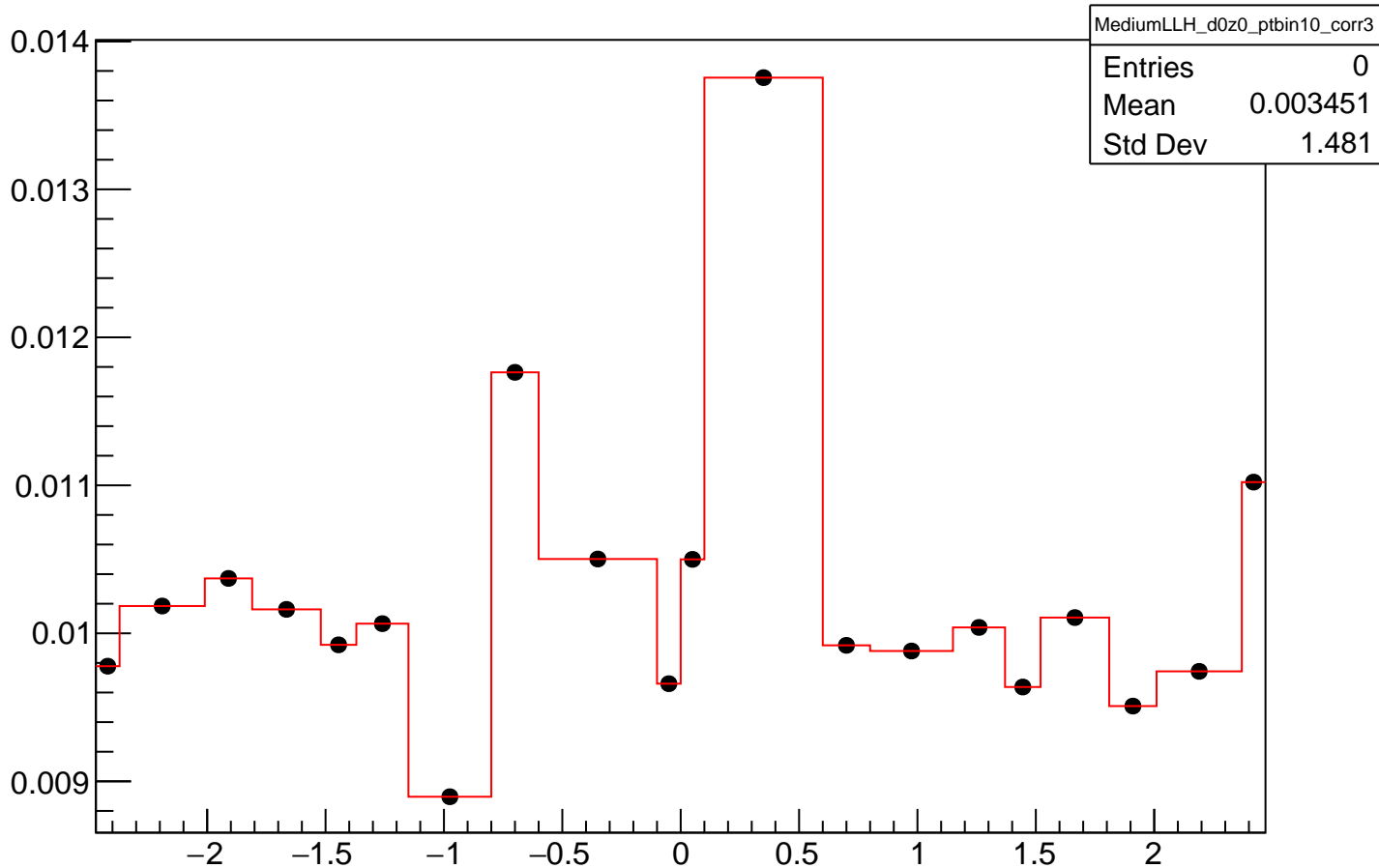
FullSim_MediumLLH_d0z0_corr3



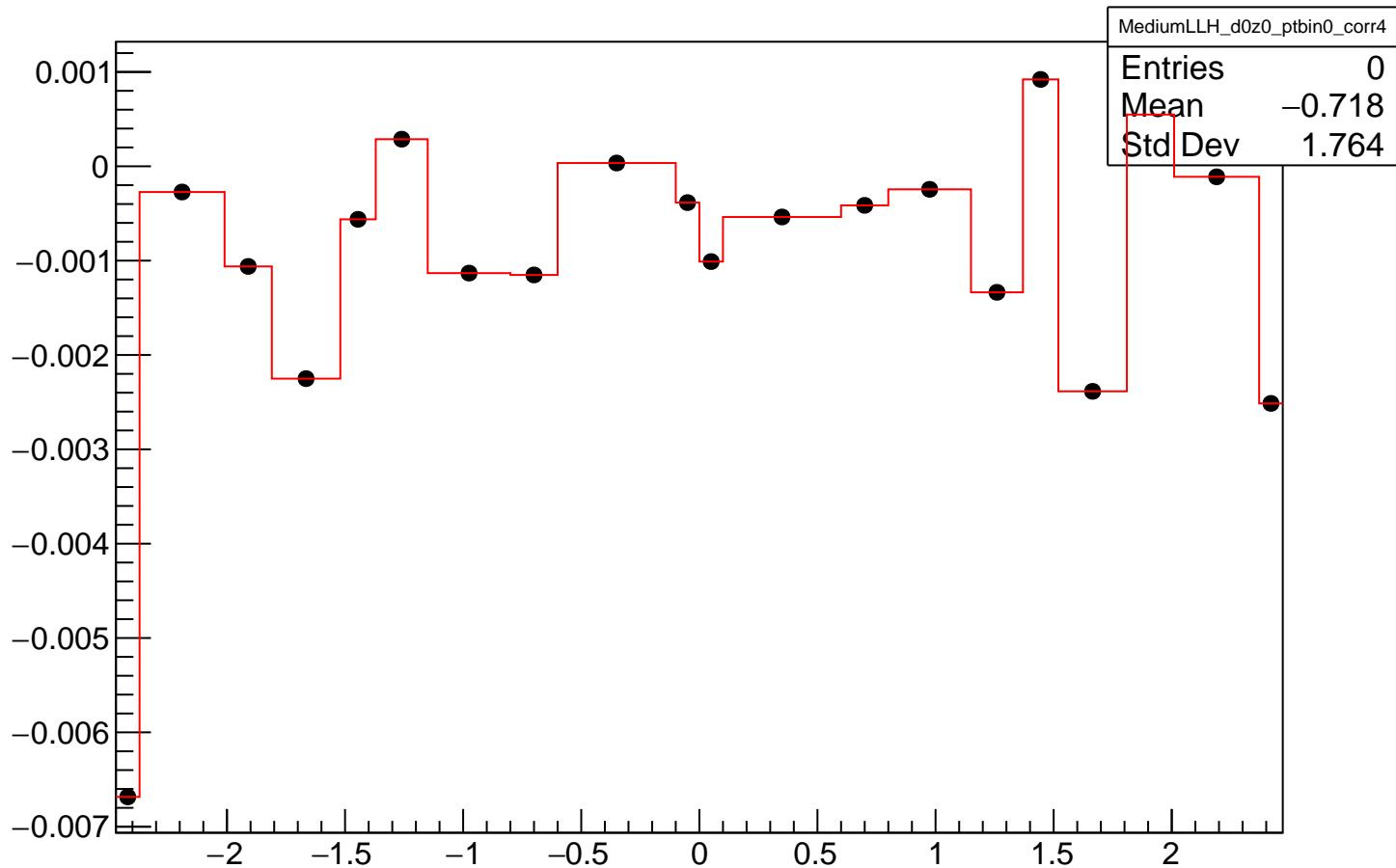
FullSim_MediumLLH_d0z0_corr3



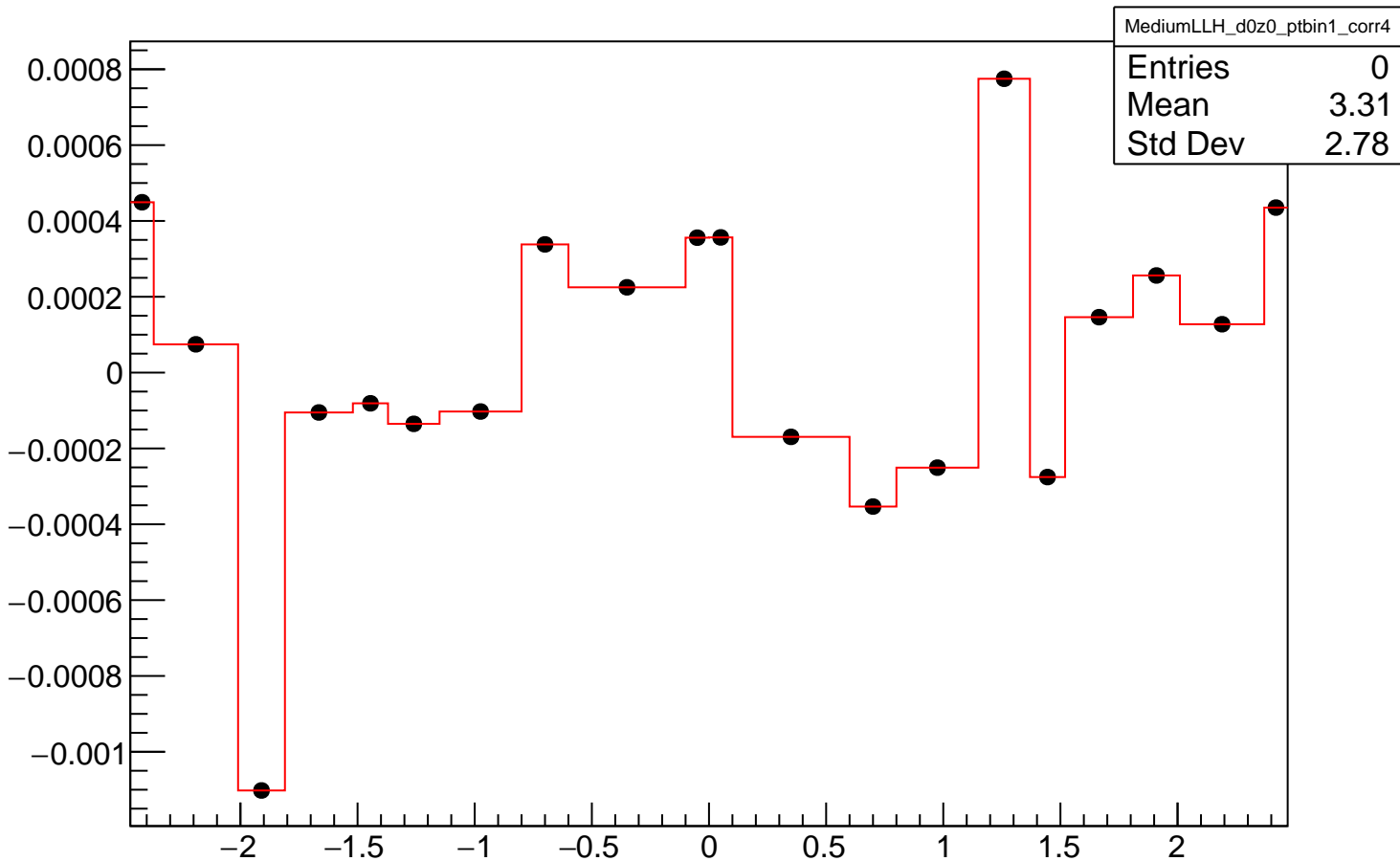
FullSim_MediumLLH_d0z0_corr3



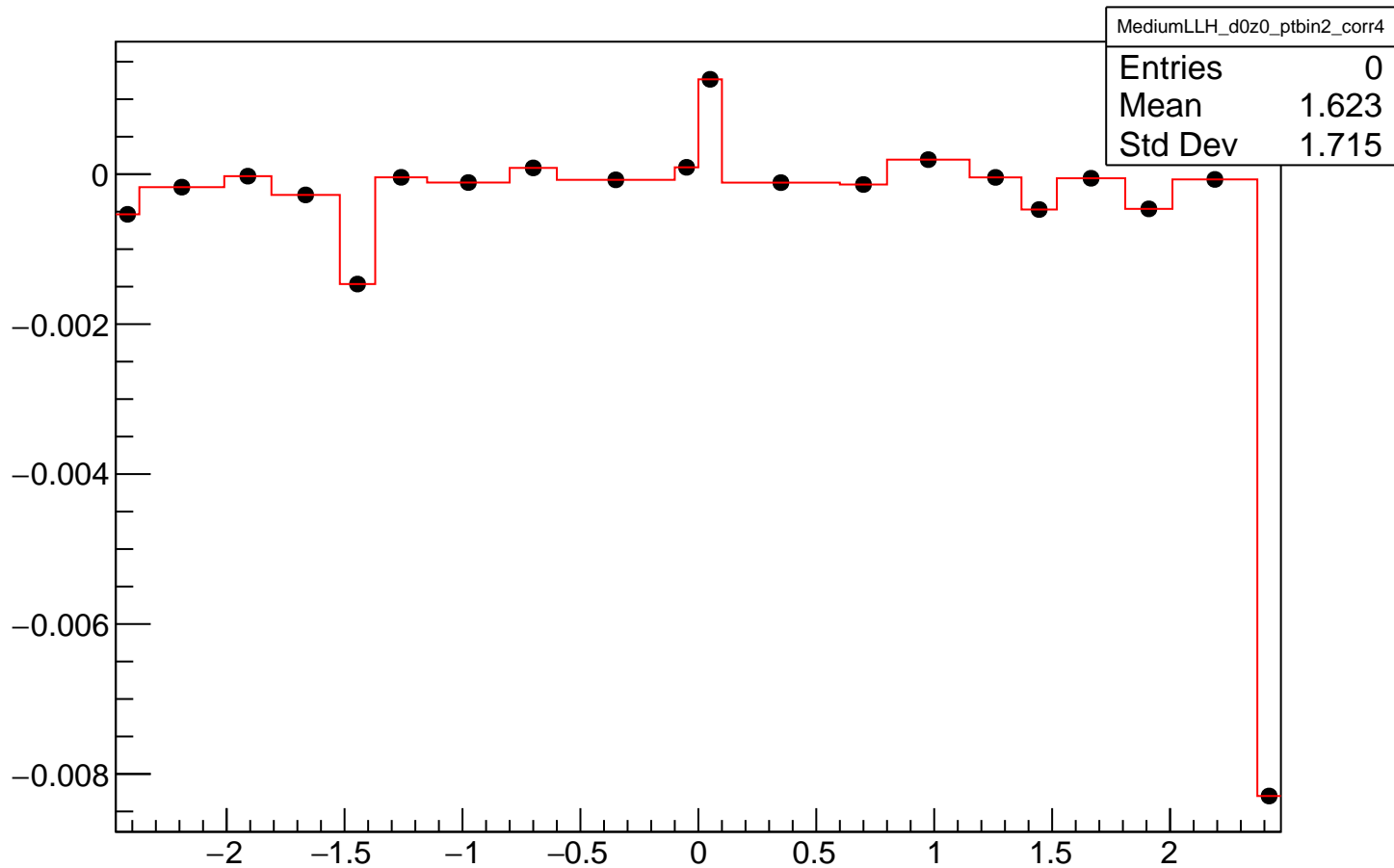
FullSim_MediumLLH_d0z0_corr4



FullSim_MediumLLH_d0z0_corr4



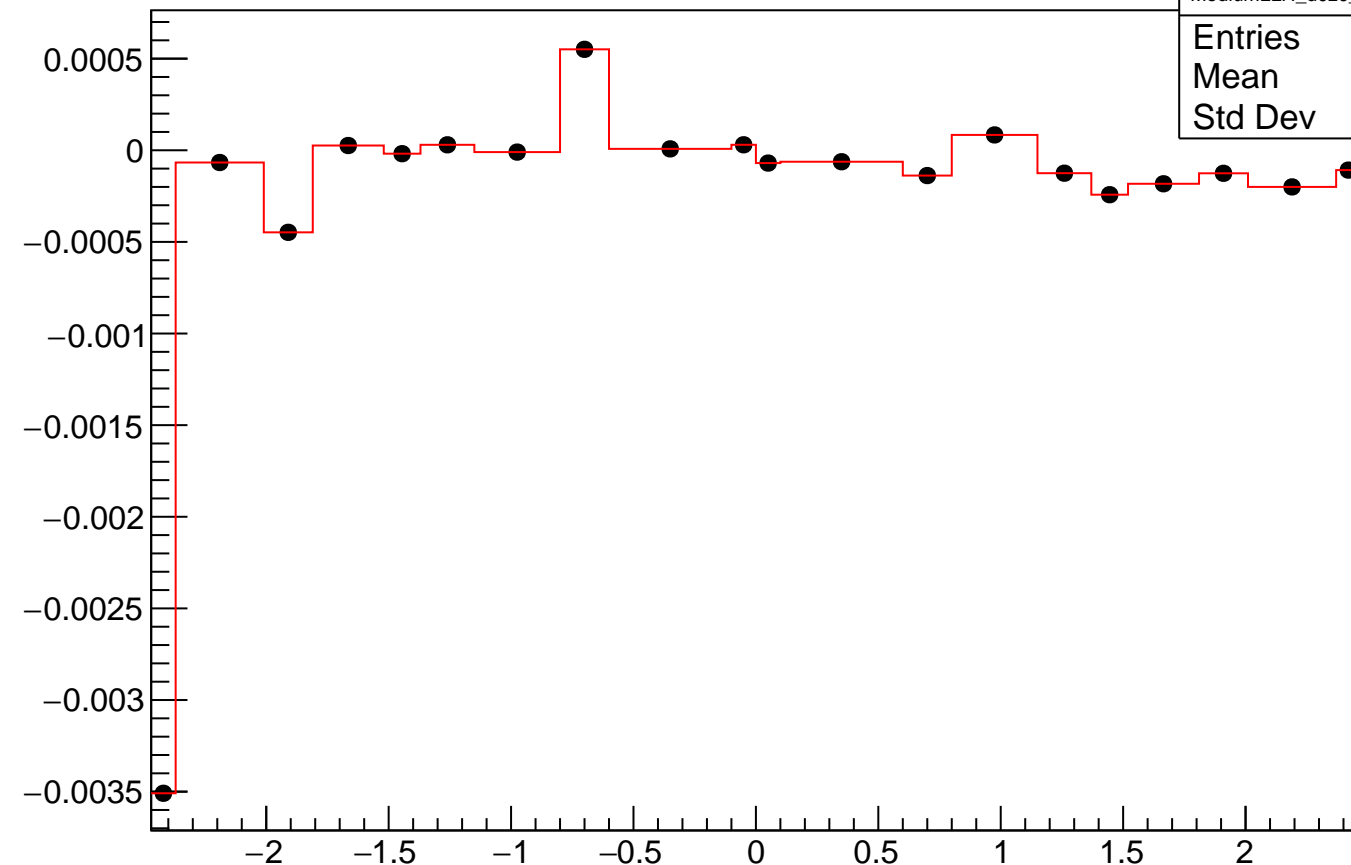
FullSim_MediumLLH_d0z0_corr4



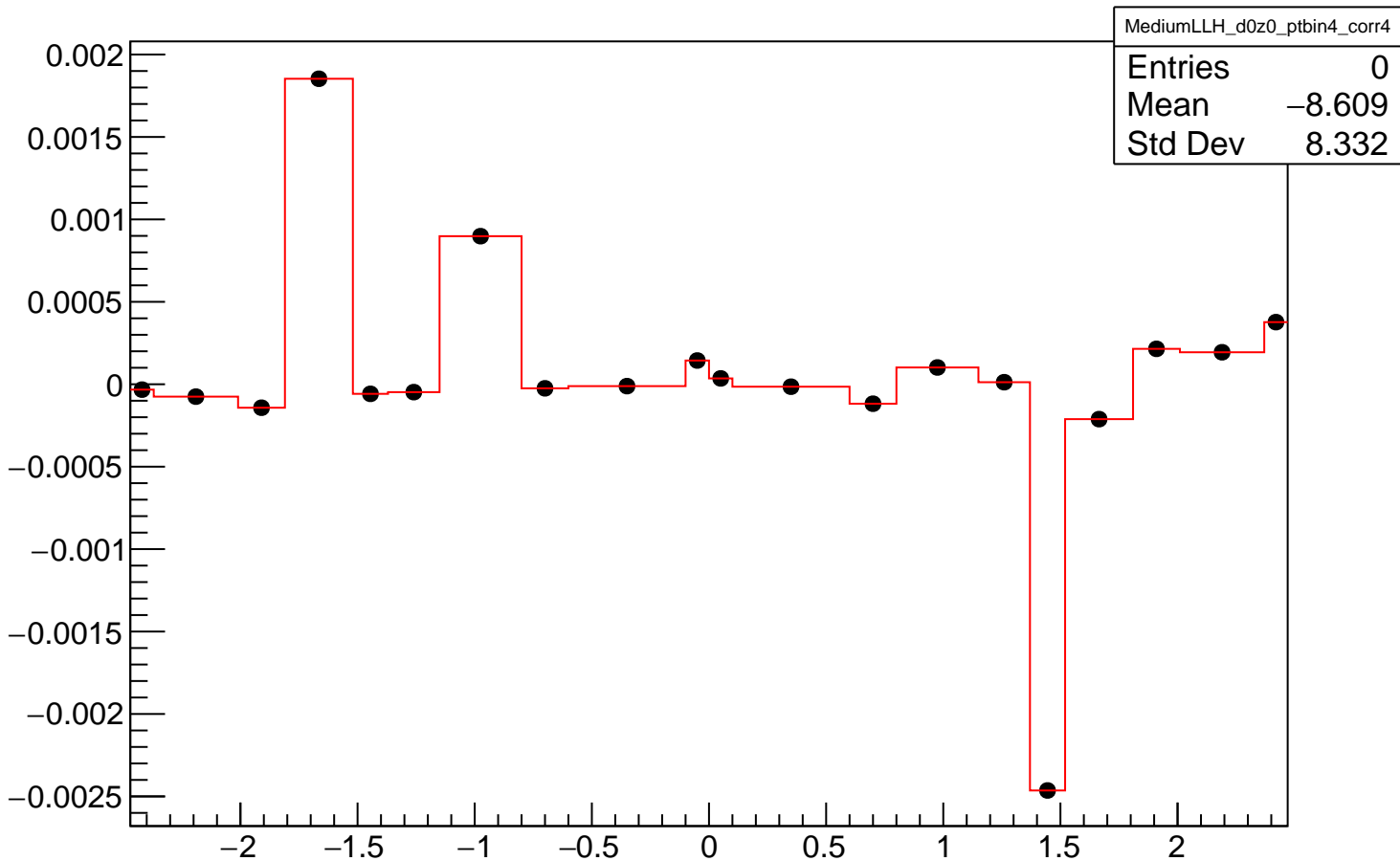
FullSim_MediumLLH_d0z0_corr4

MediumLLH_d0z0_ptbin3_corr4

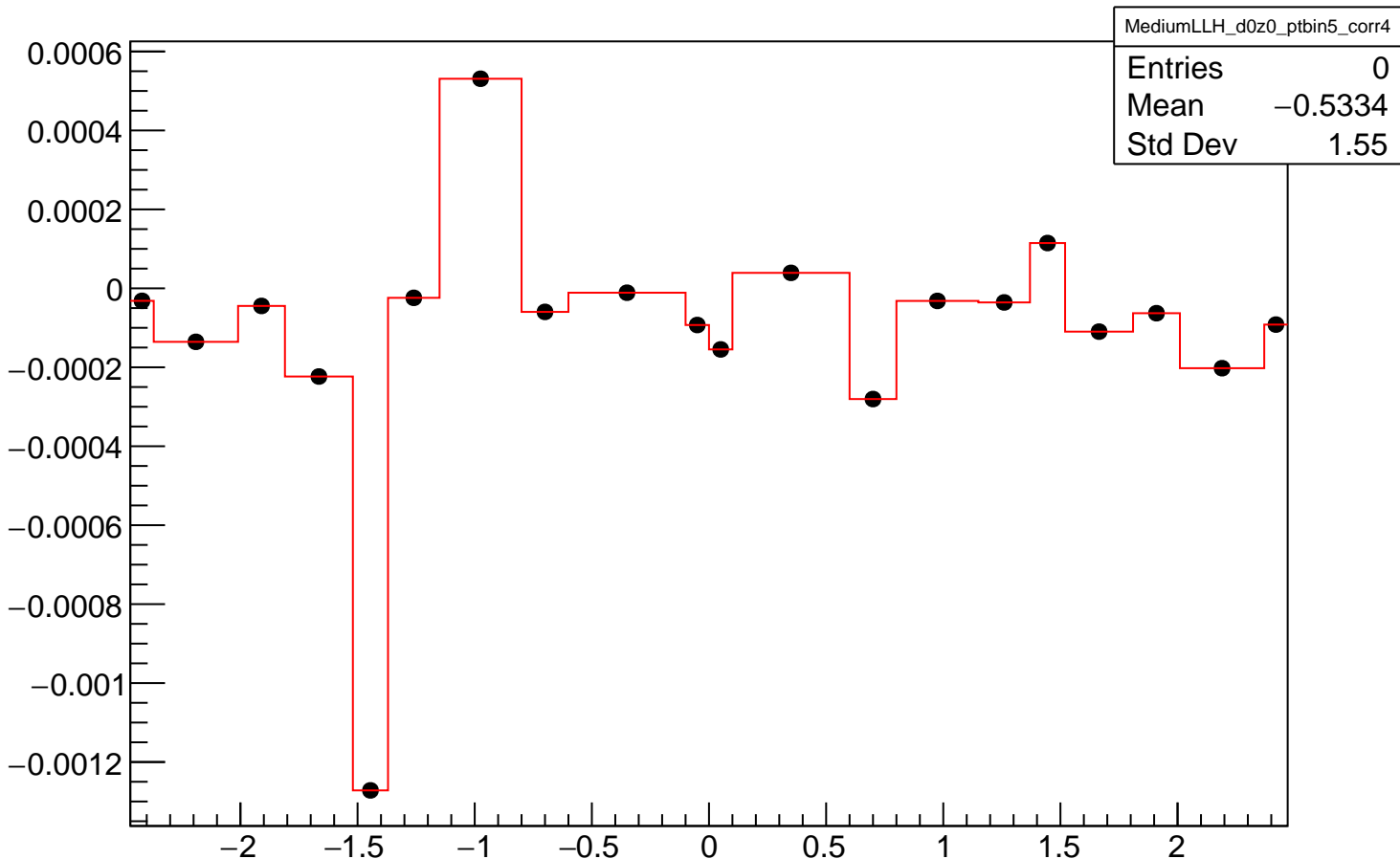
Entries	0
Mean	-1.587
Std Dev	1.741



FullSim_MediumLLH_d0z0_corr4



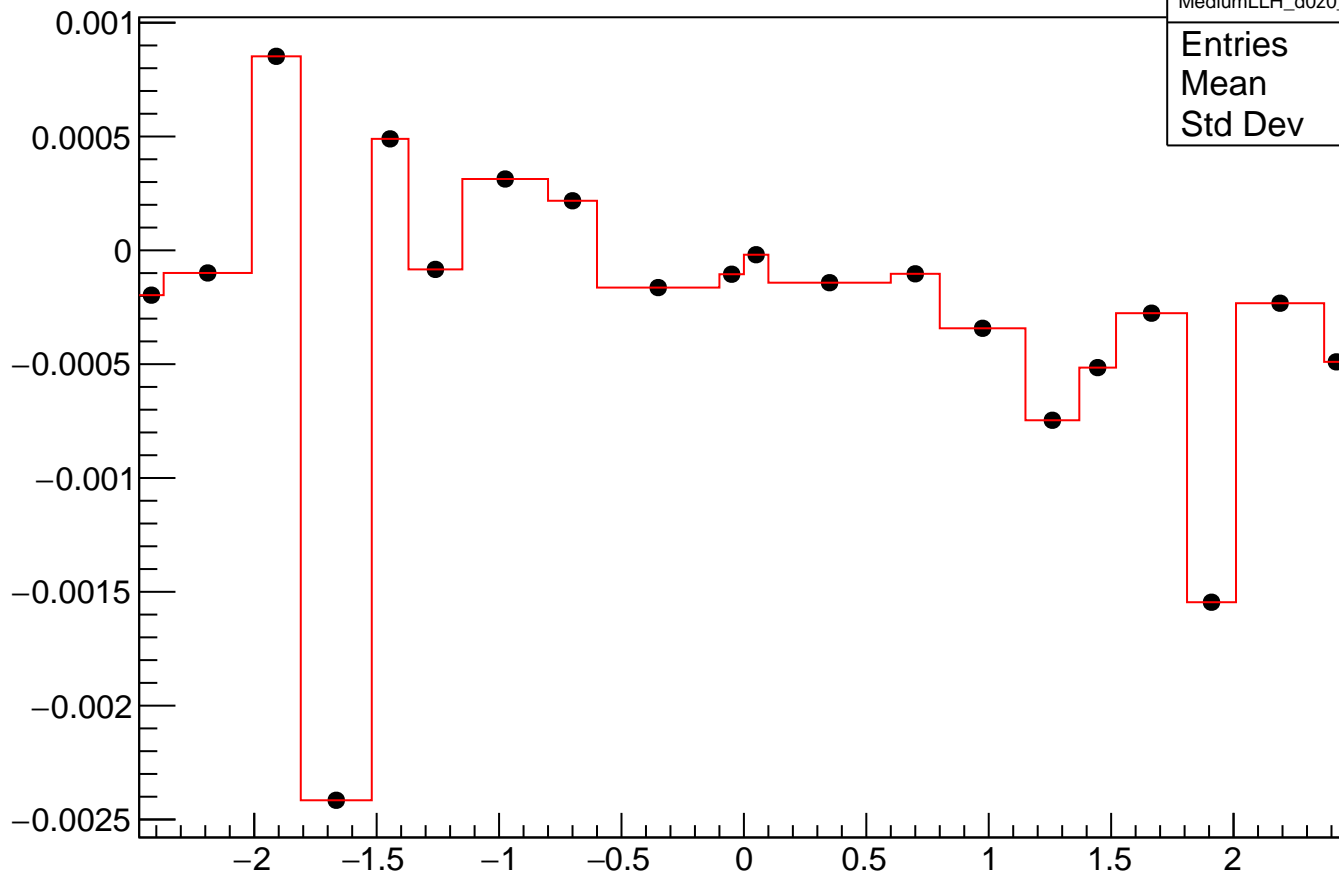
FullSim_MediumLLH_d0z0_corr4



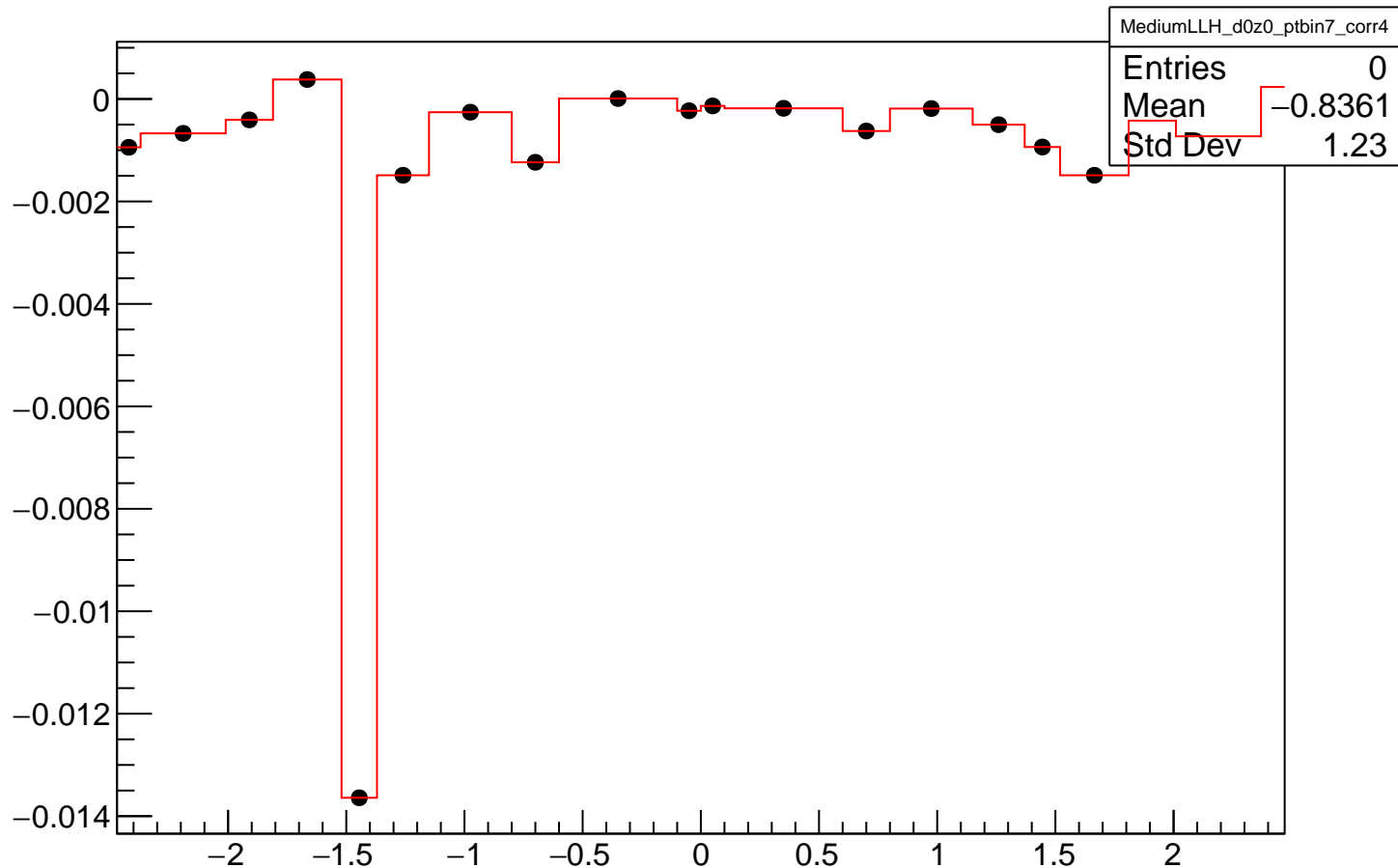
FullSim_MediumLLH_d0z0_corr4

MediumLLH_d0z0_ptbin6_corr4

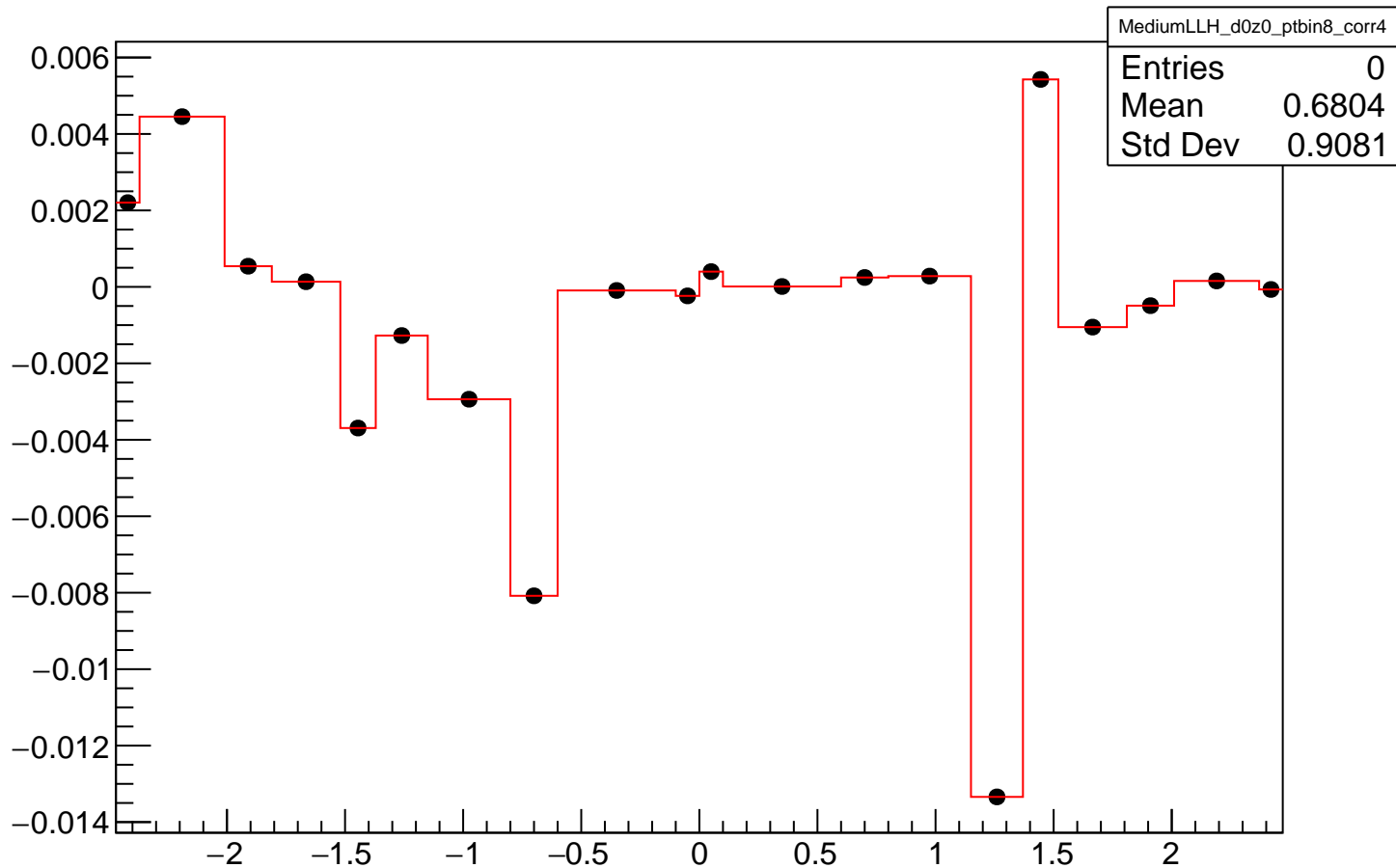
Entries	0
Mean	0.9202
Std Dev	1.478



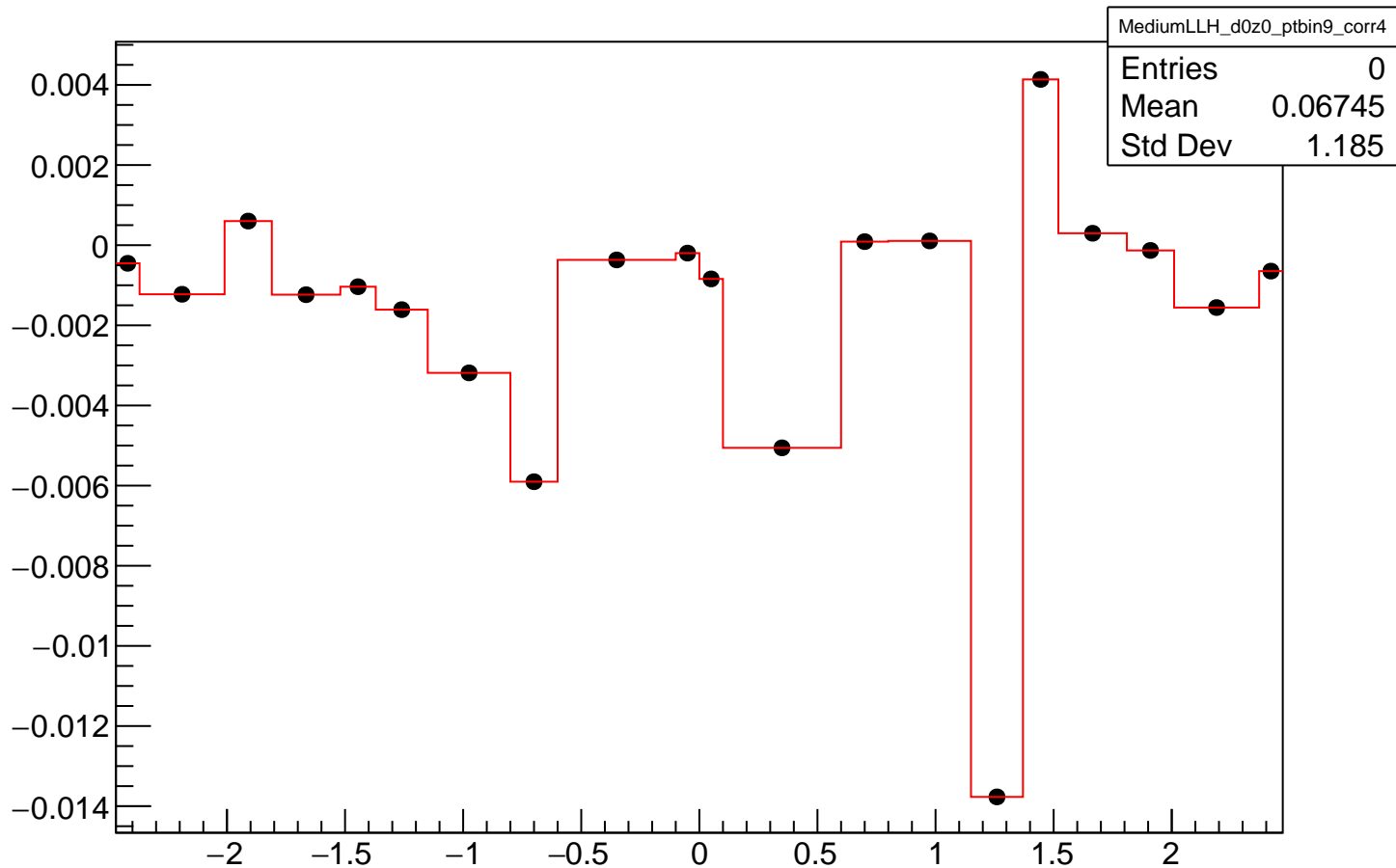
FullSim_MediumLLH_d0z0_corr4



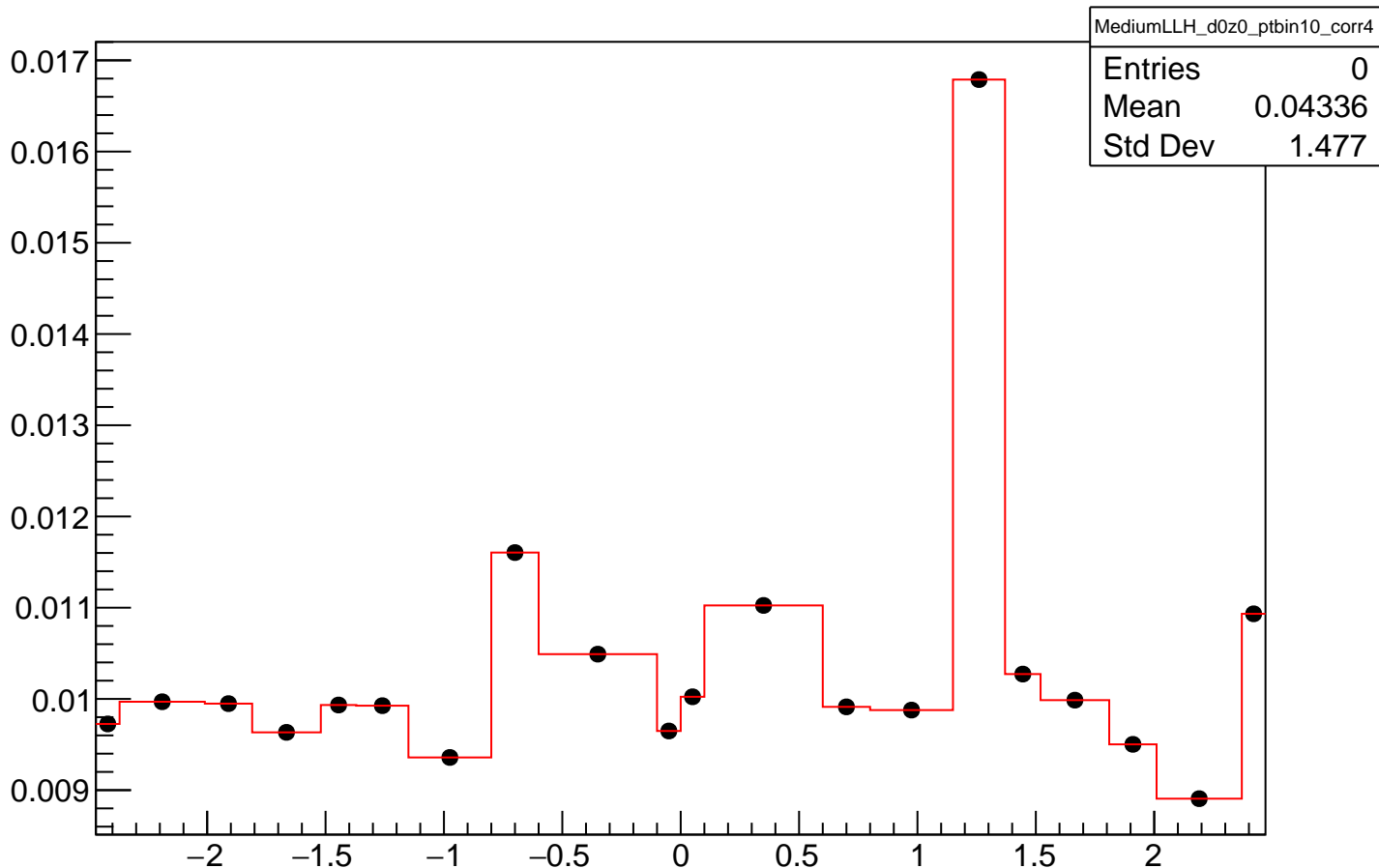
FullSim_MediumLLH_d0z0_corr4



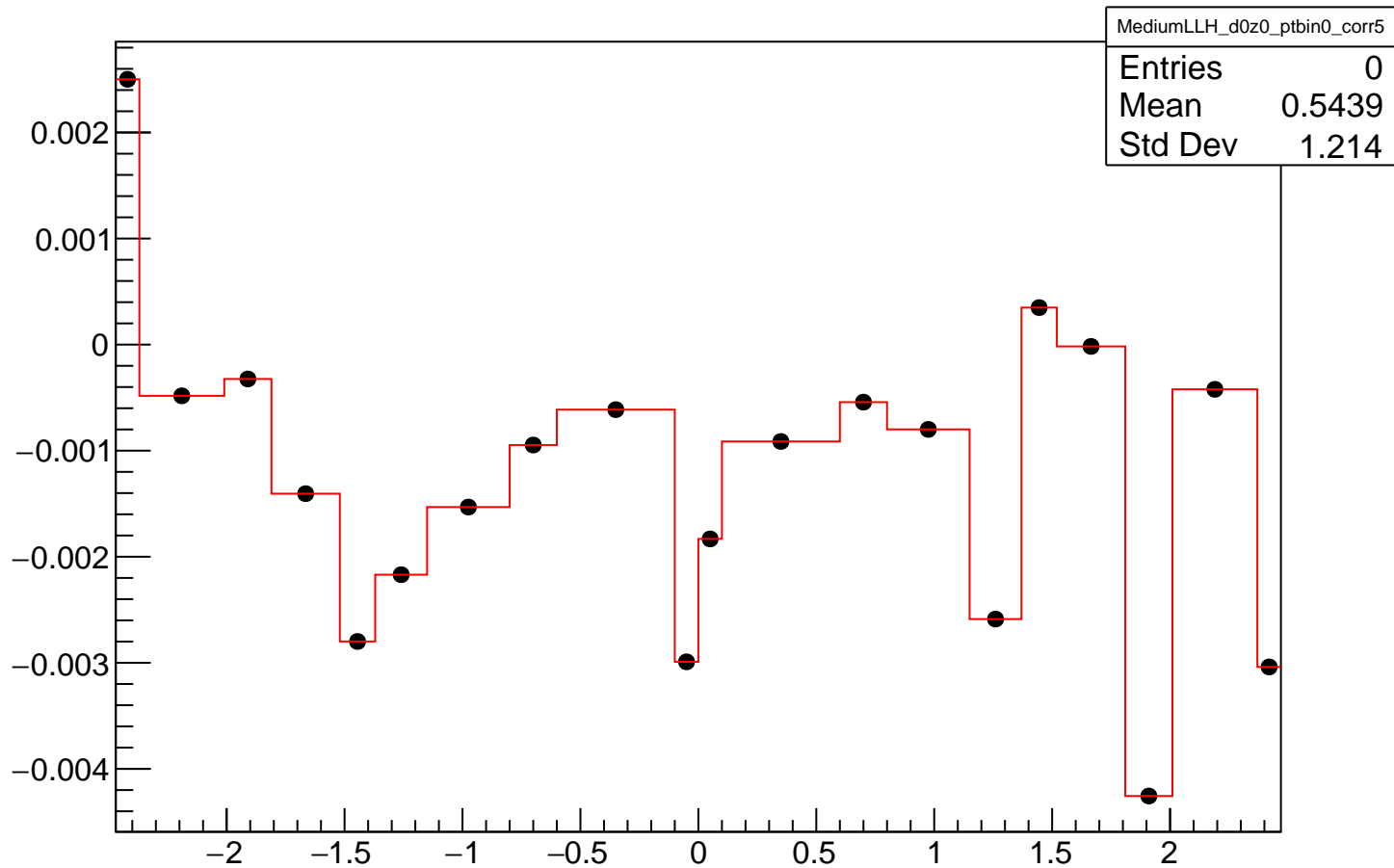
FullSim_MediumLLH_d0z0_corr4



FullSim_MediumLLH_d0z0_corr4



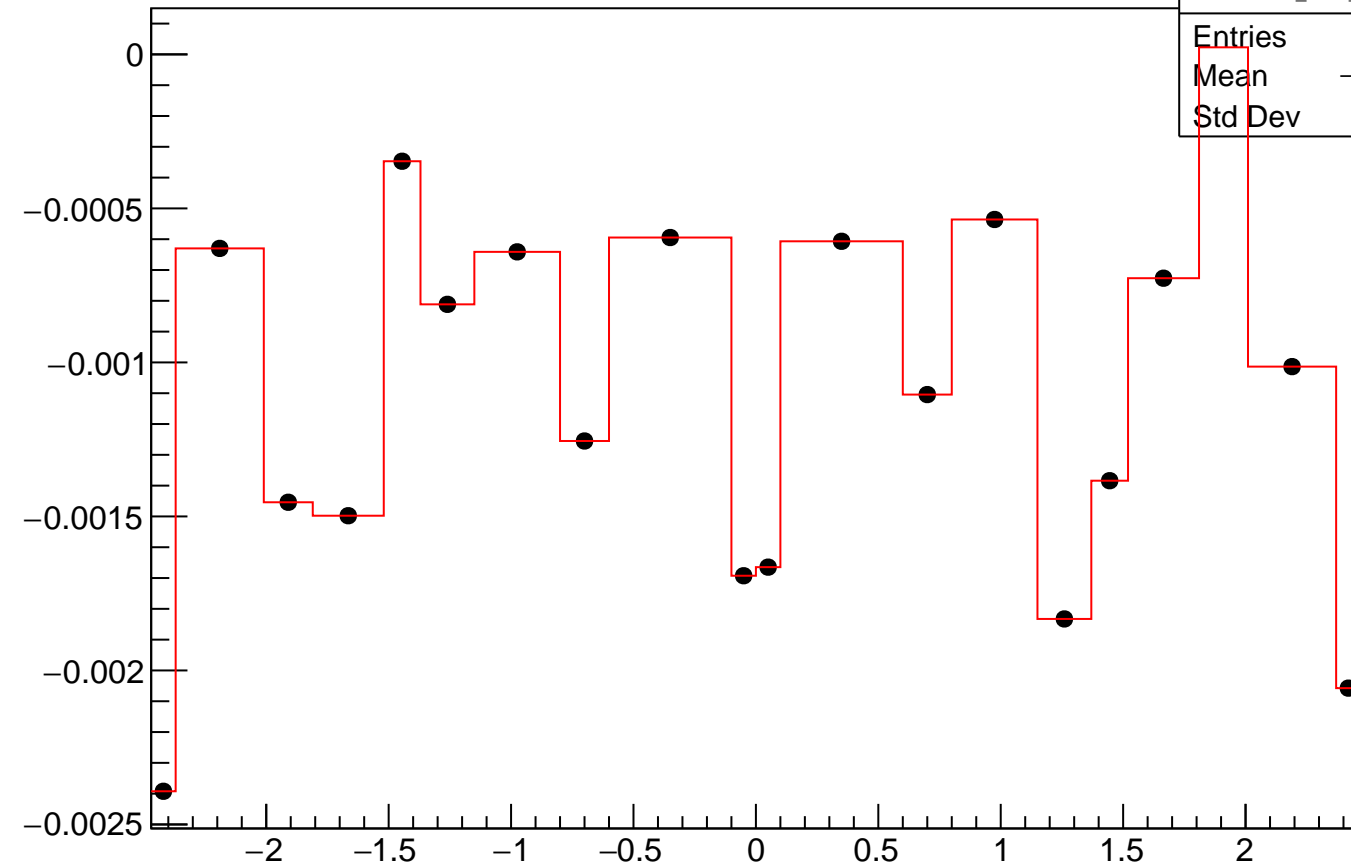
FullSim_MediumLLH_d0z0_corr5



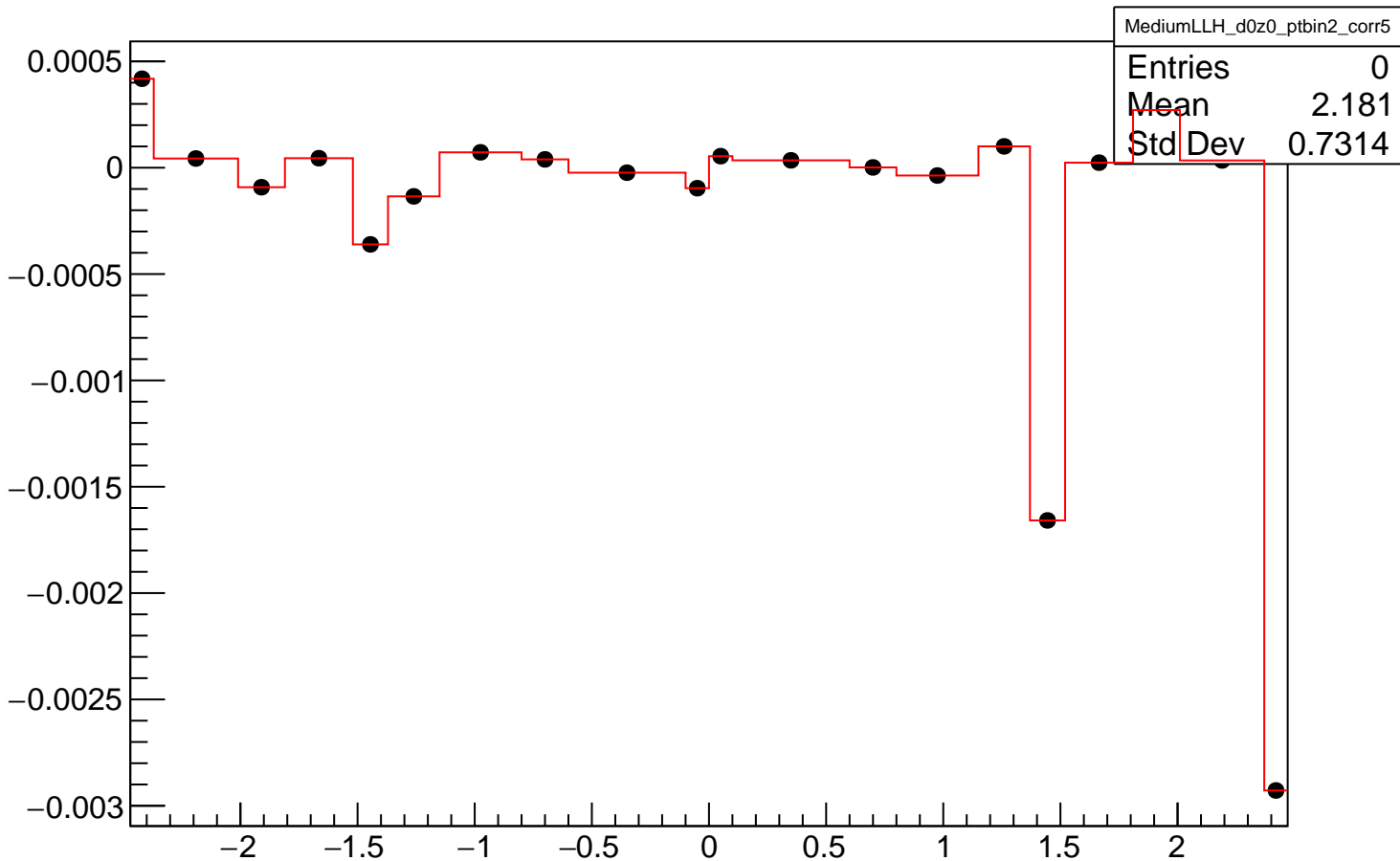
FullSim_MediumLLH_d0z0_corr5

MediumLLH_d0z0_ptbin1_corr5

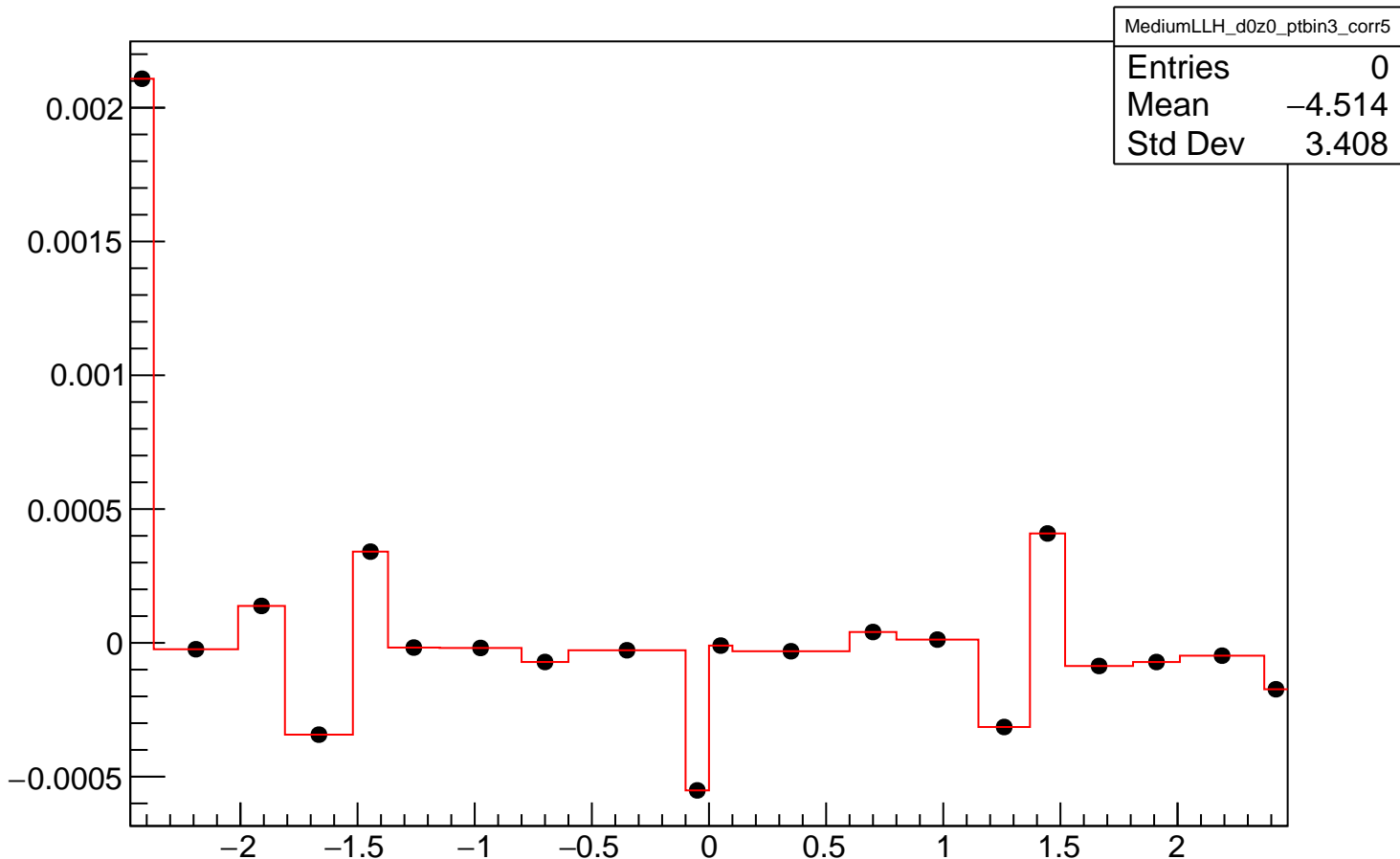
Entries	0
Mean	-0.06725
Std Dev	1.58



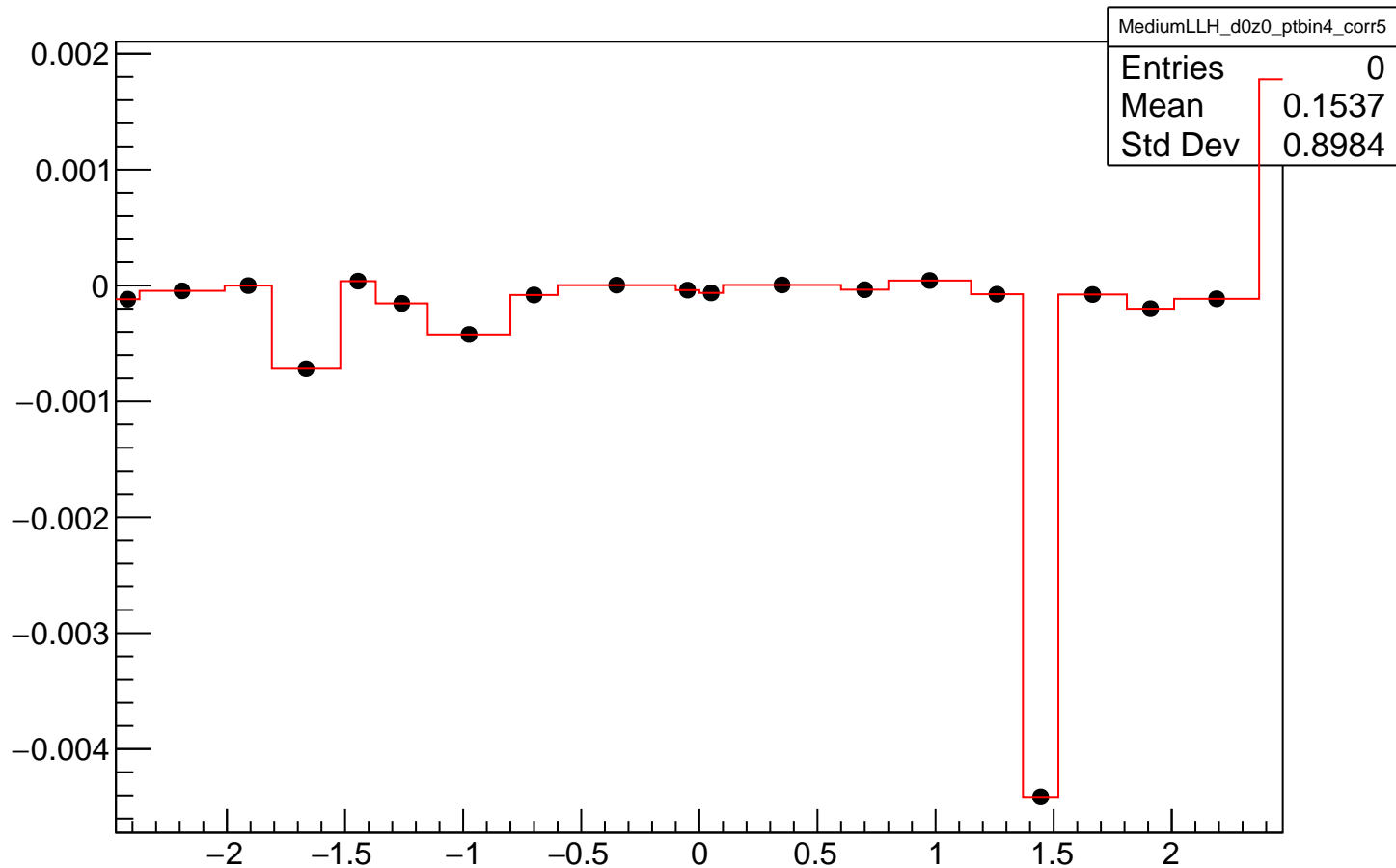
FullSim_MediumLLH_d0z0_corr5



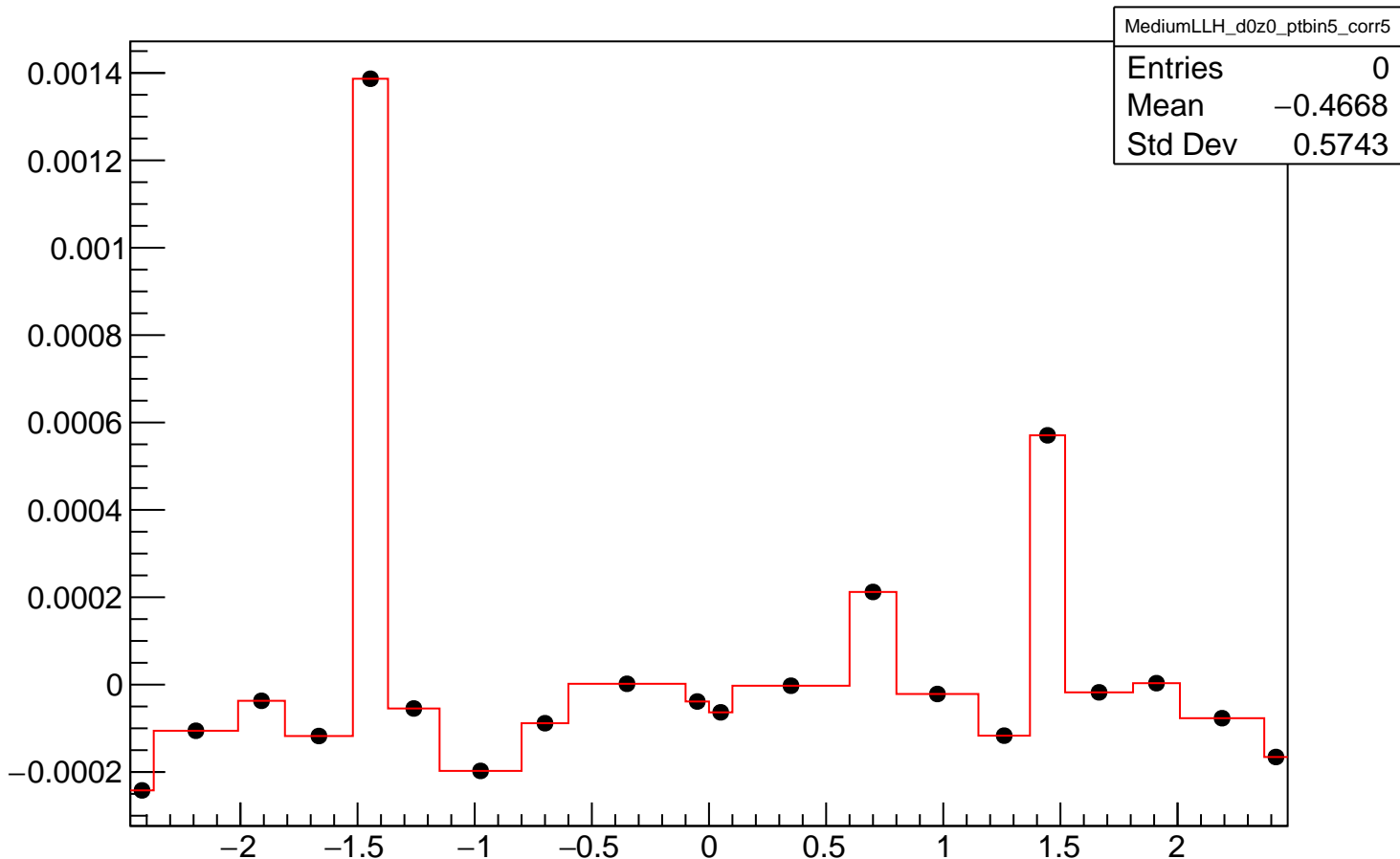
FullSim_MediumLLH_d0z0_corr5



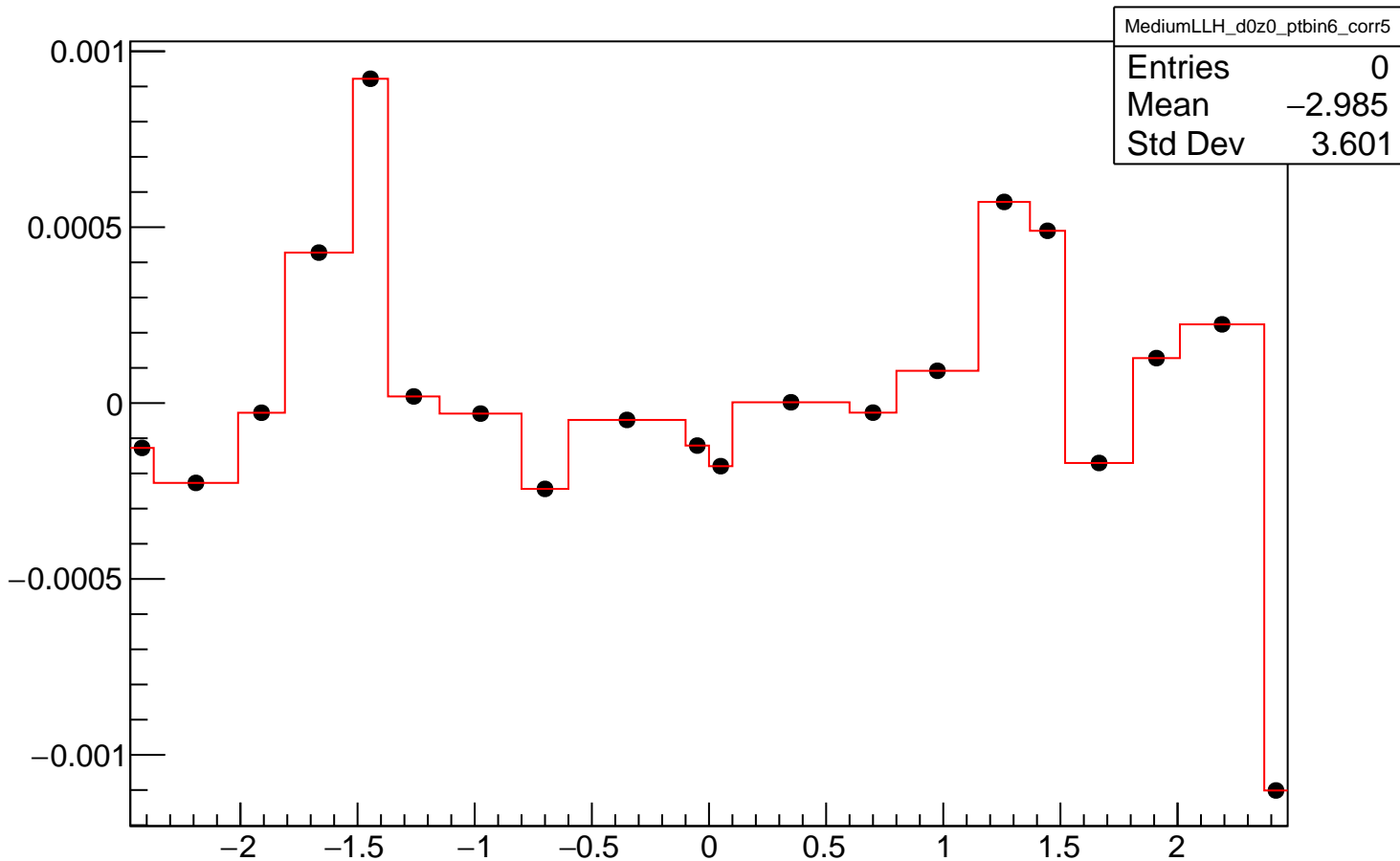
FullSim_MediumLLH_d0z0_corr5



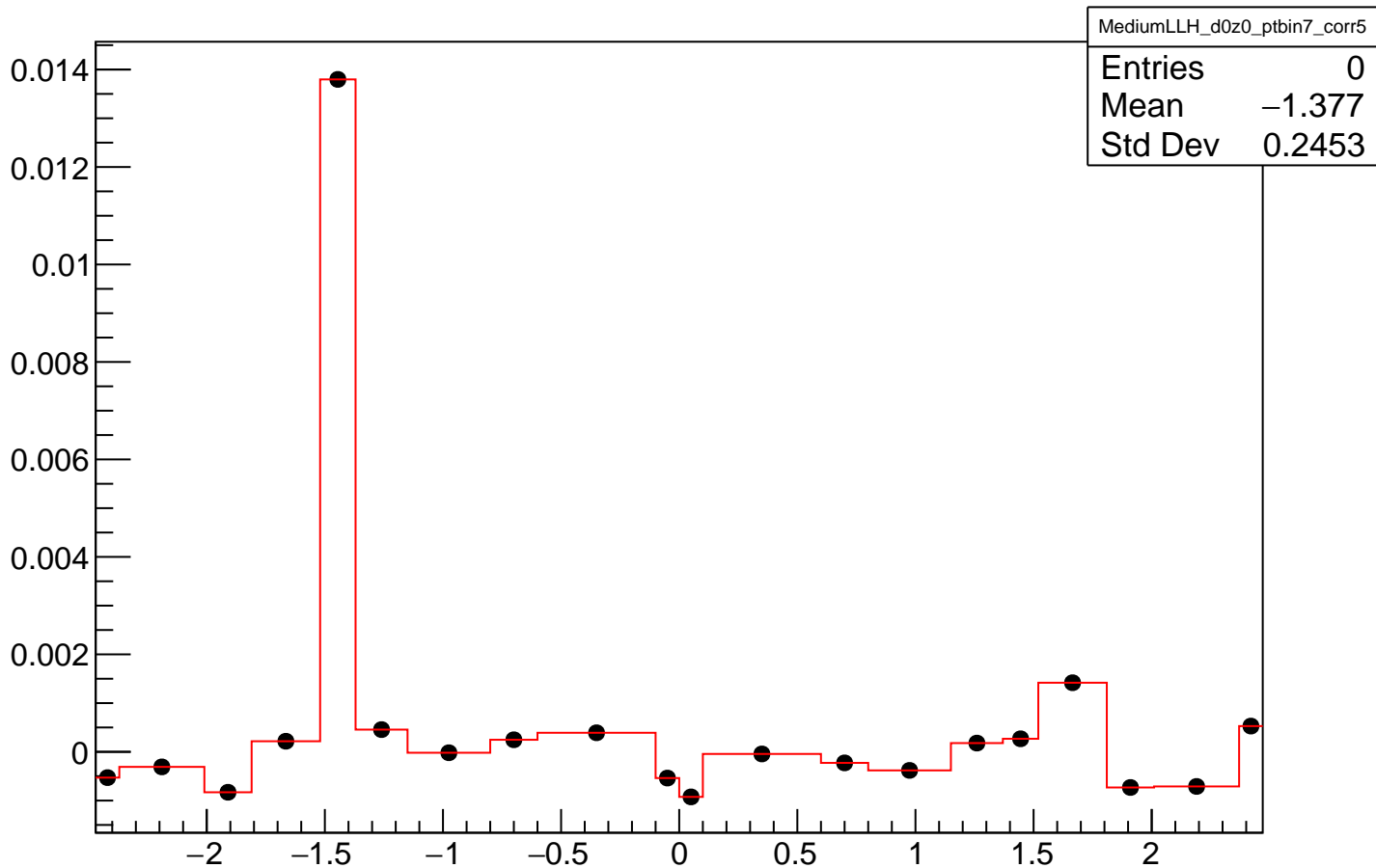
FullSim_MediumLLH_d0z0_corr5



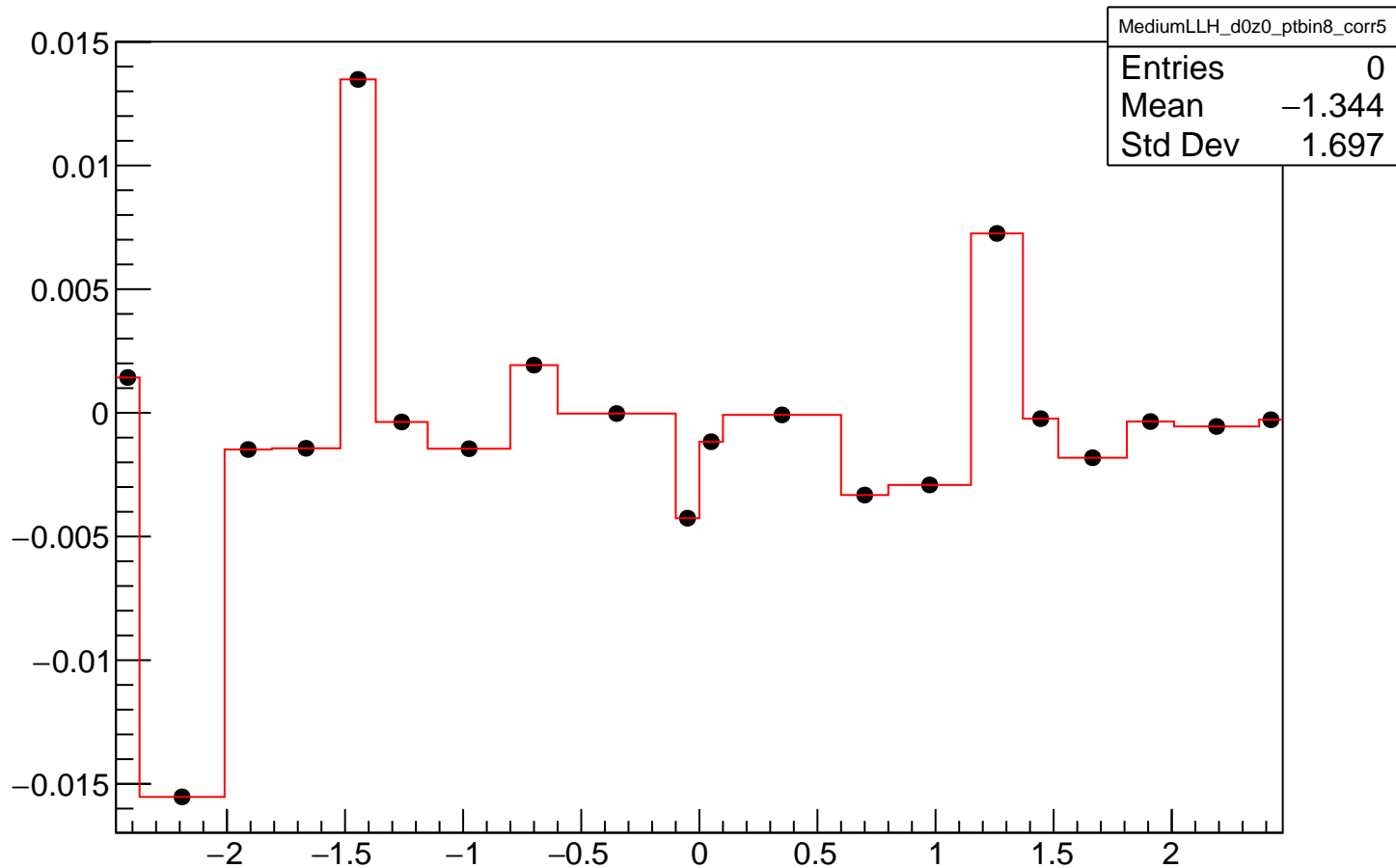
FullSim_MediumLLH_d0z0_corr5



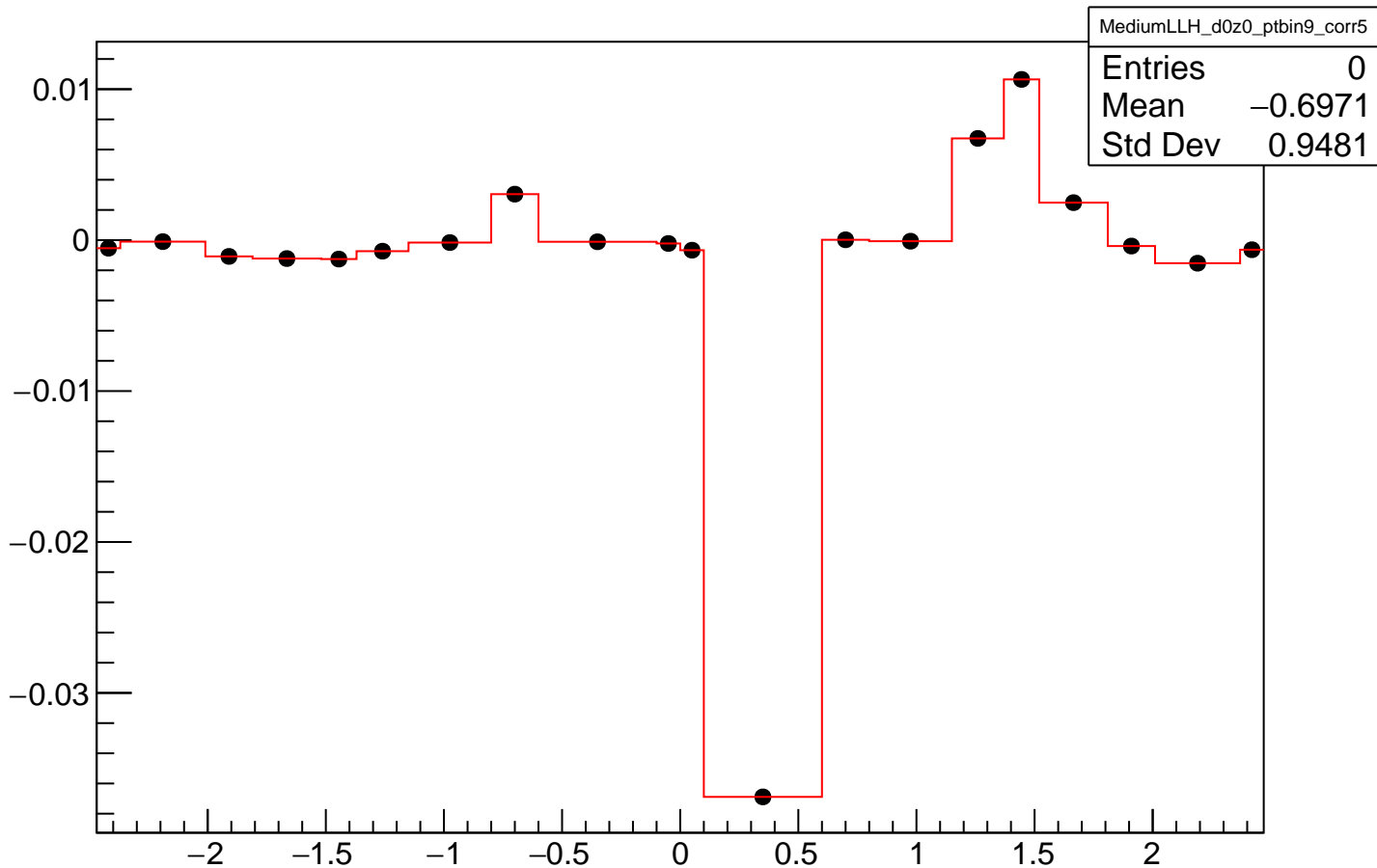
FullSim_MediumLLH_d0z0_corr5



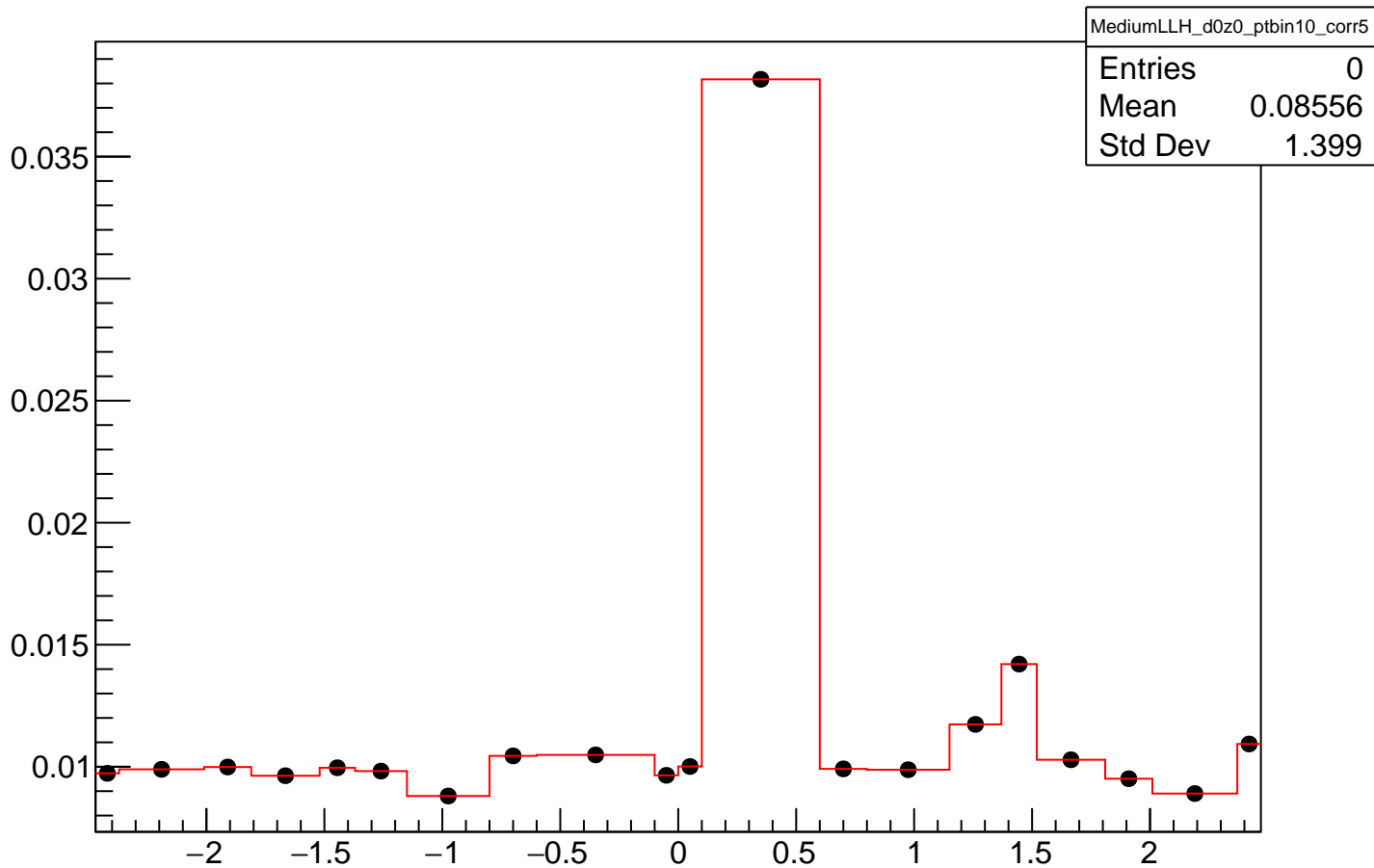
FullSim_MediumLLH_d0z0_corr5



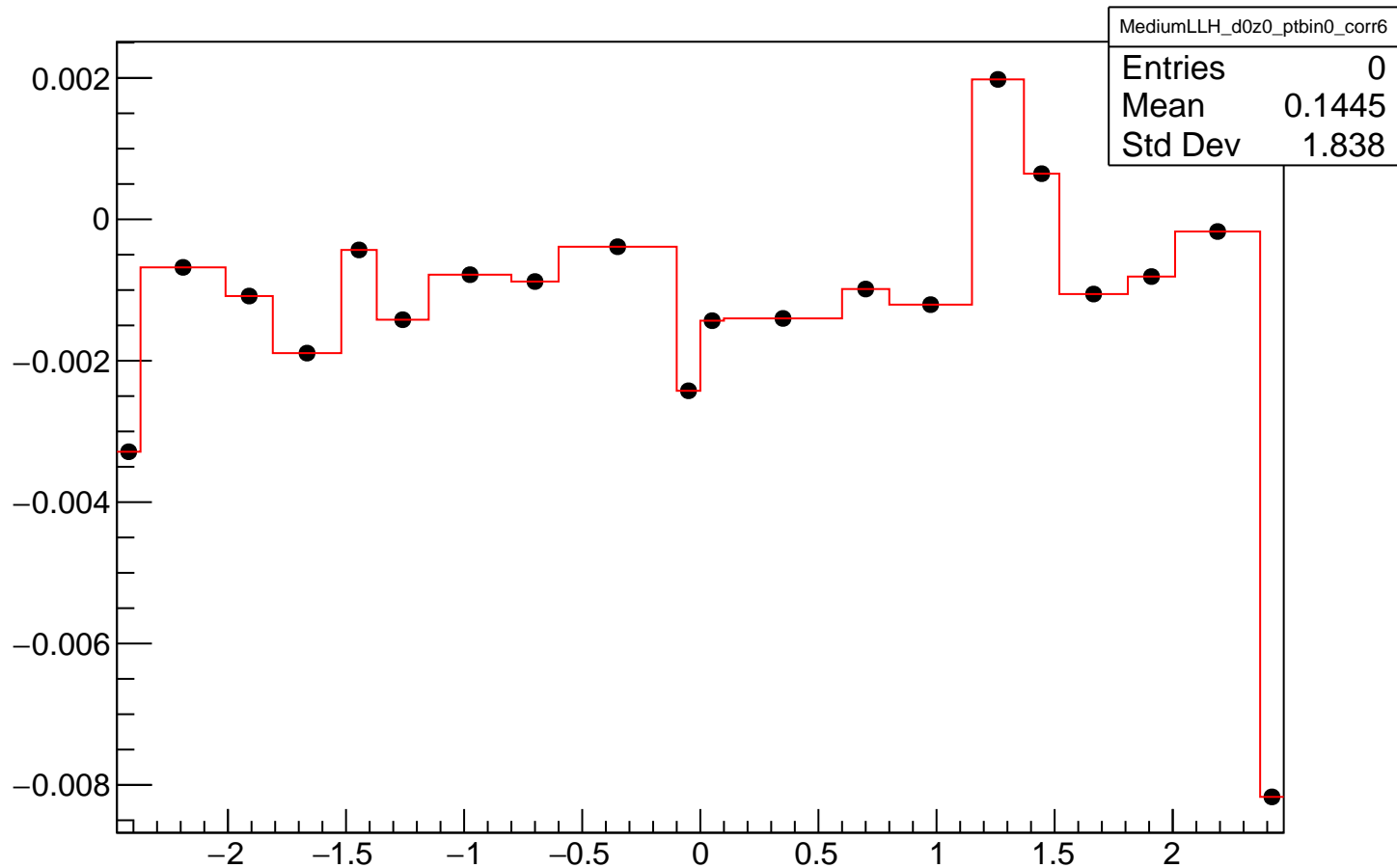
FullSim_MediumLLH_d0z0_corr5



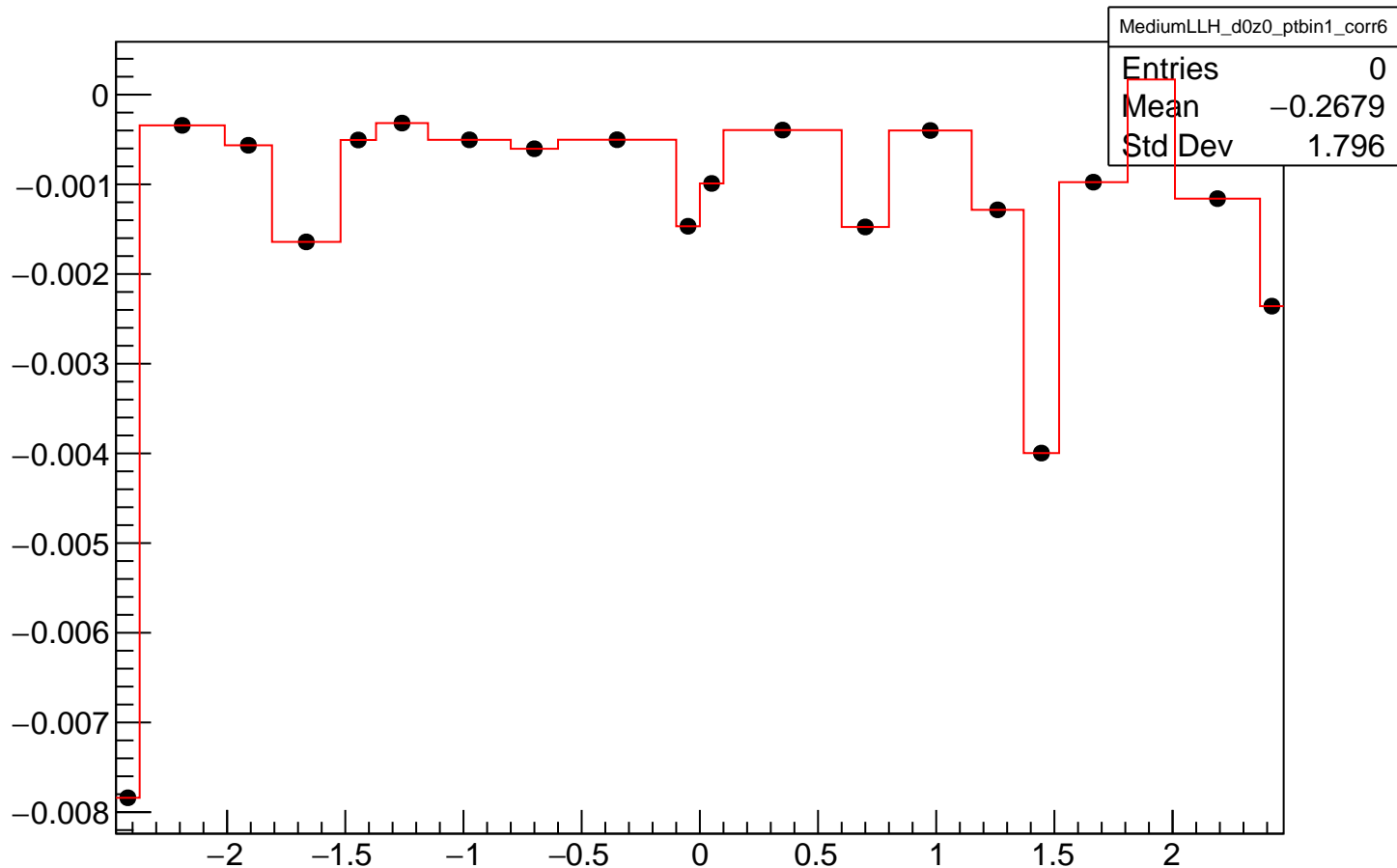
FullSim_MediumLLH_d0z0_corr5



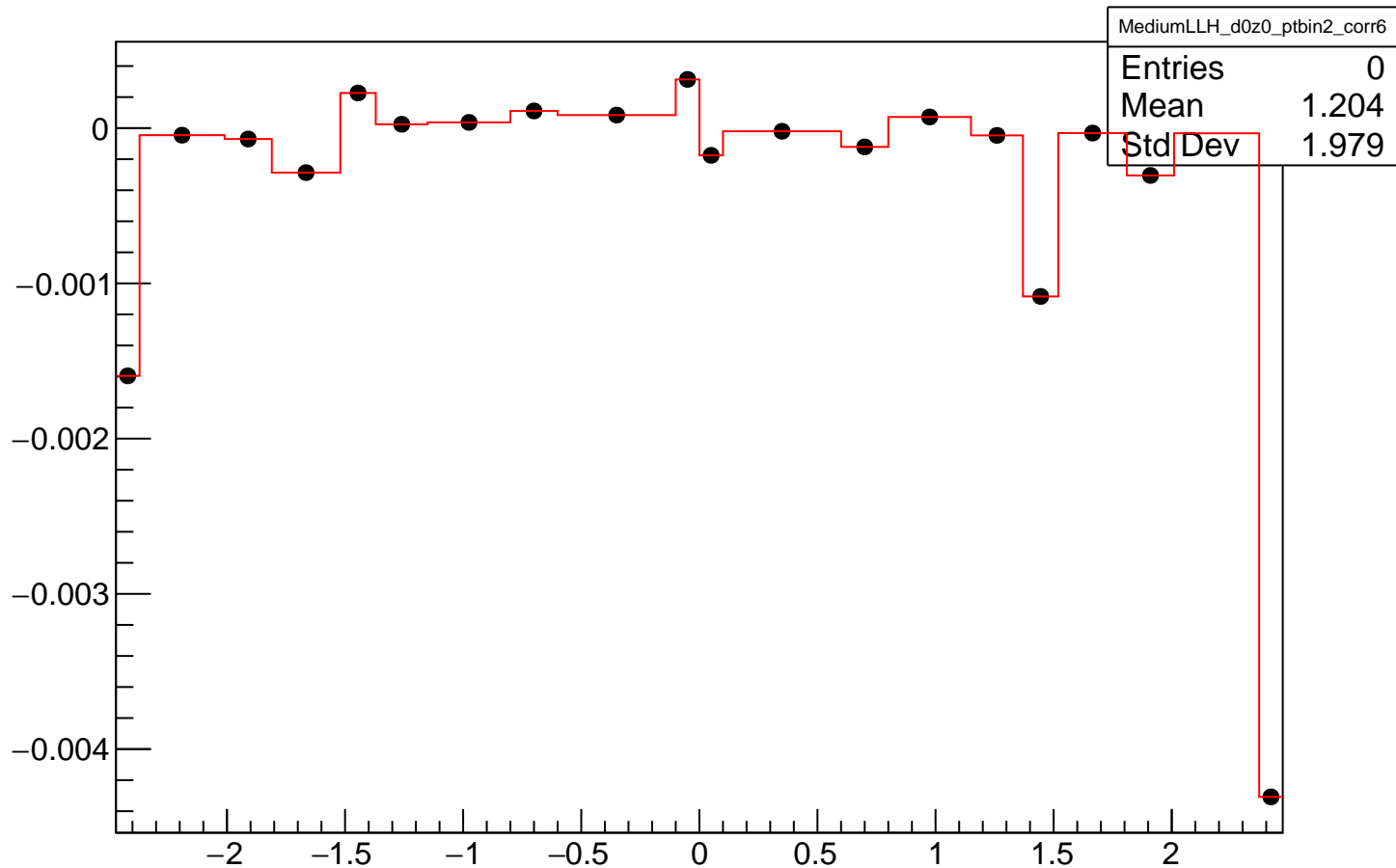
FullSim_MediumLLH_d0z0_corr6



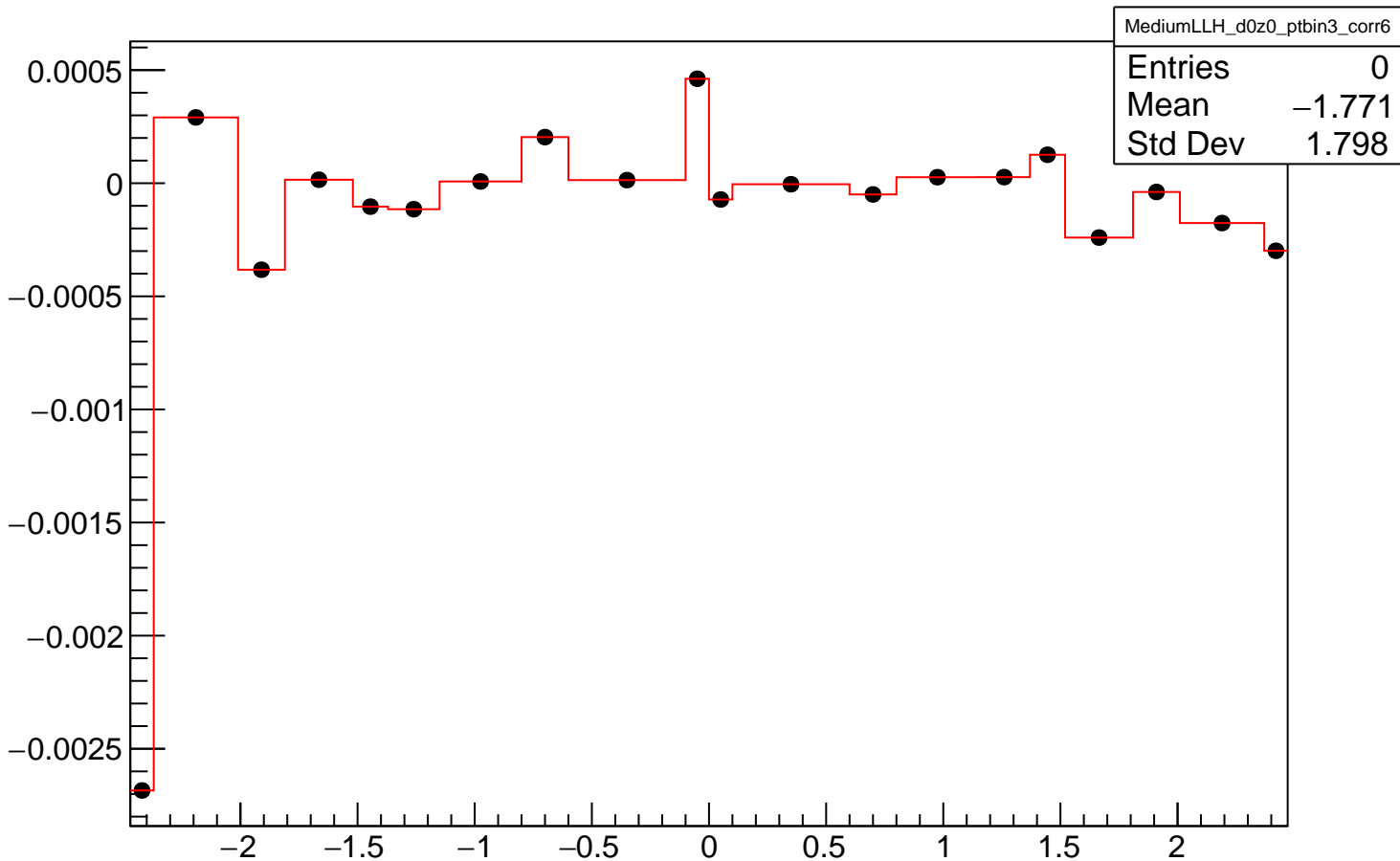
FullSim_MediumLLH_d0z0_corr6



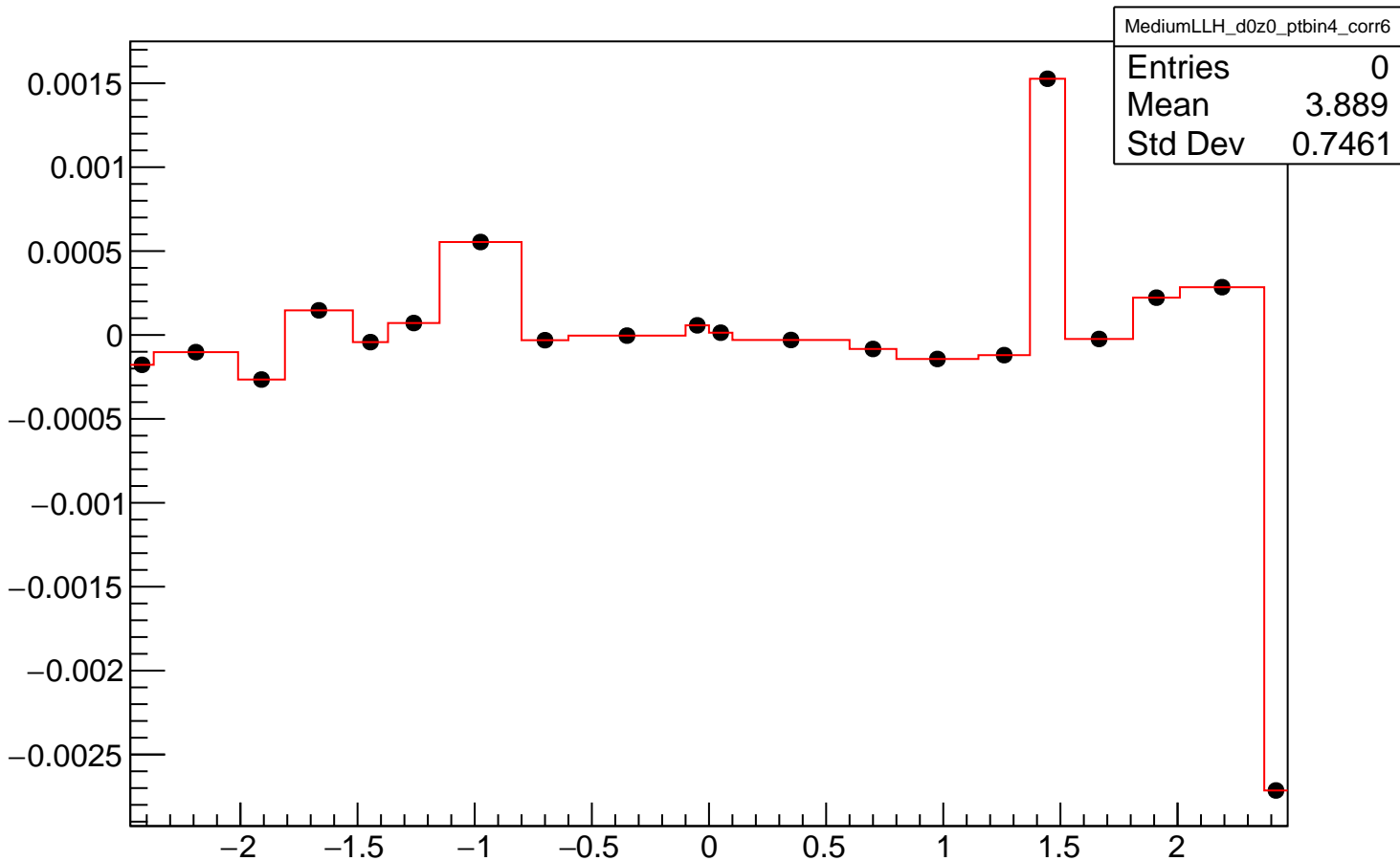
FullSim_MediumLLH_d0z0_corr6



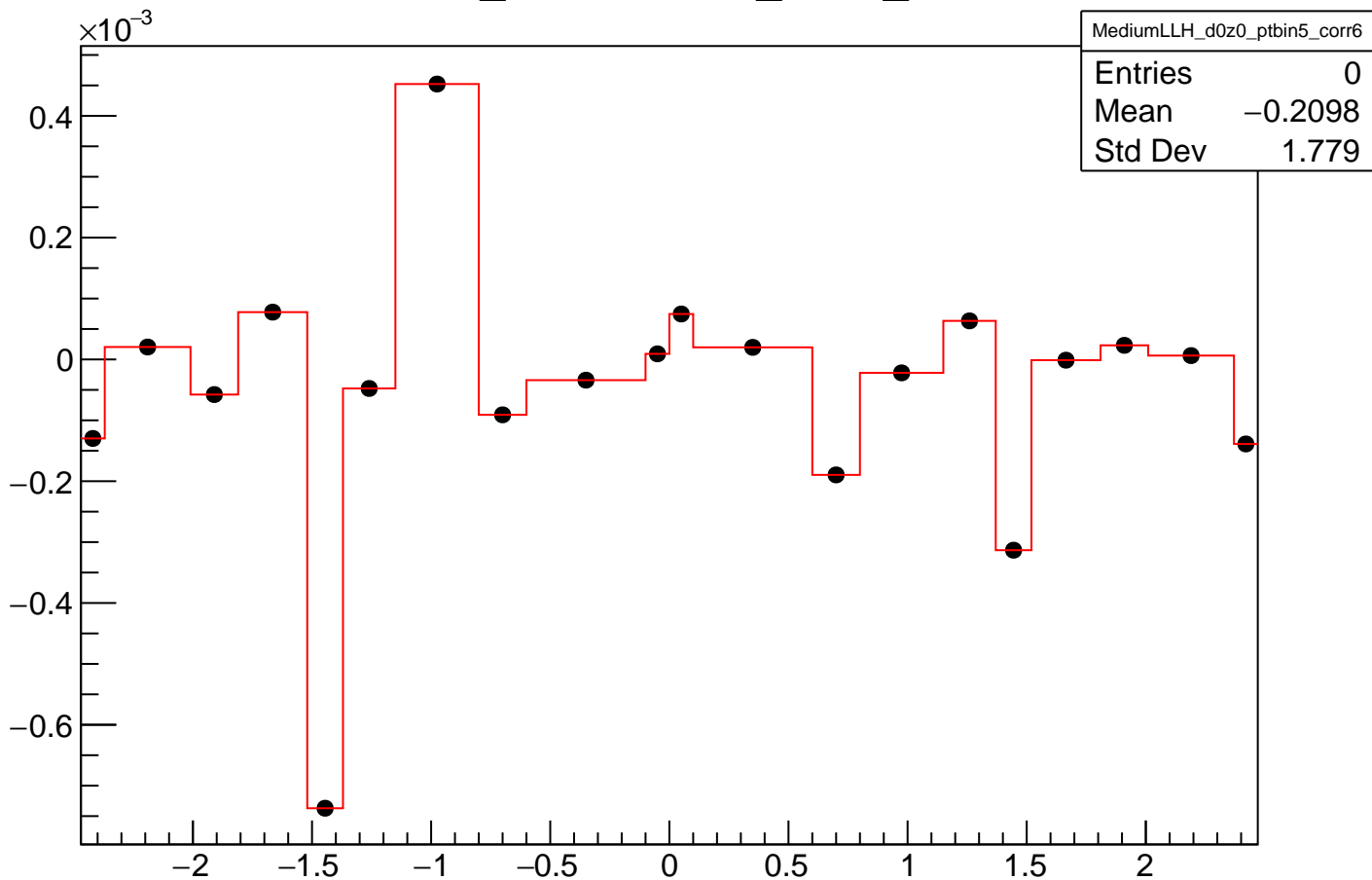
FullSim_MediumLLH_d0z0_corr6



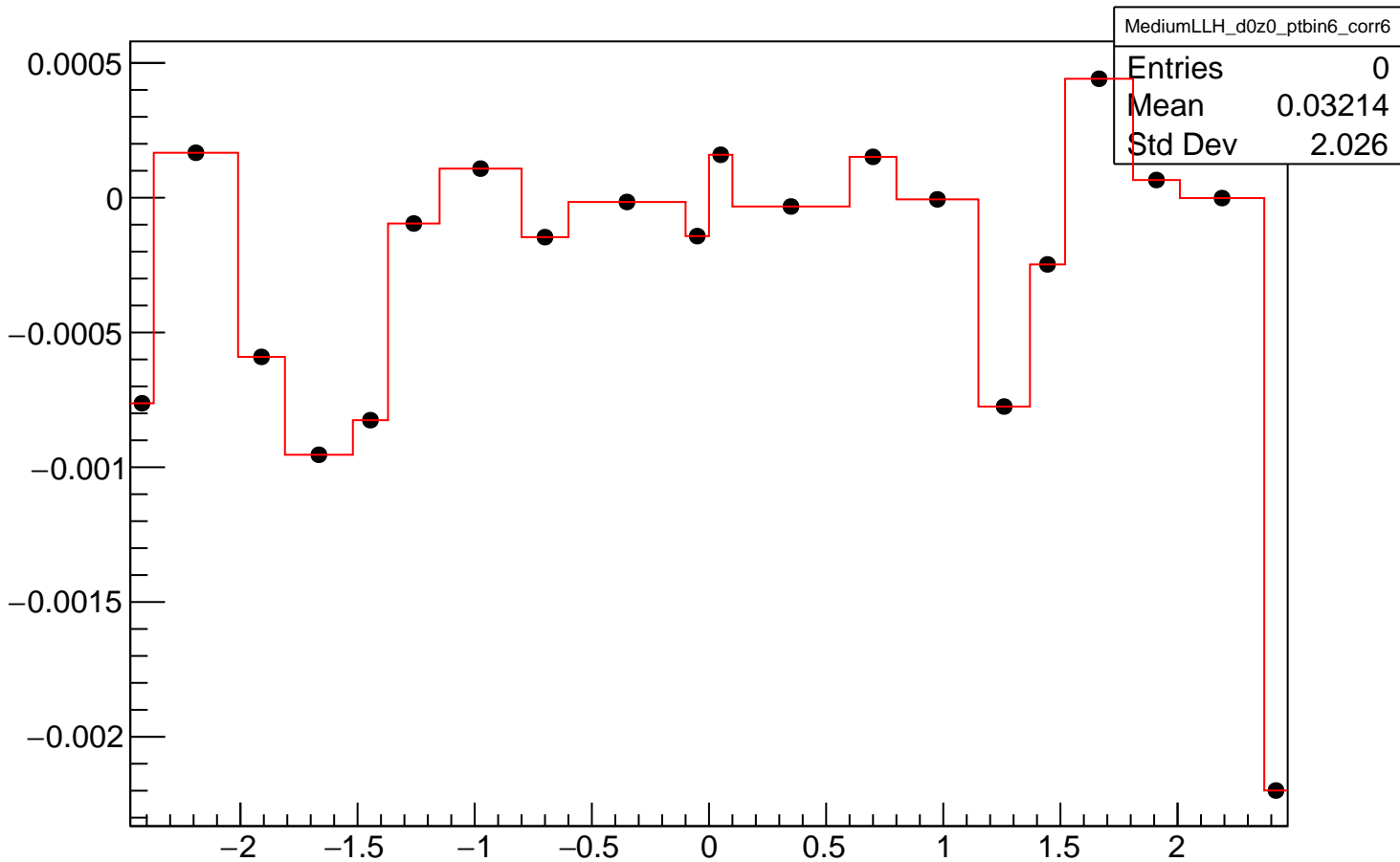
FullSim_MediumLLH_d0z0_corr6



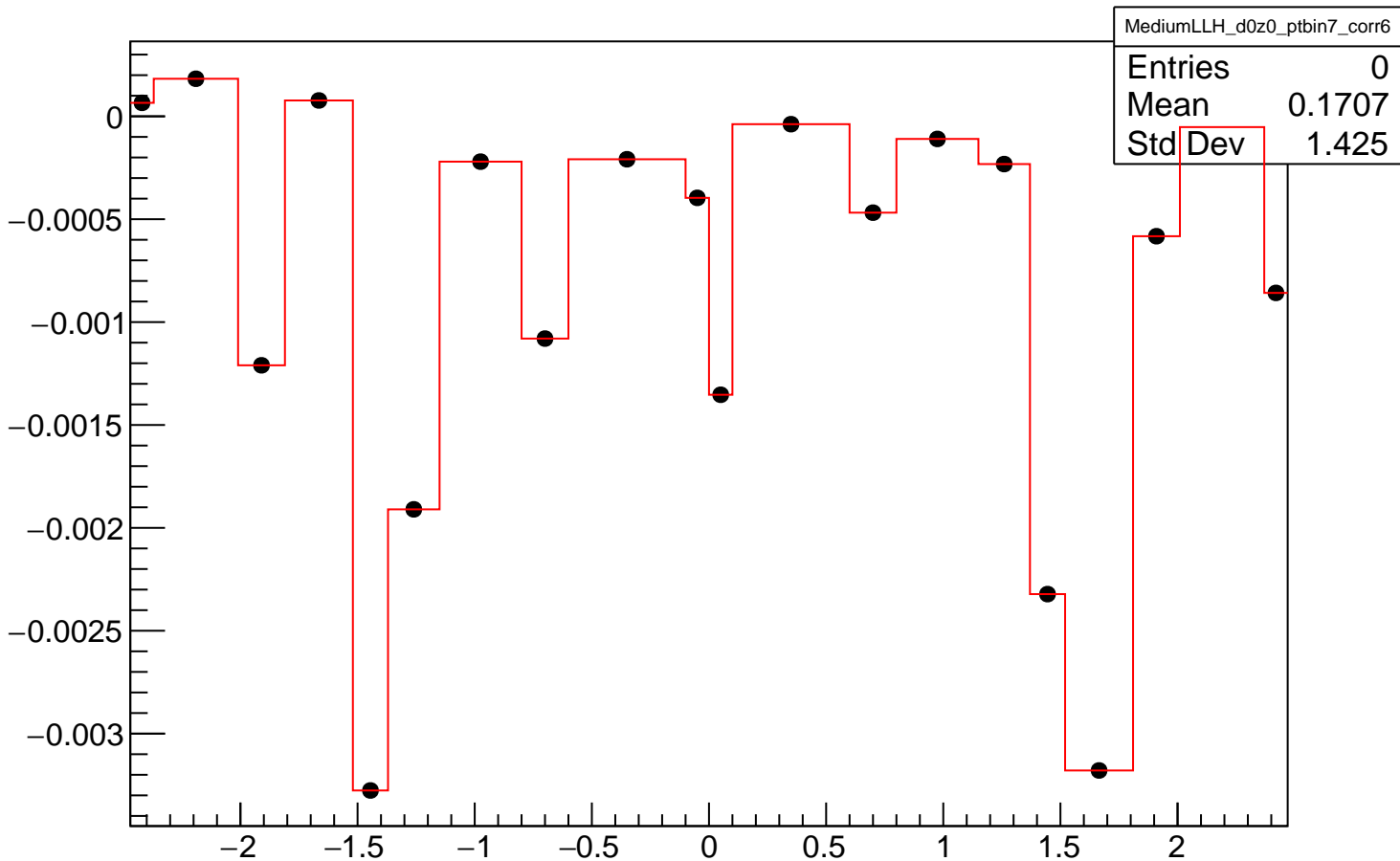
FullSim_MediumLLH_d0z0_corr6



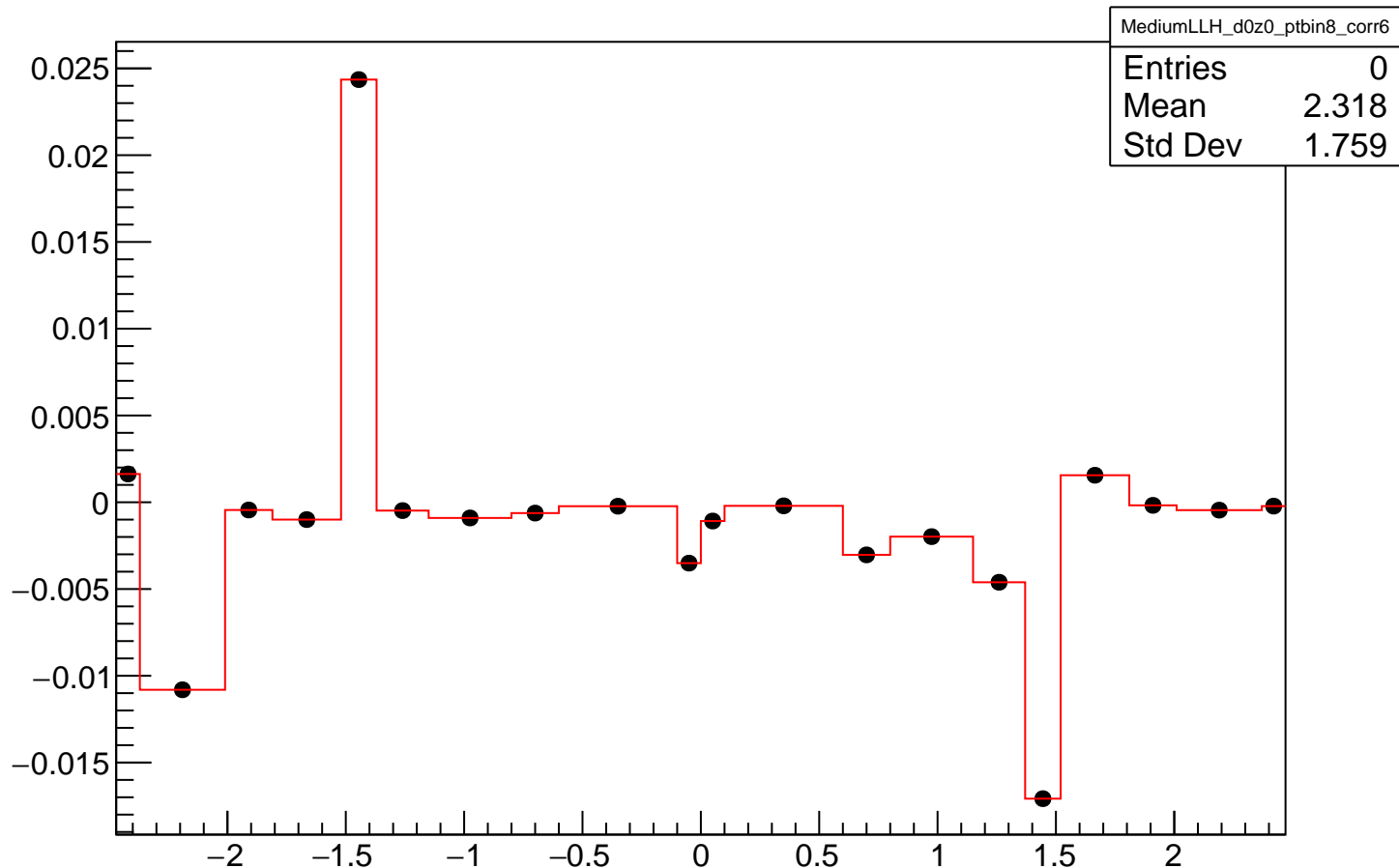
FullSim_MediumLLH_d0z0_corr6



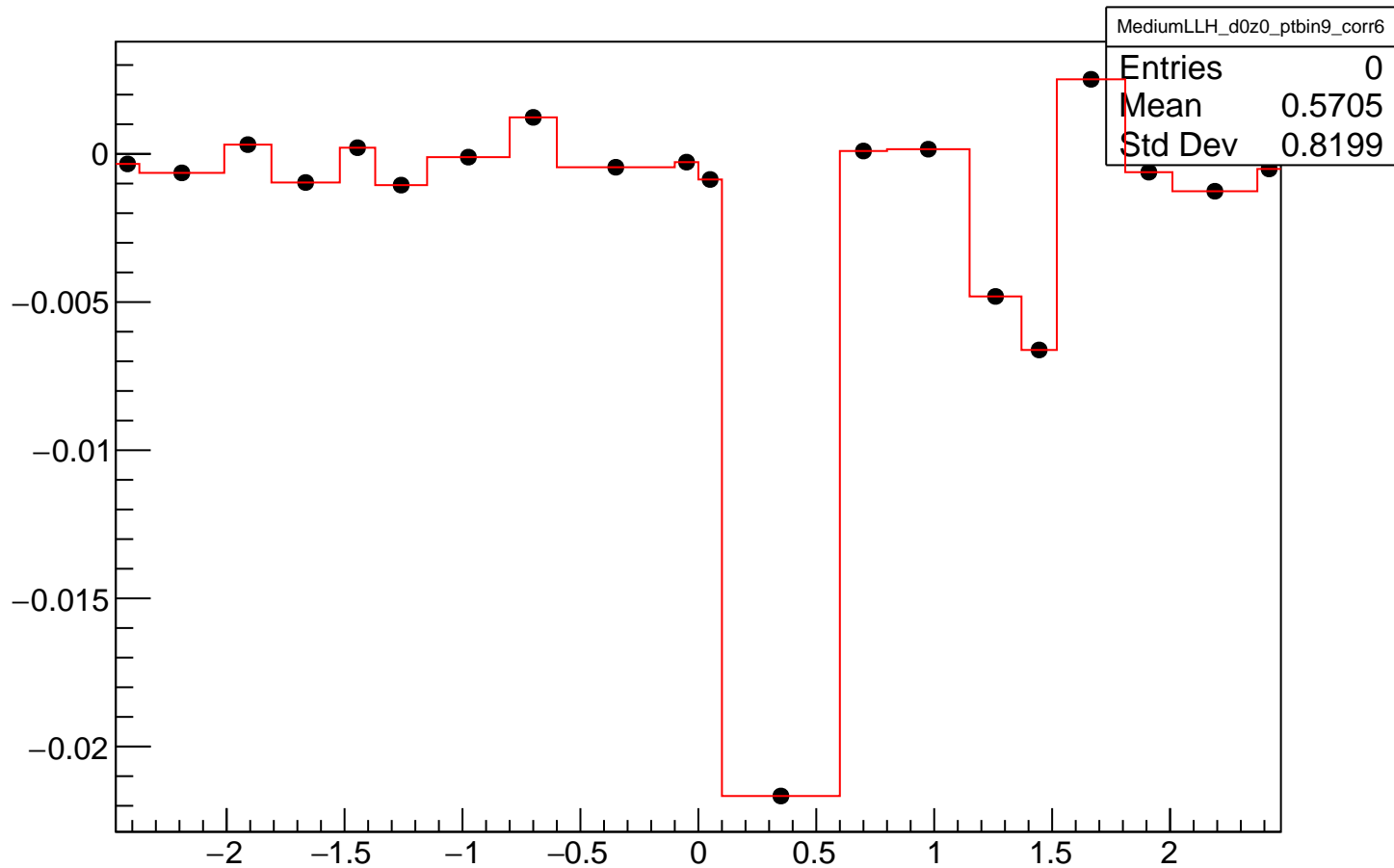
FullSim_MediumLLH_d0z0_corr6



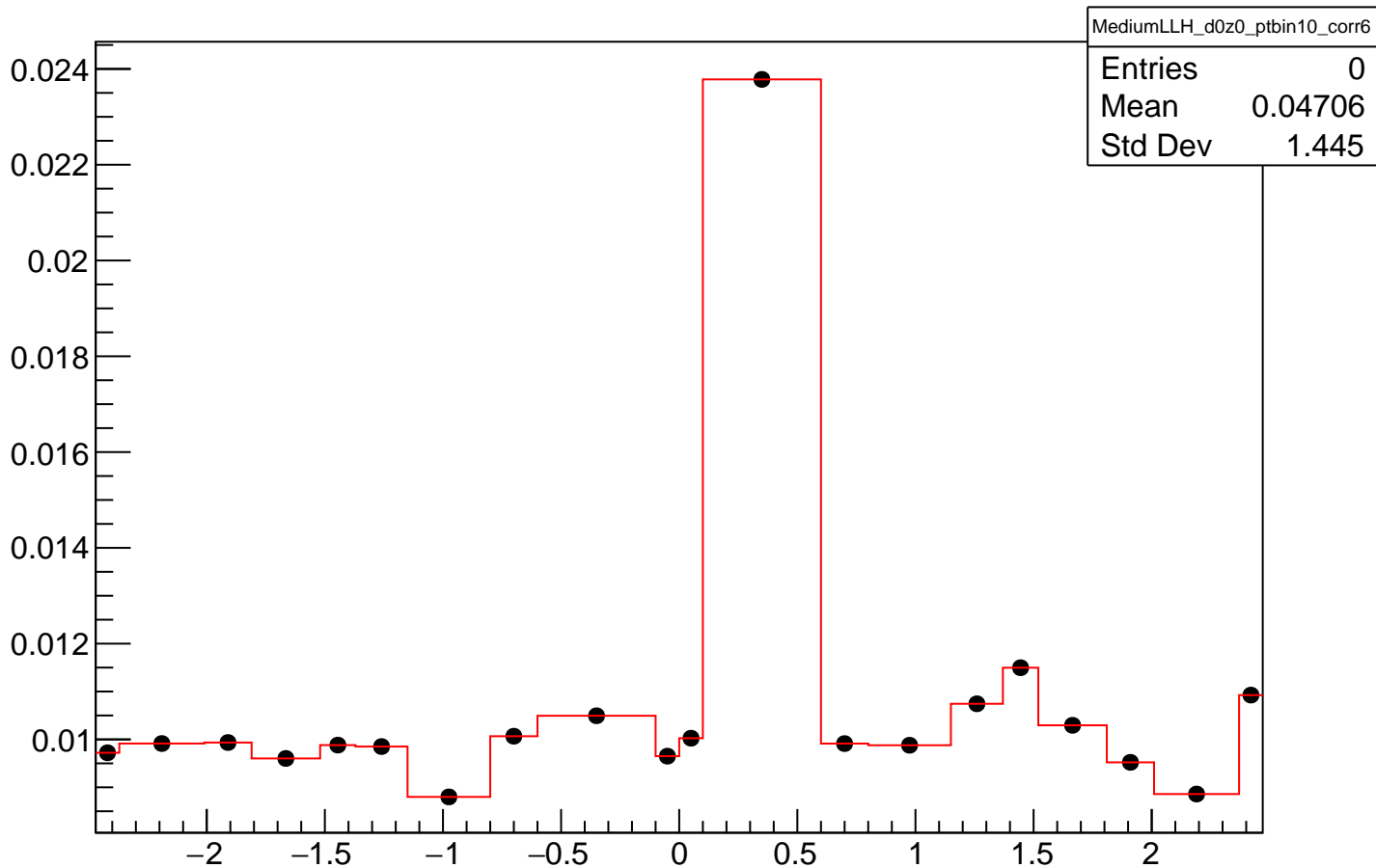
FullSim_MediumLLH_d0z0_corr6



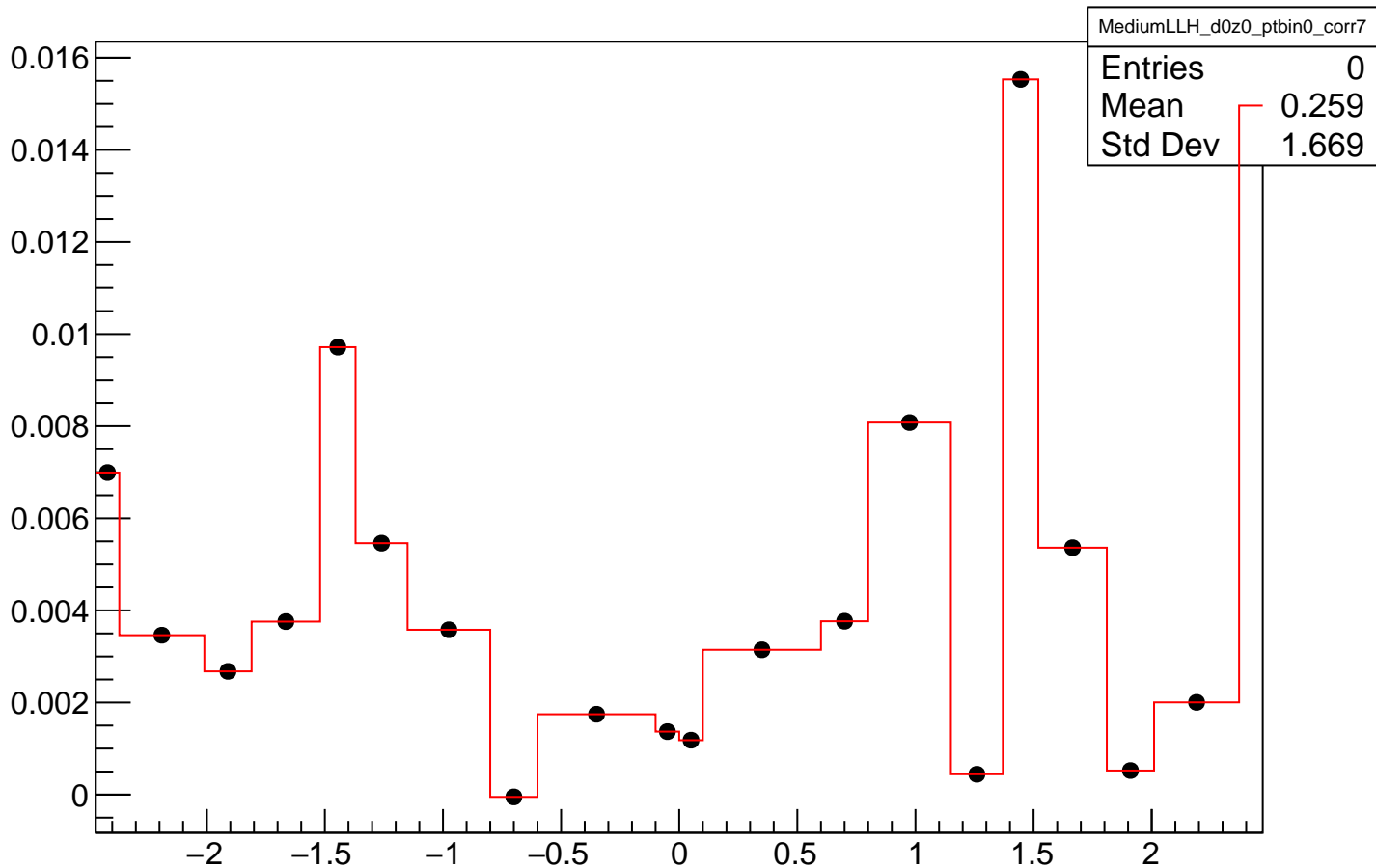
FullSim_MediumLLH_d0z0_corr6



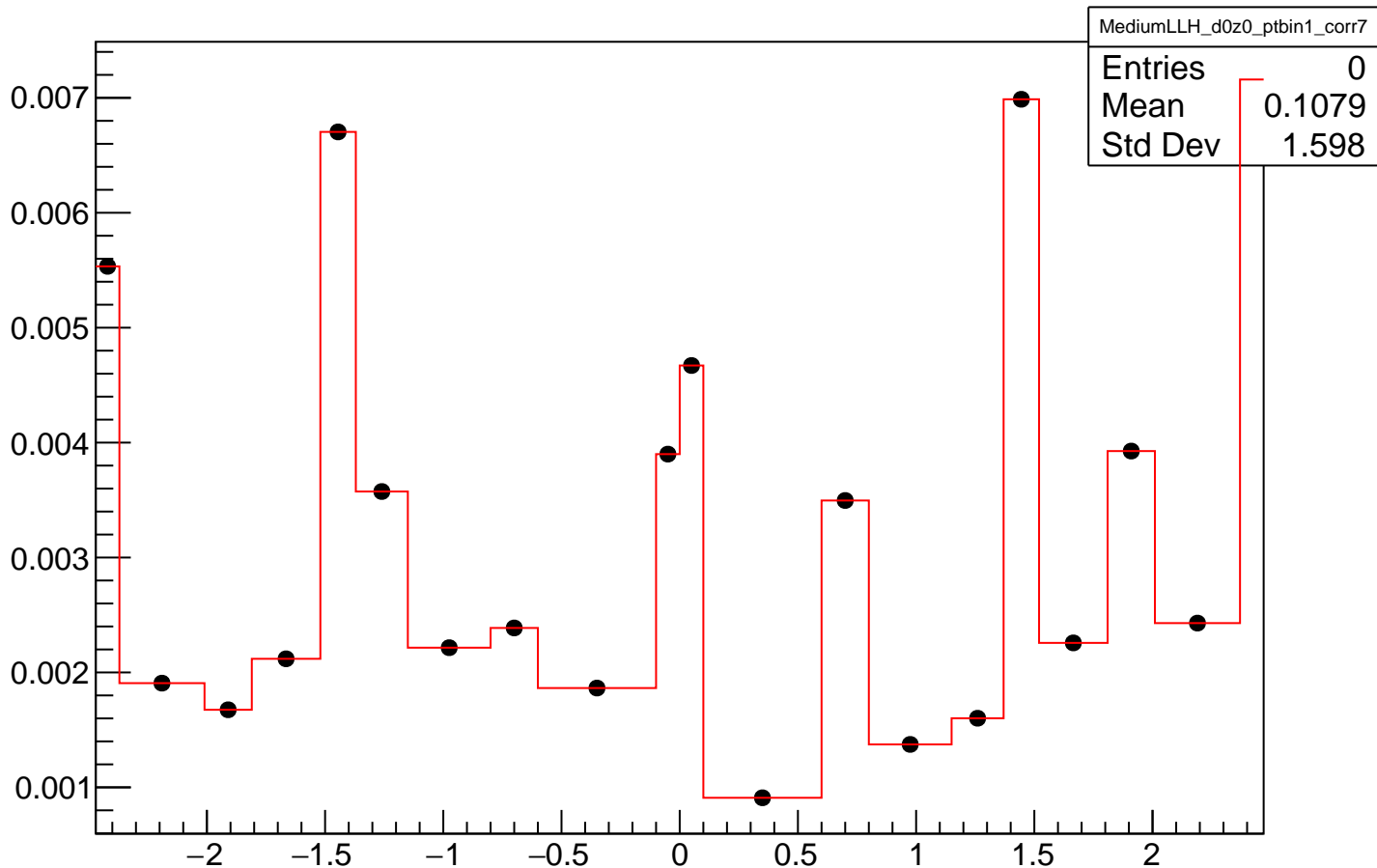
FullSim_MediumLLH_d0z0_corr6



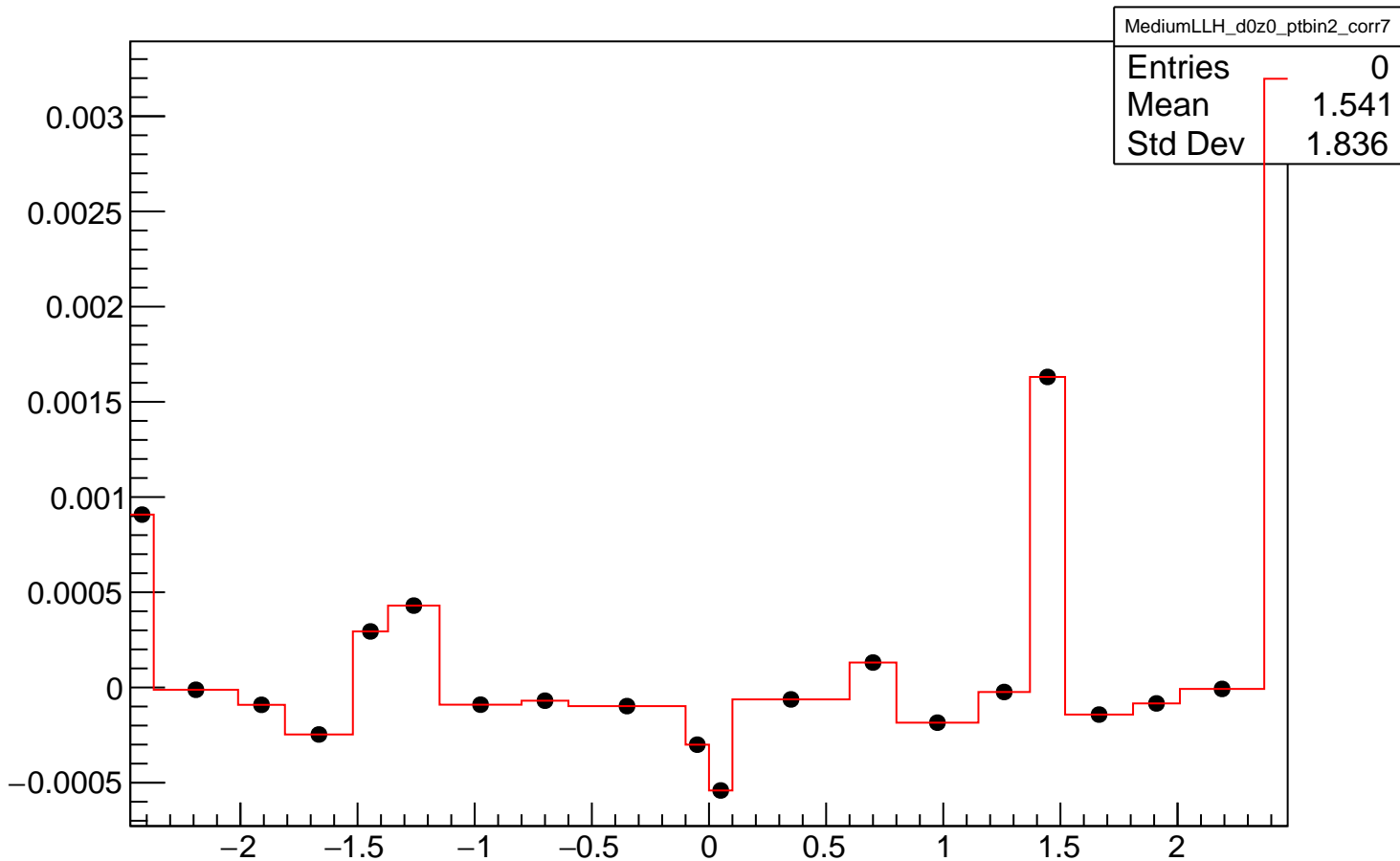
FullSim_MediumLLH_d0z0_corr7



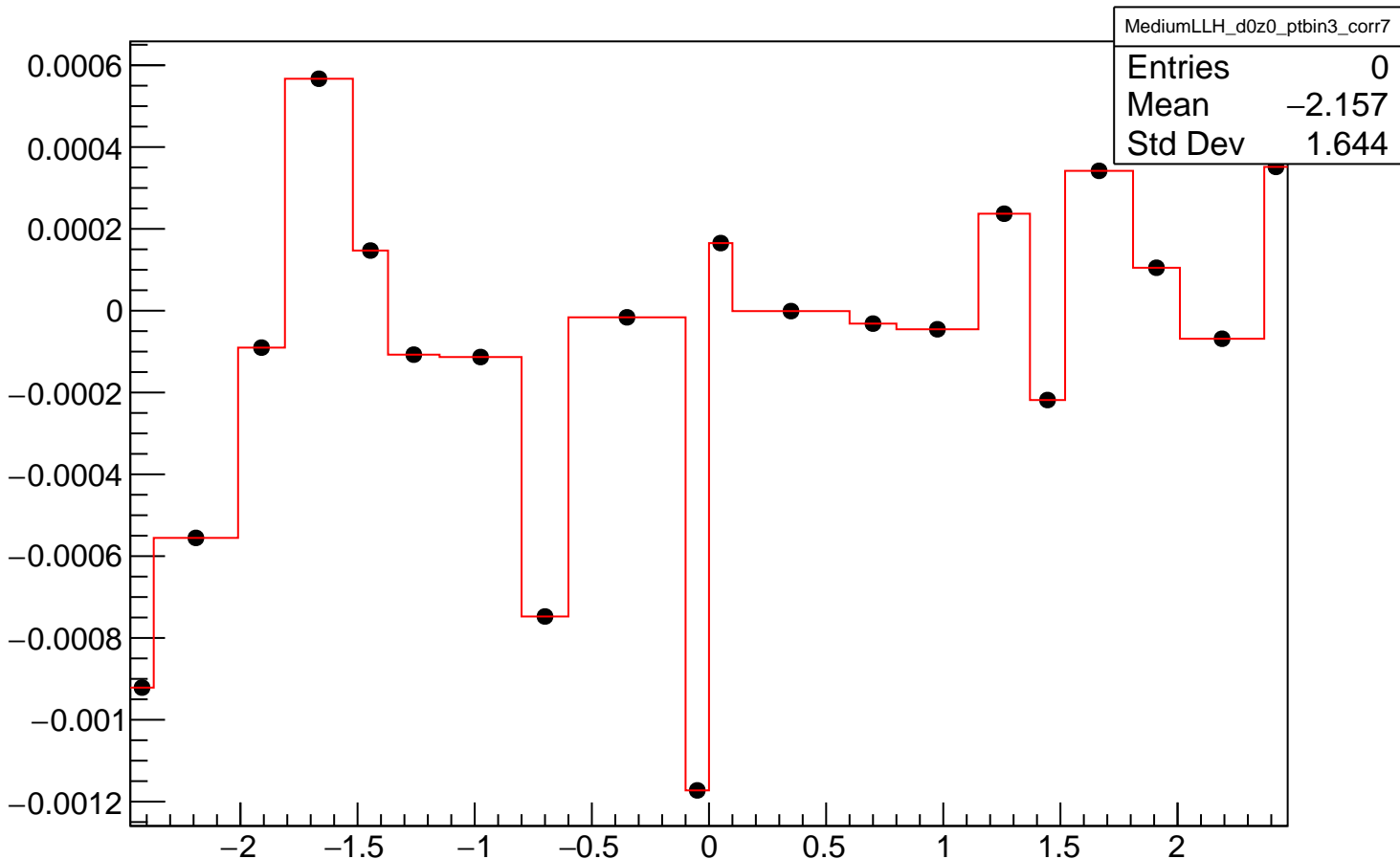
FullSim_MediumLLH_d0z0_corr7



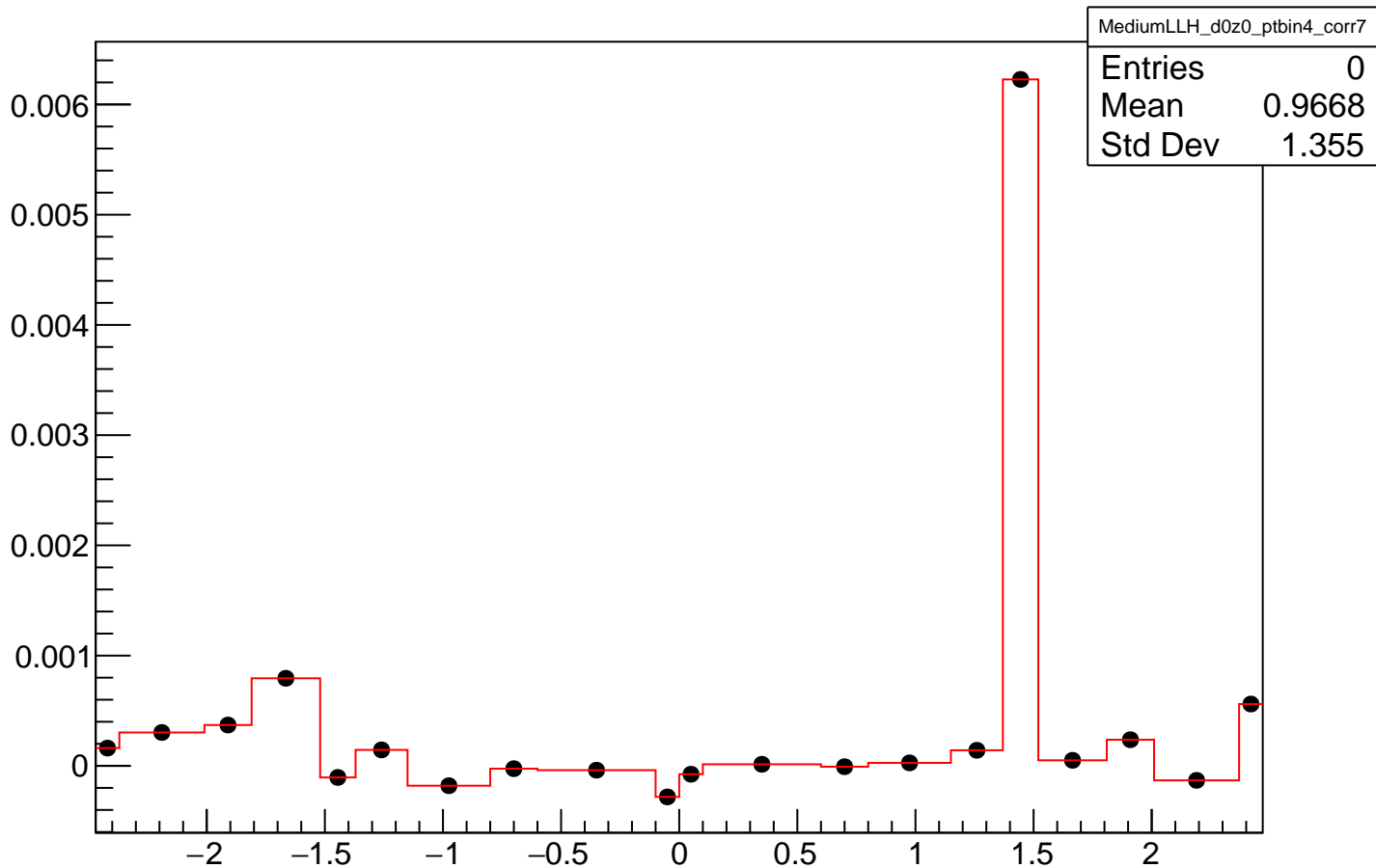
FullSim_MediumLLH_d0z0_corr7



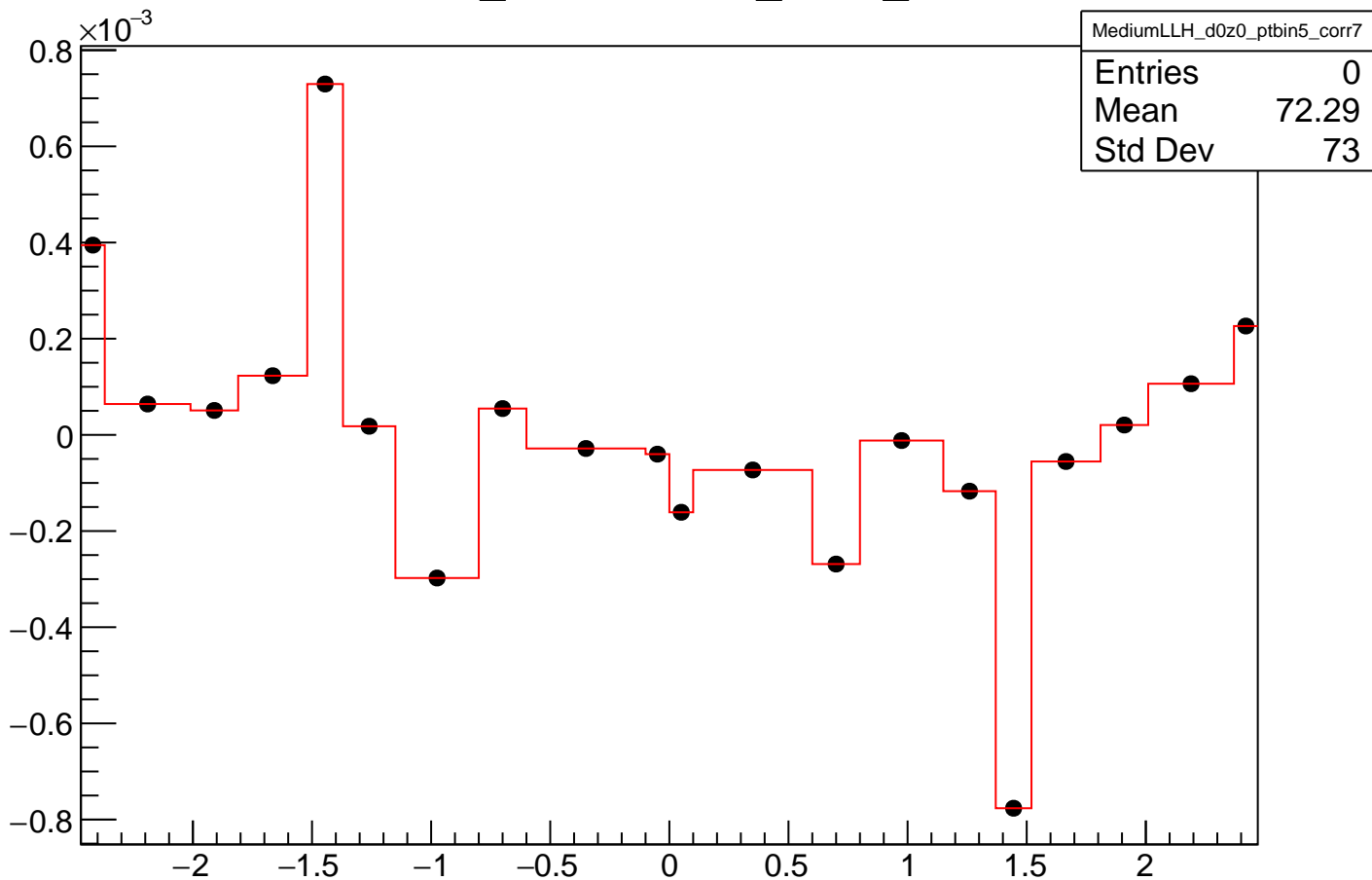
FullSim_MediumLLH_d0z0_corr7



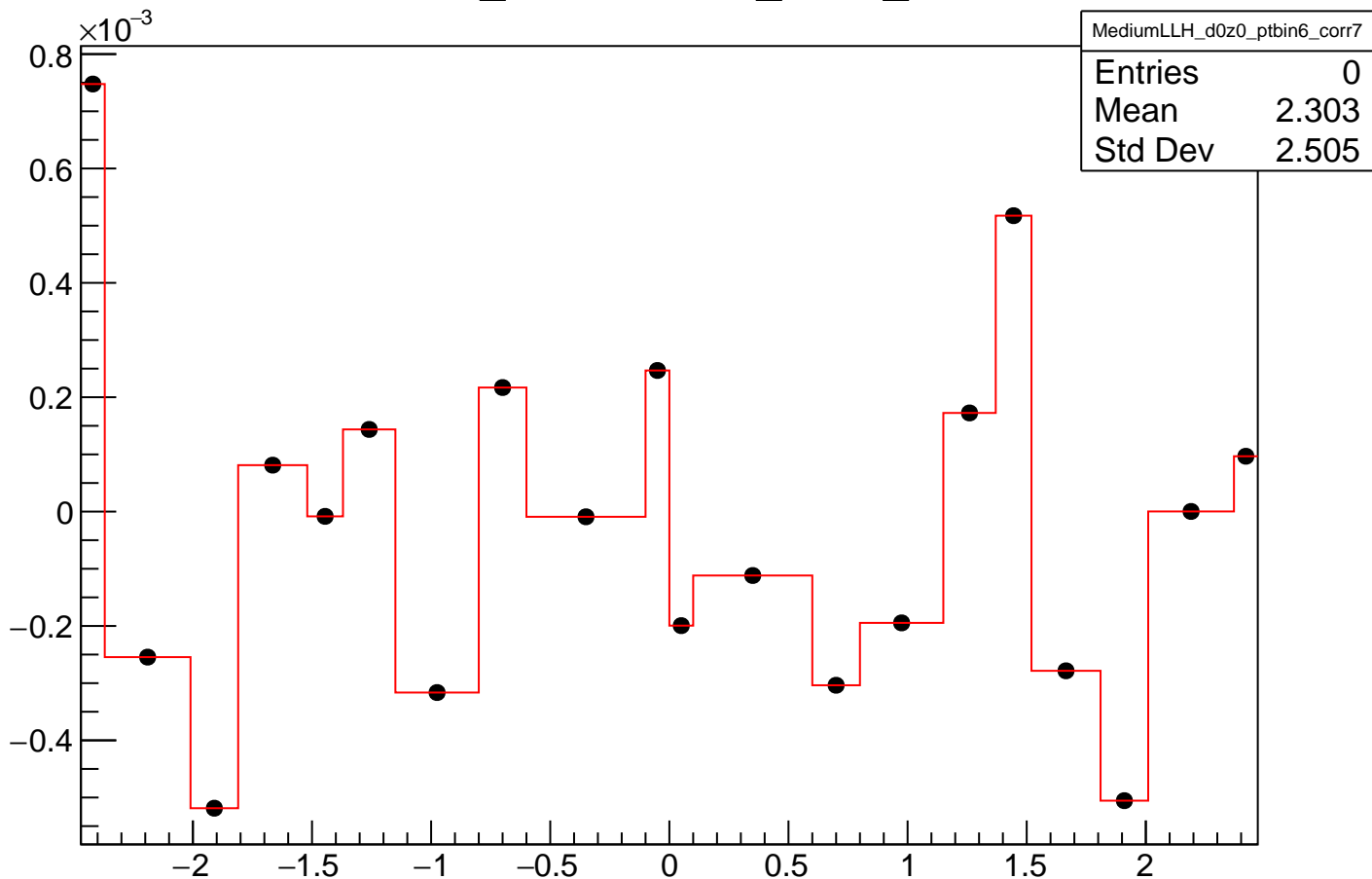
FullSim_MediumLLH_d0z0_corr7



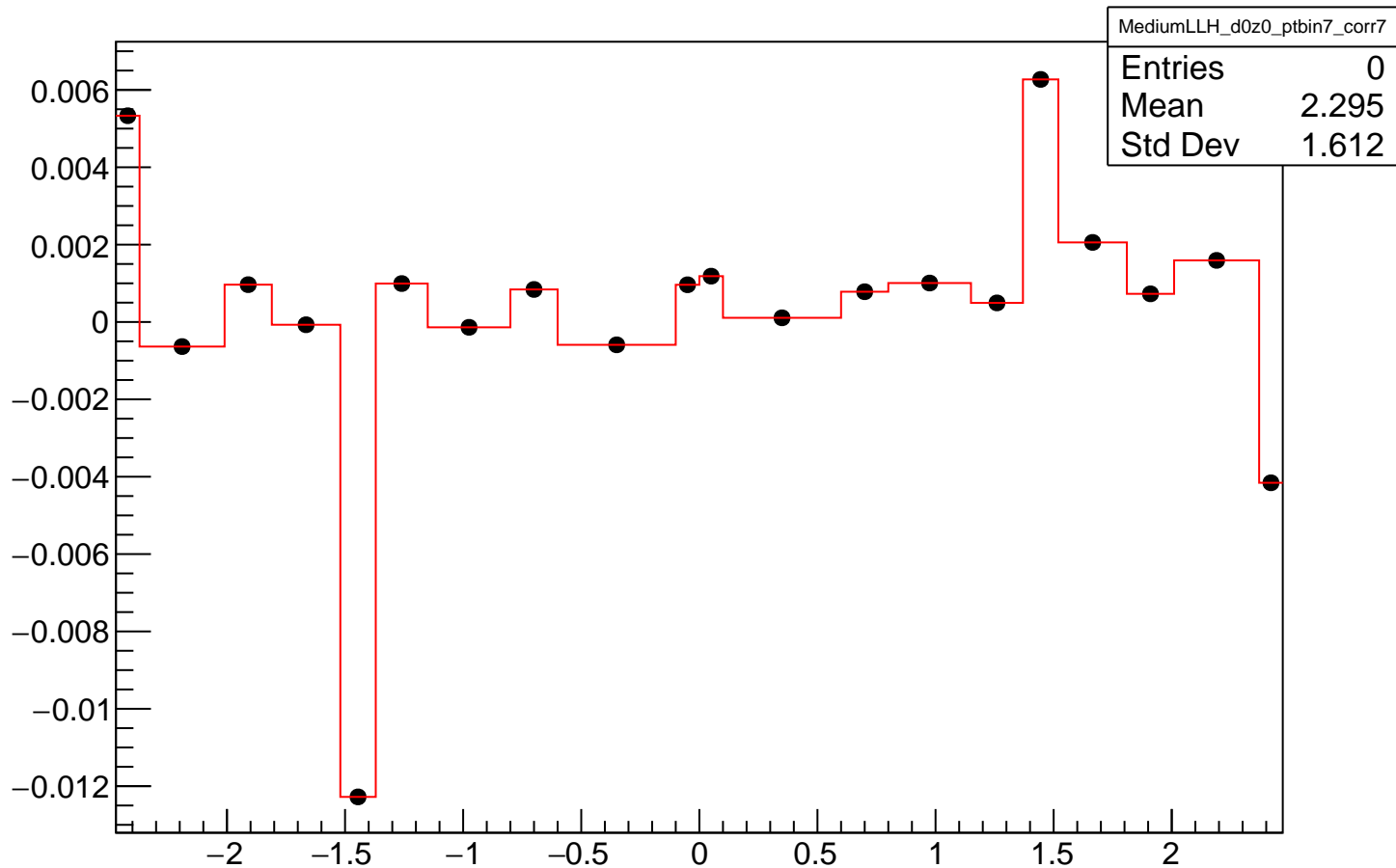
FullSim_MediumLLH_d0z0_corr7



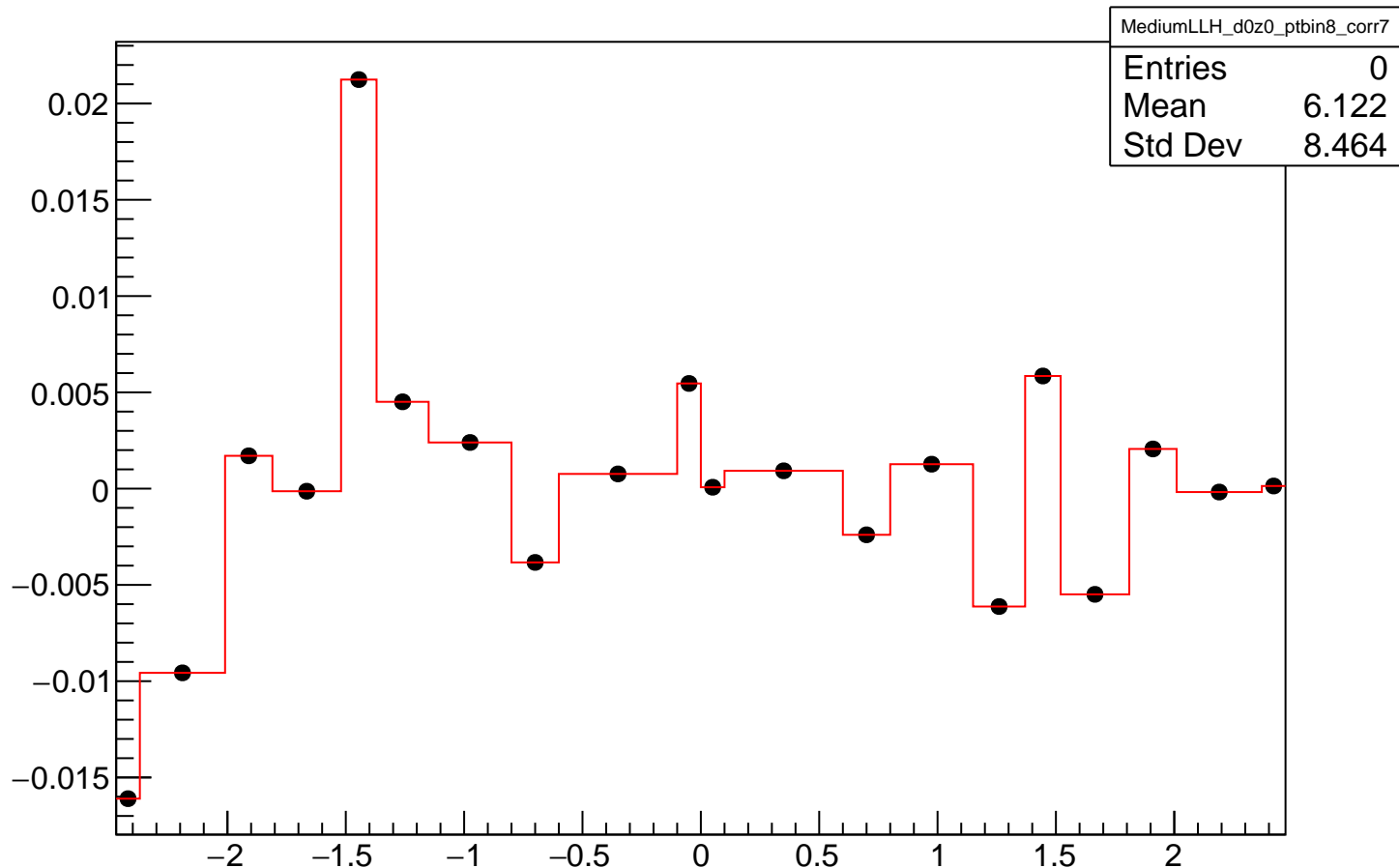
FullSim_MediumLLH_d0z0_corr7



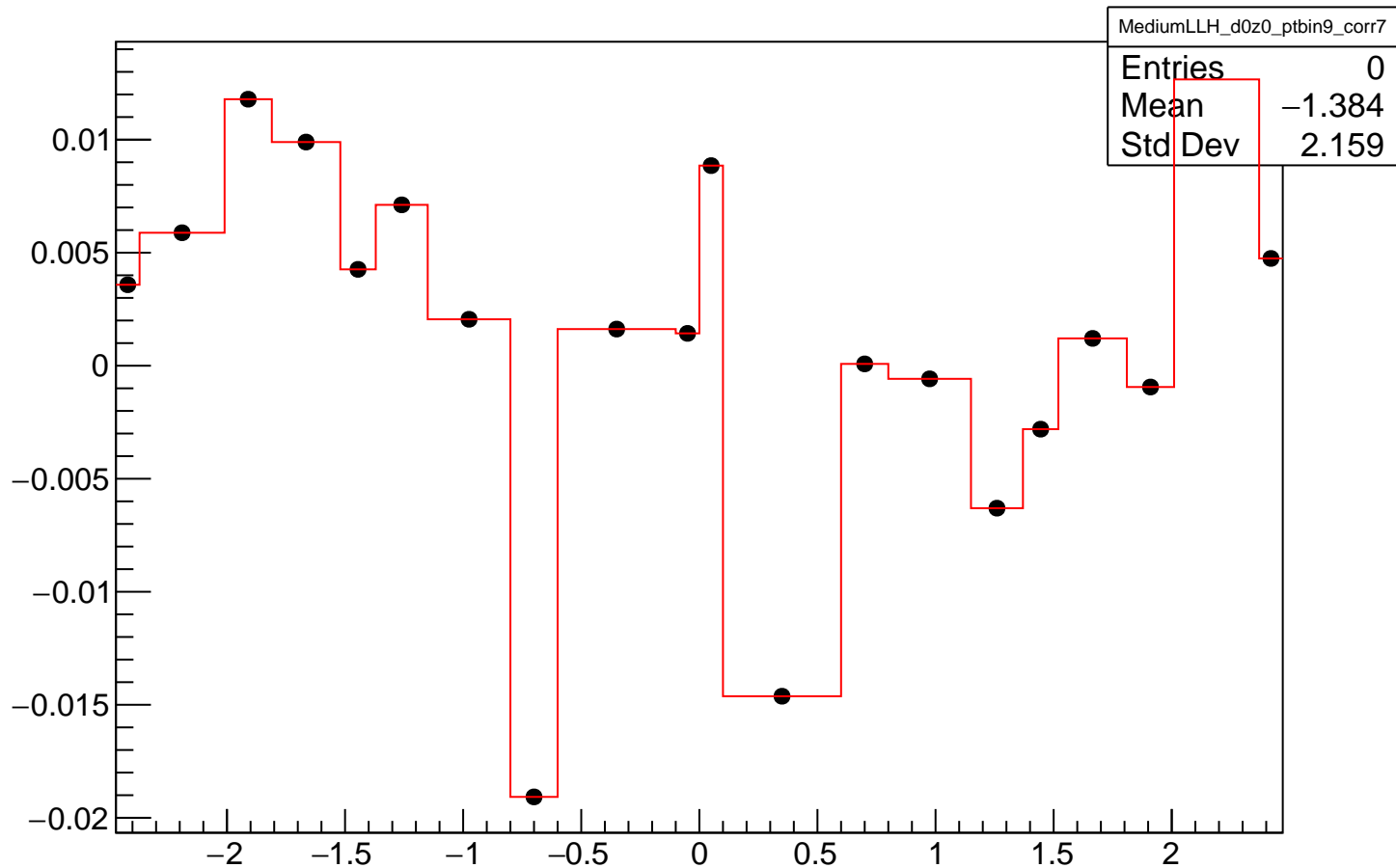
FullSim_MediumLLH_d0z0_corr7



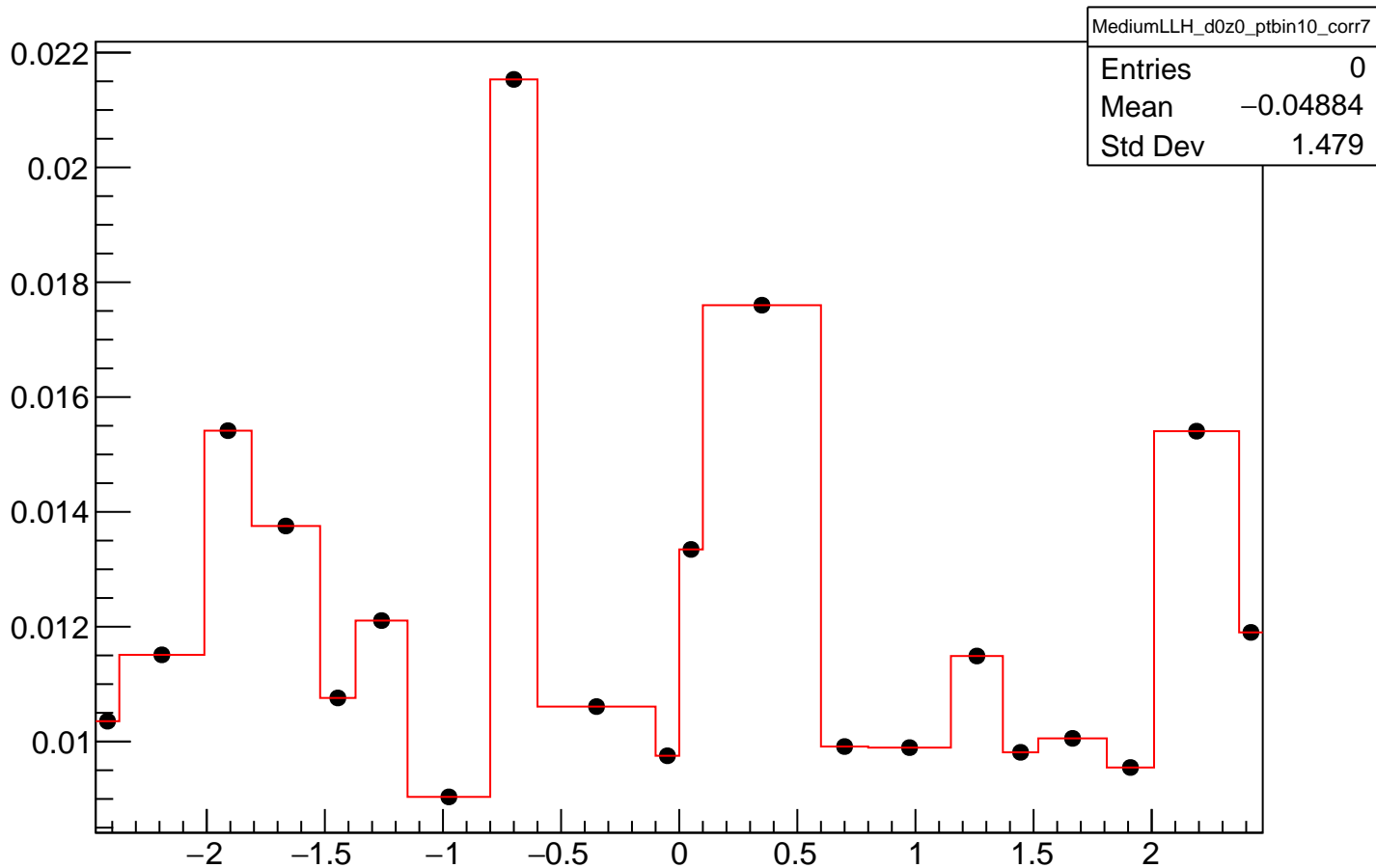
FullSim_MediumLLH_d0z0_corr7



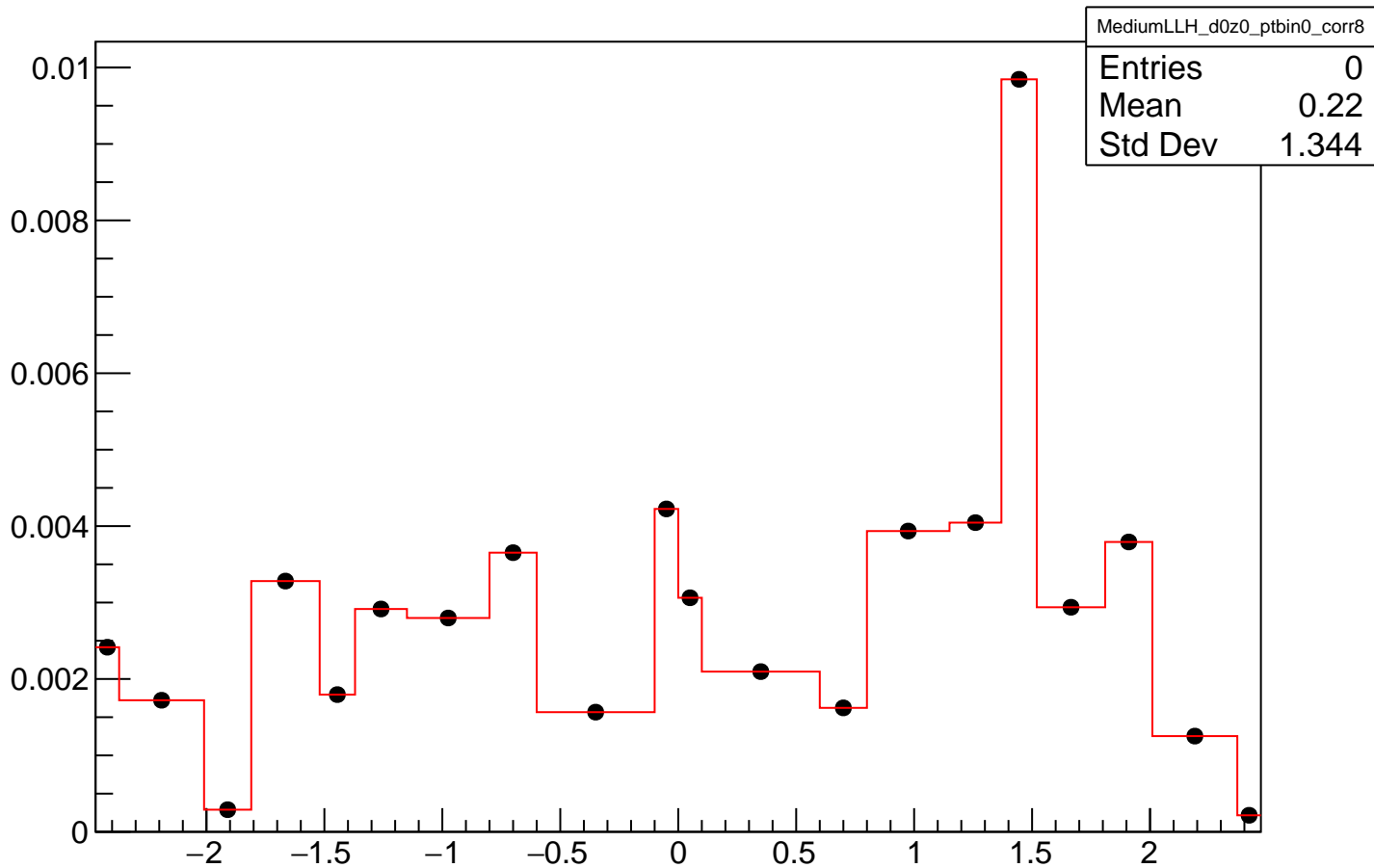
FullSim_MediumLLH_d0z0_corr7



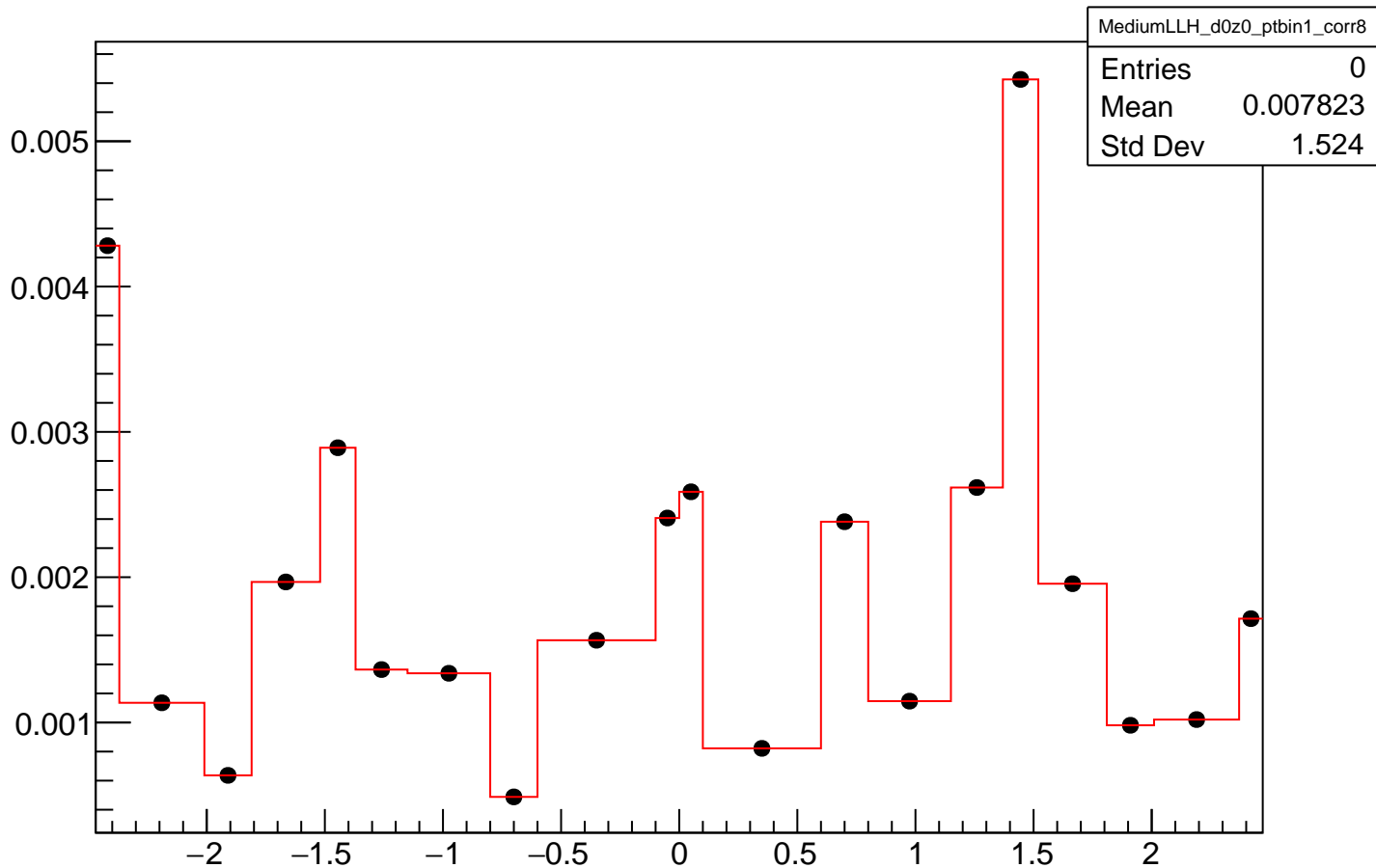
FullSim_MediumLLH_d0z0_corr7



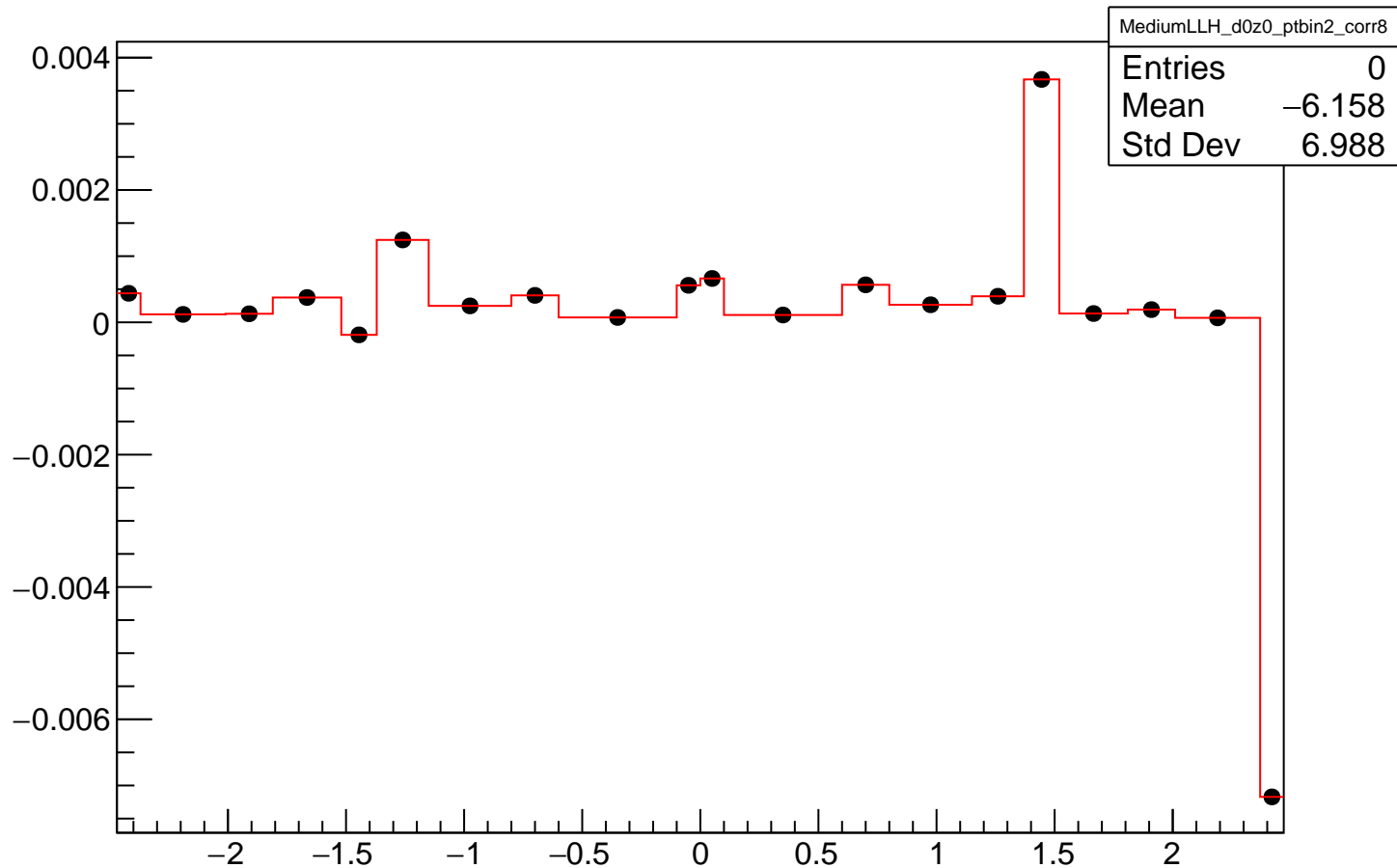
FullSim_MediumLLH_d0z0_corr8



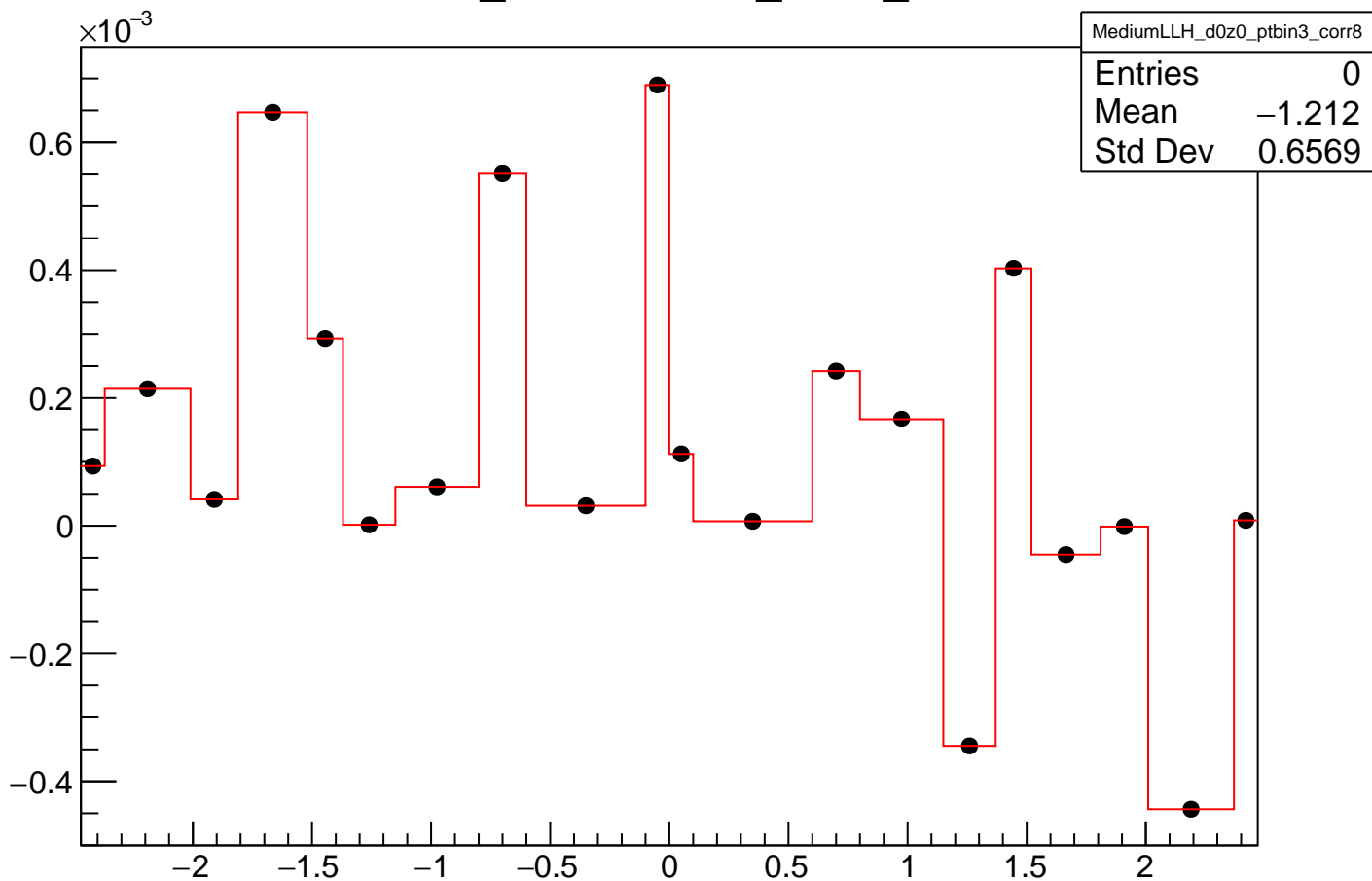
FullSim_MediumLLH_d0z0_corr8



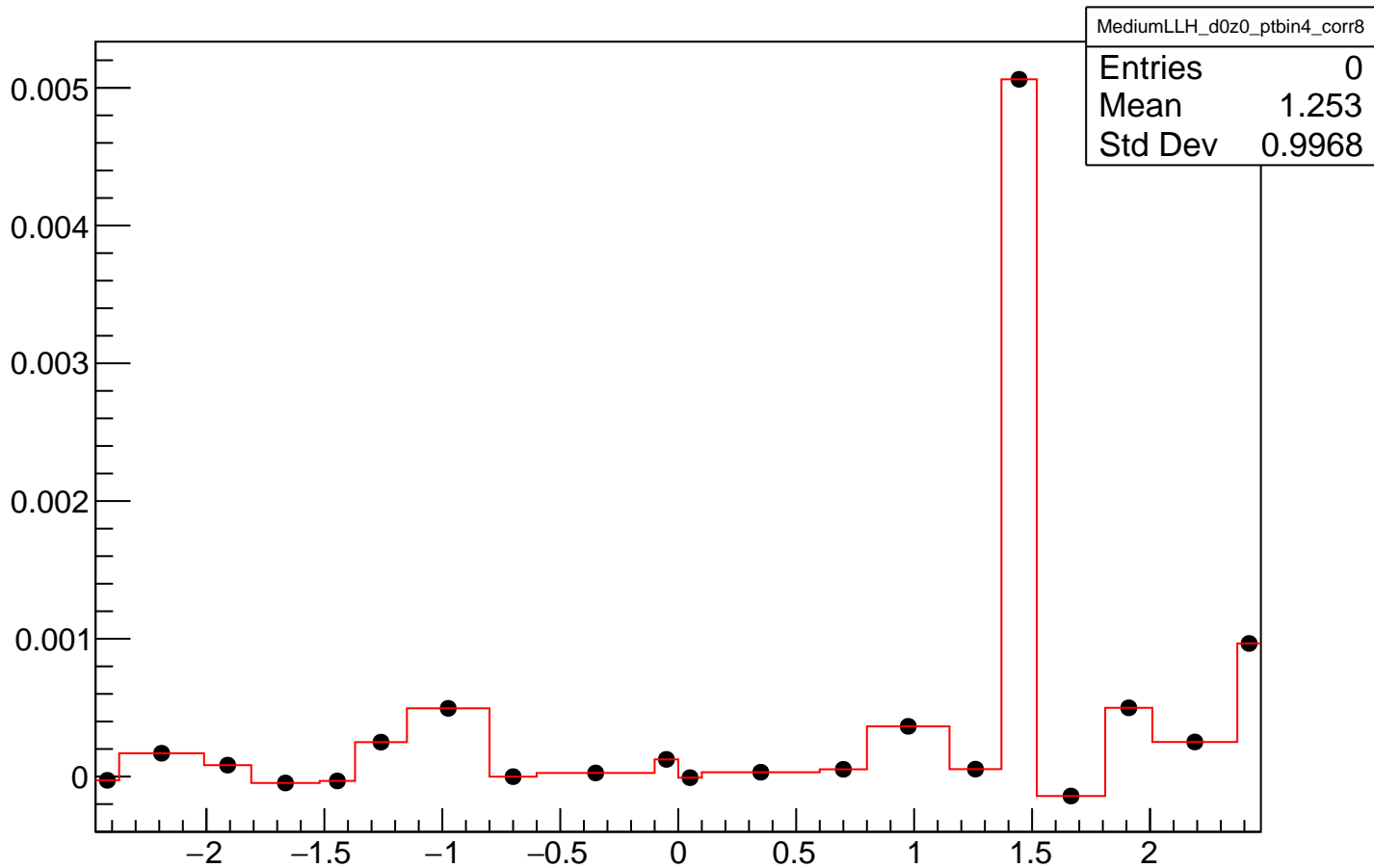
FullSim_MediumLLH_d0z0_corr8



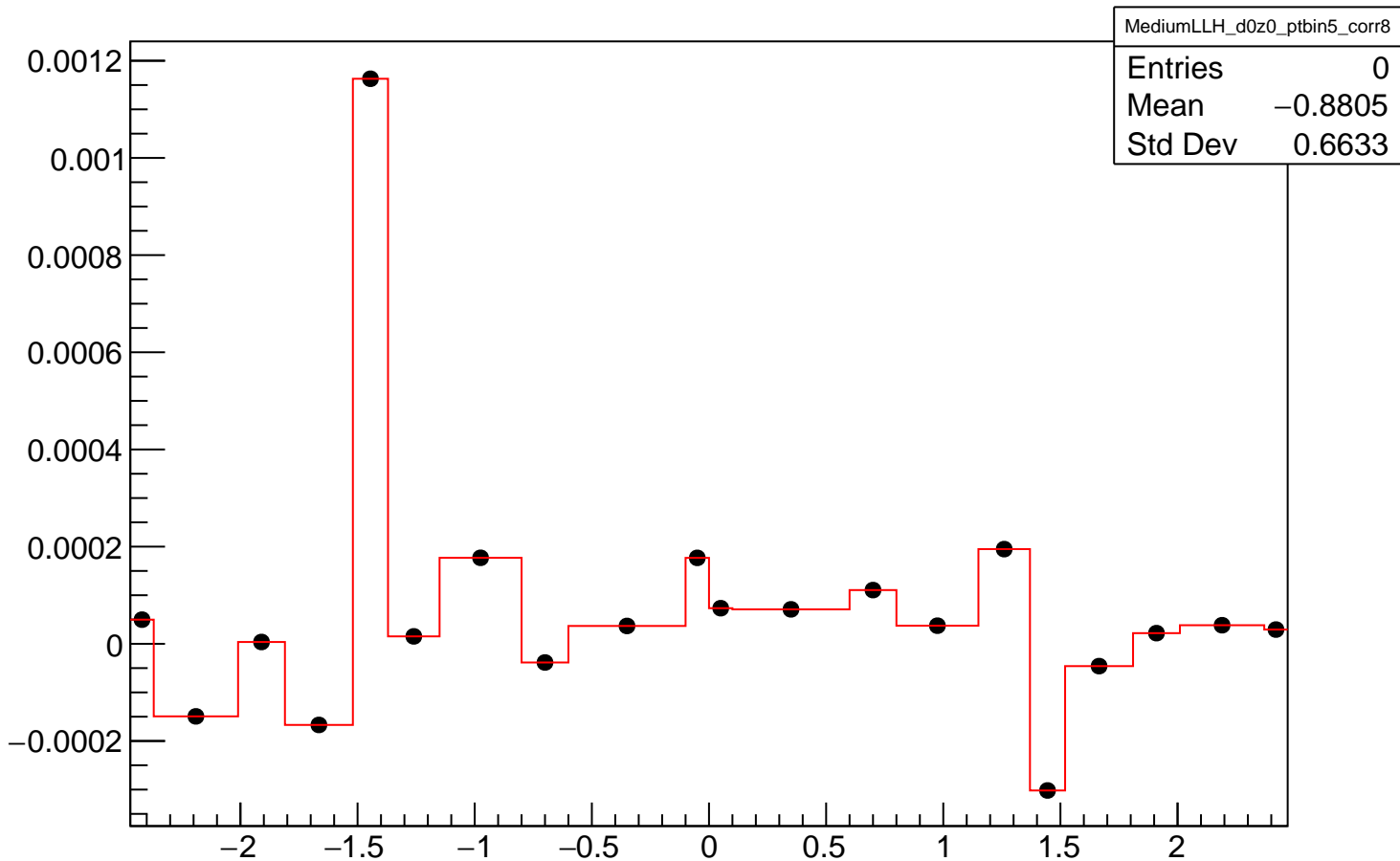
FullSim_MediumLLH_d0z0_corr8



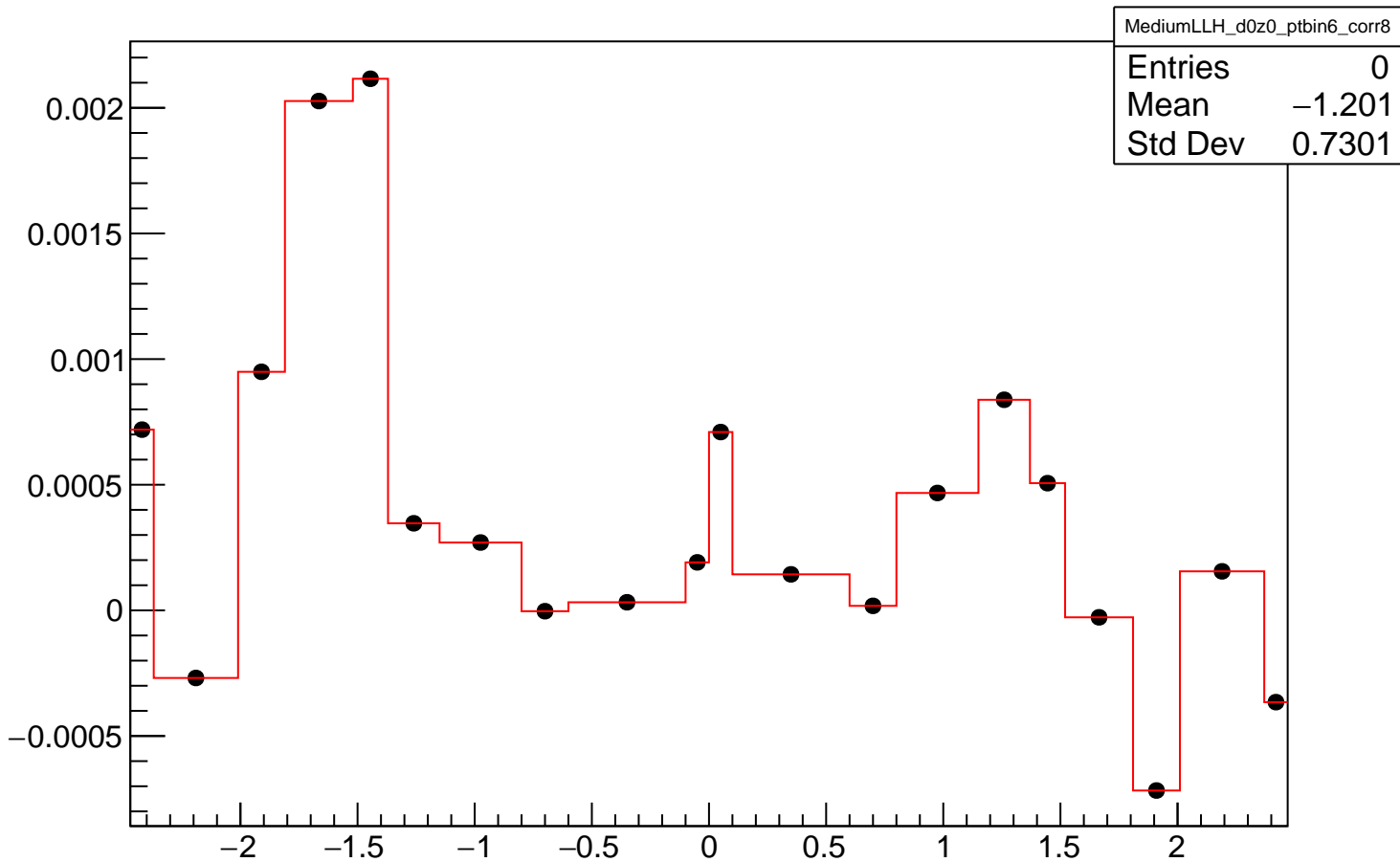
FullSim_MediumLLH_d0z0_corr8



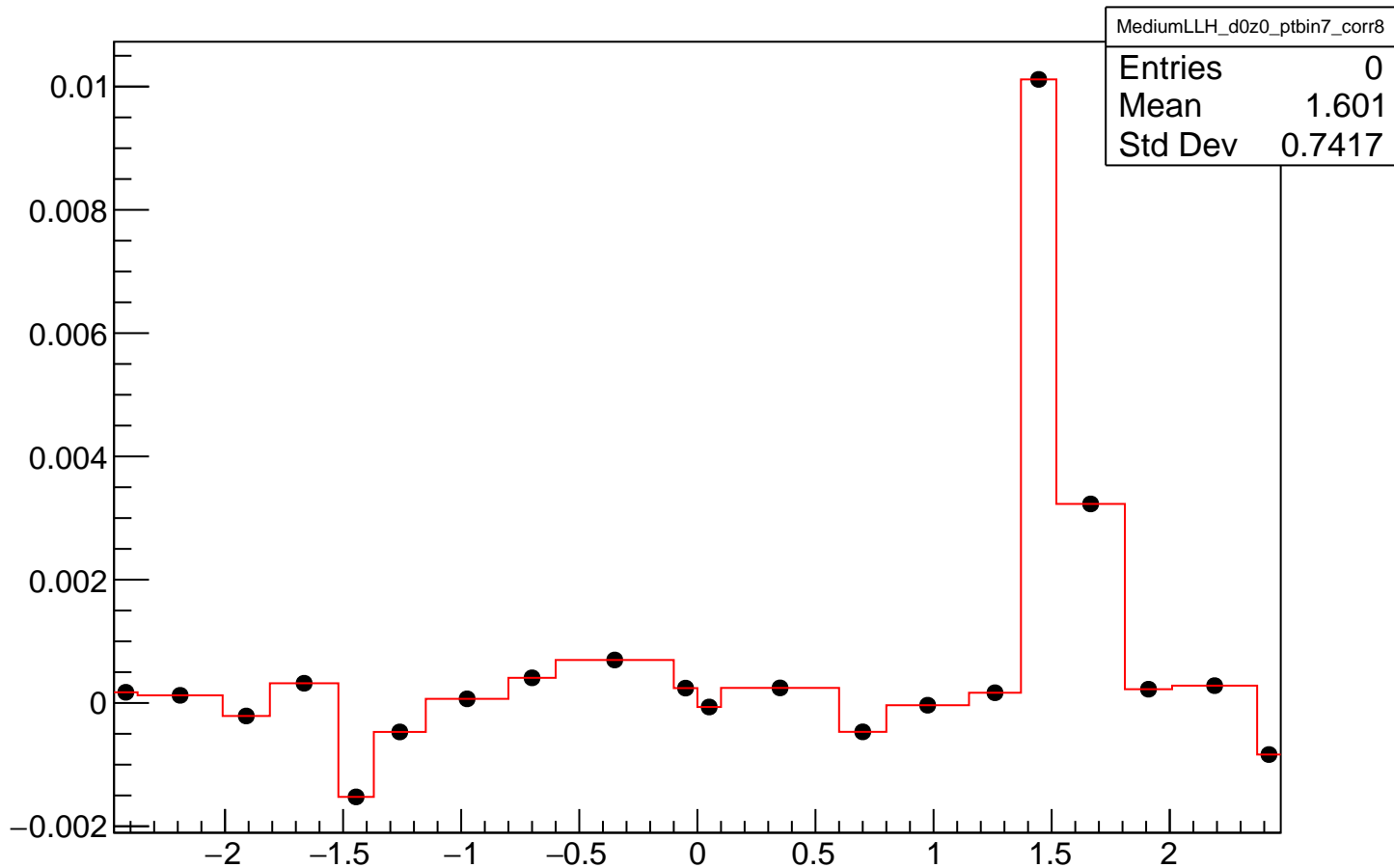
FullSim_MediumLLH_d0z0_corr8



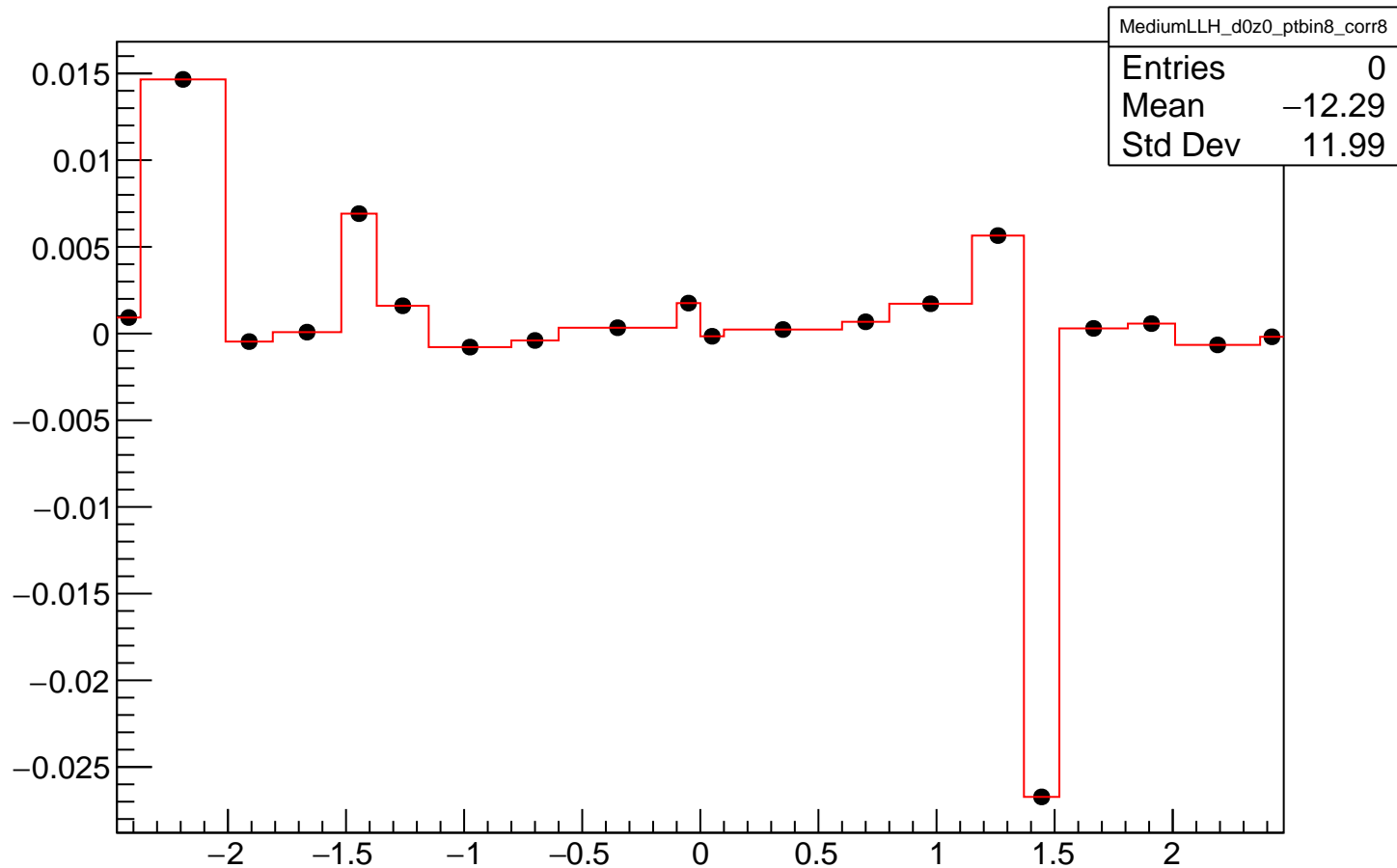
FullSim_MediumLLH_d0z0_corr8



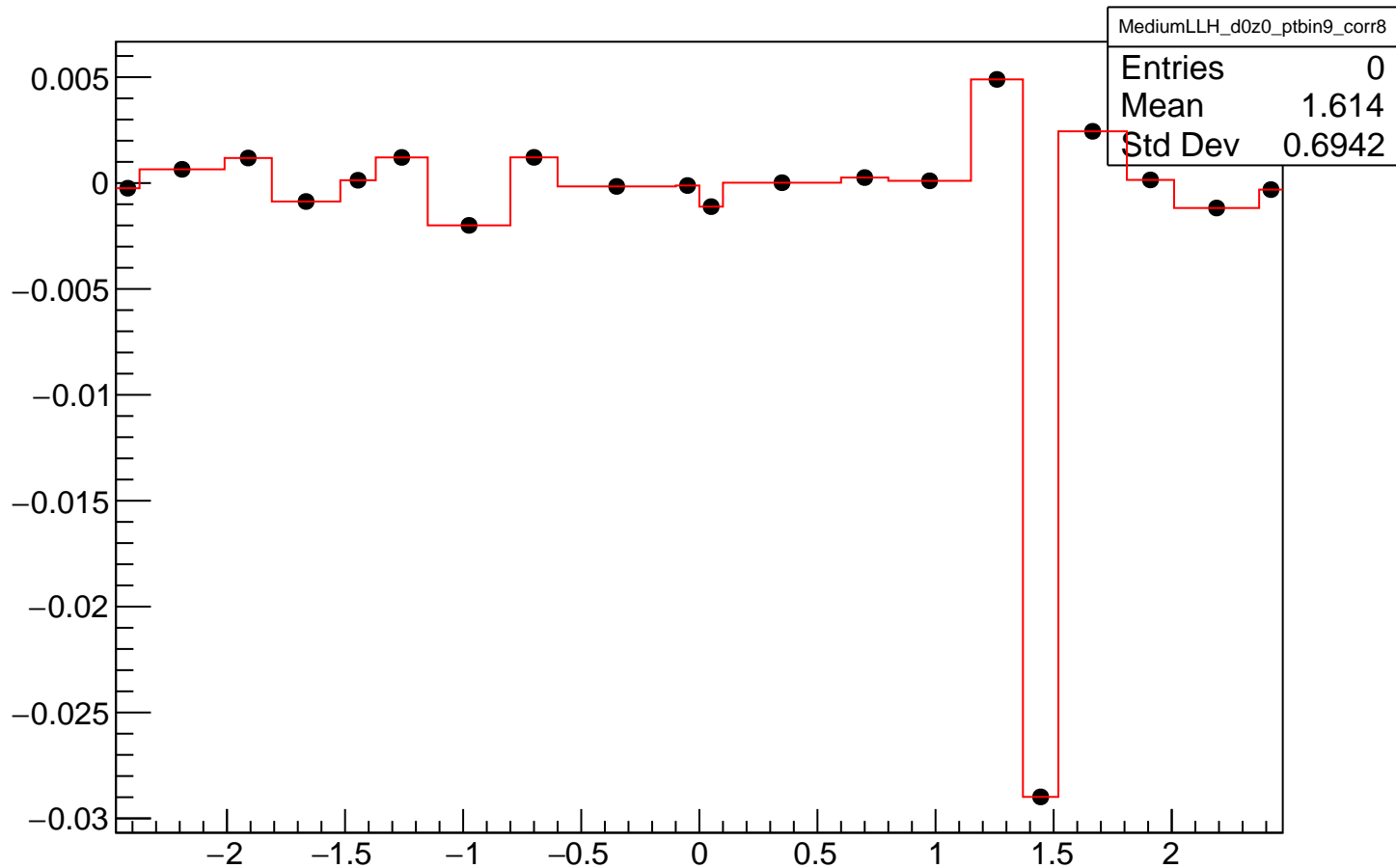
FullSim_MediumLLH_d0z0_corr8



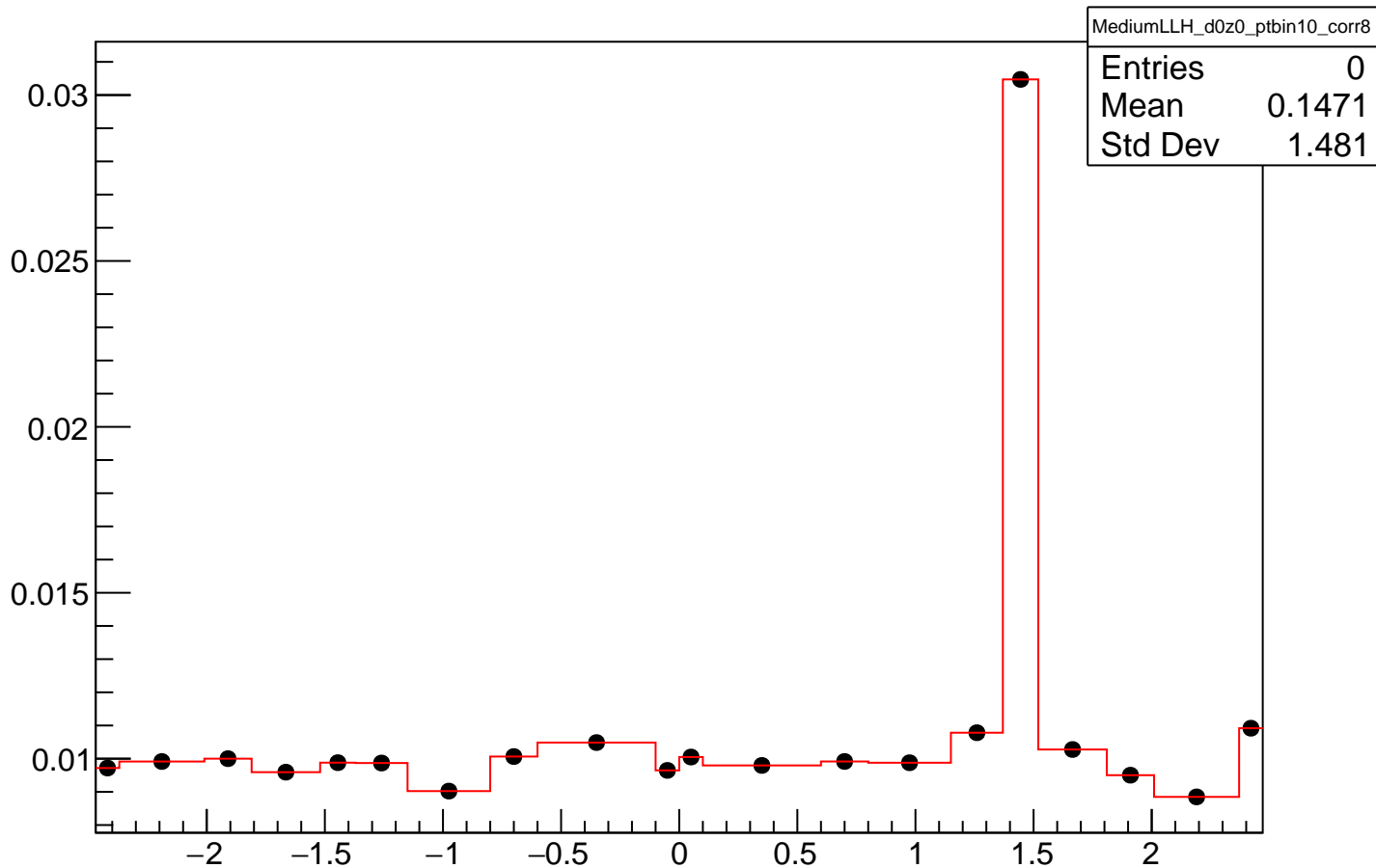
FullSim_MediumLLH_d0z0_corr8



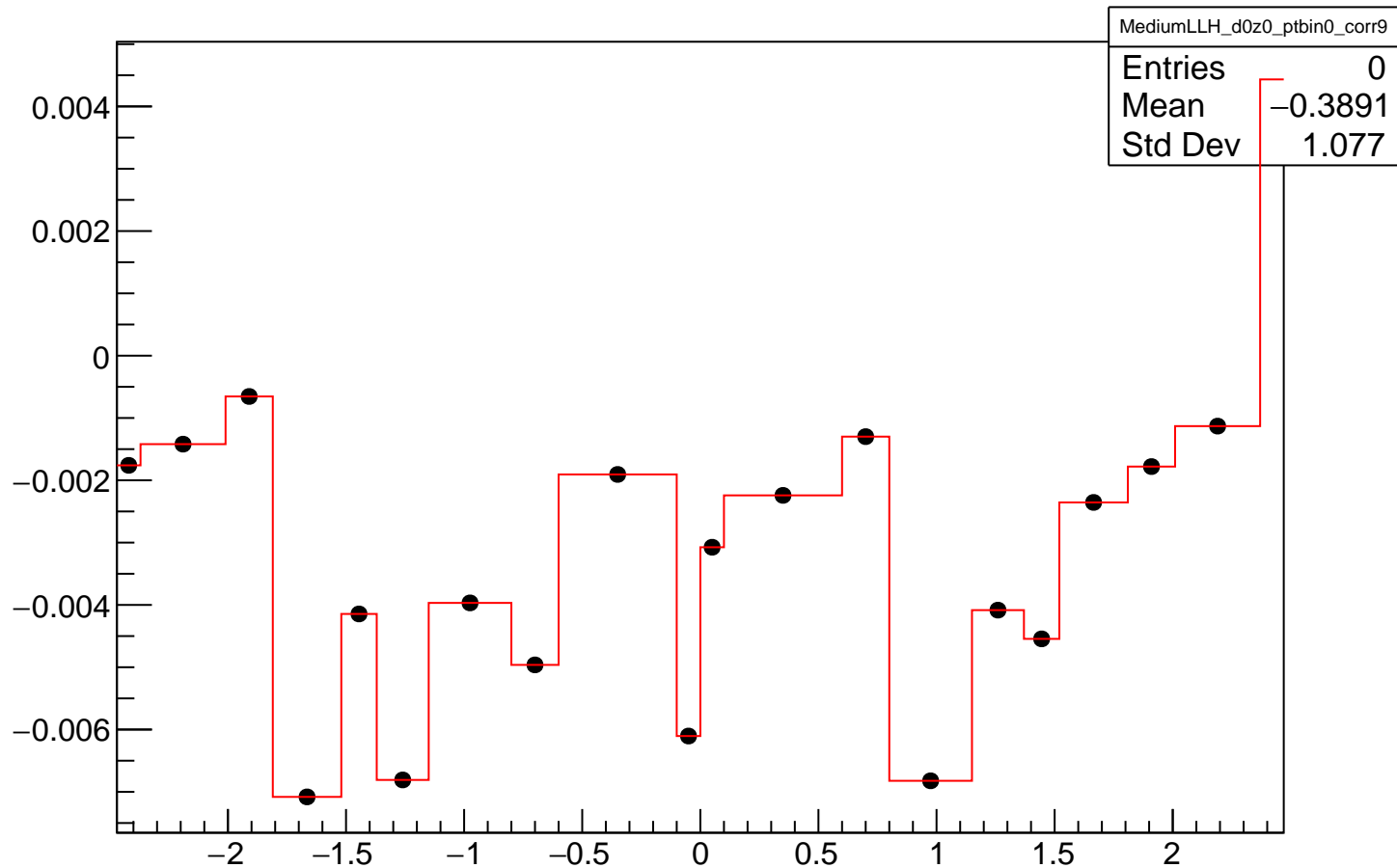
FullSim_MediumLLH_d0z0_corr8



FullSim_MediumLLH_d0z0_corr8



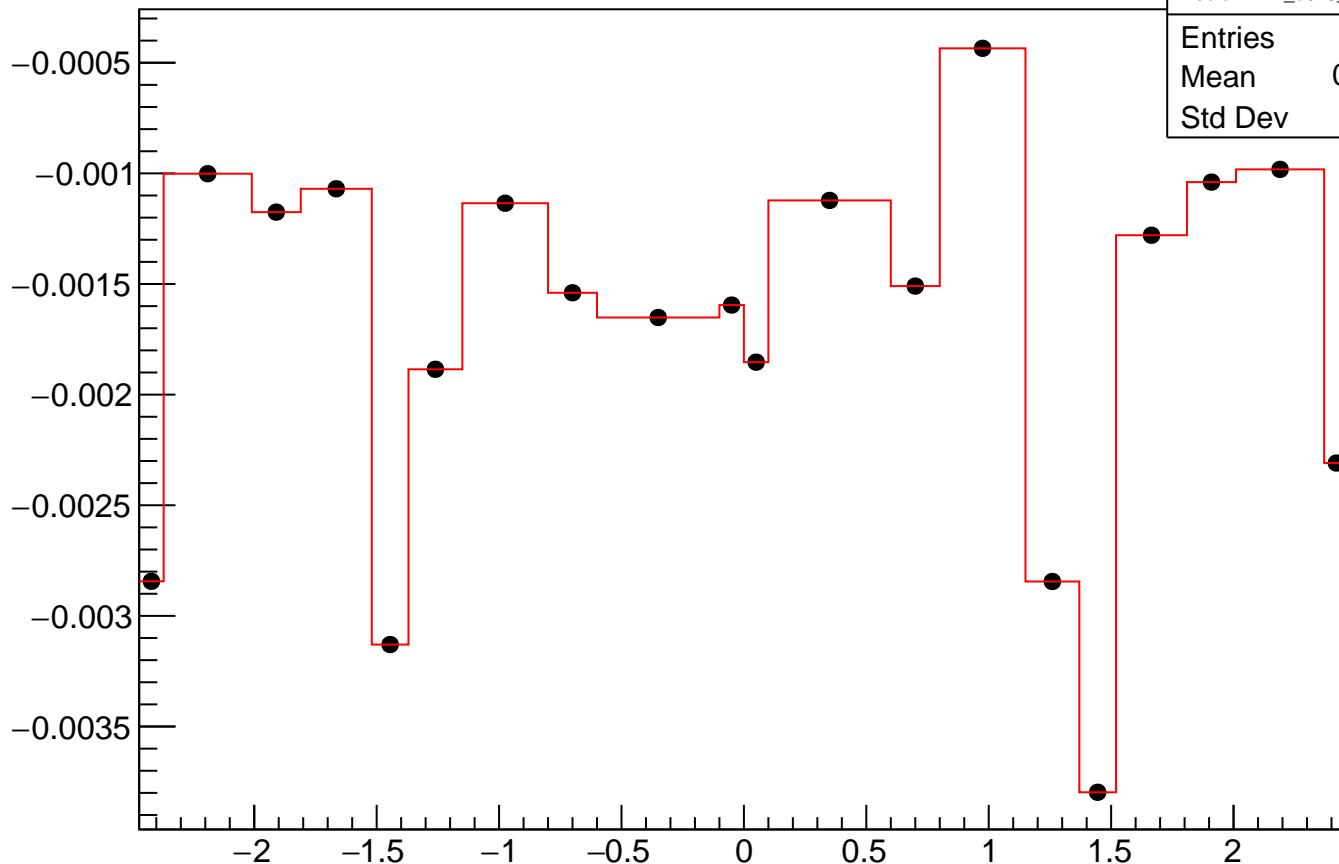
FullSim_MediumLLH_d0z0_corr9



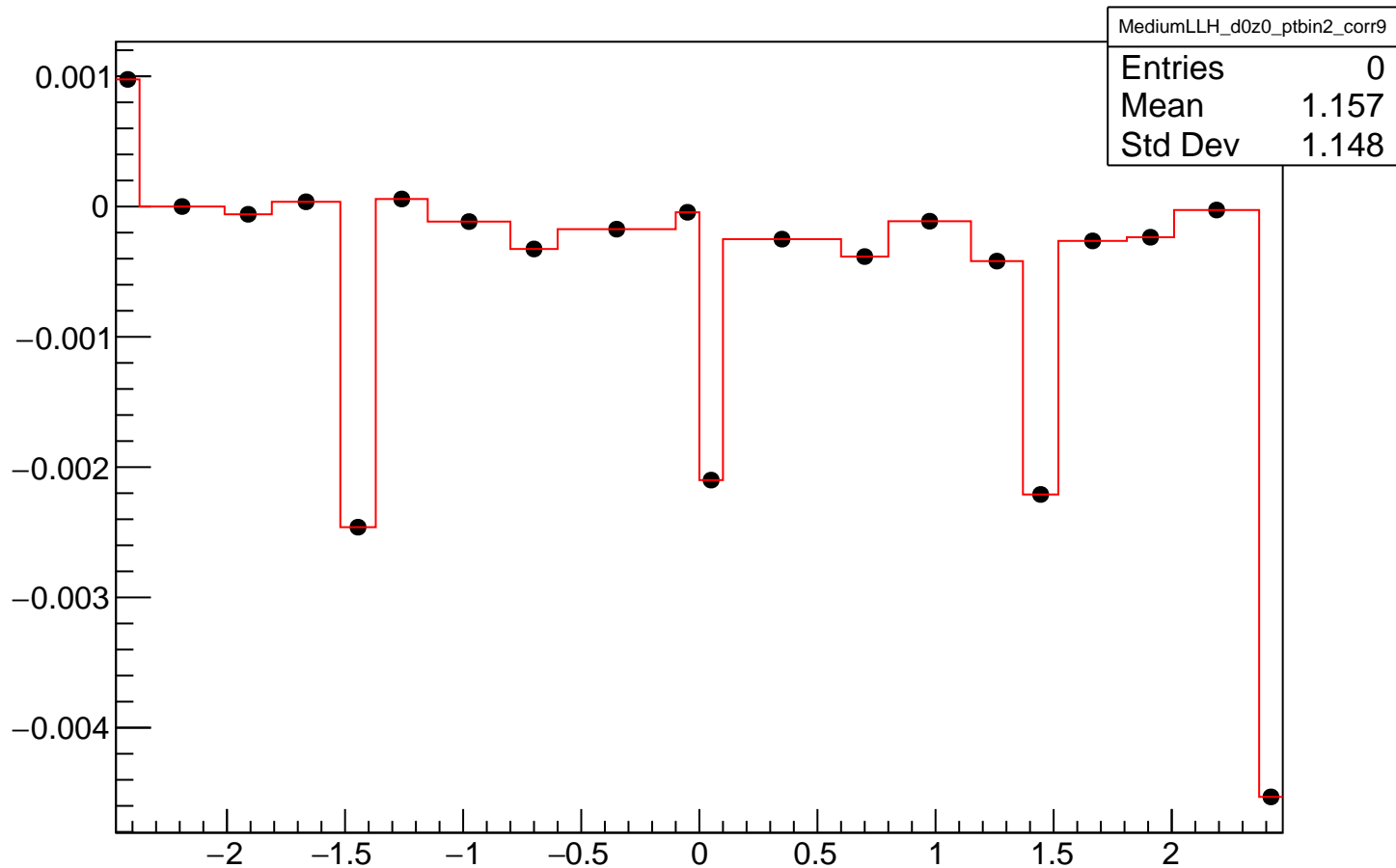
FullSim_MediumLLH_d0z0_corr9

MediumLLH_d0z0_ptbin1_corr9

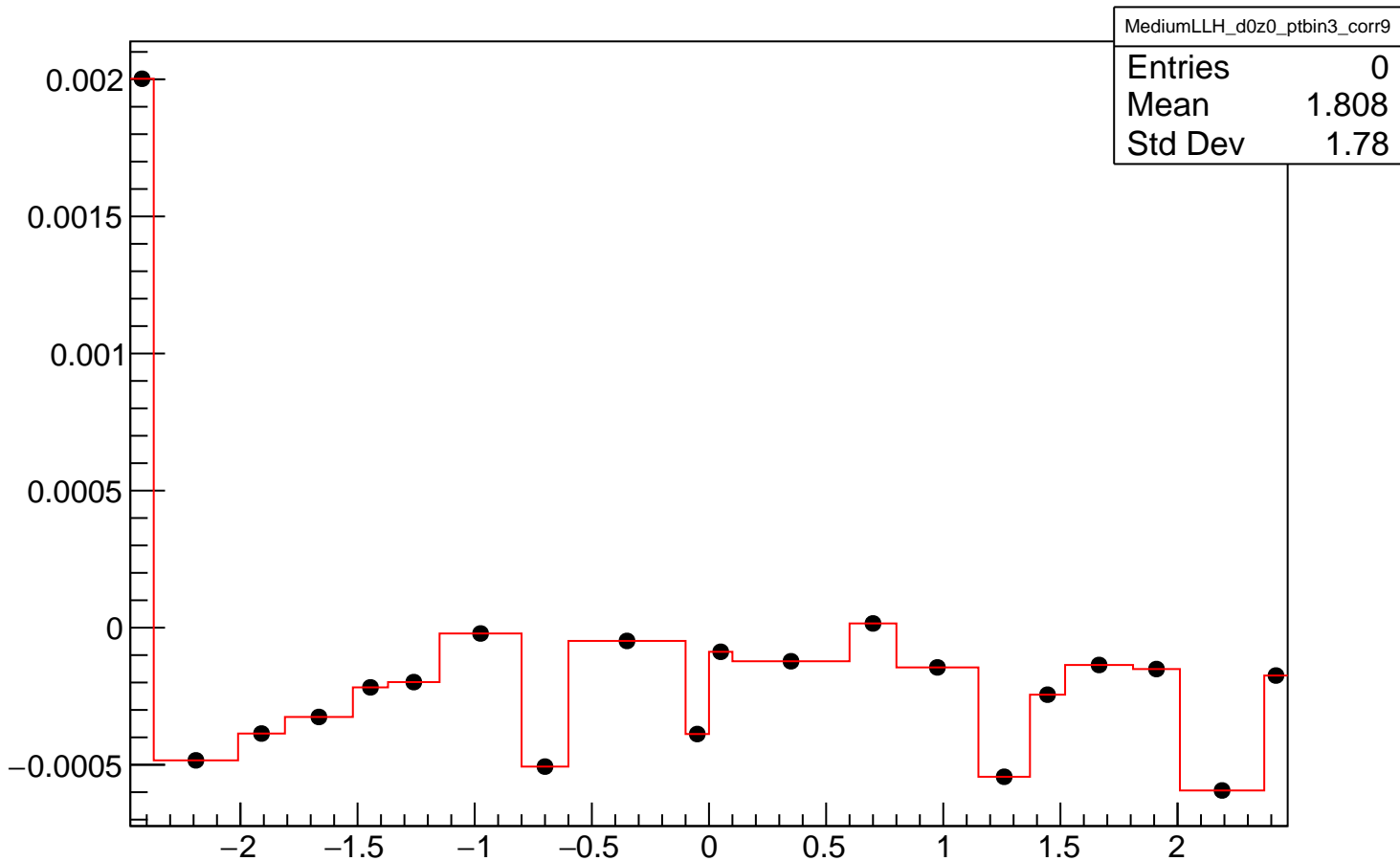
Entries	0
Mean	0.001491
Std Dev	1.526



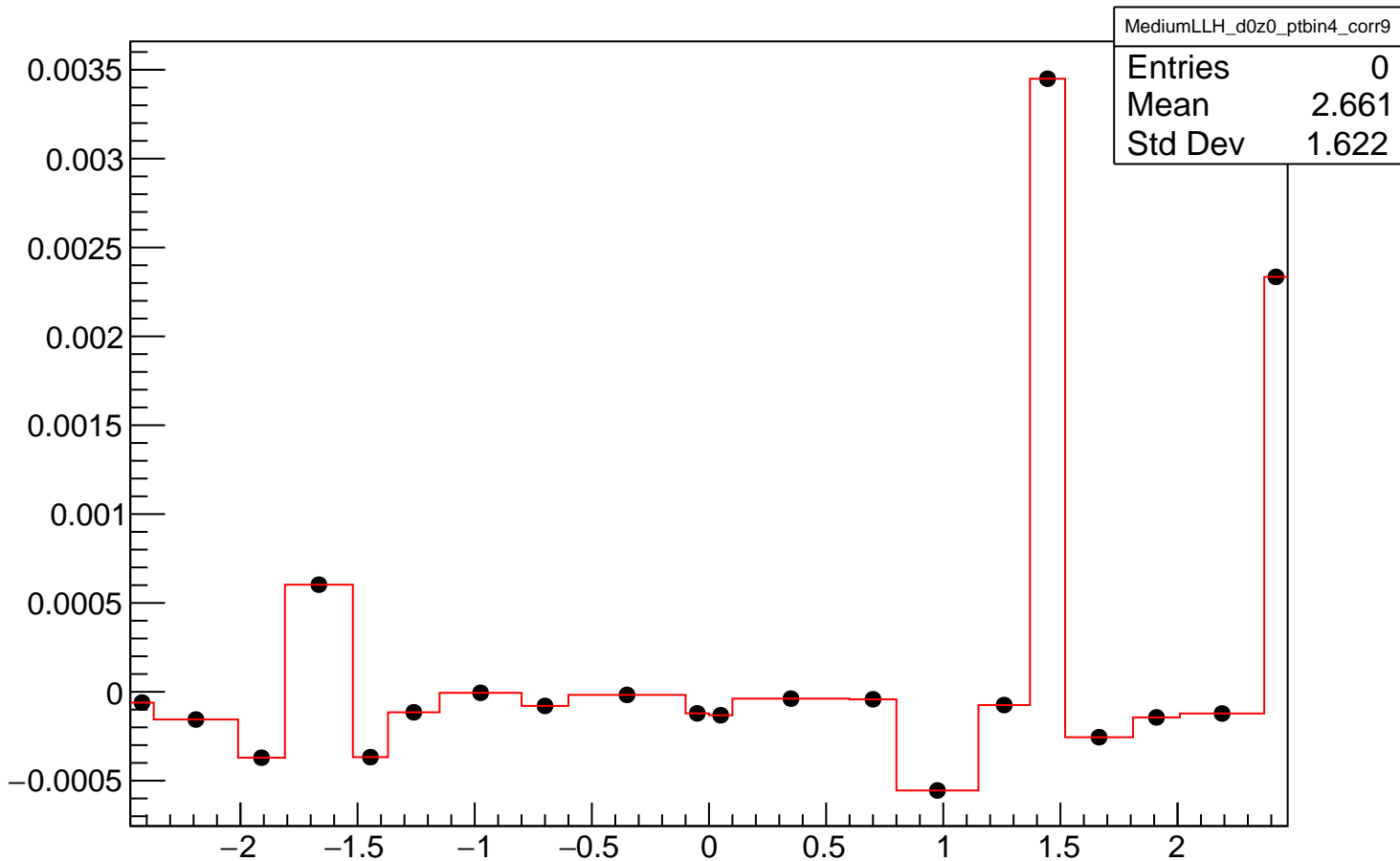
FullSim_MediumLLH_d0z0_corr9



FullSim_MediumLLH_d0z0_corr9



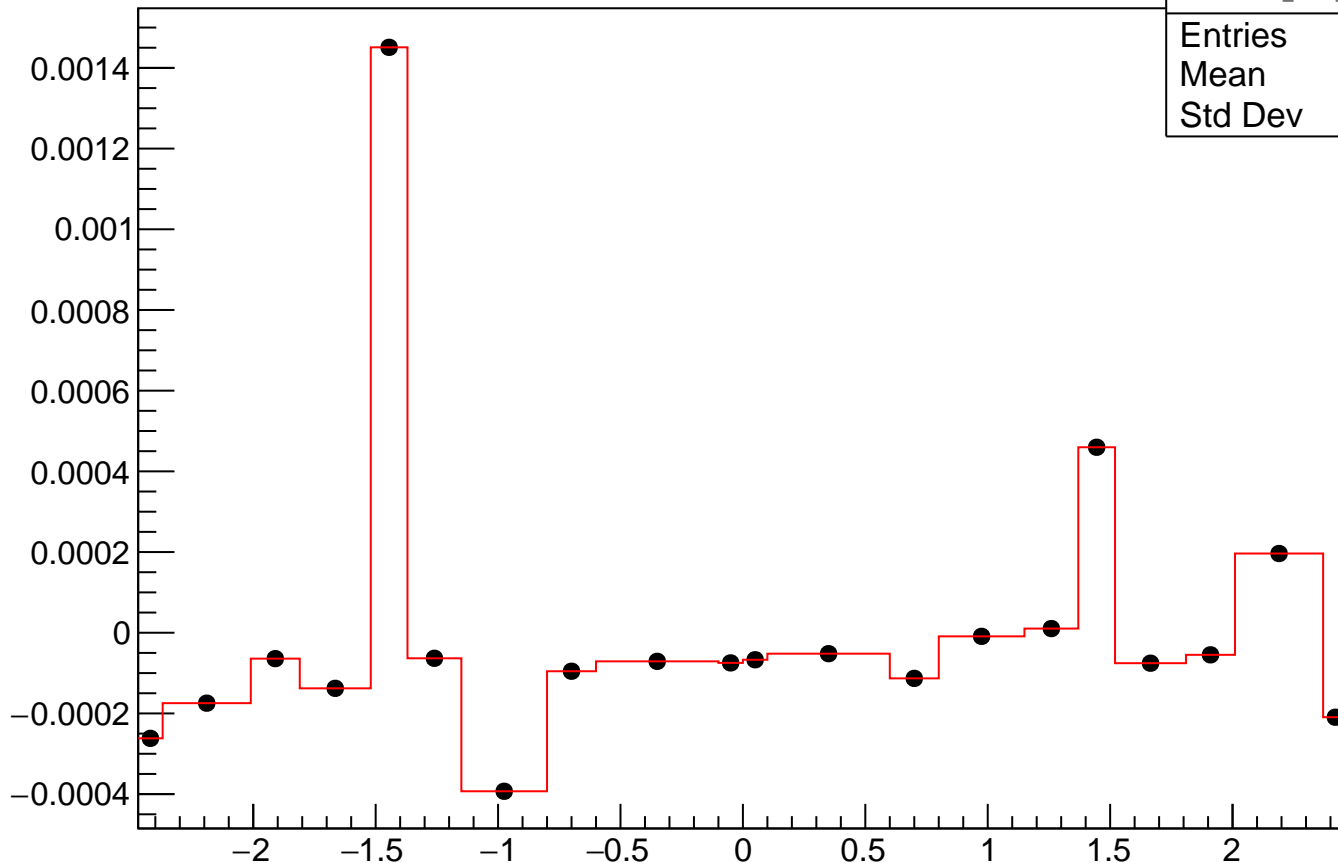
FullSim_MediumLLH_d0z0_corr9



FullSim_MediumLLH_d0z0_corr9

MediumLLH_d0z0_ptbin5_corr9

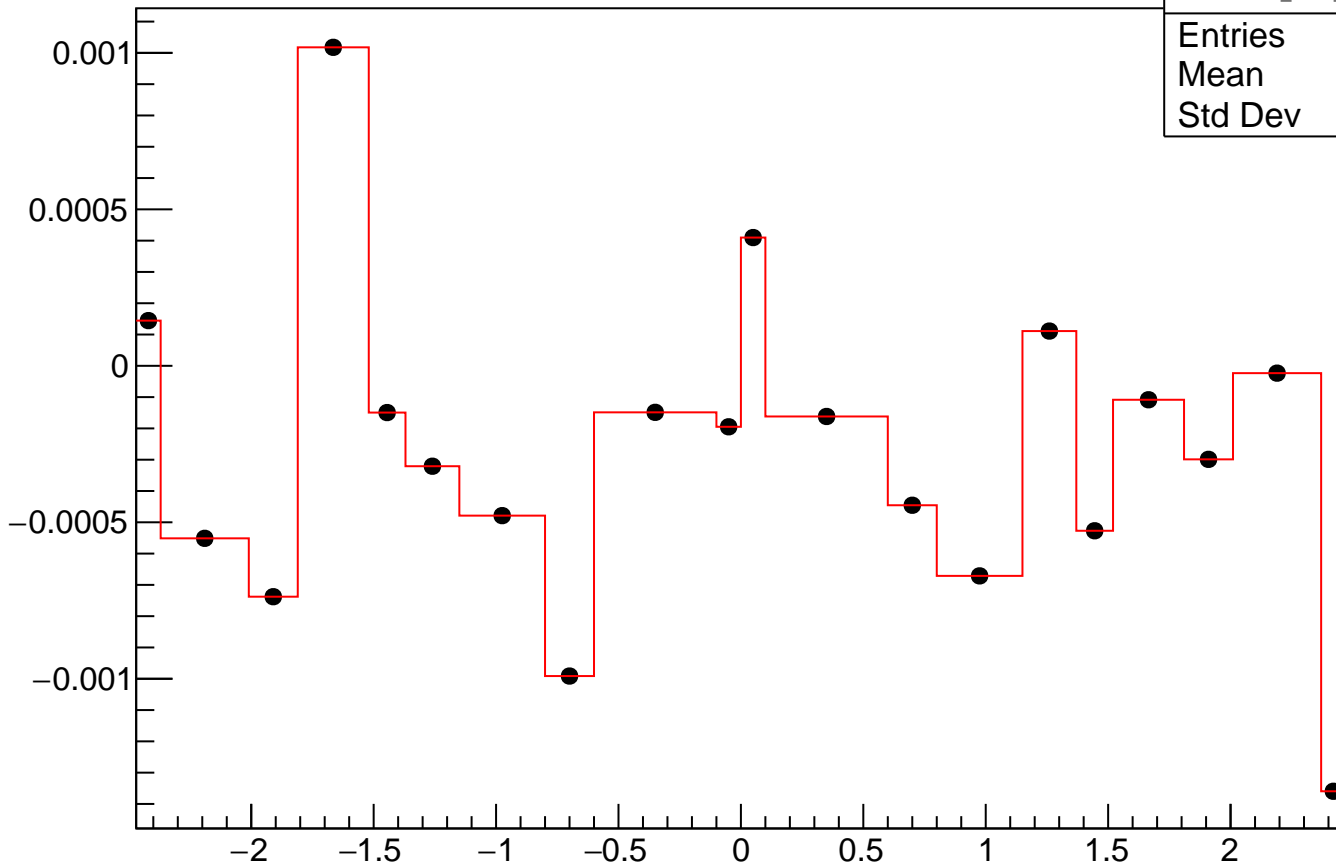
Entries	0
Mean	0.45
Std Dev	1.253



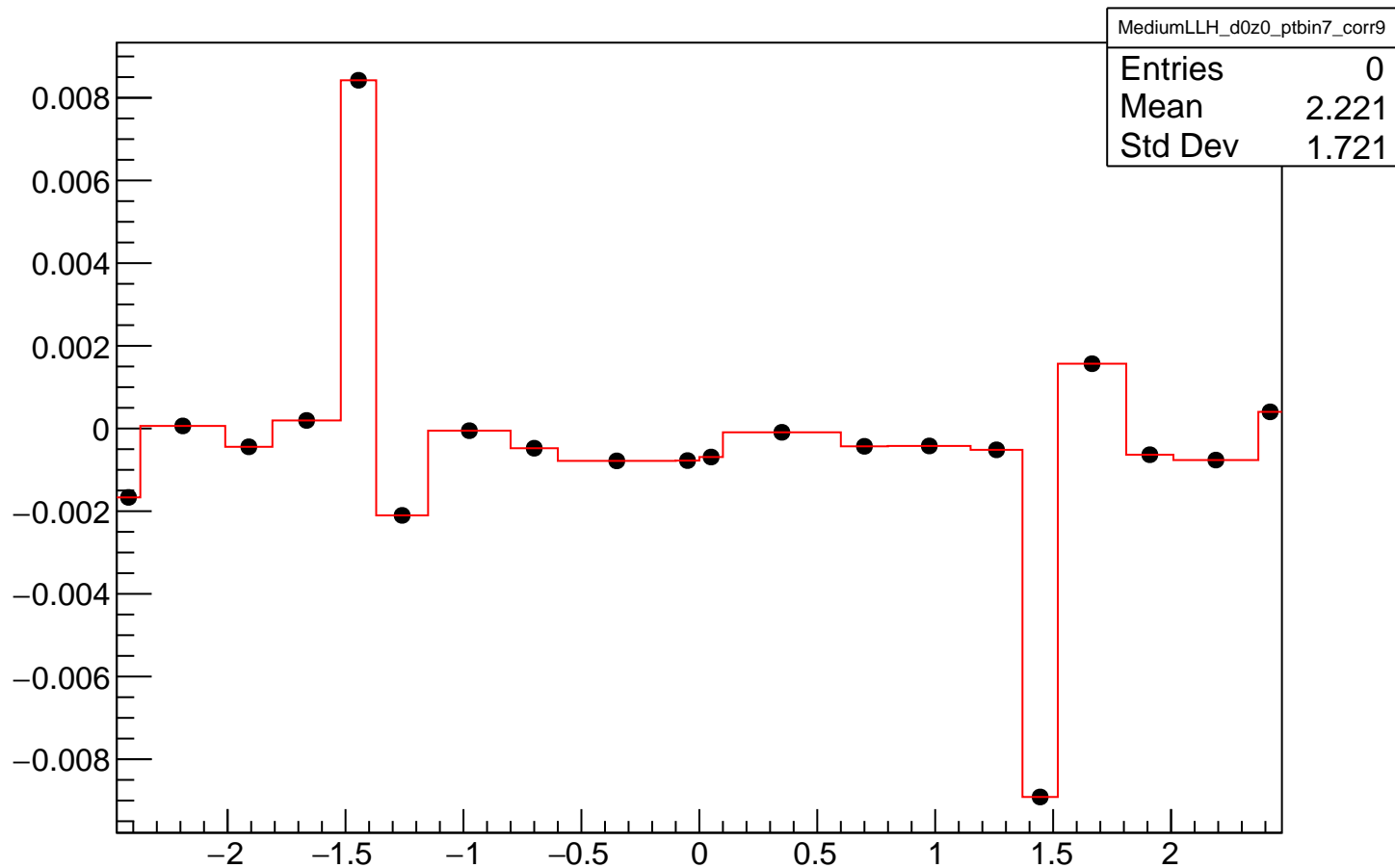
FullSim_MediumLLH_d0z0_corr9

MediumLLH_d0z0_ptbin6_corr9

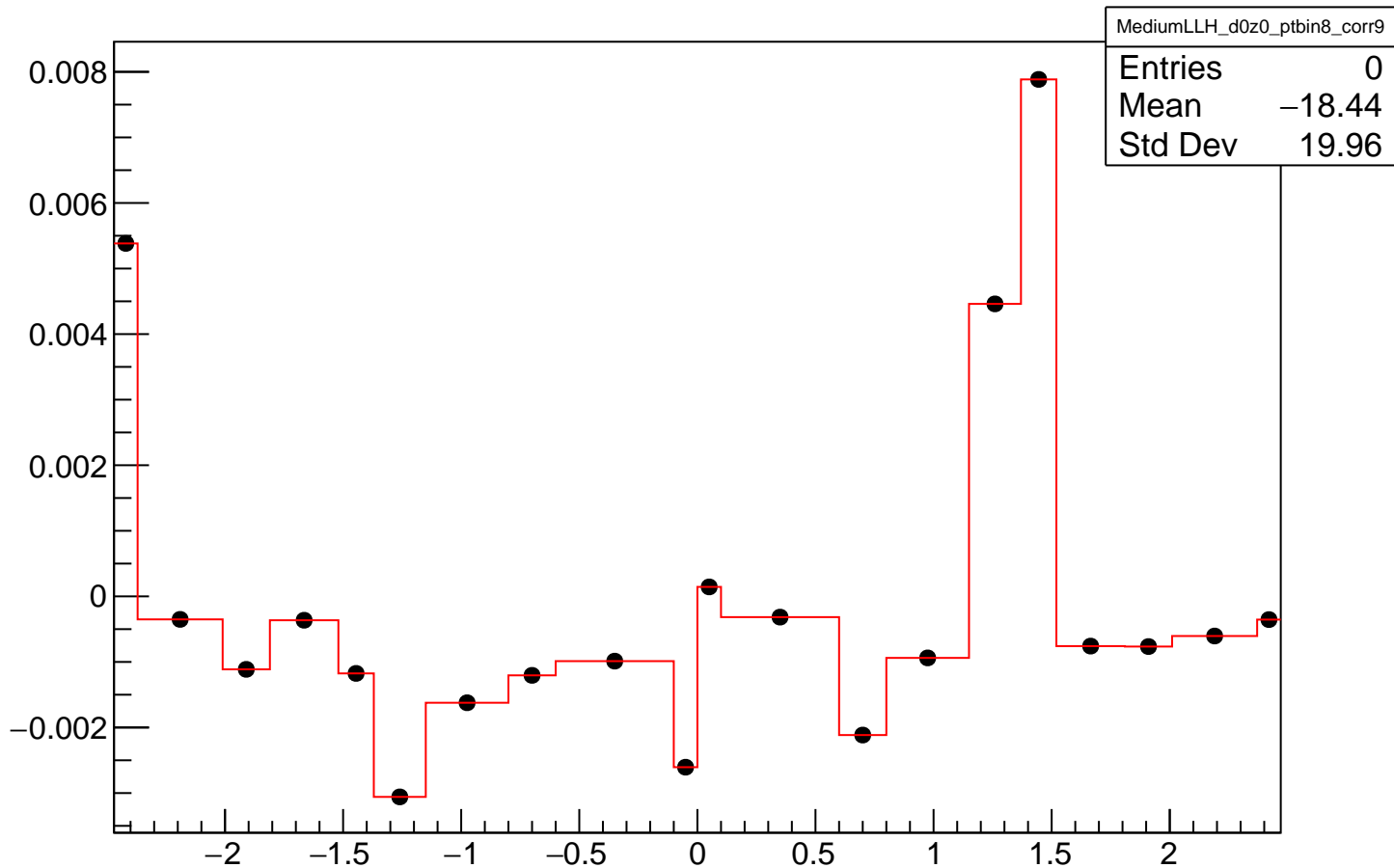
Entries	0
Mean	0.602
Std Dev	1.523



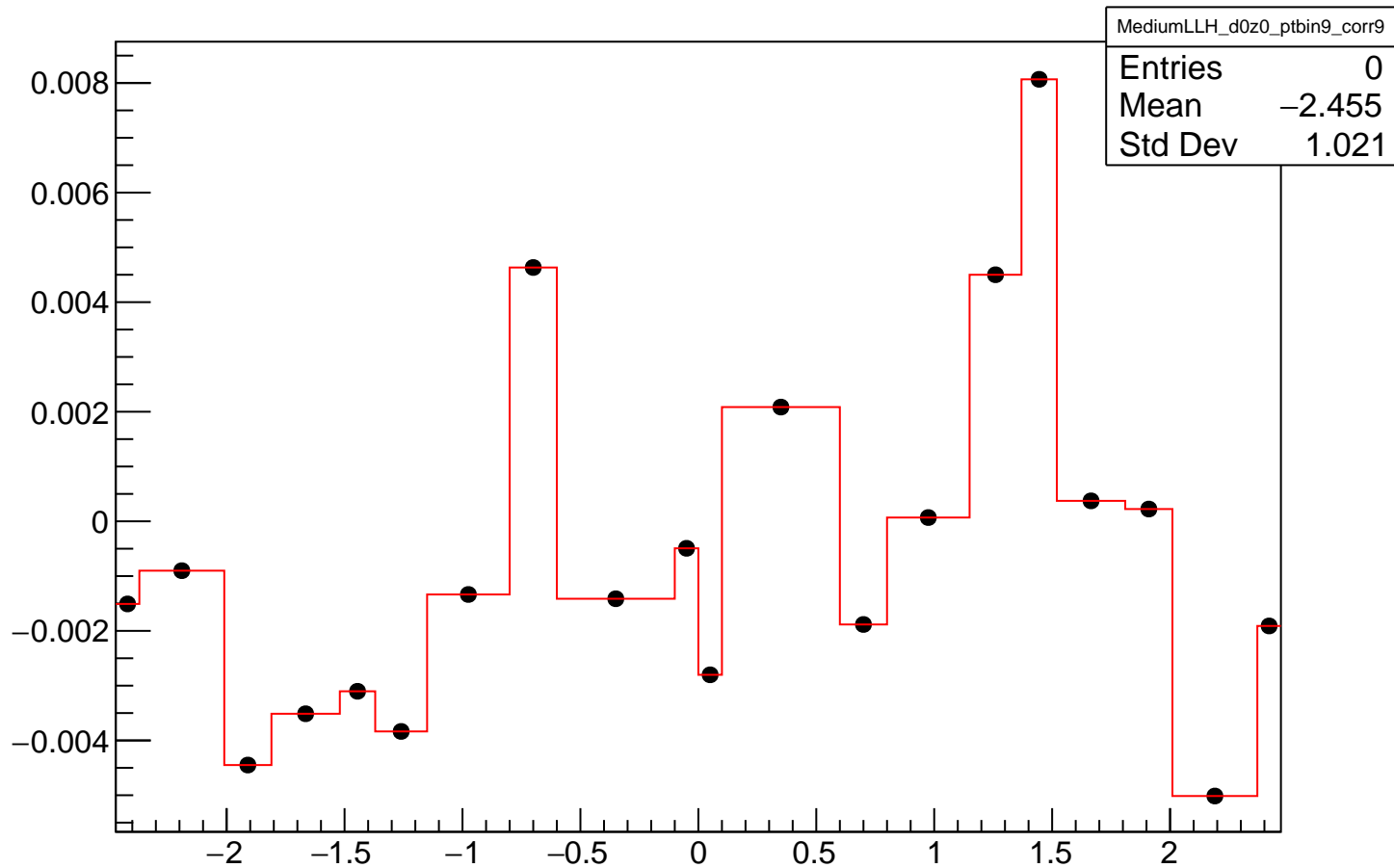
FullSim_MediumLLH_d0z0_corr9



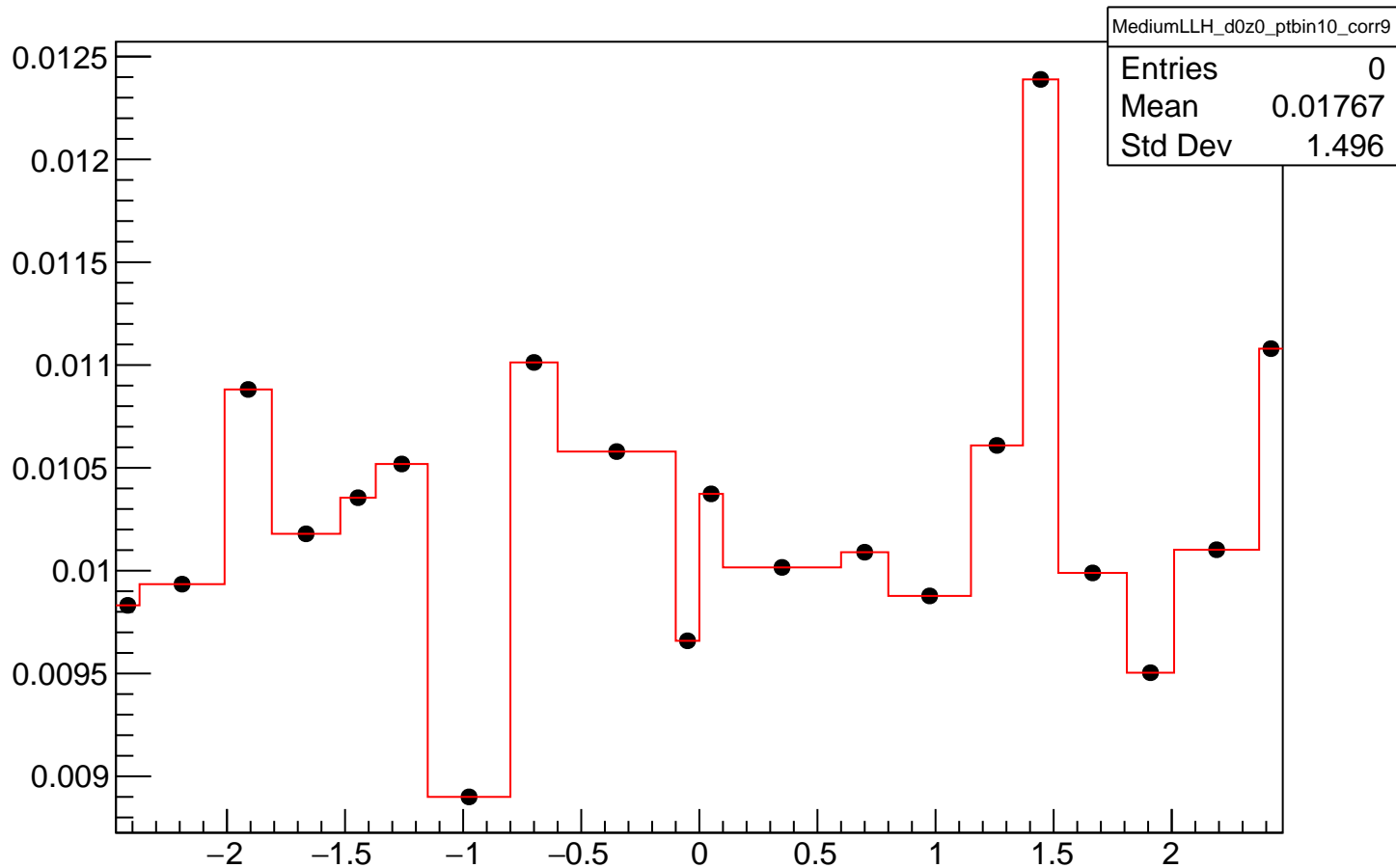
FullSim_MediumLLH_d0z0_corr9



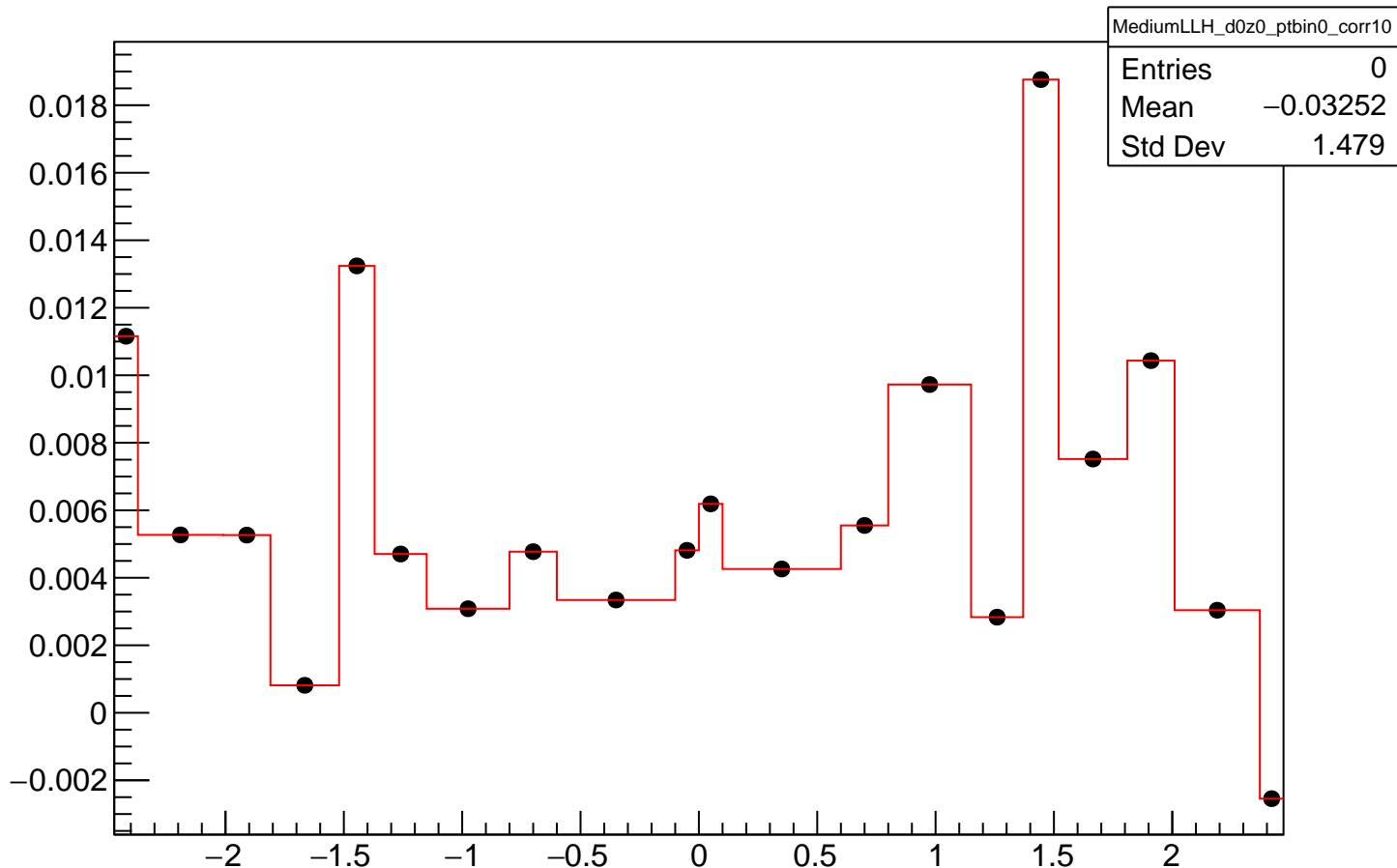
FullSim_MediumLLH_d0z0_corr9



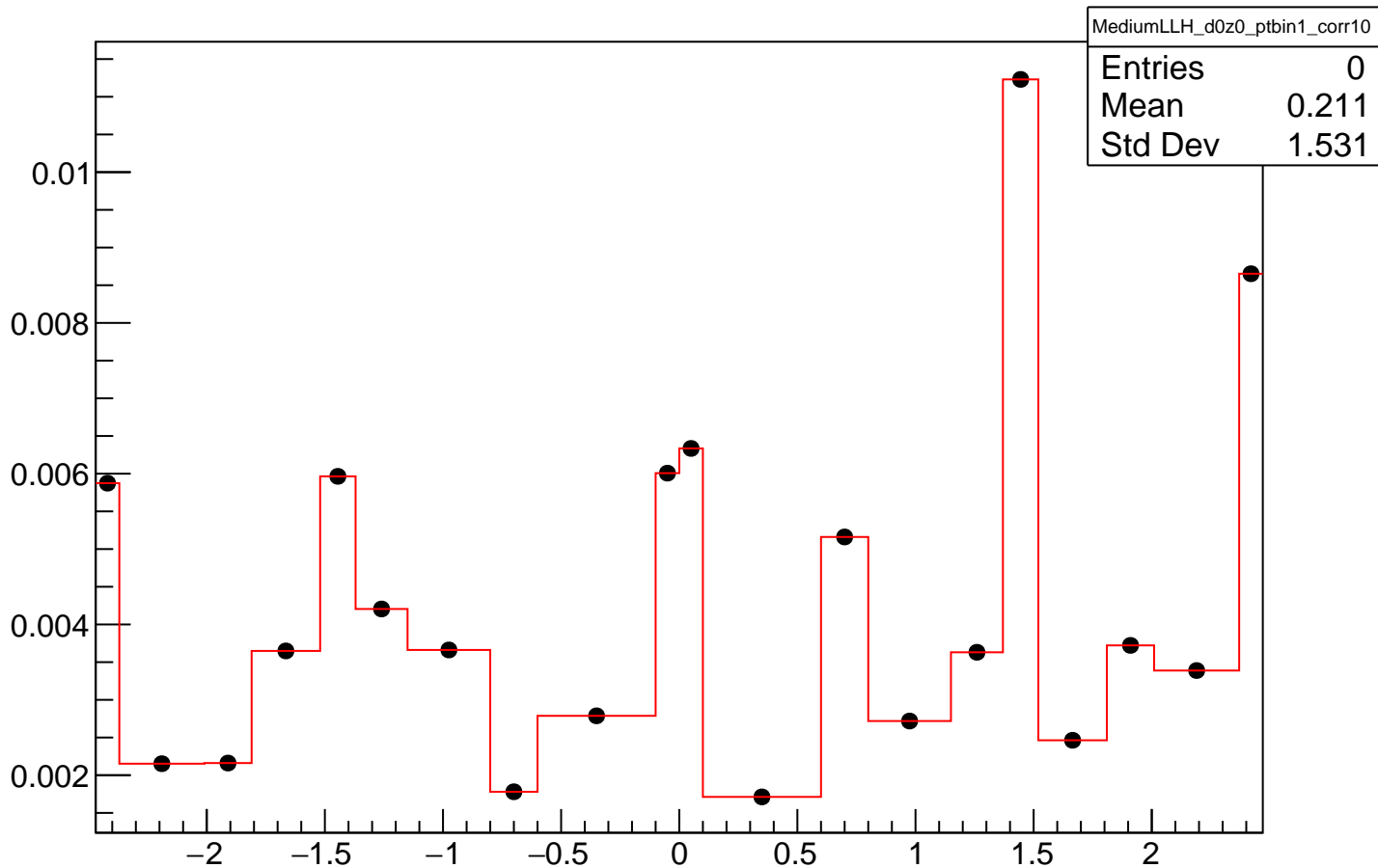
FullSim_MediumLLH_d0z0_corr9



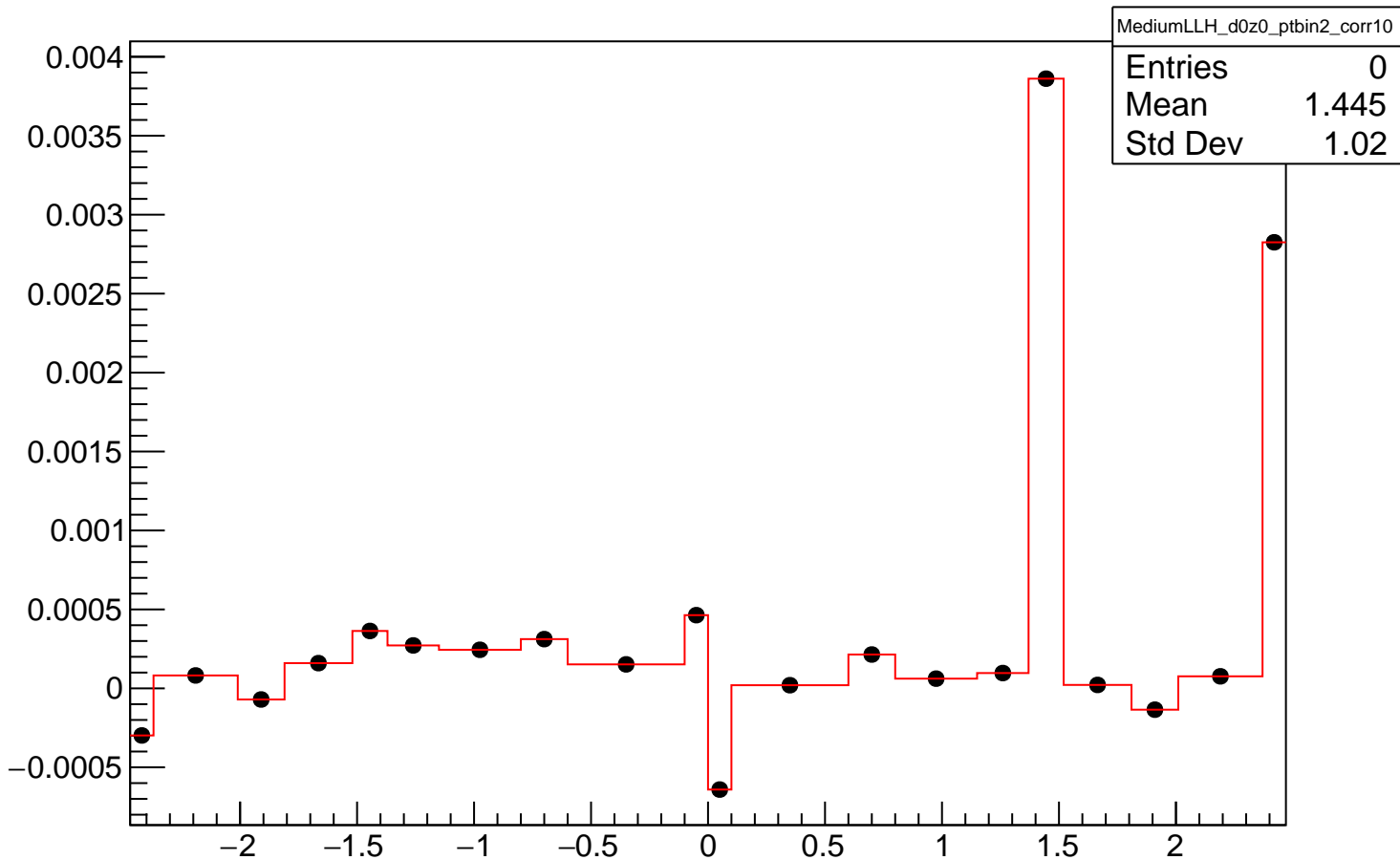
FullSim_MediumLLH_d0z0_corr10



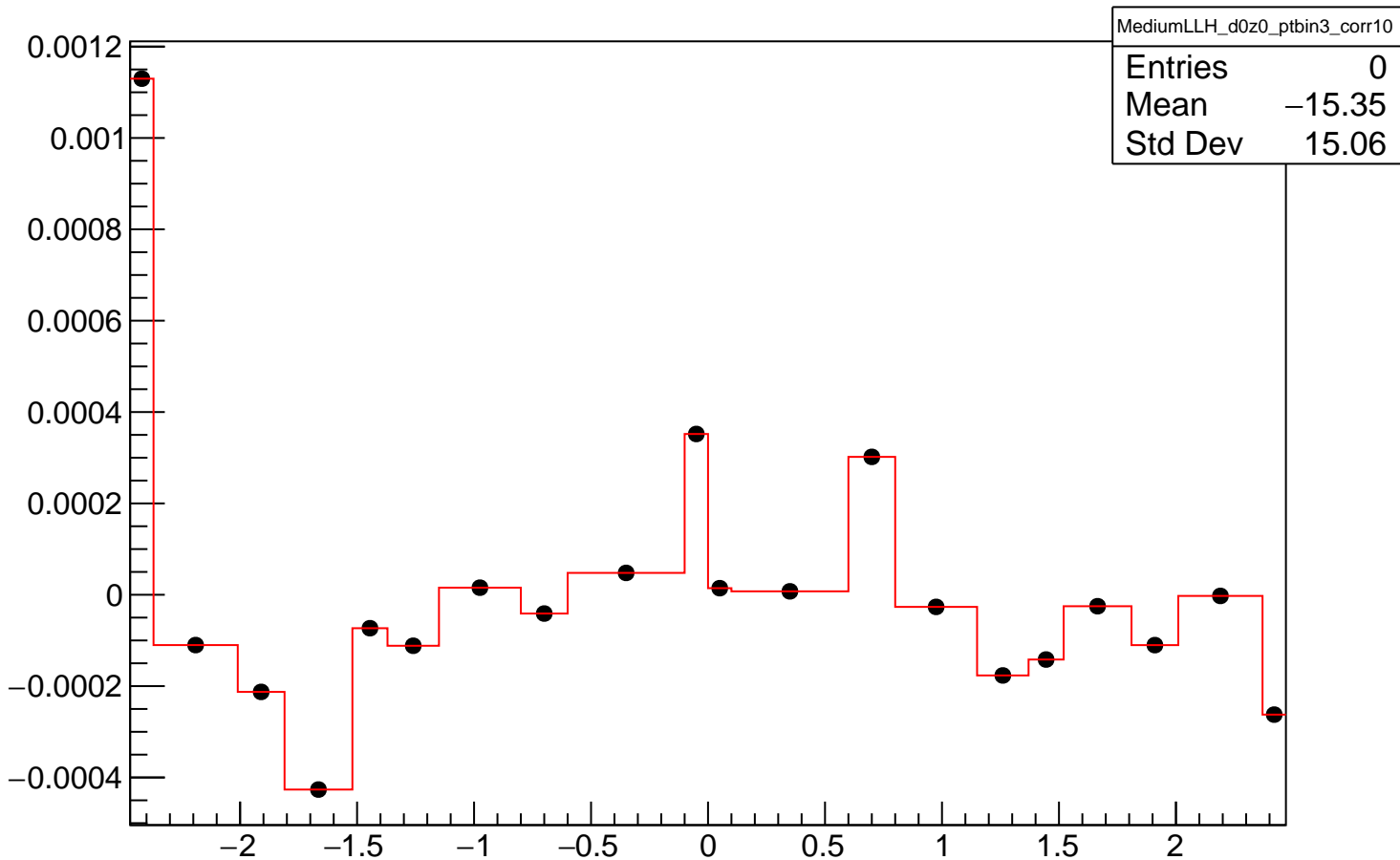
FullSim_MediumLLH_d0z0_corr10



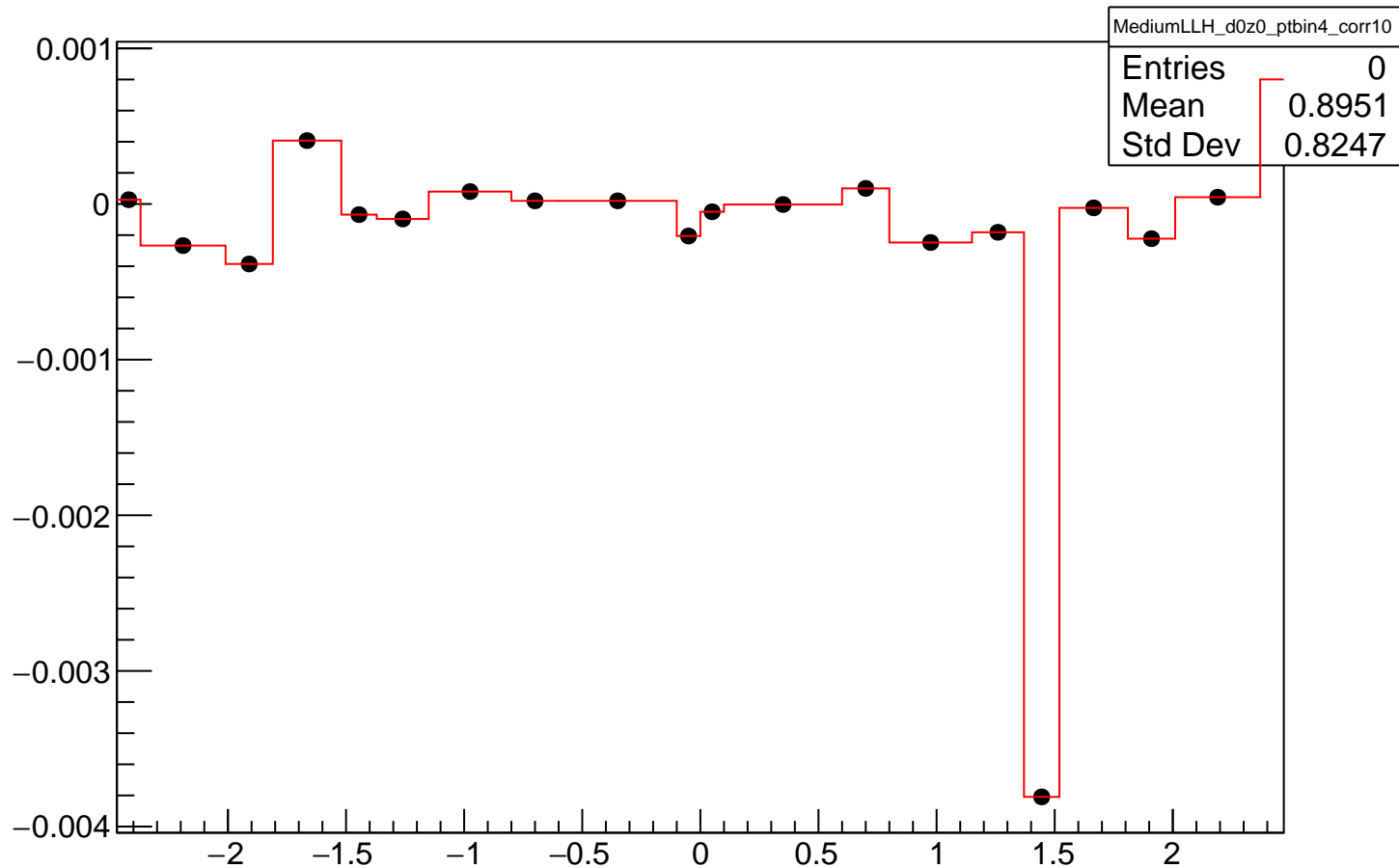
FullSim_MediumLLH_d0z0_corr10



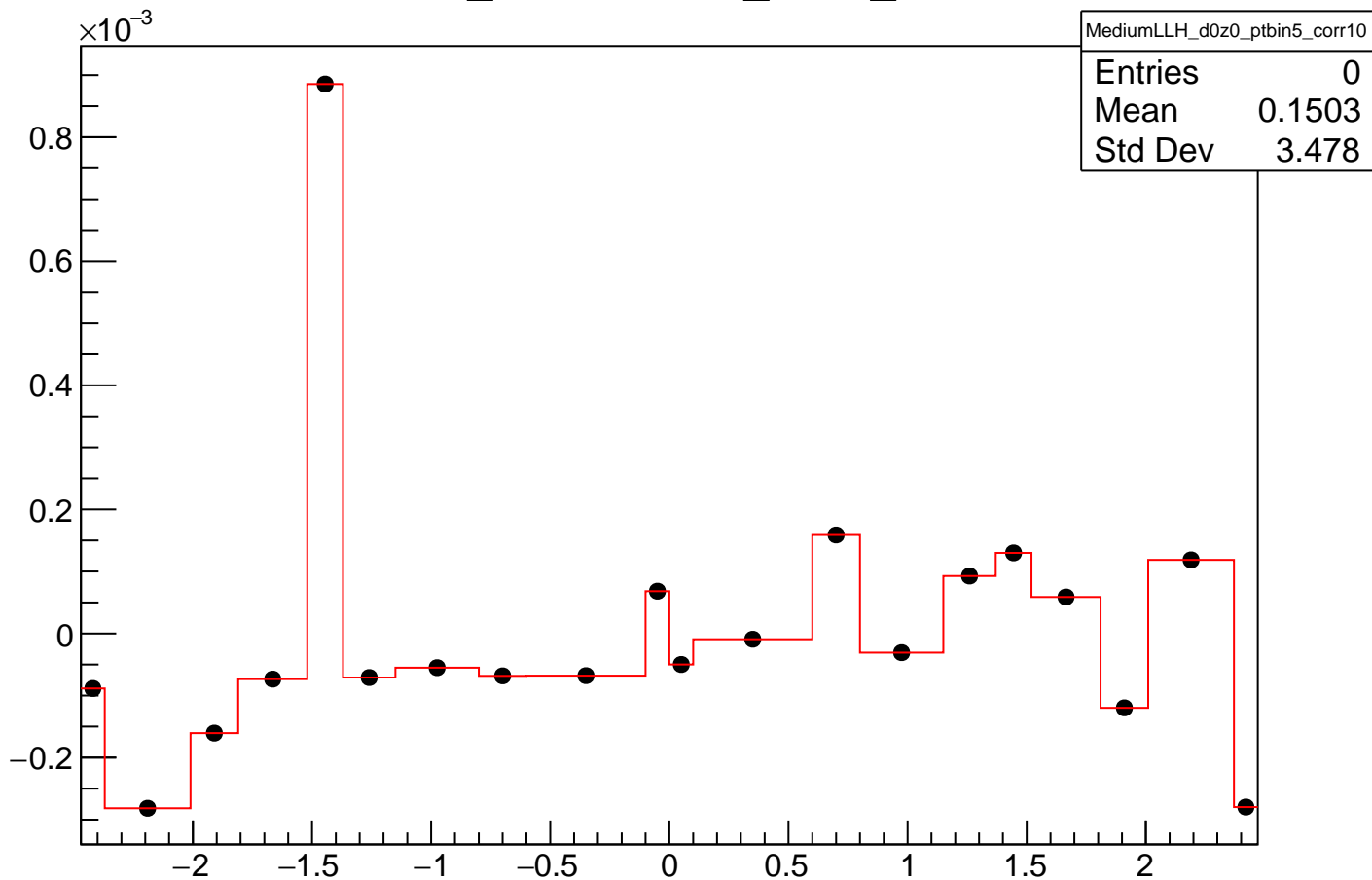
FullSim_MediumLLH_d0z0_corr10



FullSim_MediumLLH_d0z0_corr10

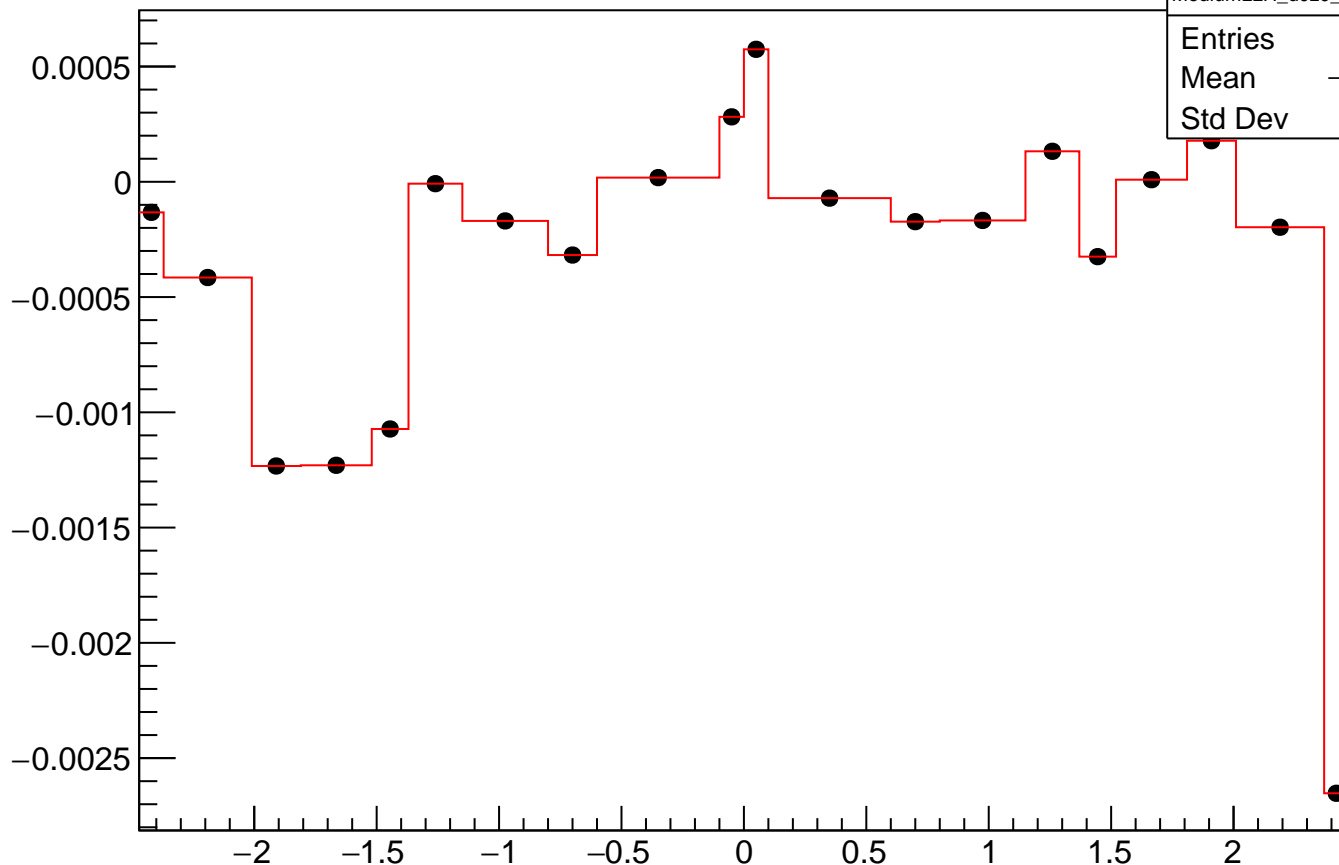


FullSim_MediumLLH_d0z0_corr10

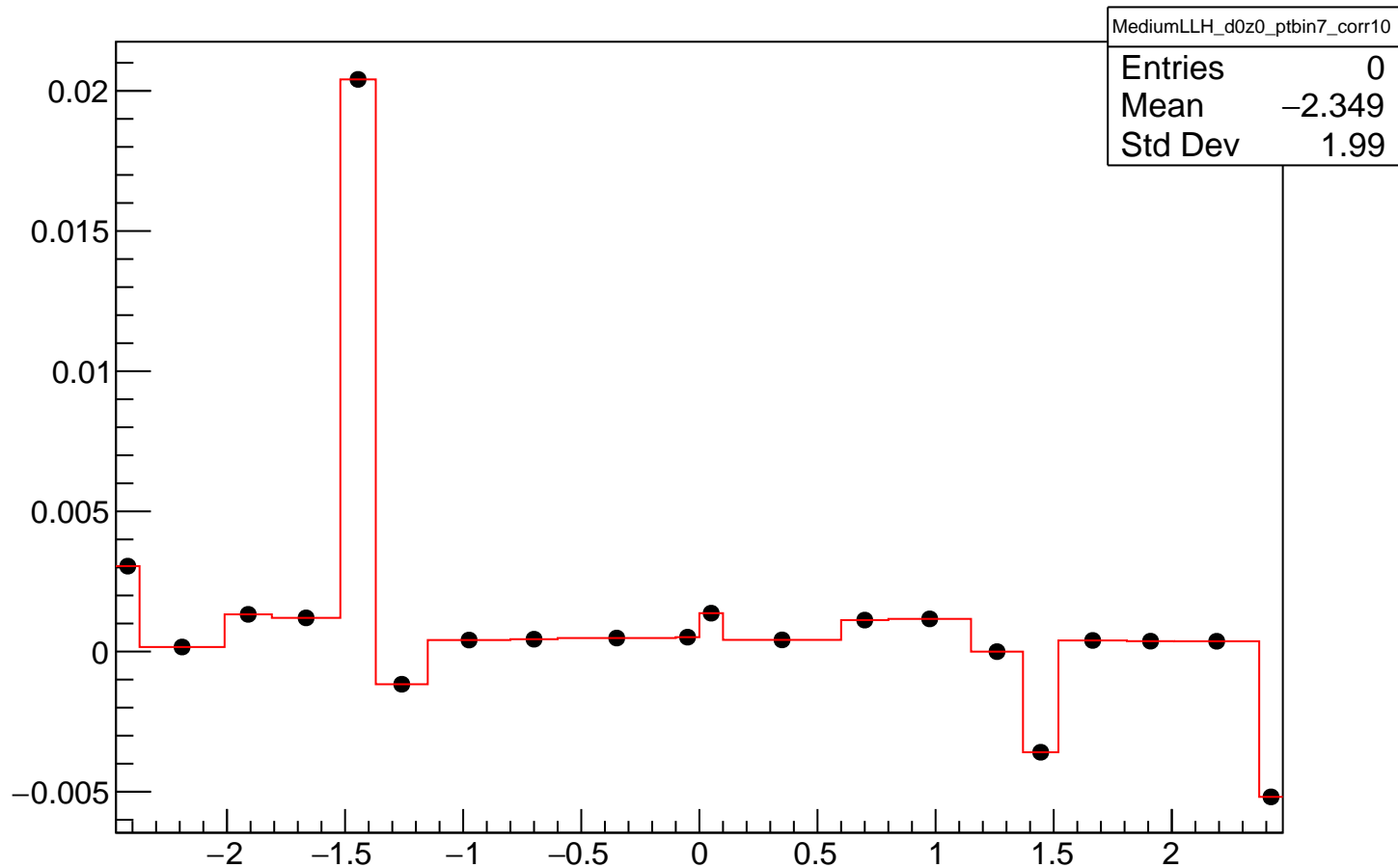


FullSim_MediumLLH_d0z0_corr10

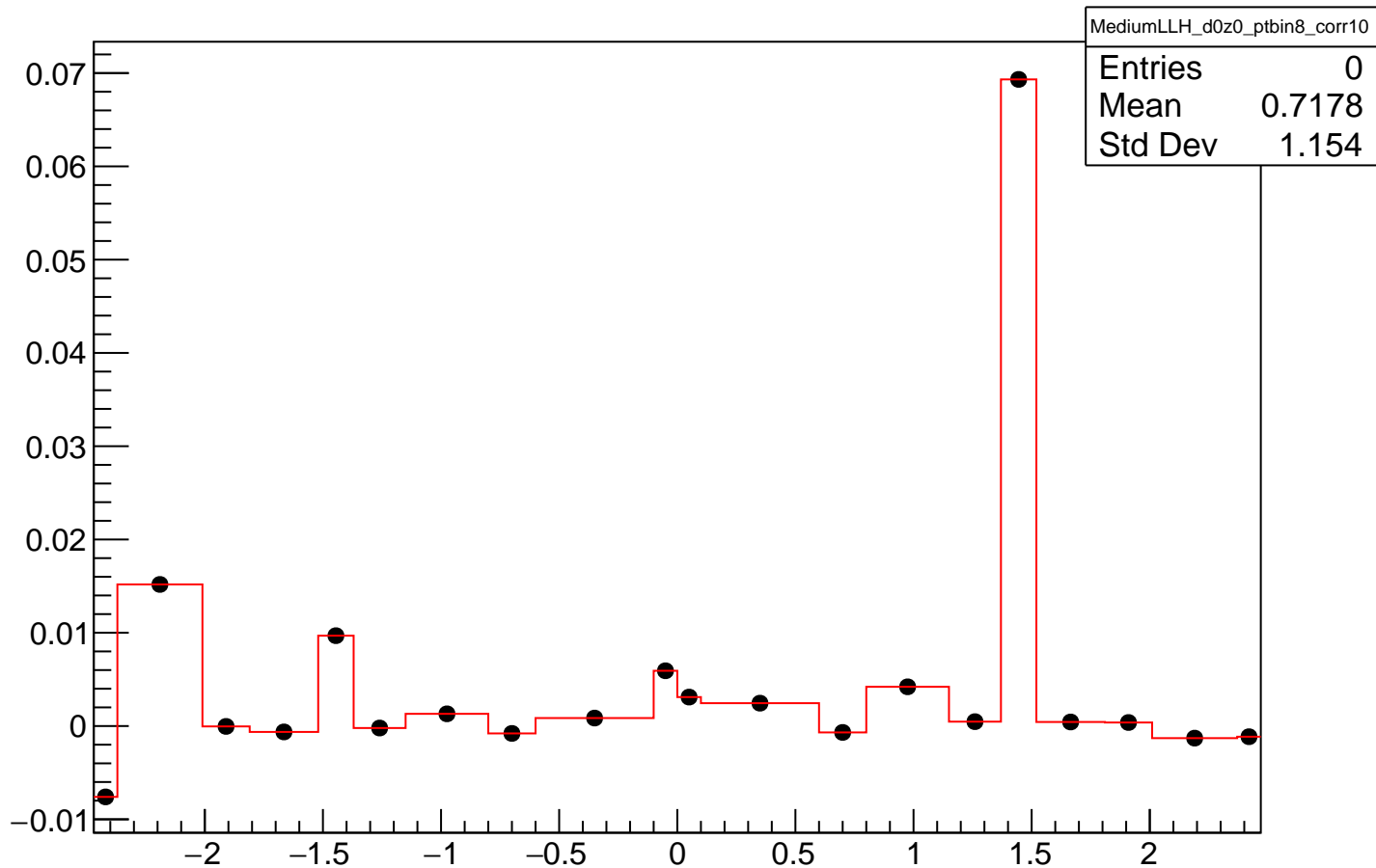
MediumLLH_d0z0_ptbin6_corr10	
Entries	0
Mean	-0.06905
Std Dev	2.065



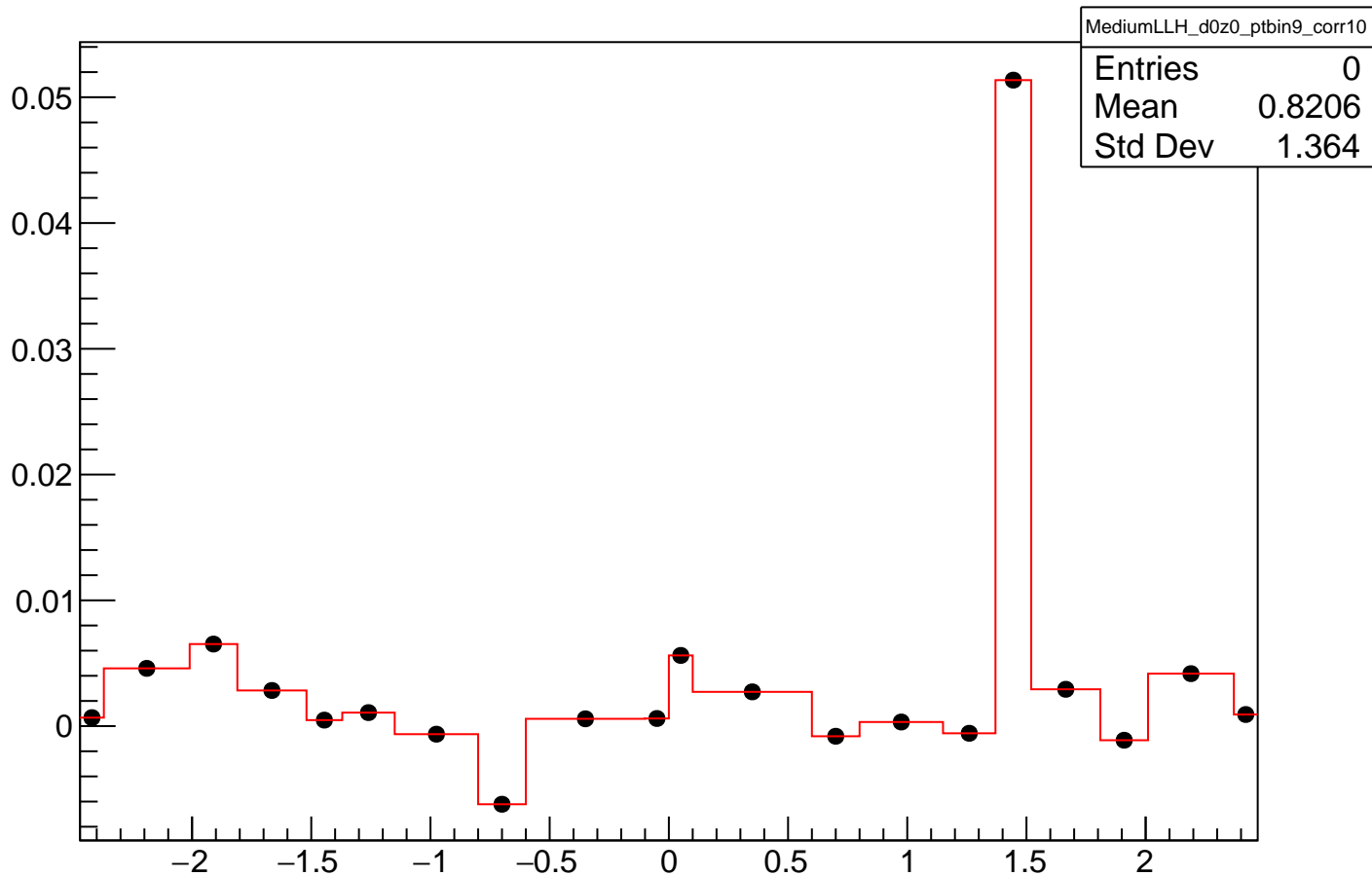
FullSim_MediumLLH_d0z0_corr10



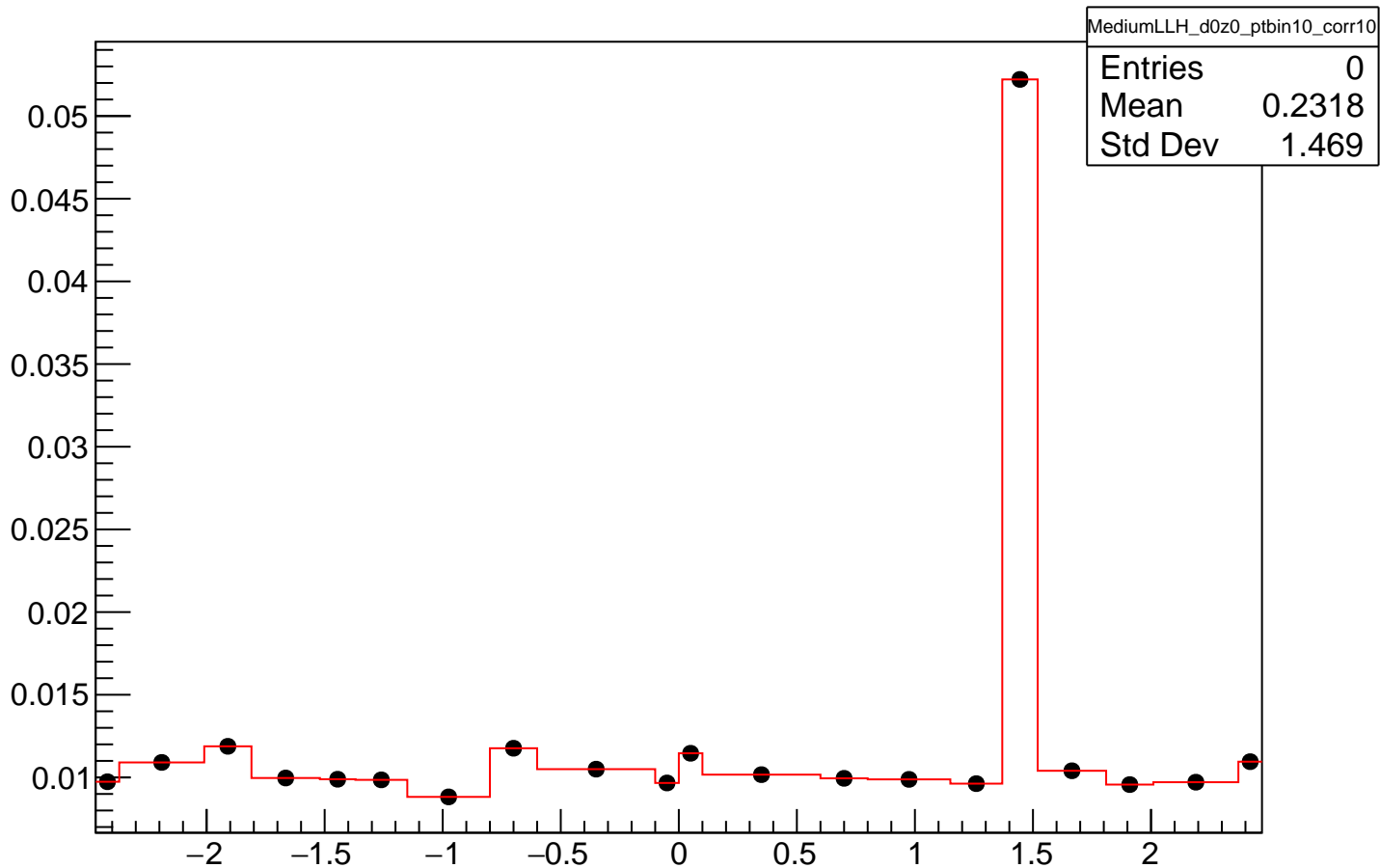
FullSim_MediumLLH_d0z0_corr10



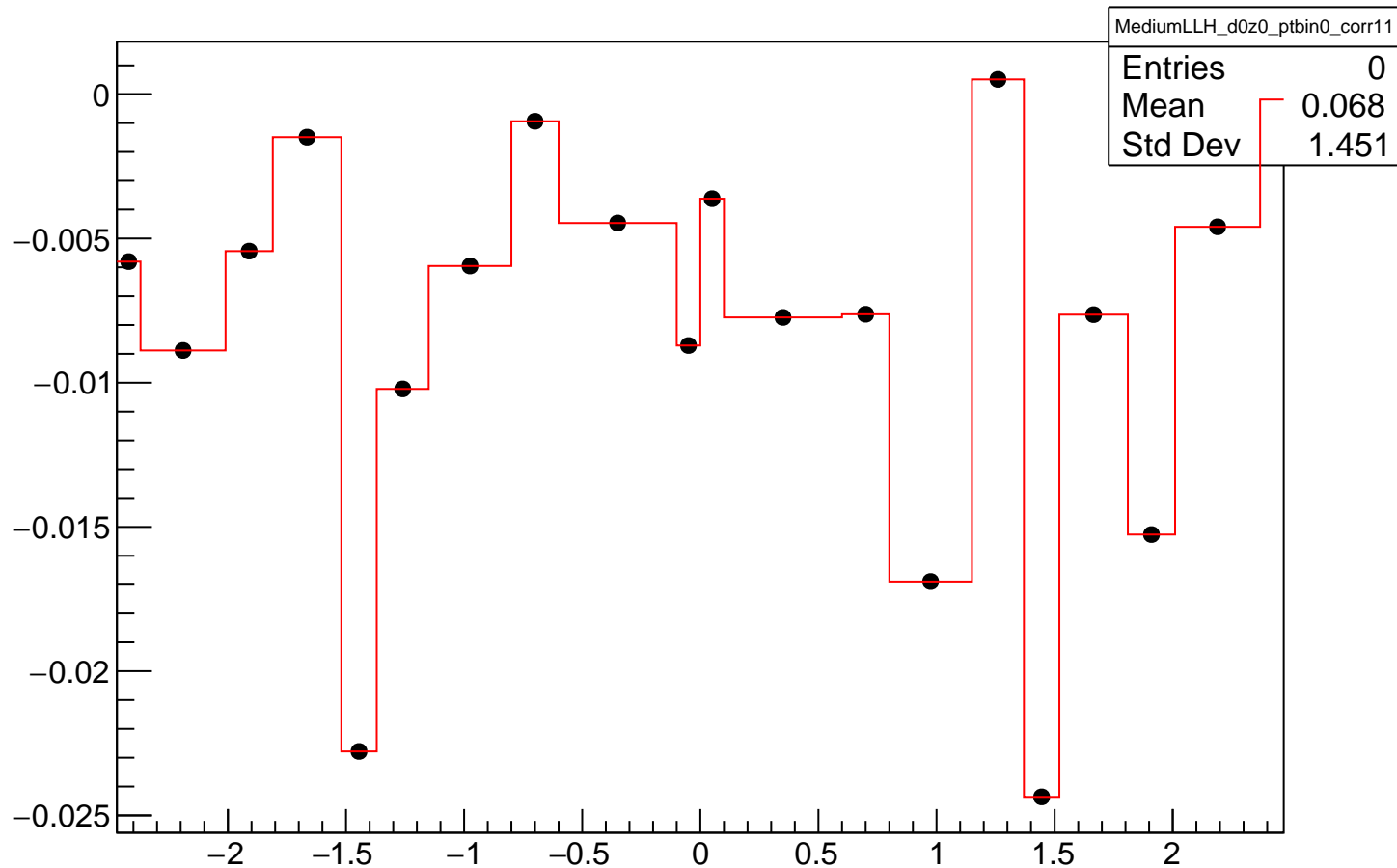
FullSim_MediumLLH_d0z0_corr10



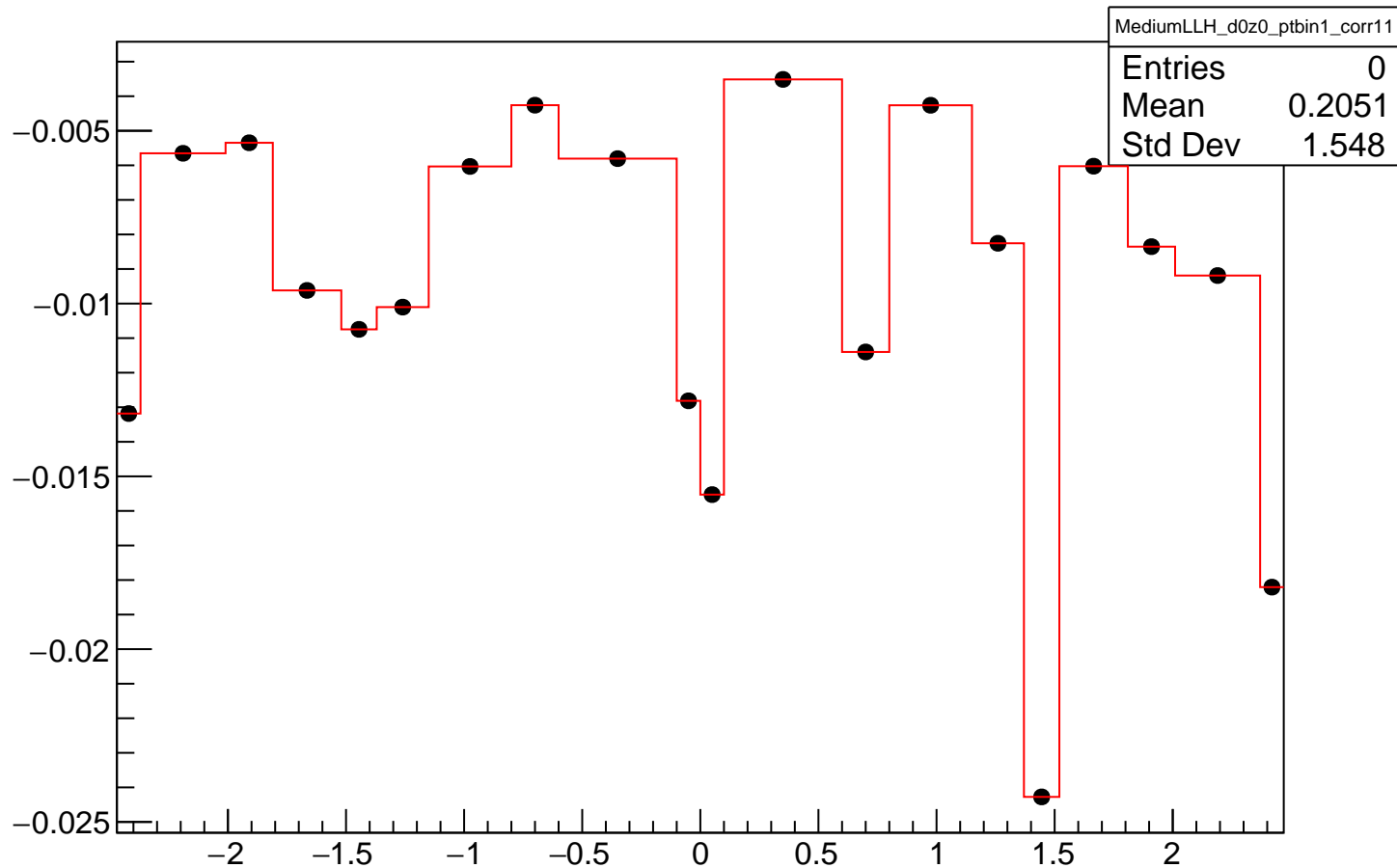
FullSim_MediumLLH_d0z0_corr10



FullSim_MediumLLH_d0z0_corr11

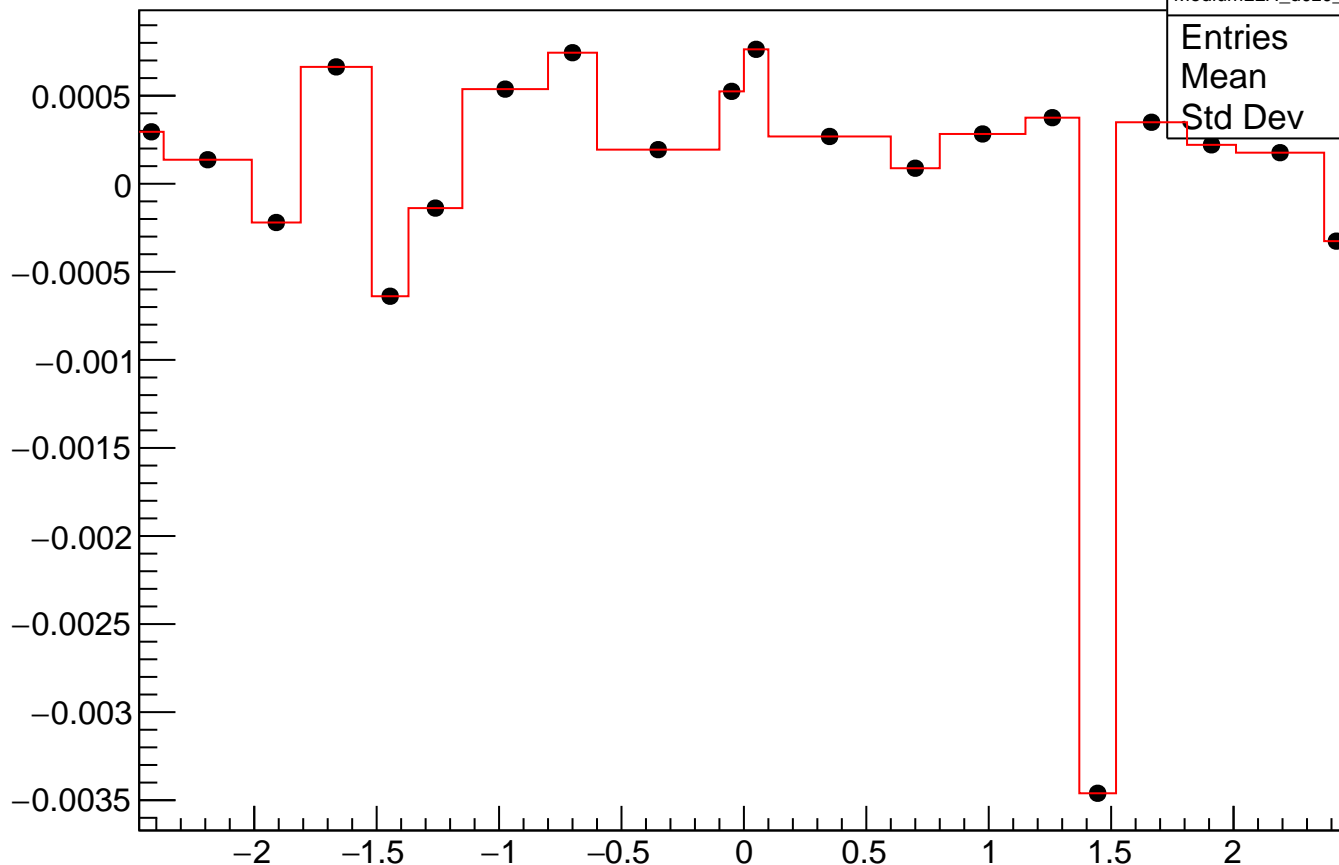


FullSim_MediumLLH_d0z0_corr11

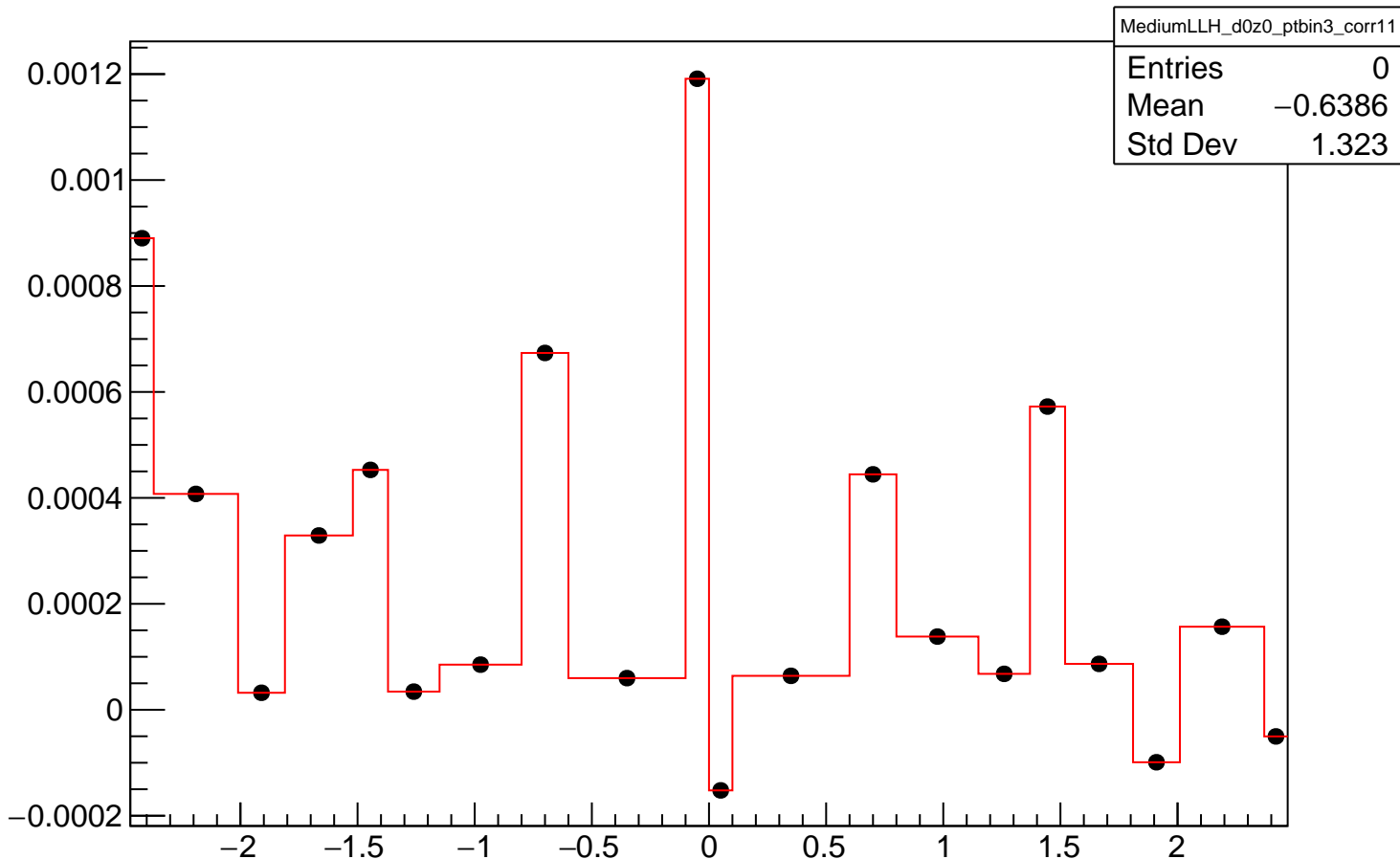


FullSim_MediumLLH_d0z0_corr11

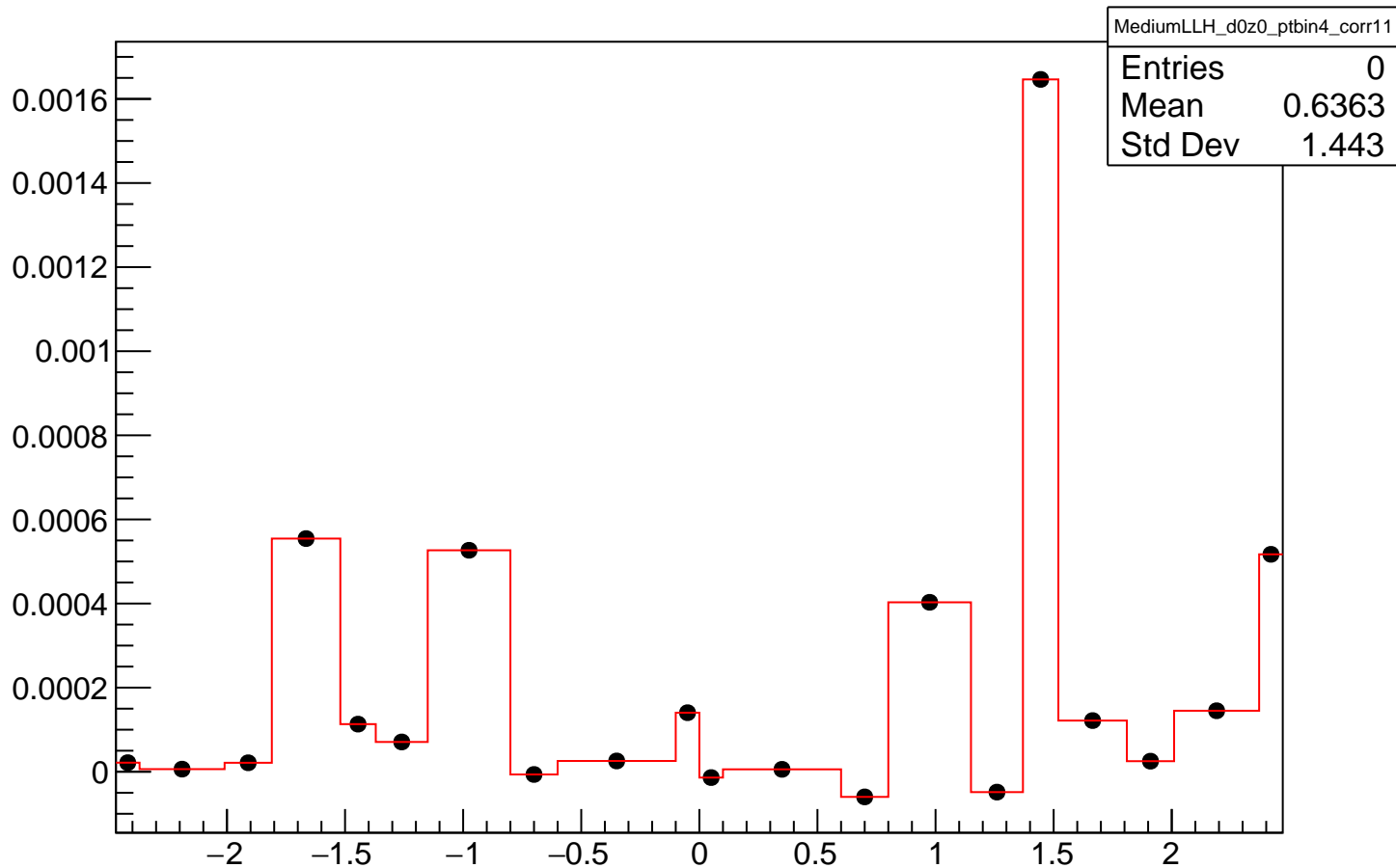
MediumLLH_d0z0_ptbin2_corr11	
Entries	0
Mean	-6.212
Std Dev	6.476



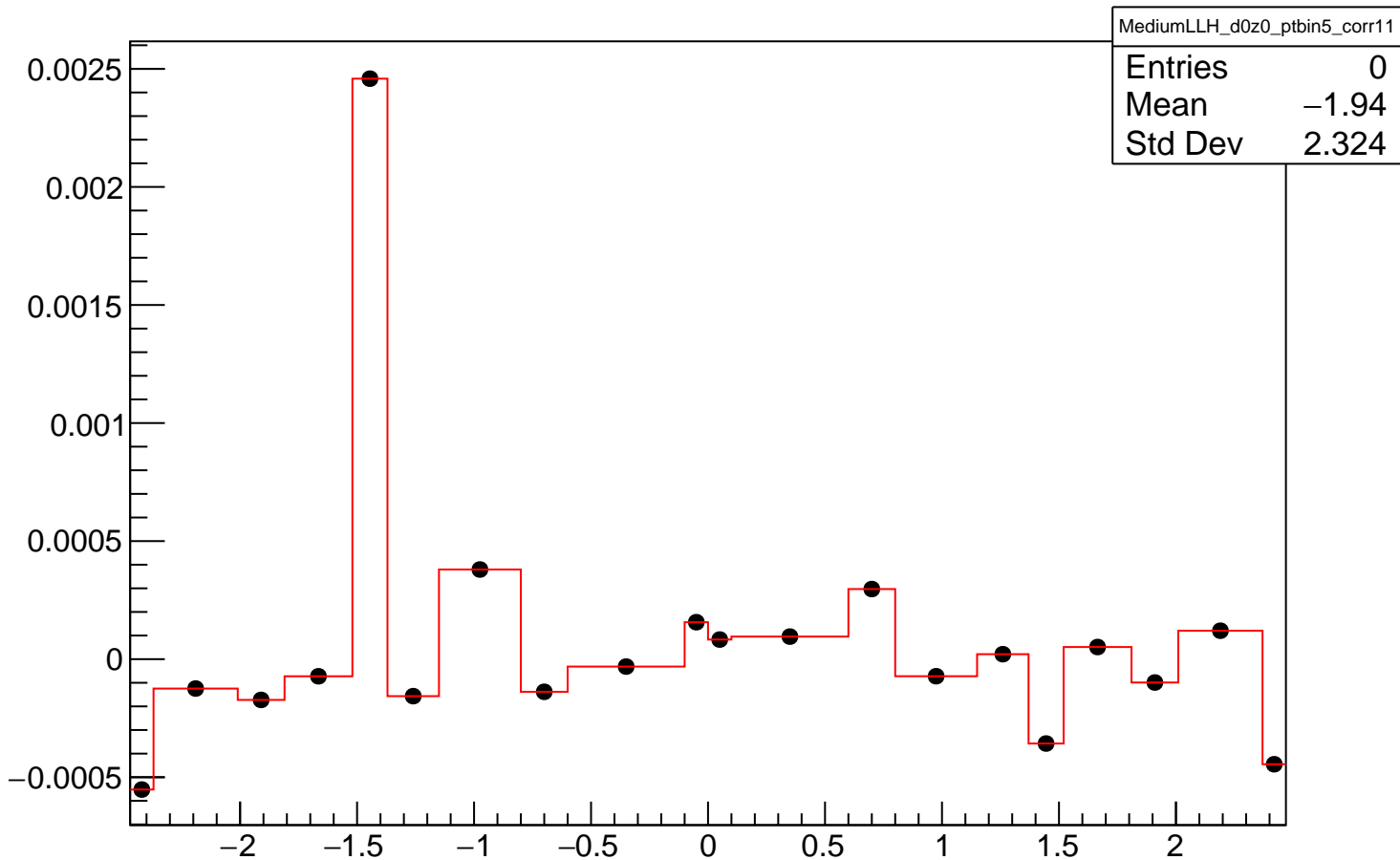
FullSim_MediumLLH_d0z0_corr11



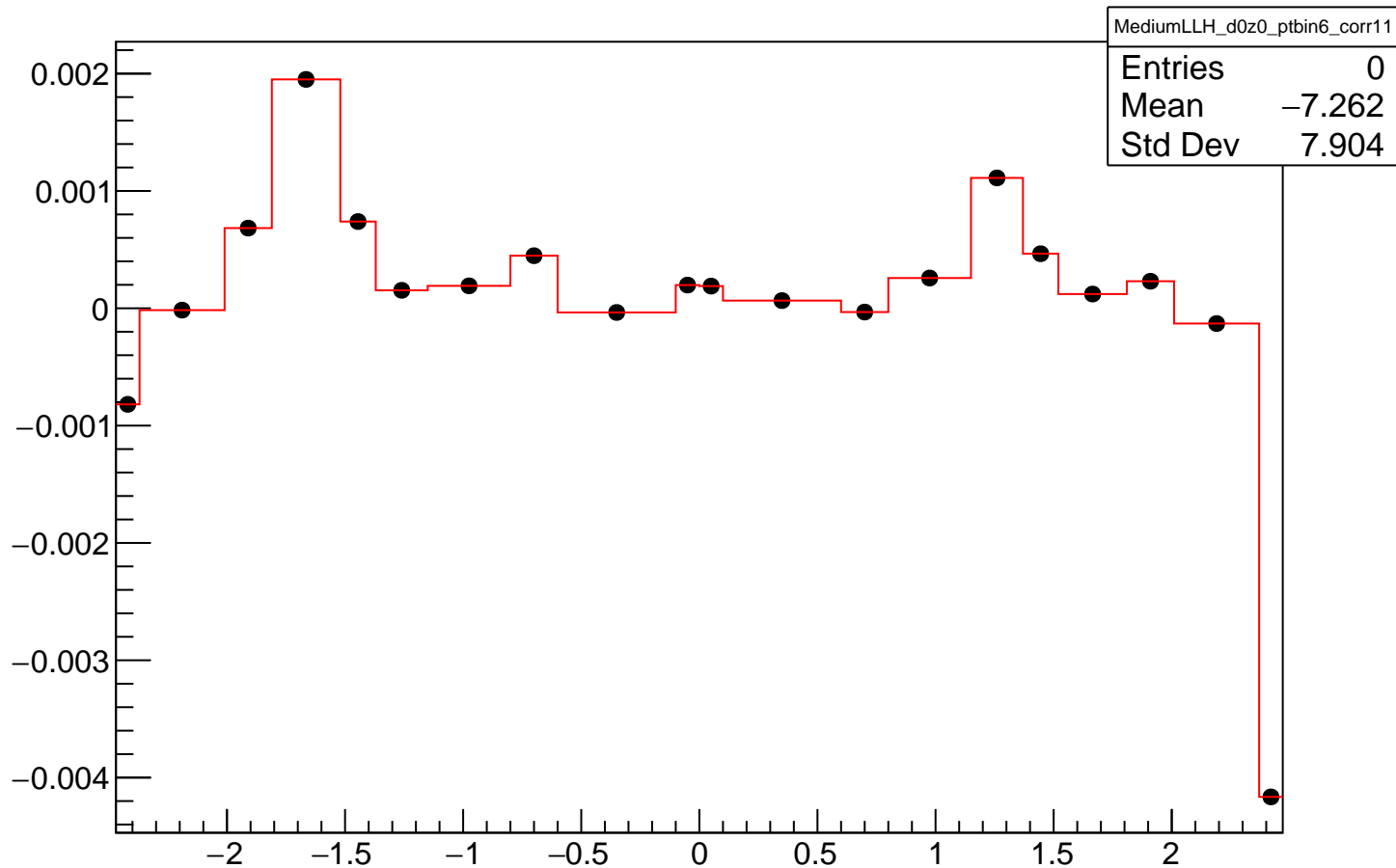
FullSim_MediumLLH_d0z0_corr11



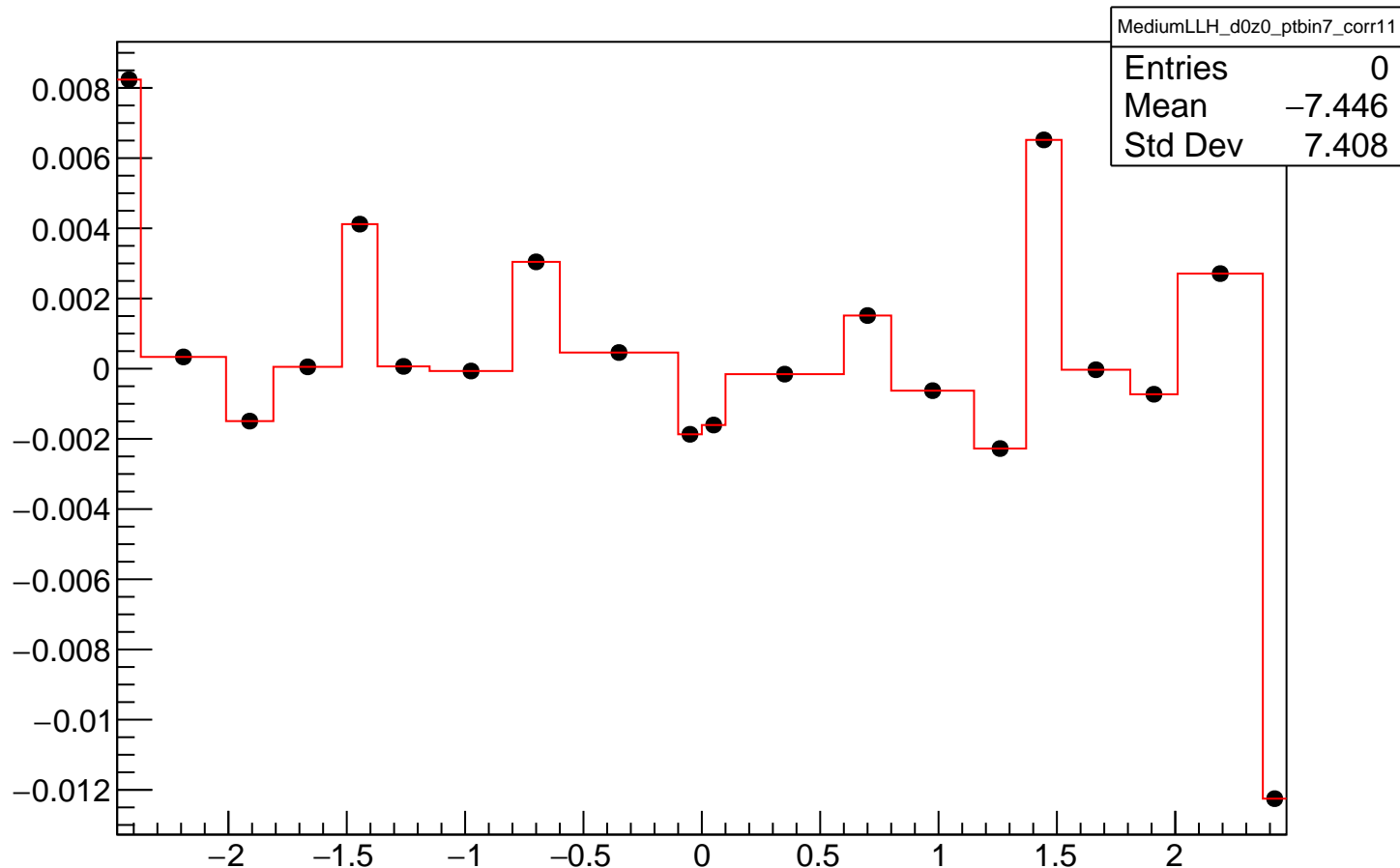
FullSim_MediumLLH_d0z0_corr11



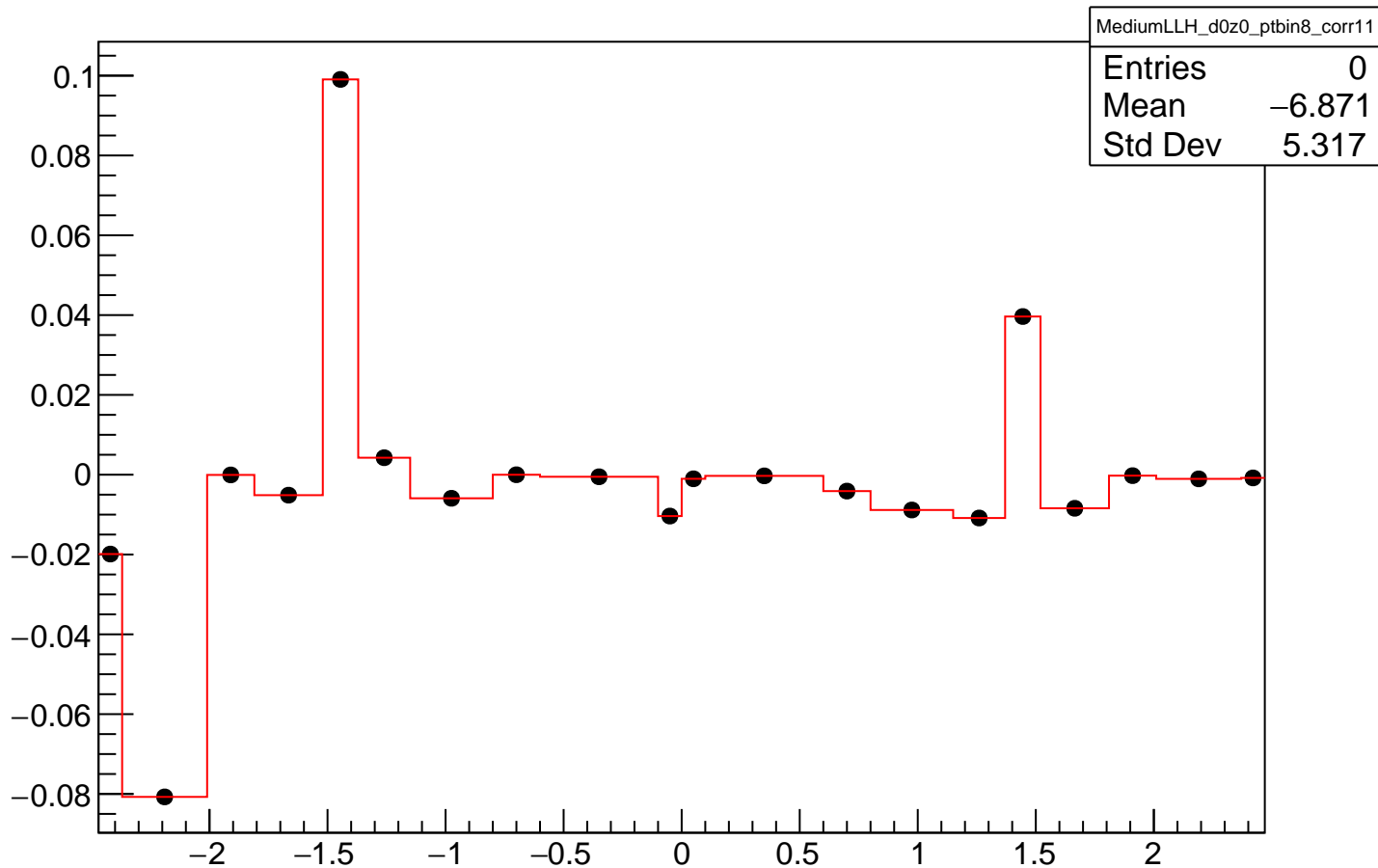
FullSim_MediumLLH_d0z0_corr11



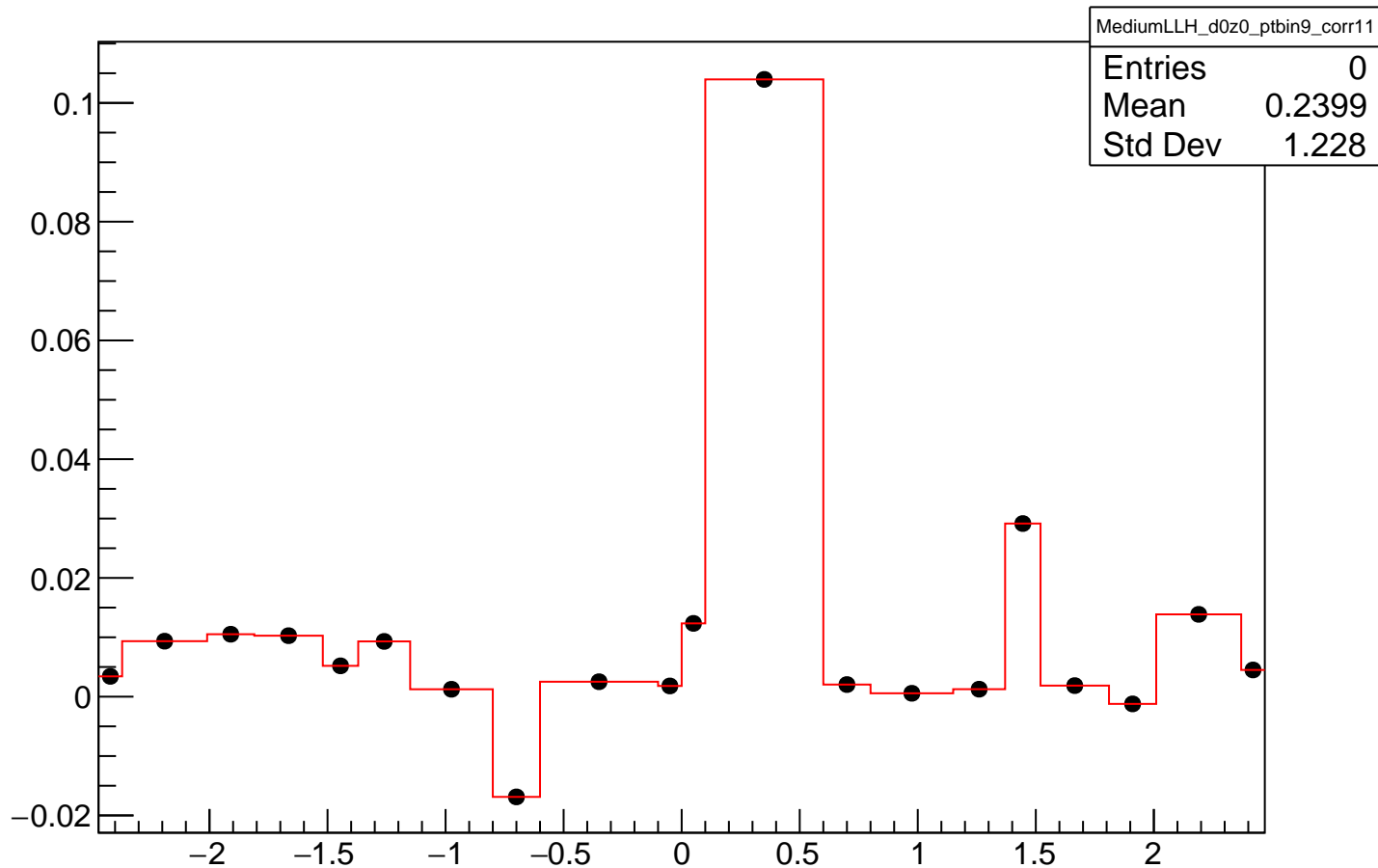
FullSim_MediumLLH_d0z0_corr11



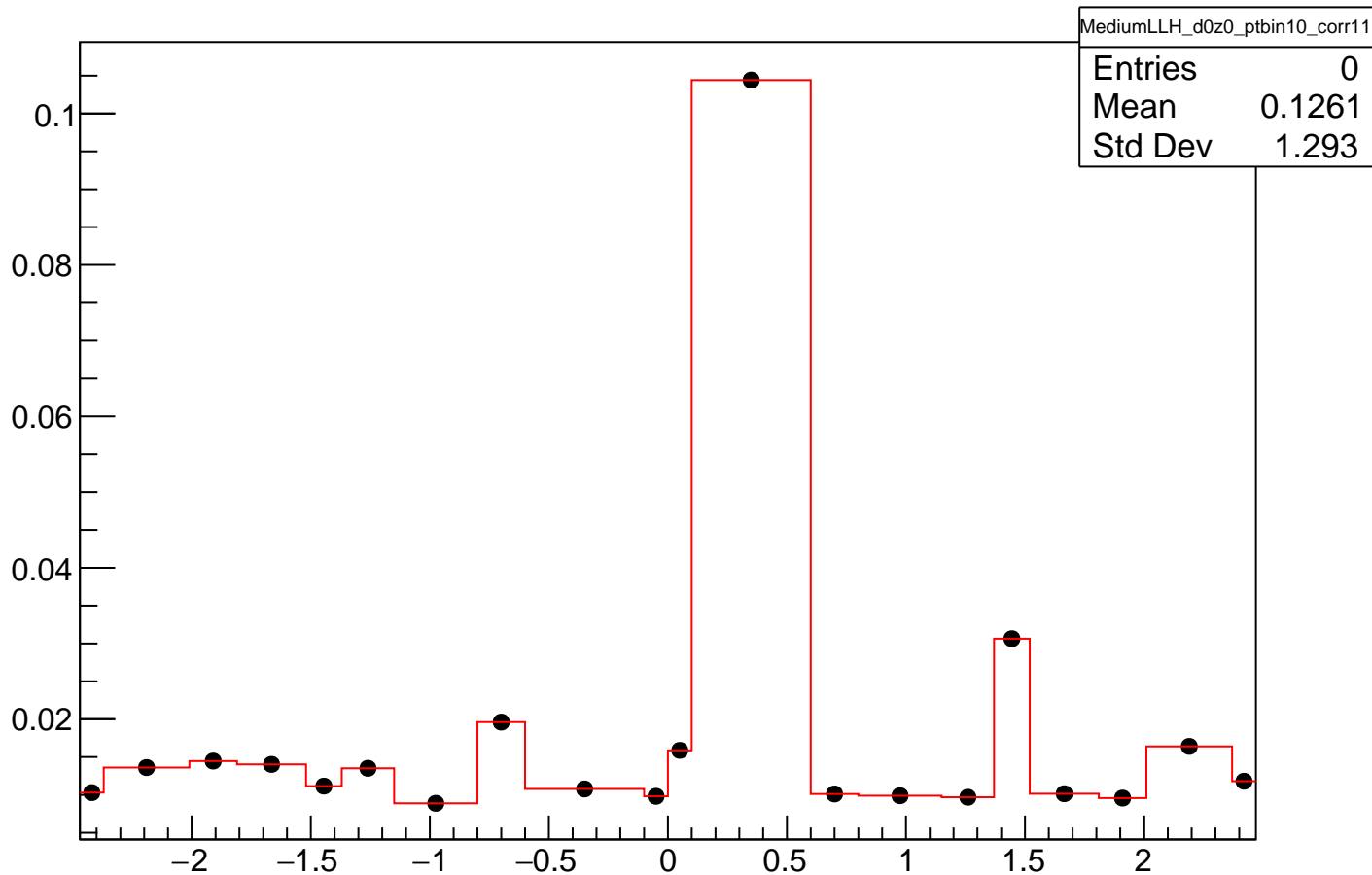
FullSim_MediumLLH_d0z0_corr11



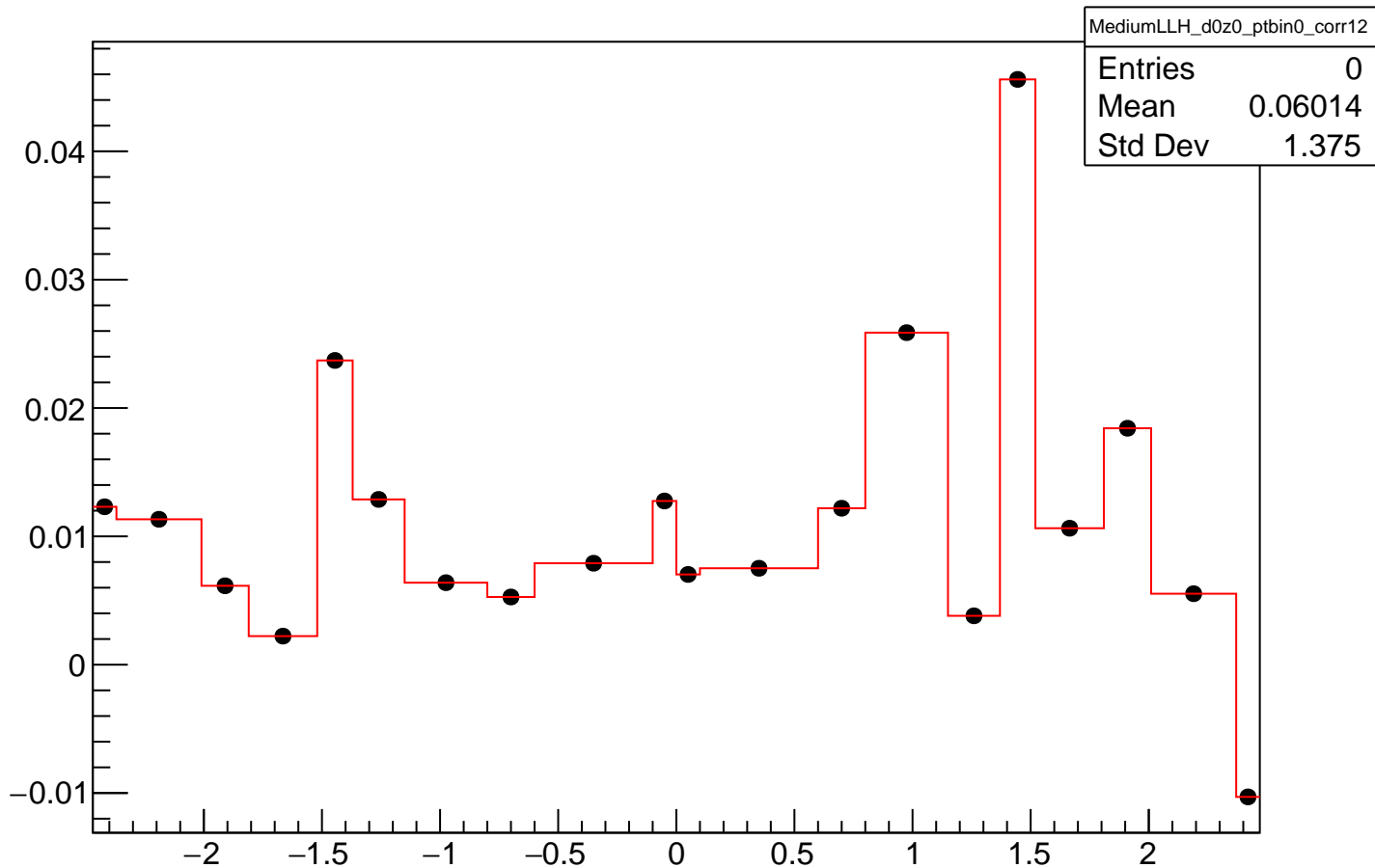
FullSim_MediumLLH_d0z0_corr11



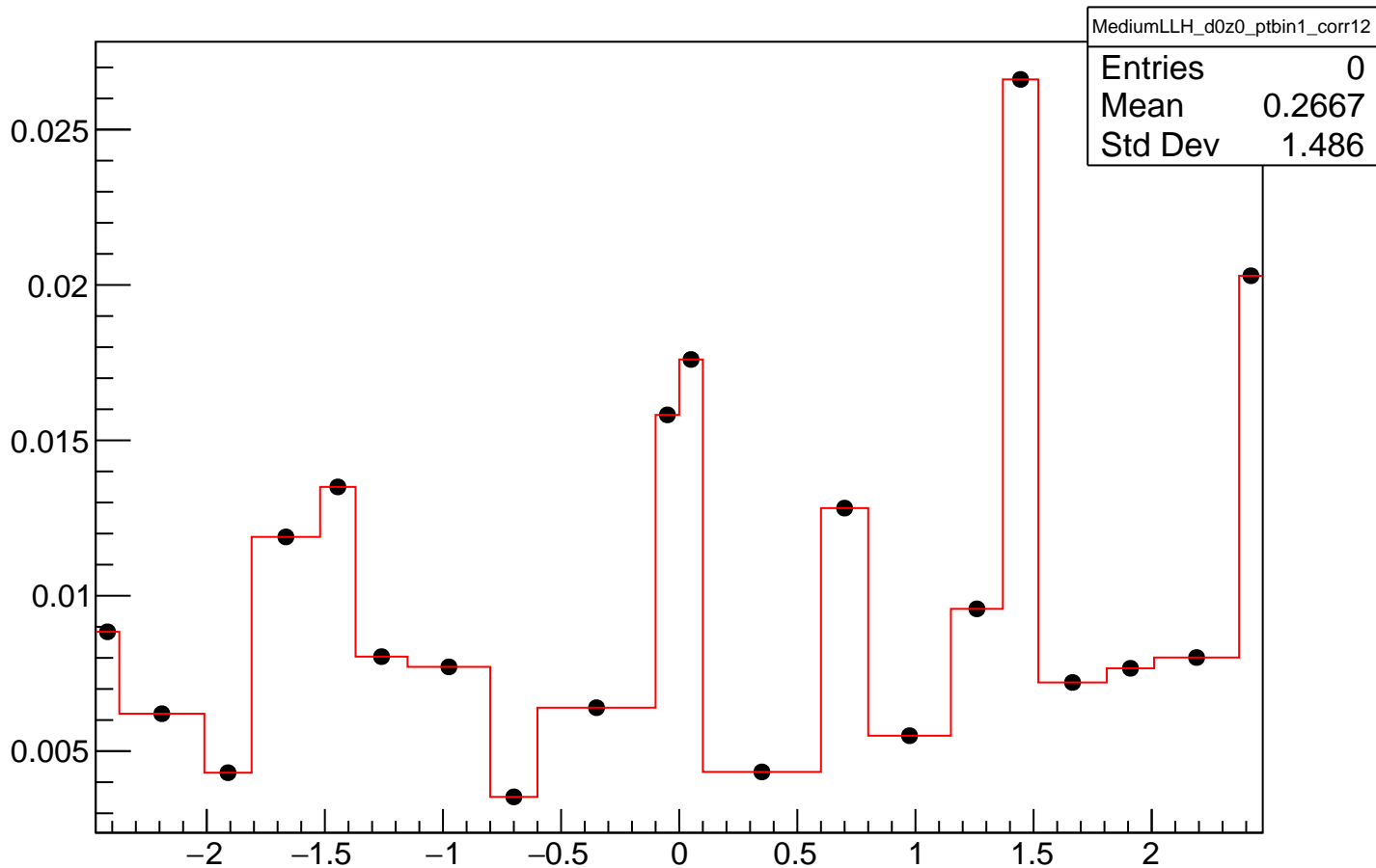
FullSim_MediumLLH_d0z0_corr11



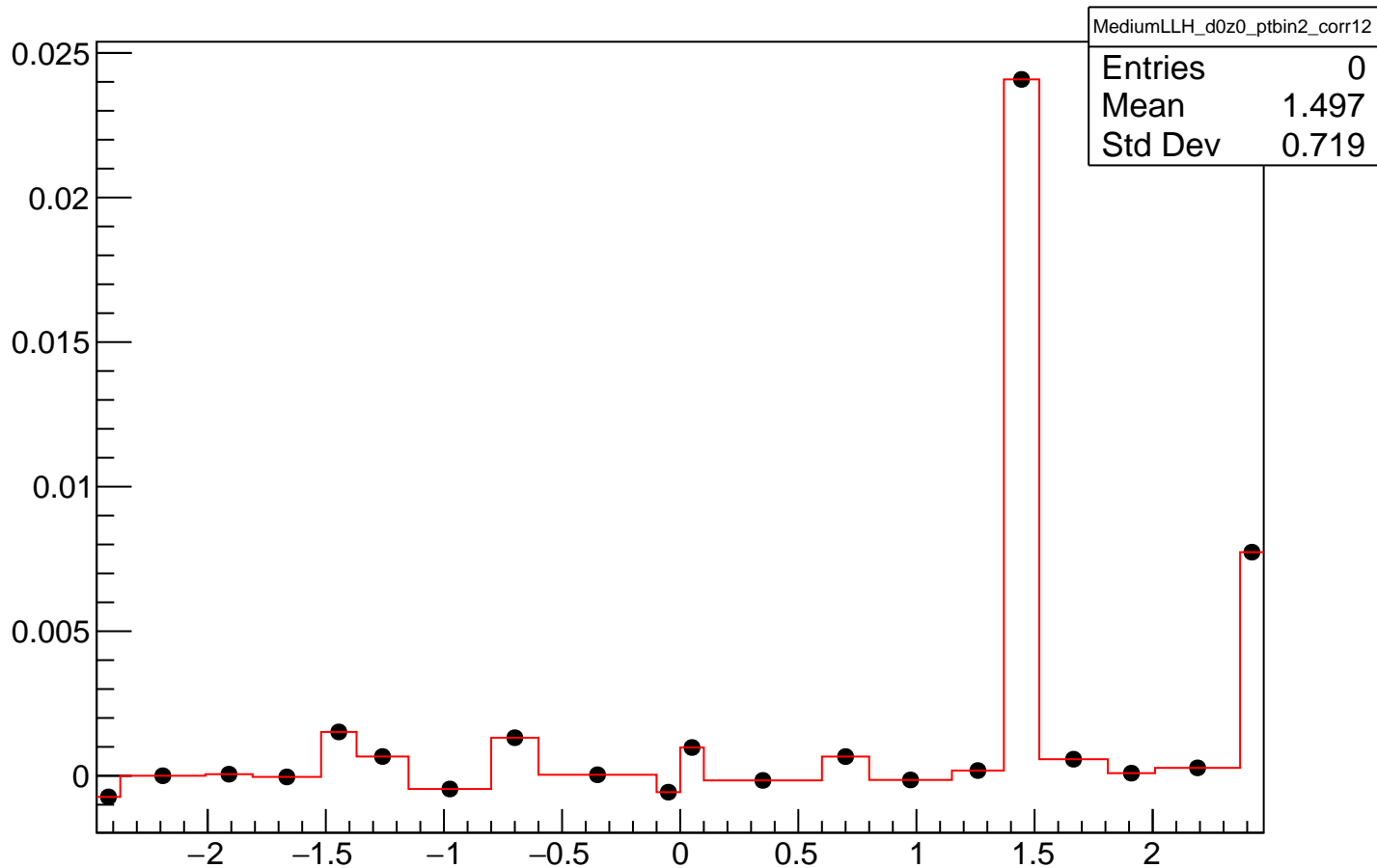
FullSim_MediumLLH_d0z0_corr12



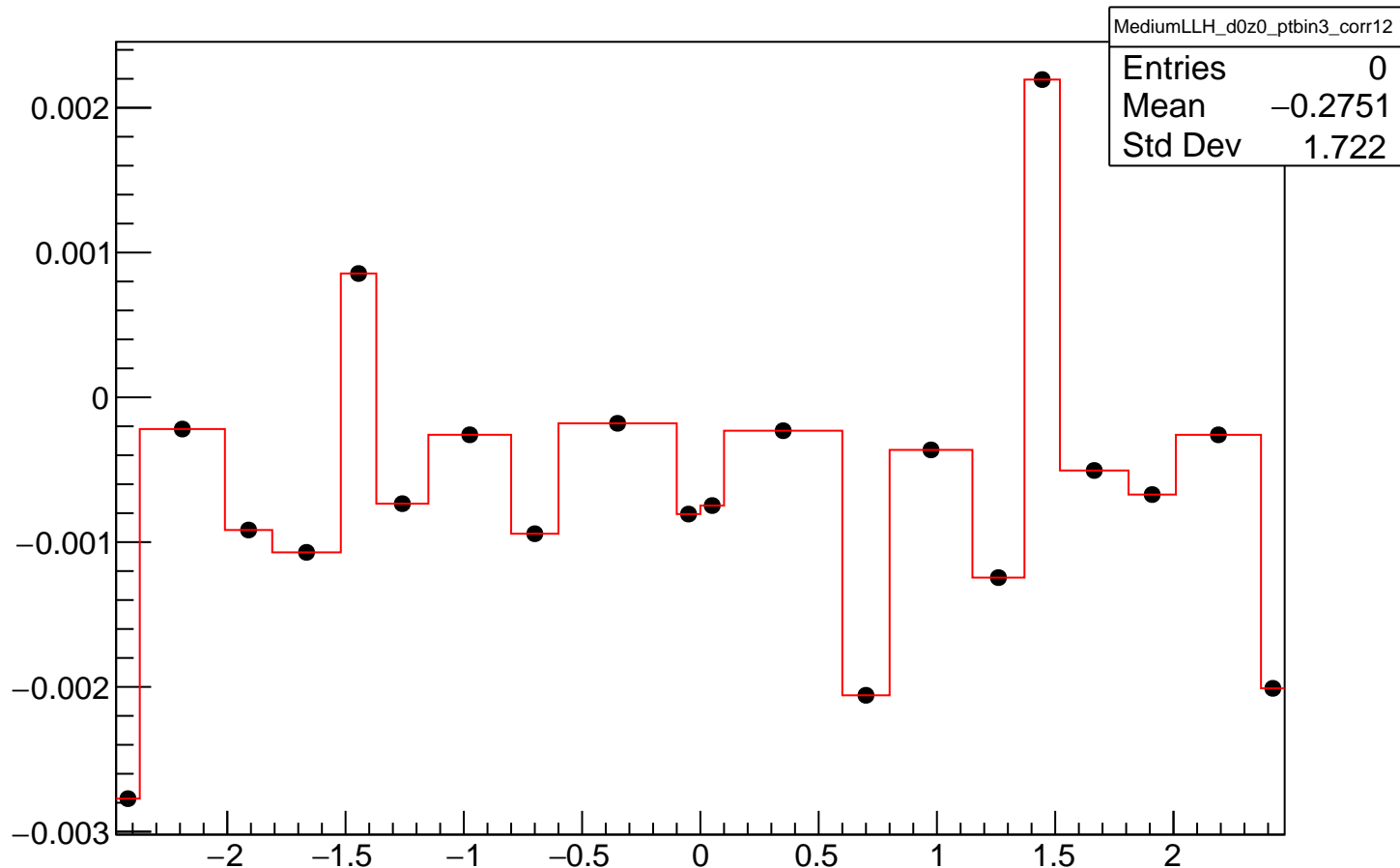
FullSim_MediumLLH_d0z0_corr12



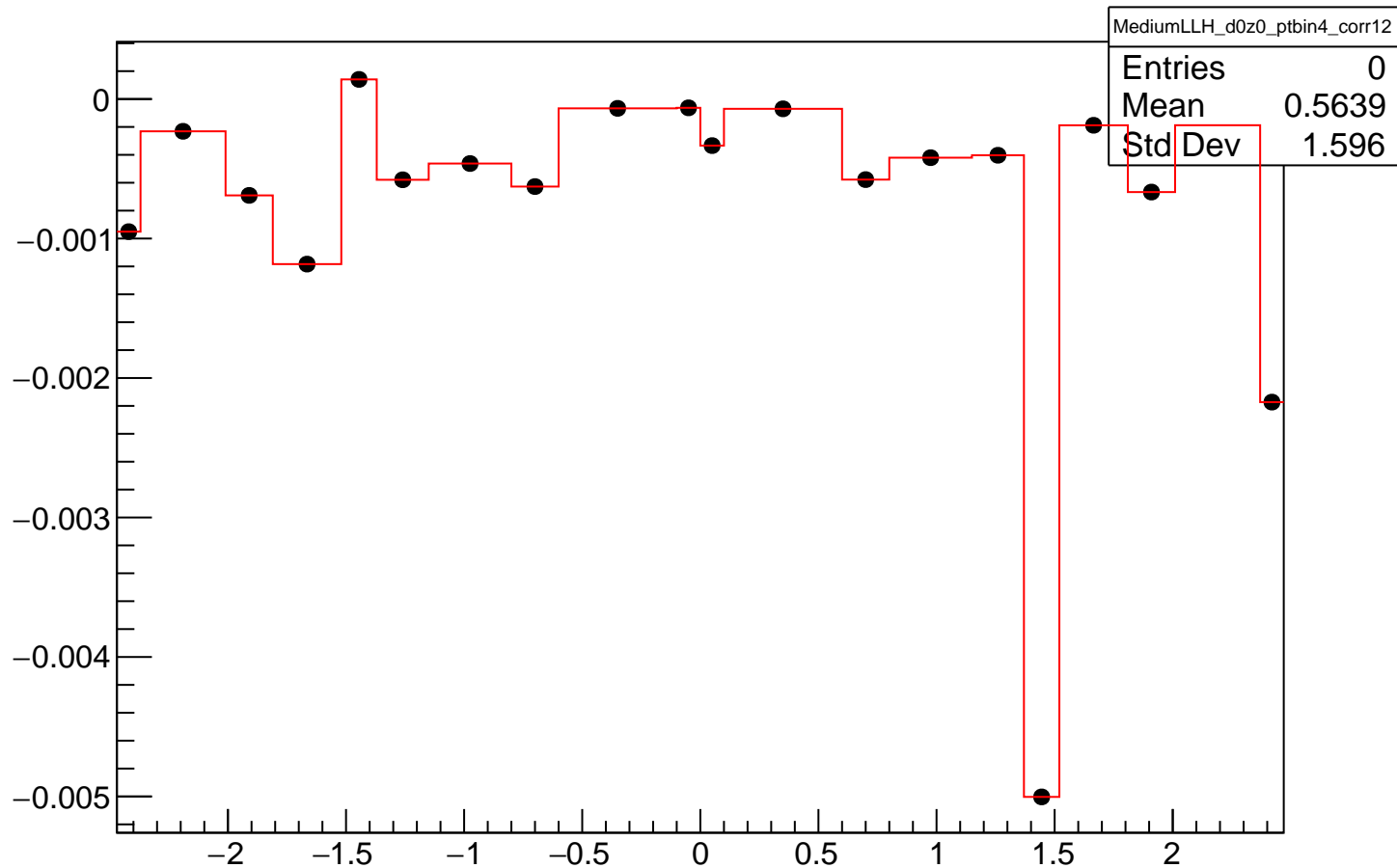
FullSim_MediumLLH_d0z0_corr12



FullSim_MediumLLH_d0z0_corr12



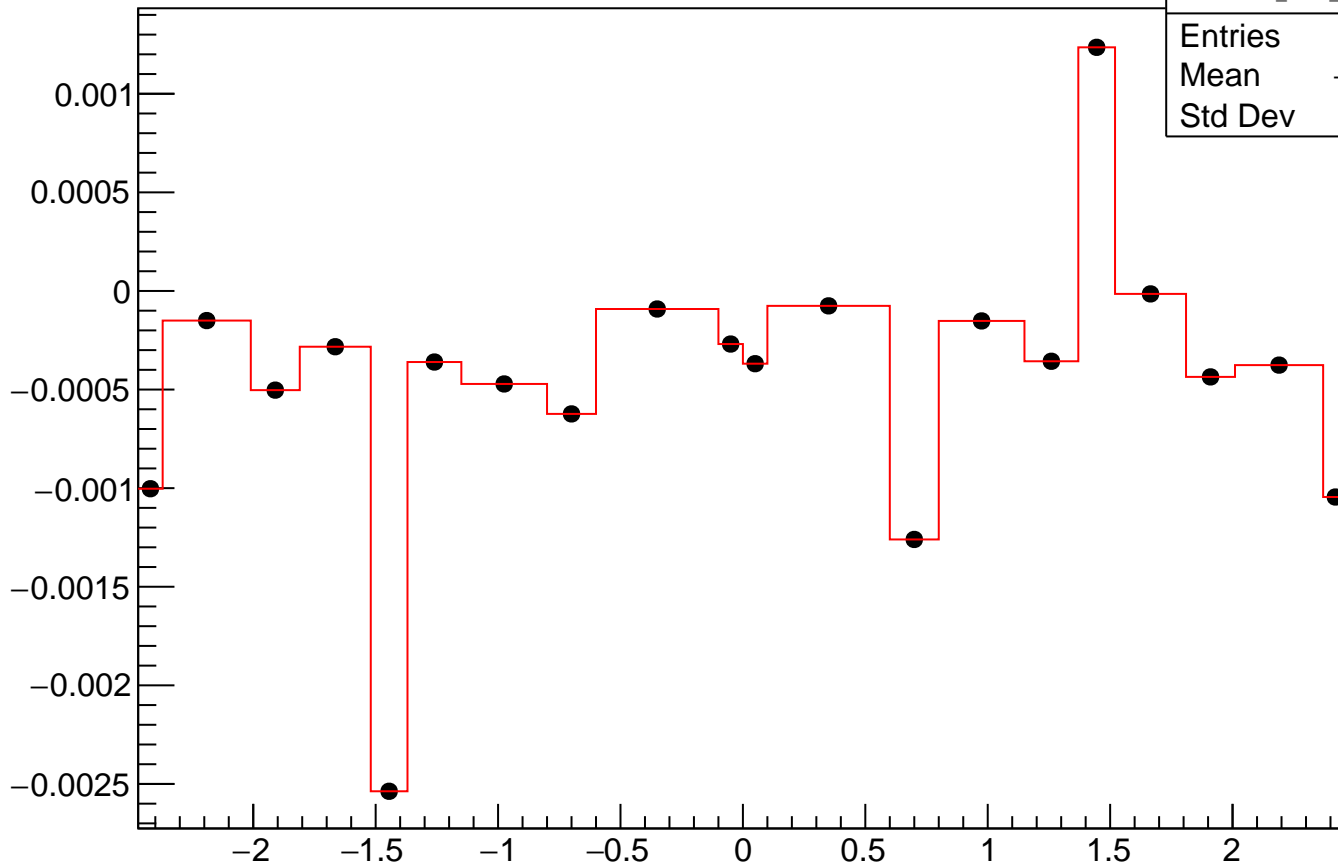
FullSim_MediumLLH_d0z0_corr12



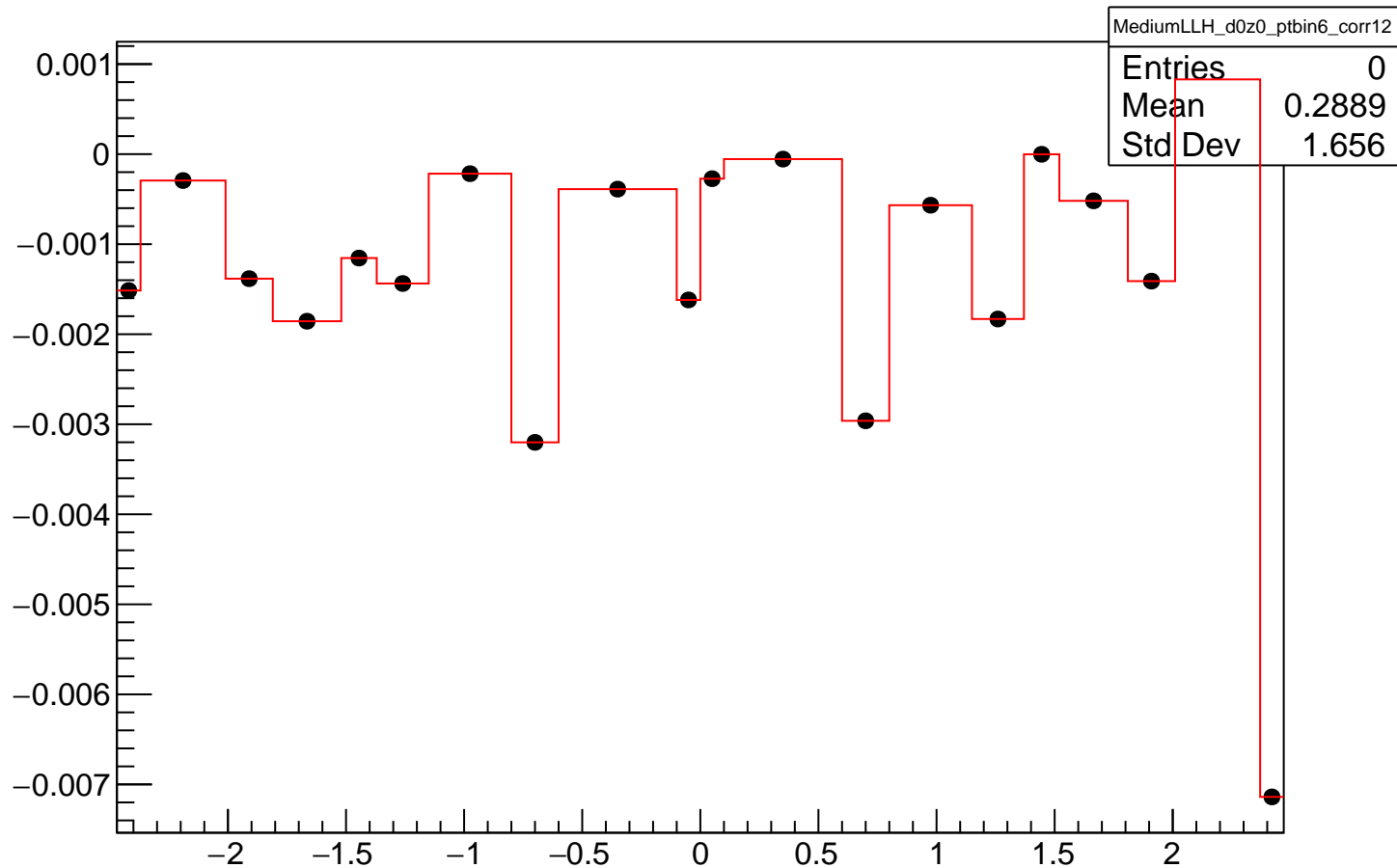
FullSim_MediumLLH_d0z0_corr12

MediumLLH_d0z0_ptbin5_corr12

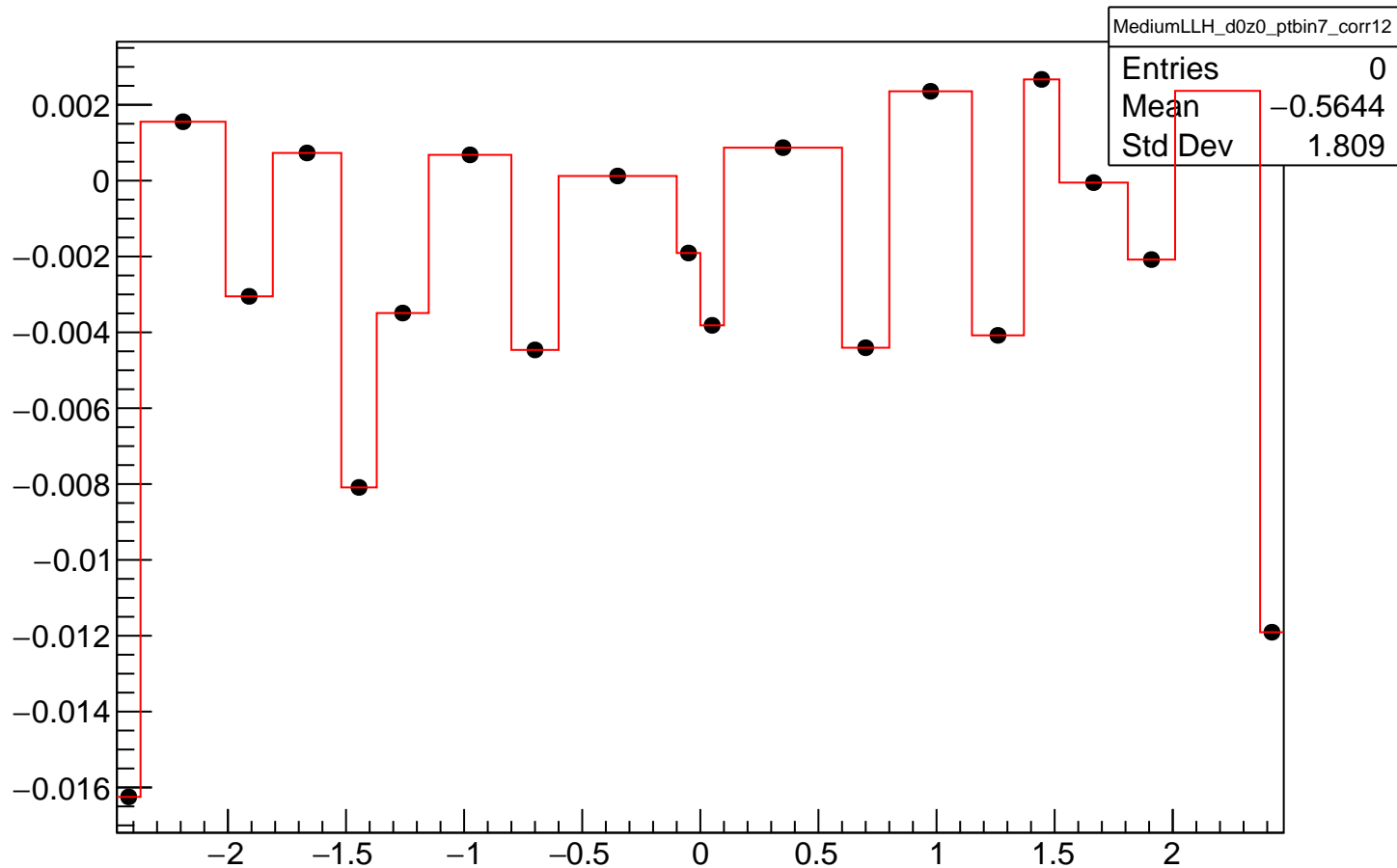
Entries	0
Mean	-0.5799
Std Dev	1.519



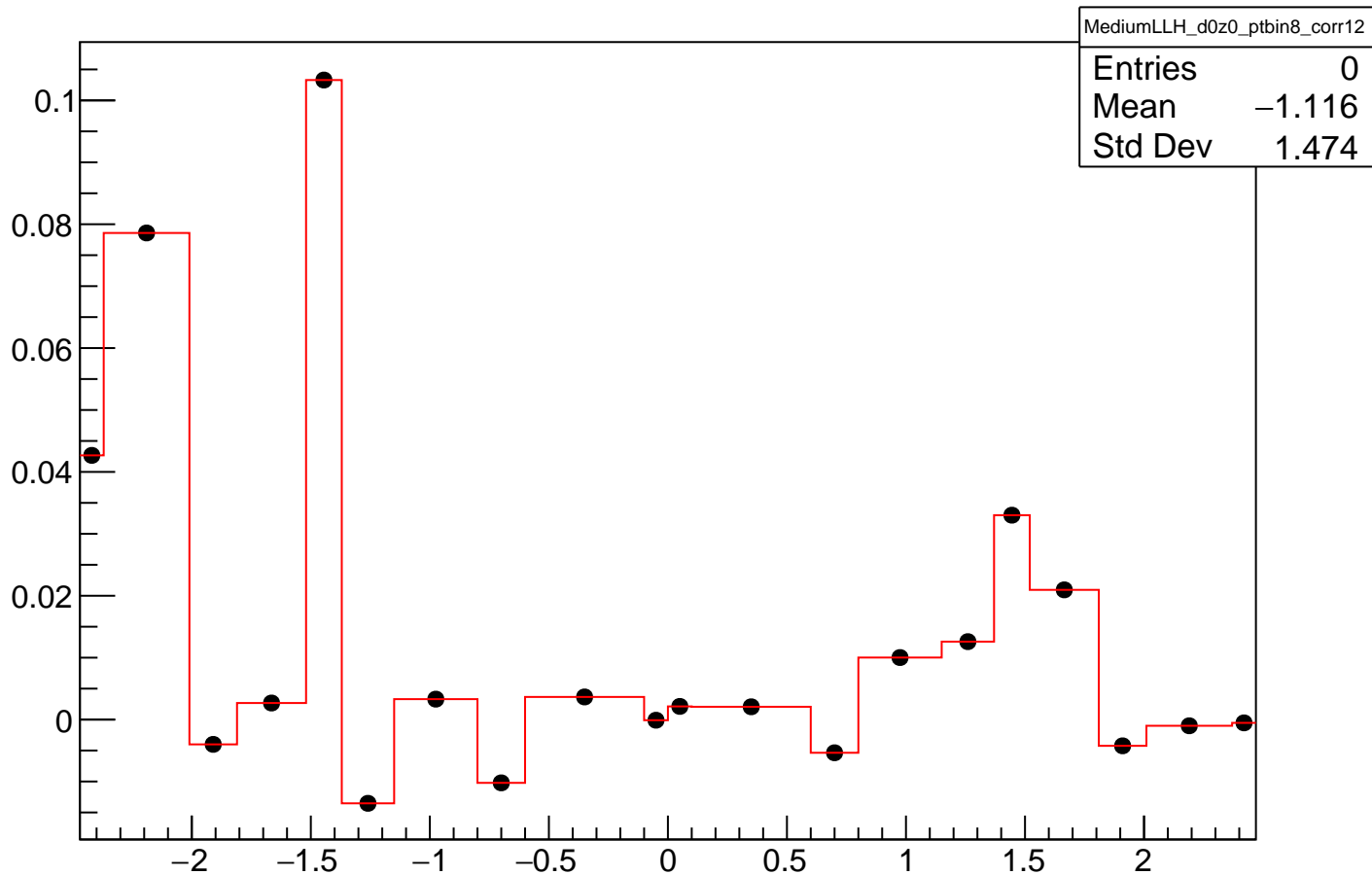
FullSim_MediumLLH_d0z0_corr12



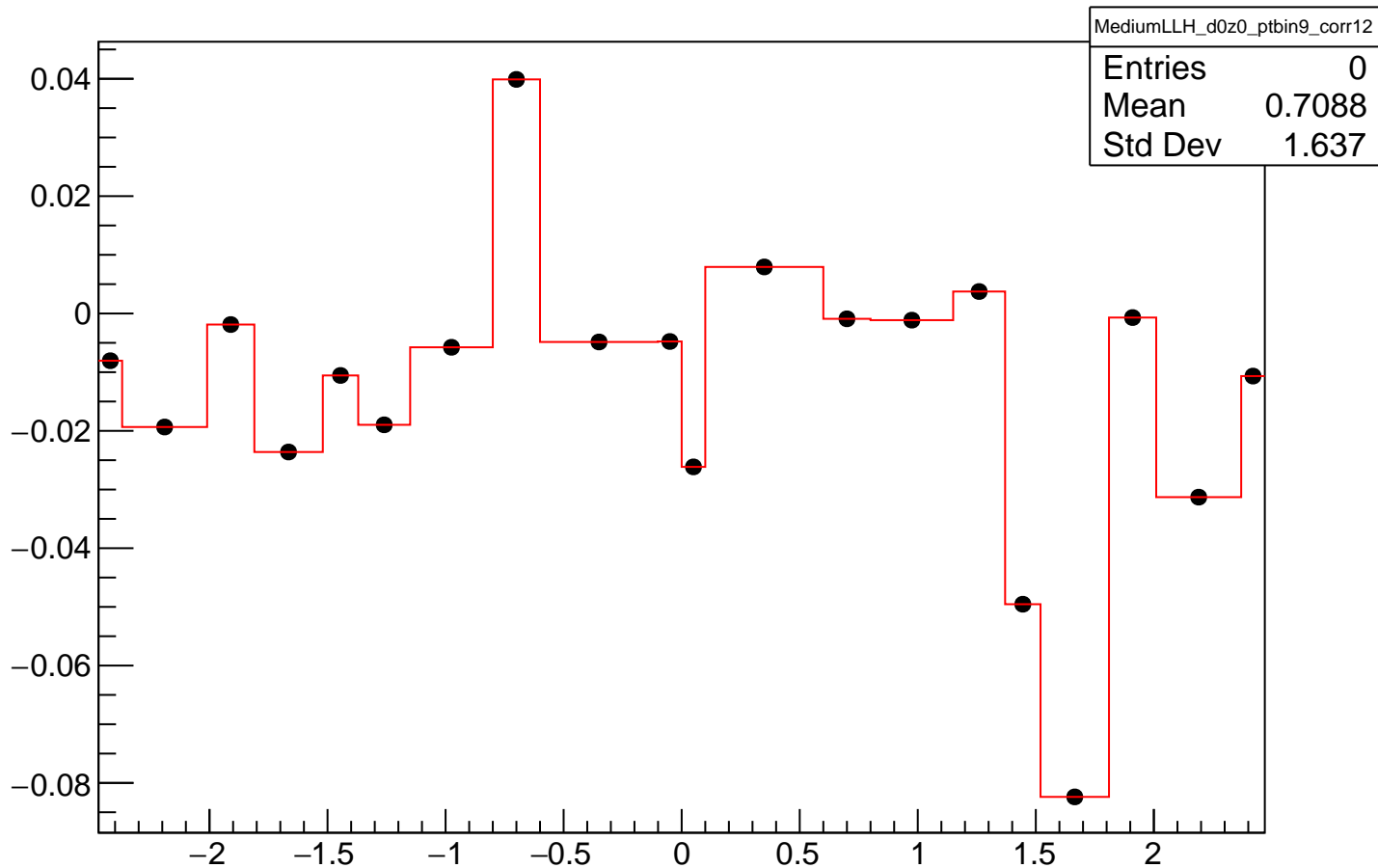
FullSim_MediumLLH_d0z0_corr12



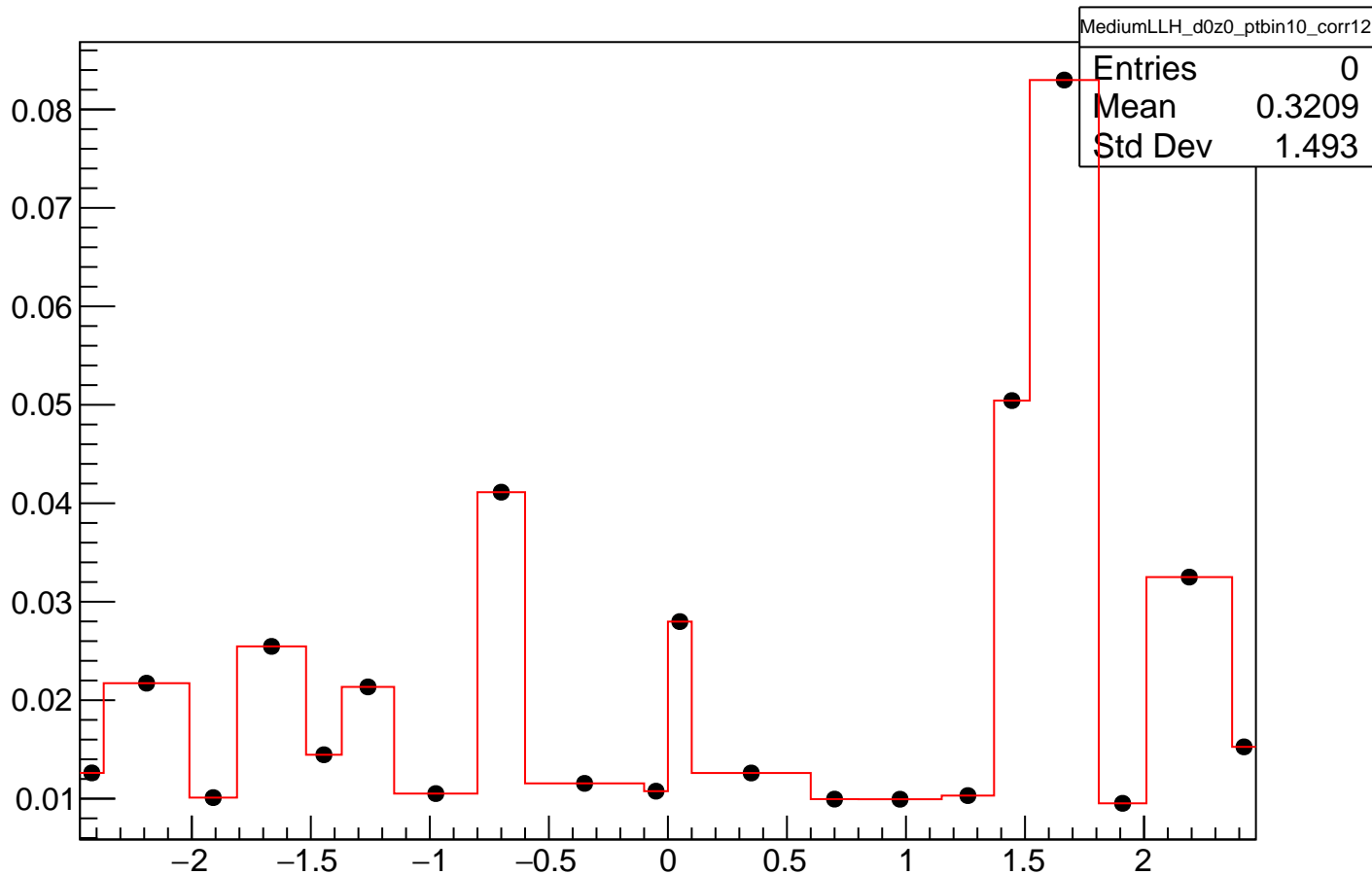
FullSim_MediumLLH_d0z0_corr12



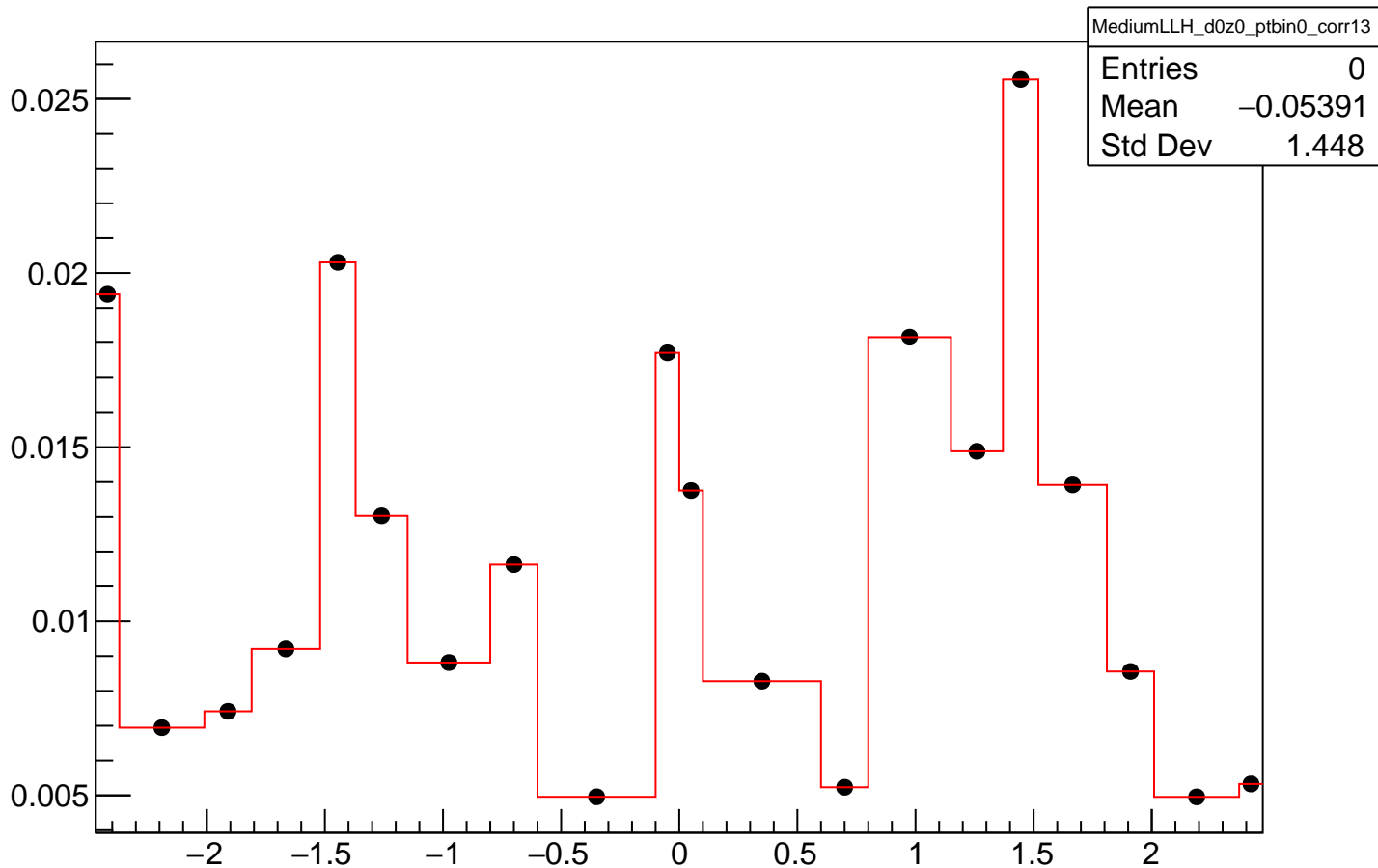
FullSim_MediumLLH_d0z0_corr12



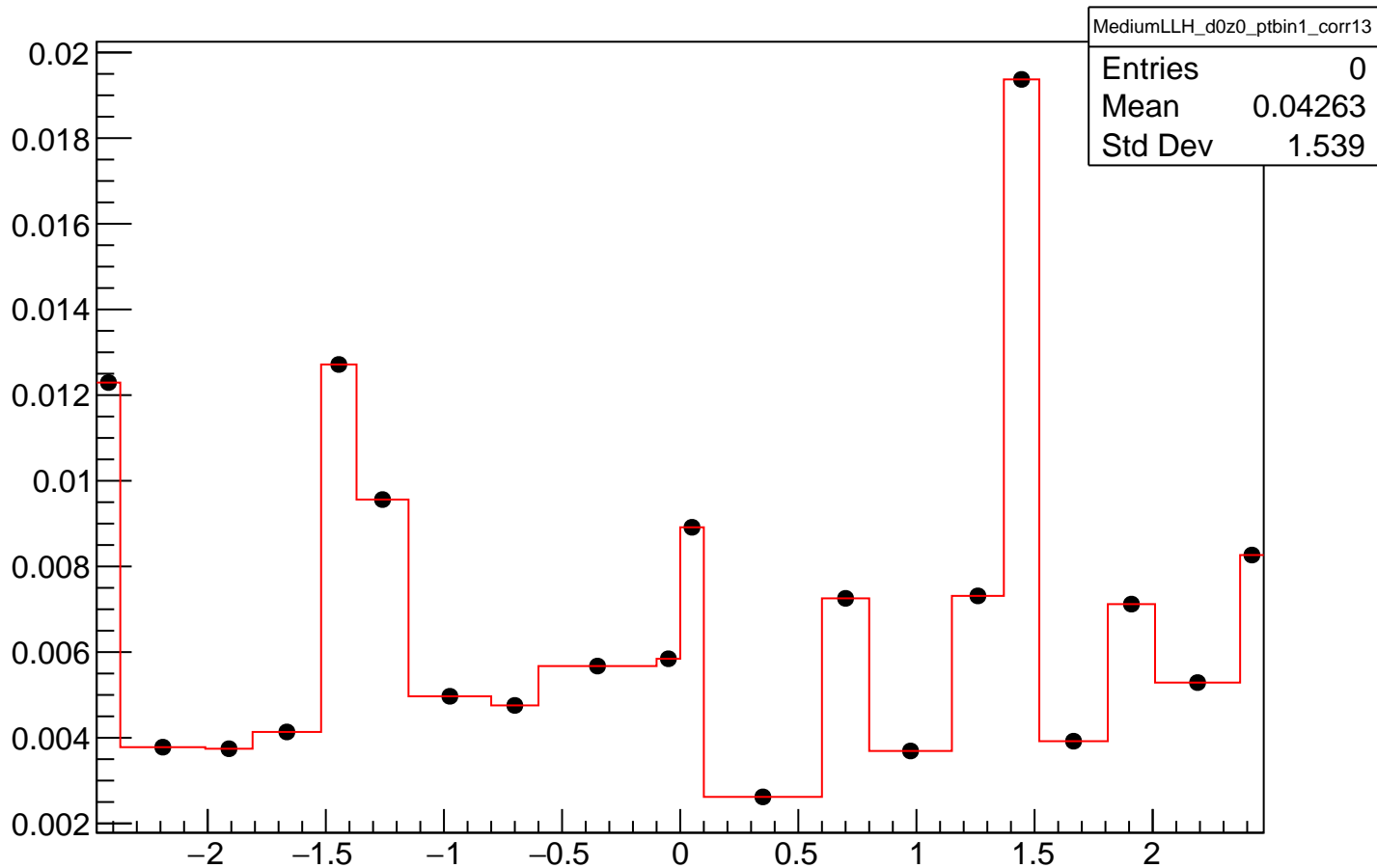
FullSim_MediumLLH_d0z0_corr12



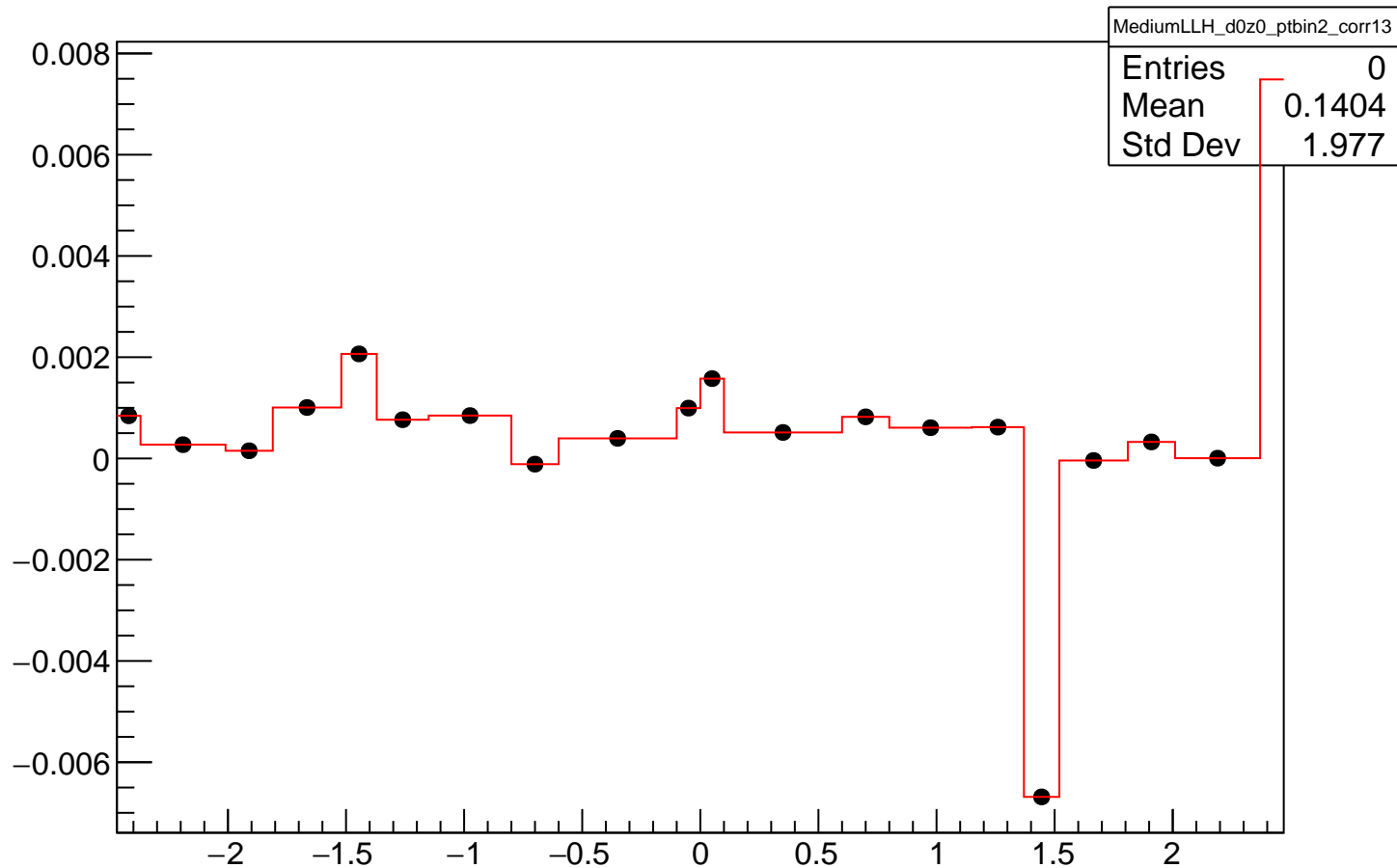
FullSim_MediumLLH_d0z0_corr13



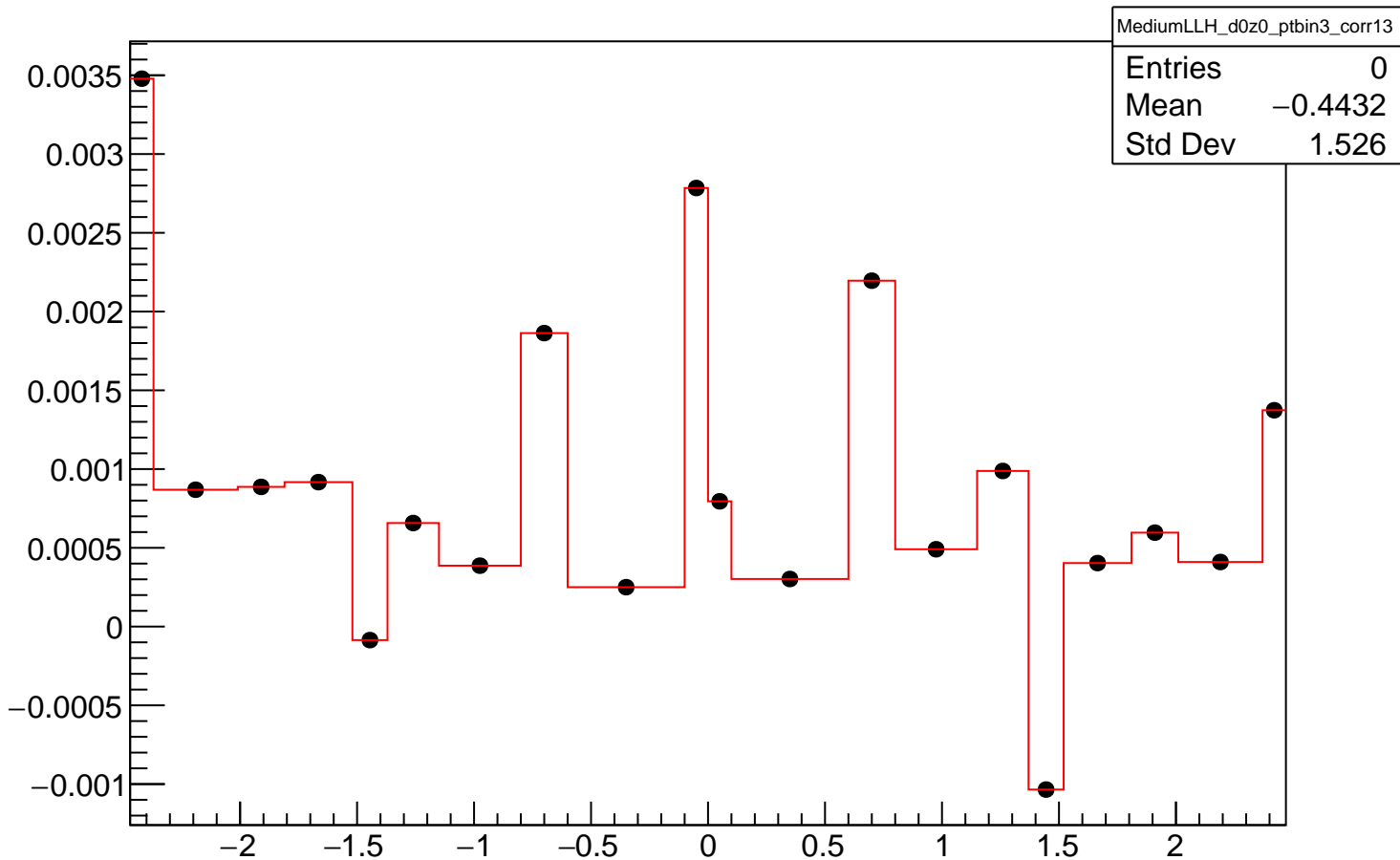
FullSim_MediumLLH_d0z0_corr13



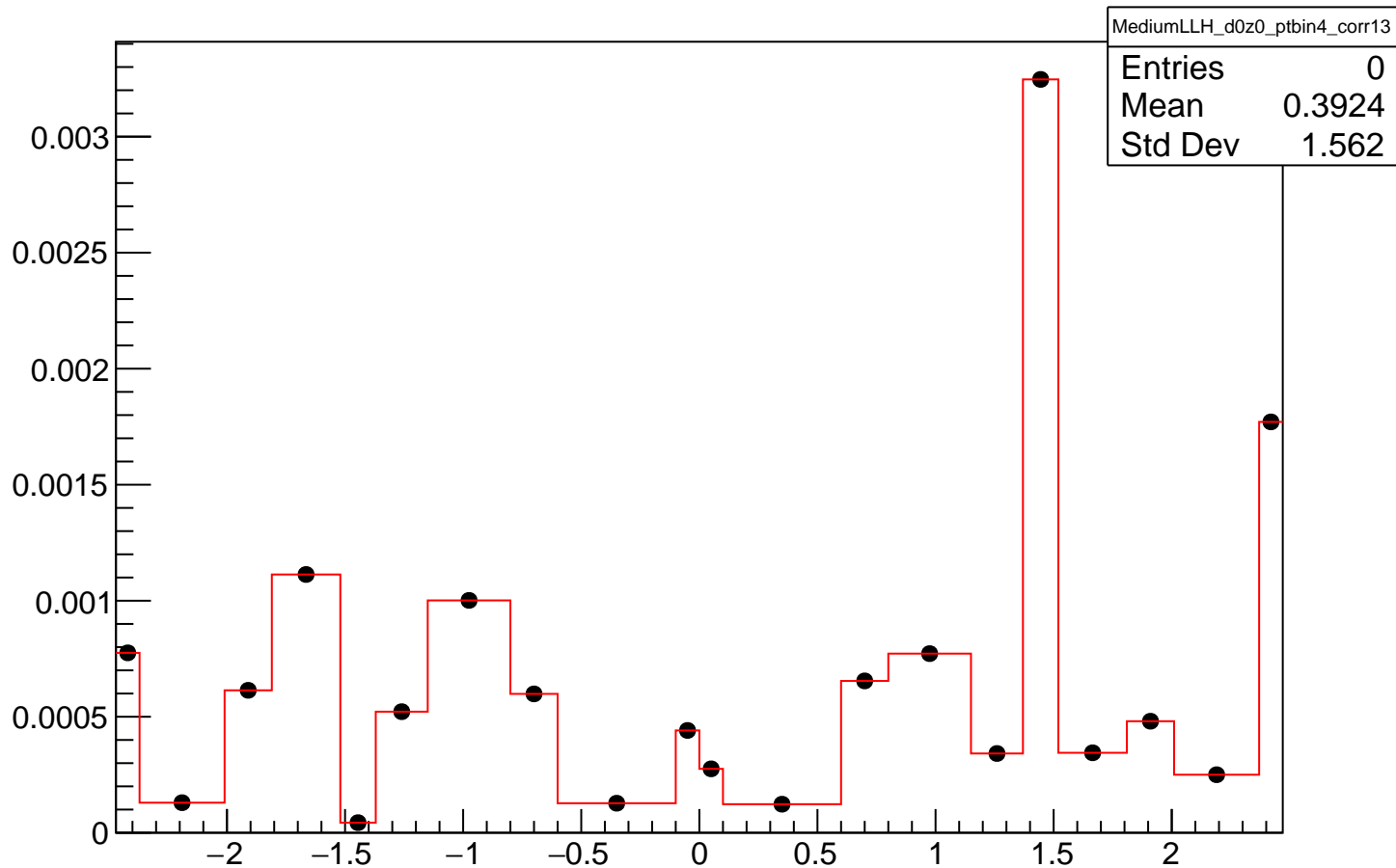
FullSim_MediumLLH_d0z0_corr13



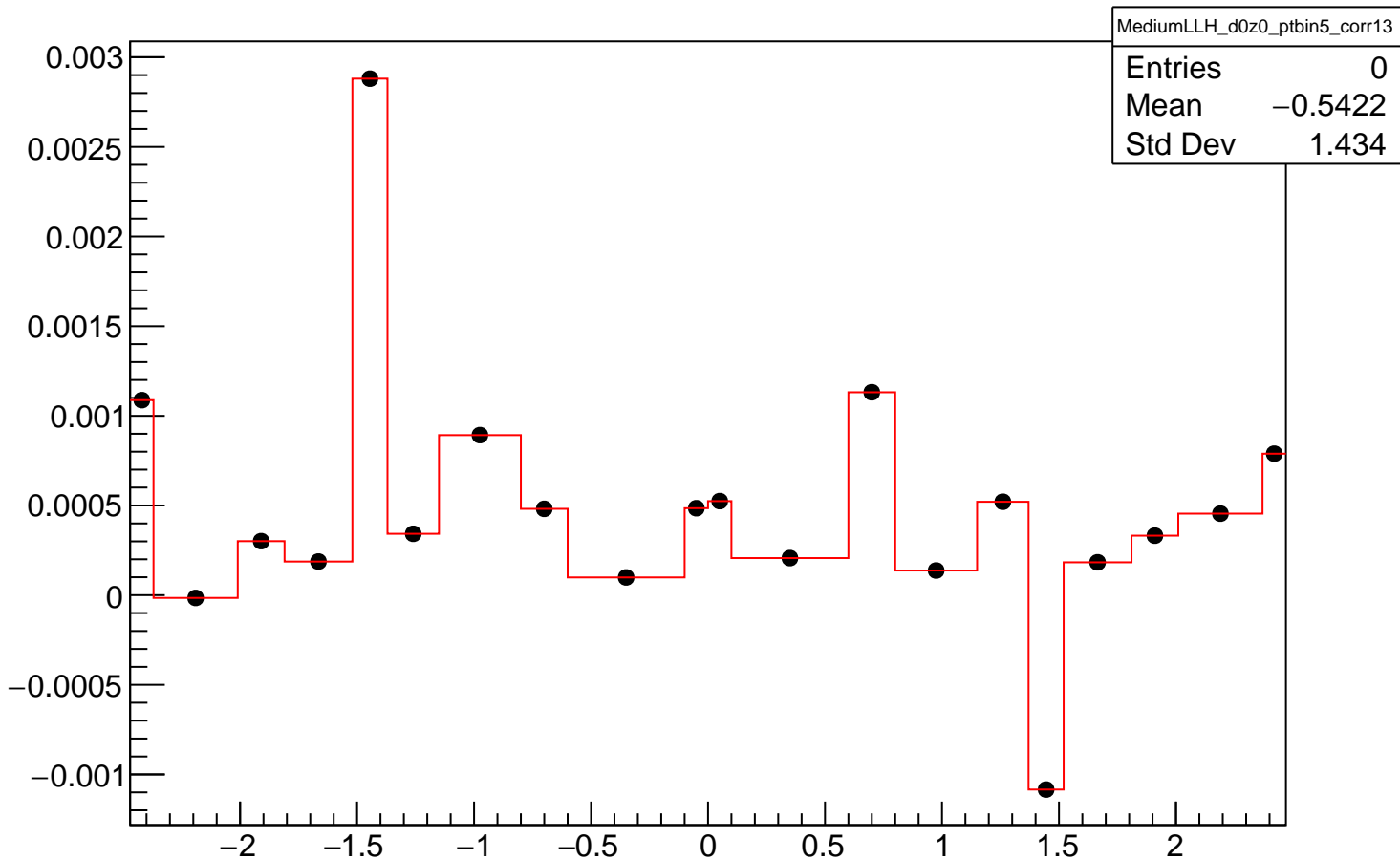
FullSim_MediumLLH_d0z0_corr13



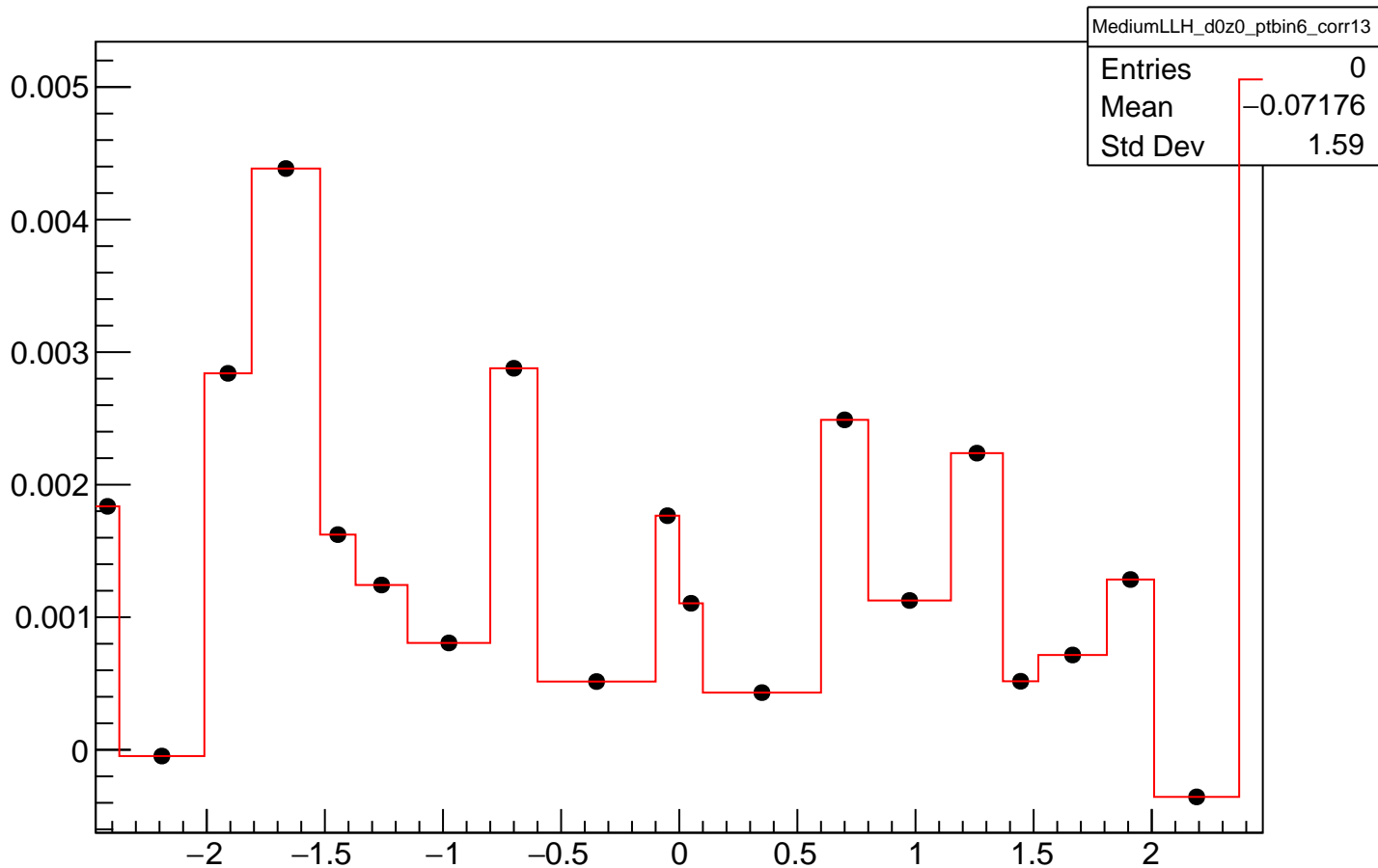
FullSim_MediumLLH_d0z0_corr13



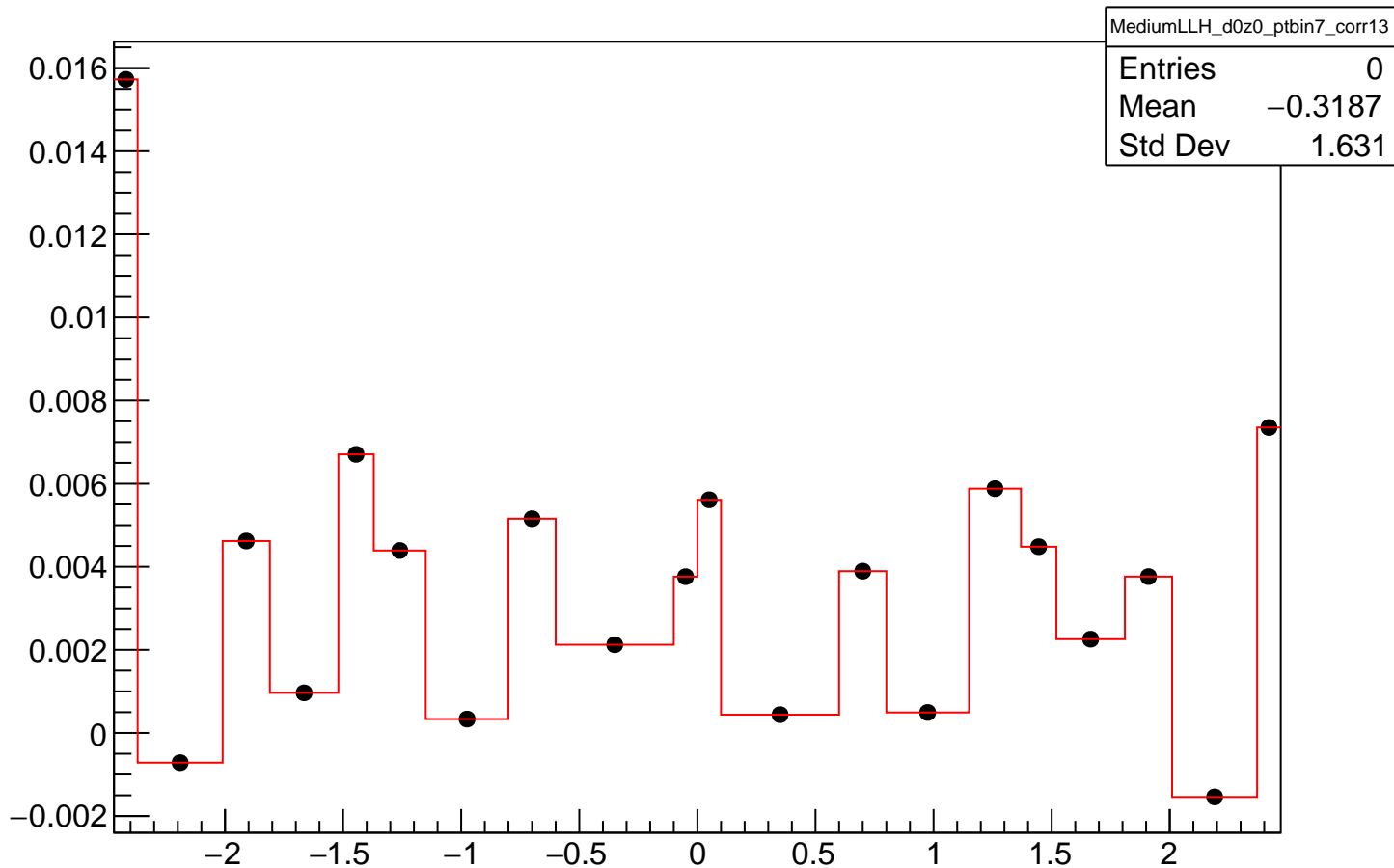
FullSim_MediumLLH_d0z0_corr13



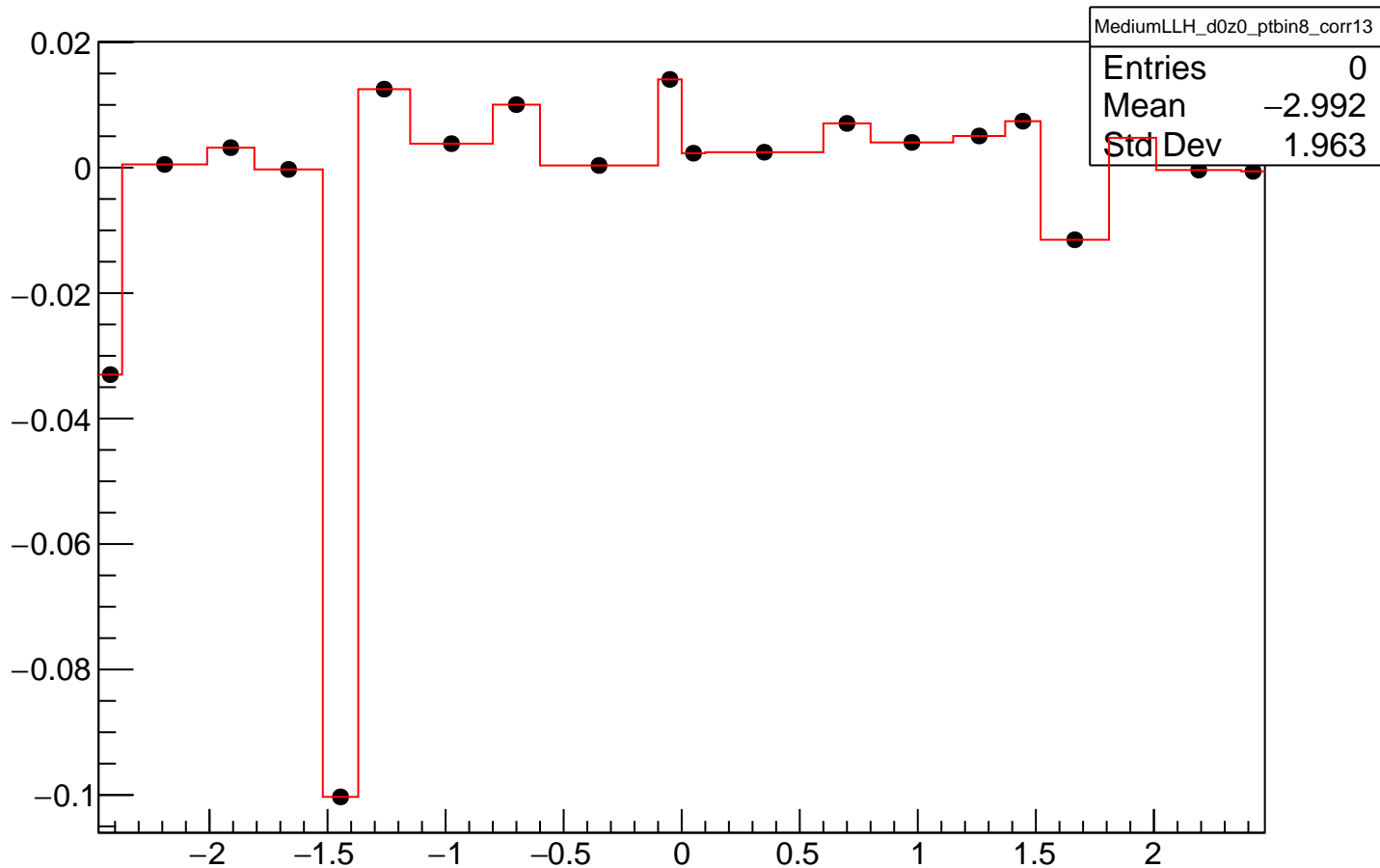
FullSim_MediumLLH_d0z0_corr13



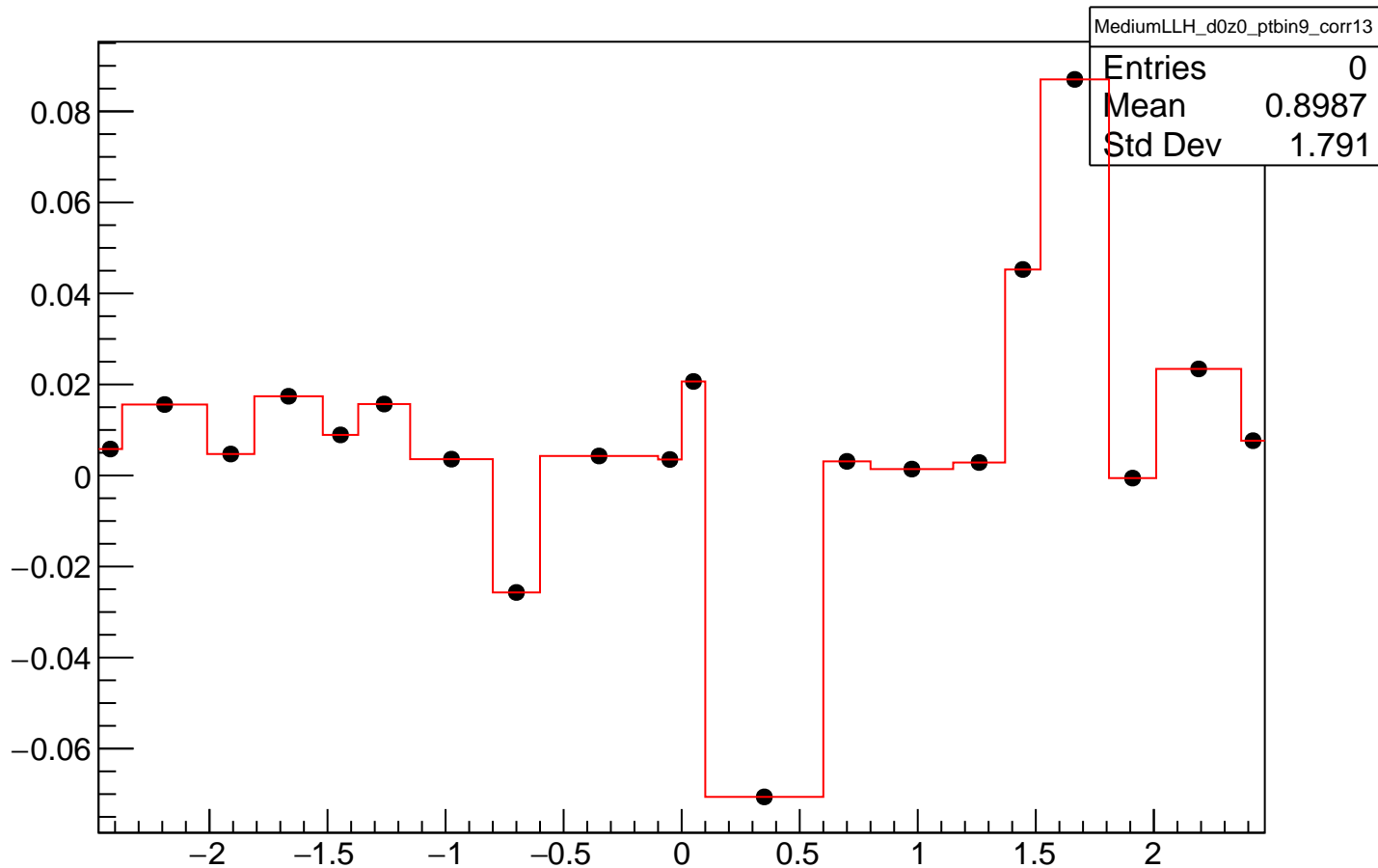
FullSim_MediumLLH_d0z0_corr13



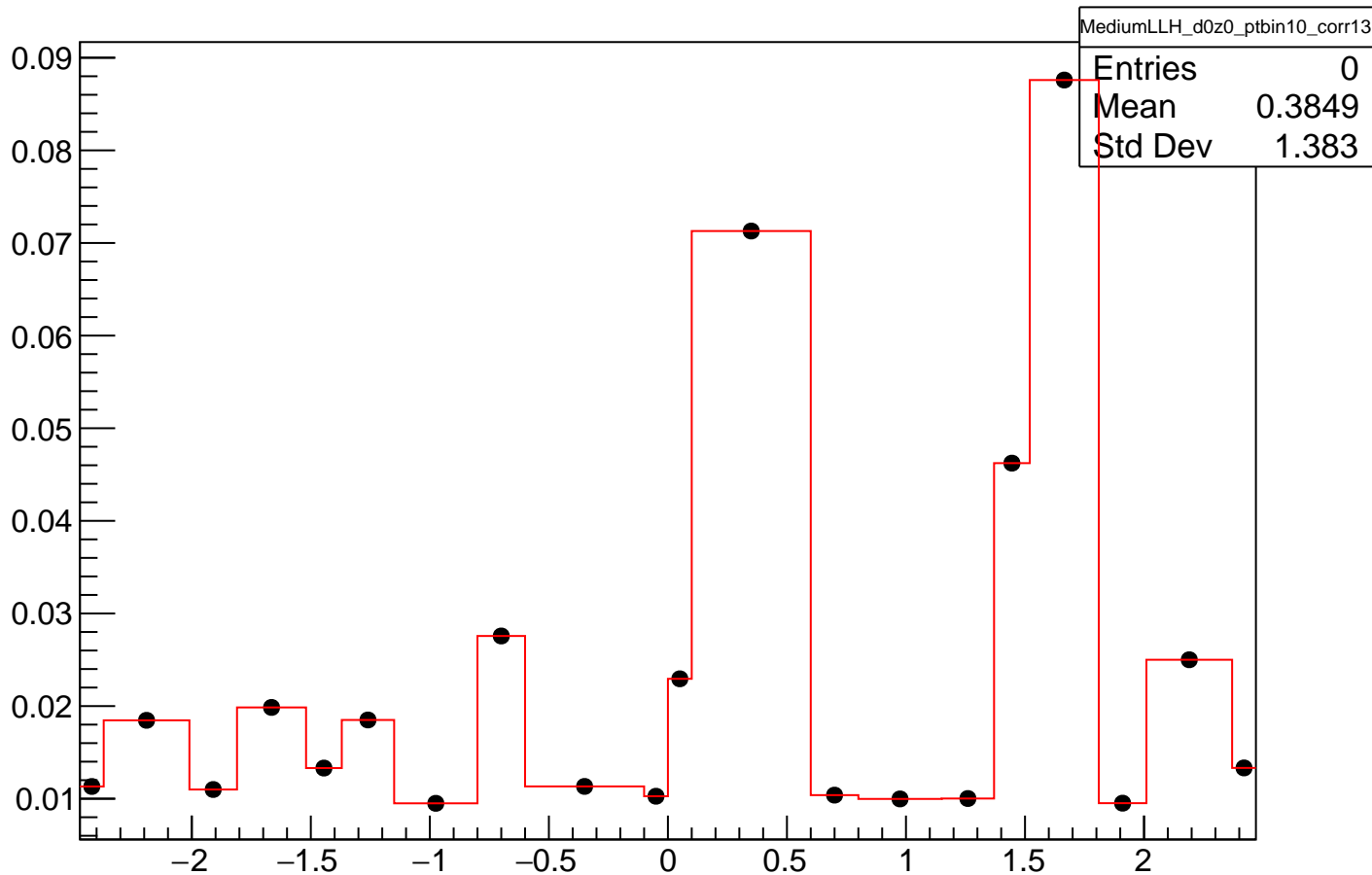
FullSim_MediumLLH_d0z0_corr13



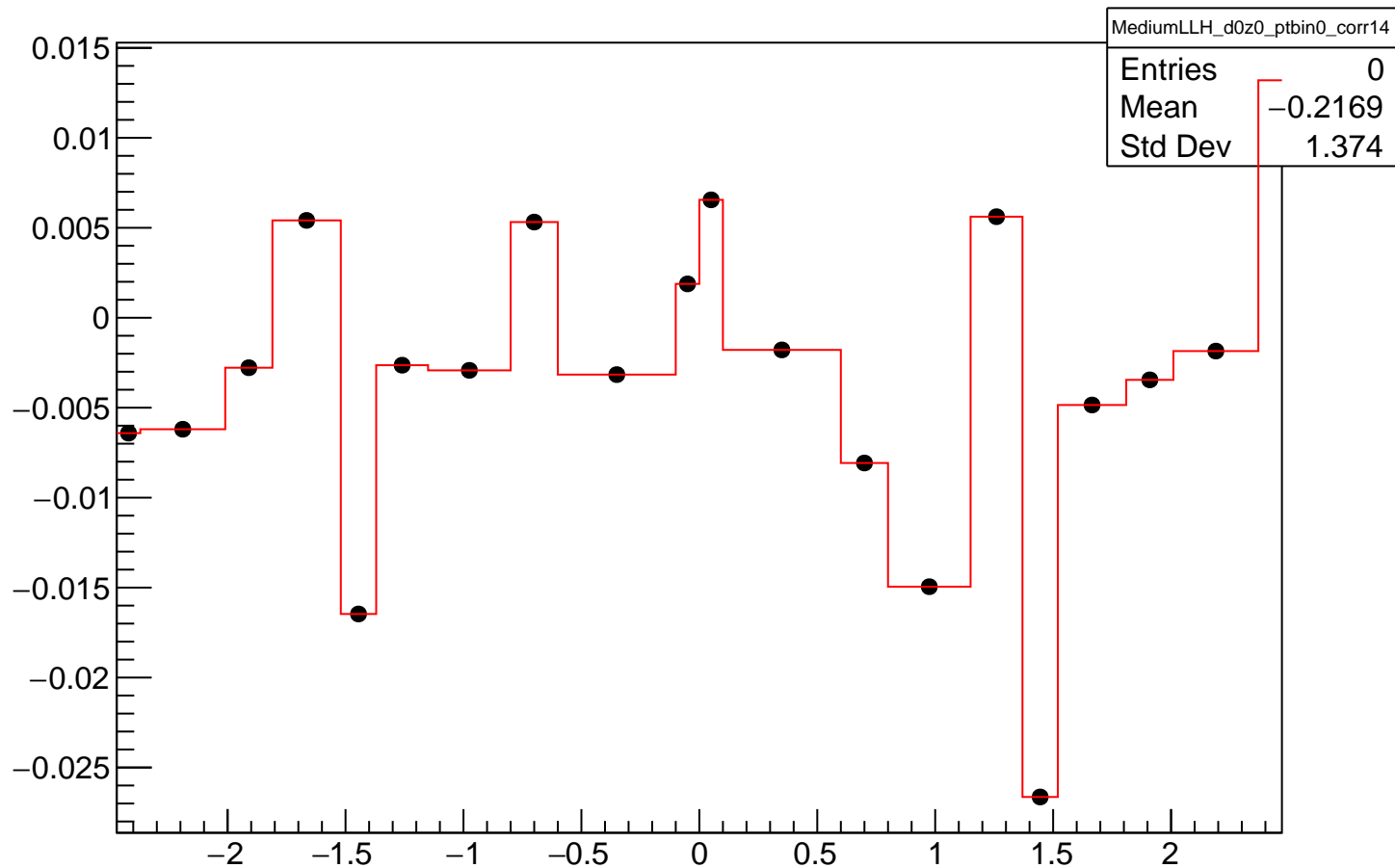
FullSim_MediumLLH_d0z0_corr13



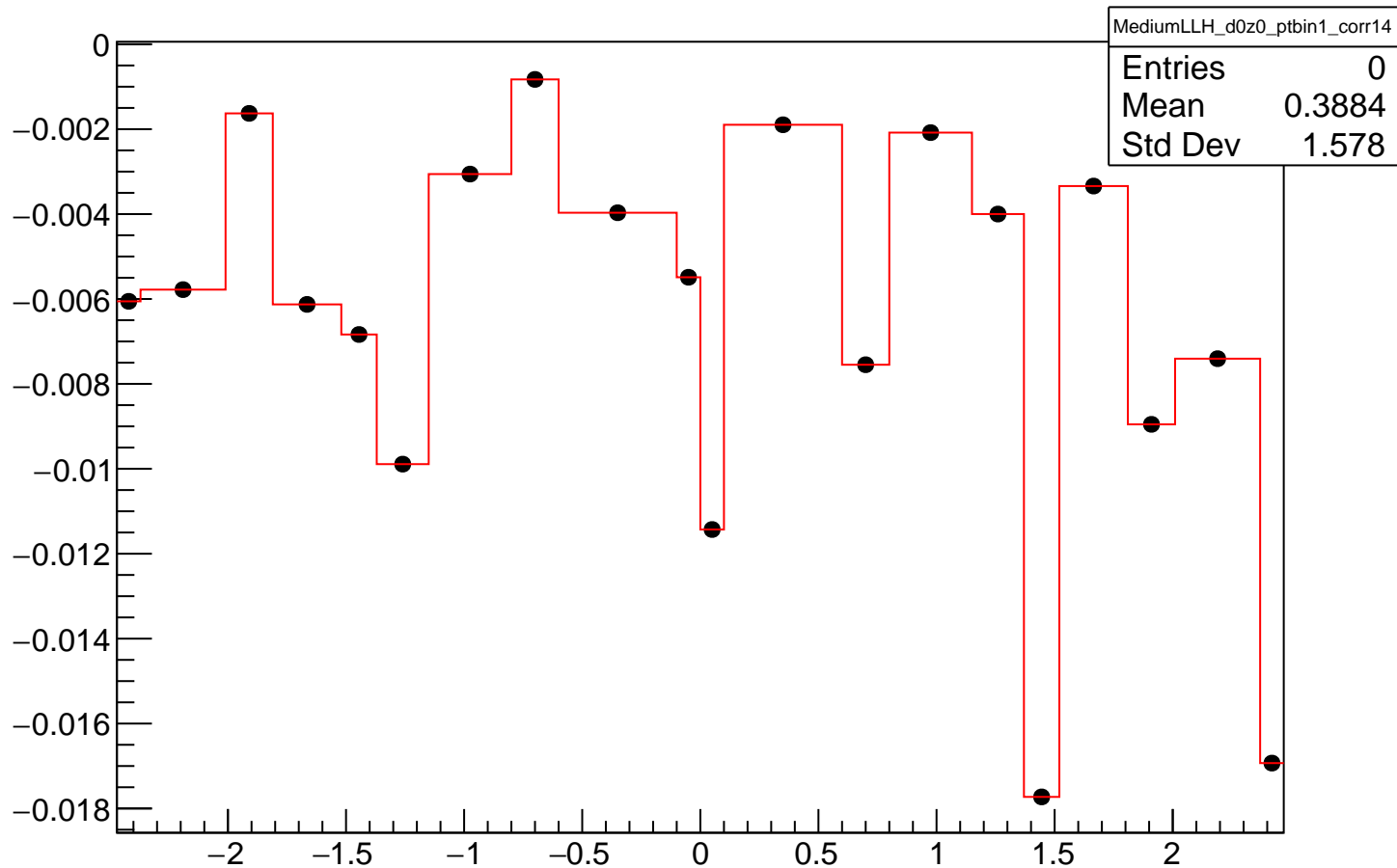
FullSim_MediumLLH_d0z0_corr13



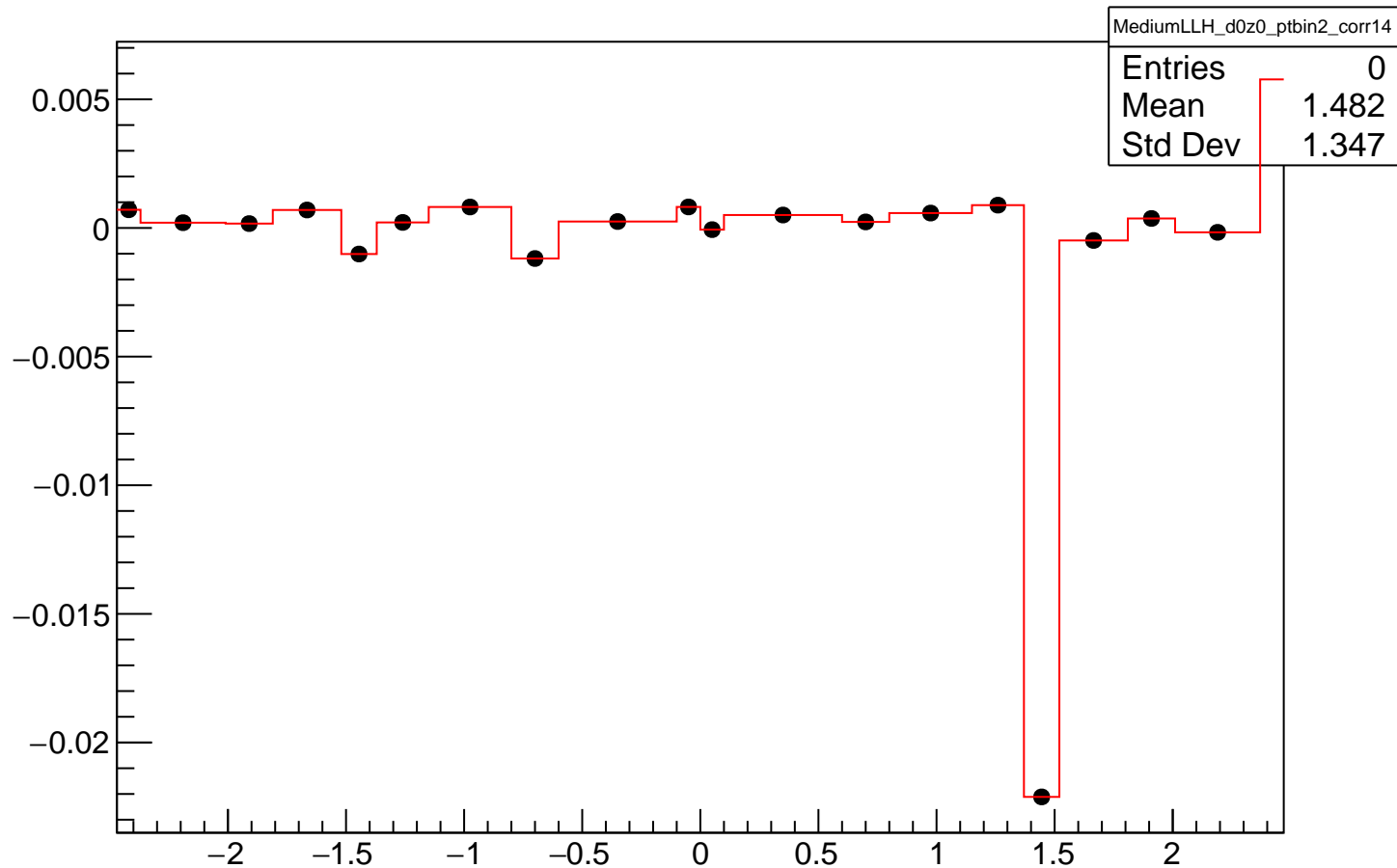
FullSim_MediumLLH_d0z0_corr14



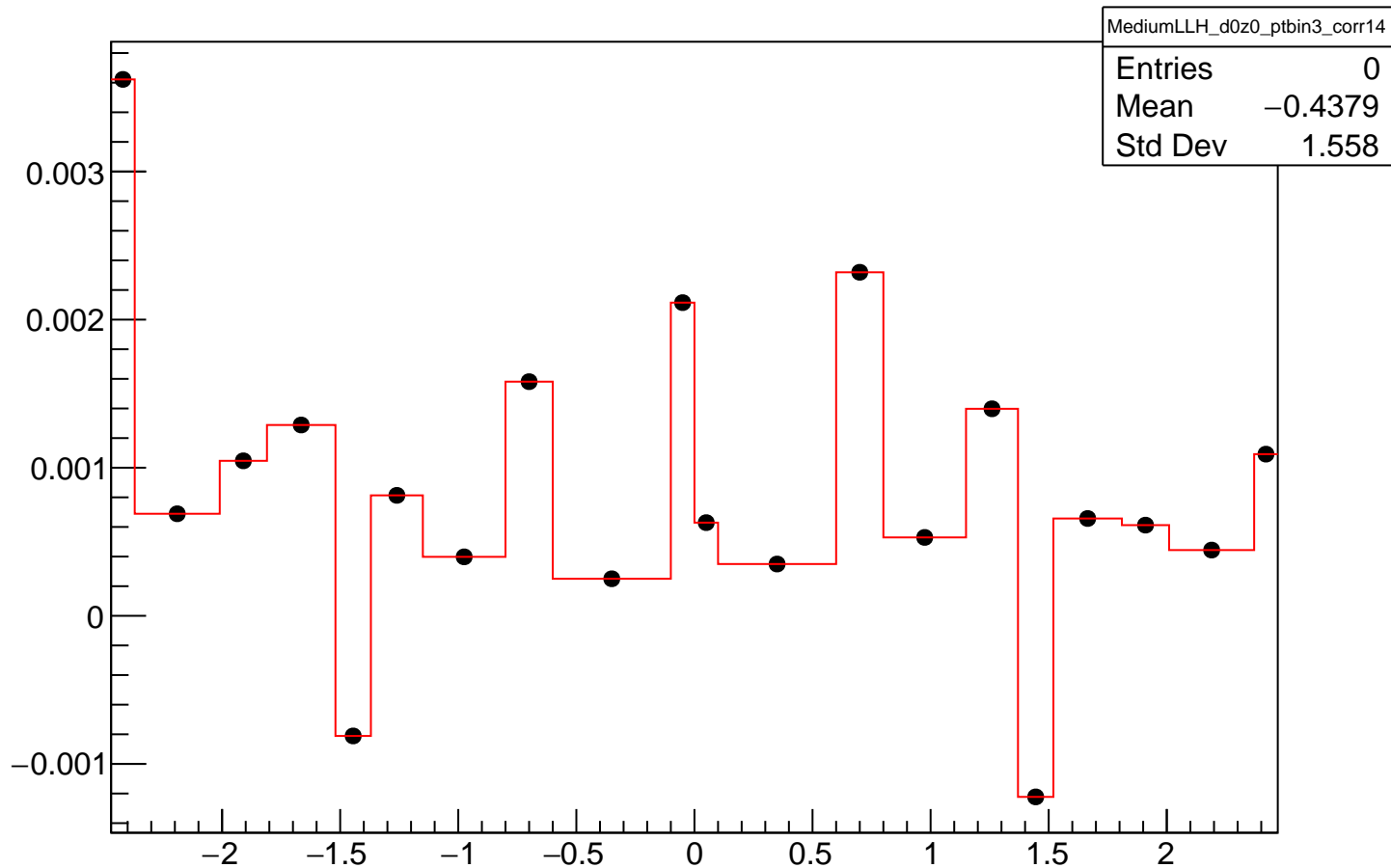
FullSim_MediumLLH_d0z0_corr14



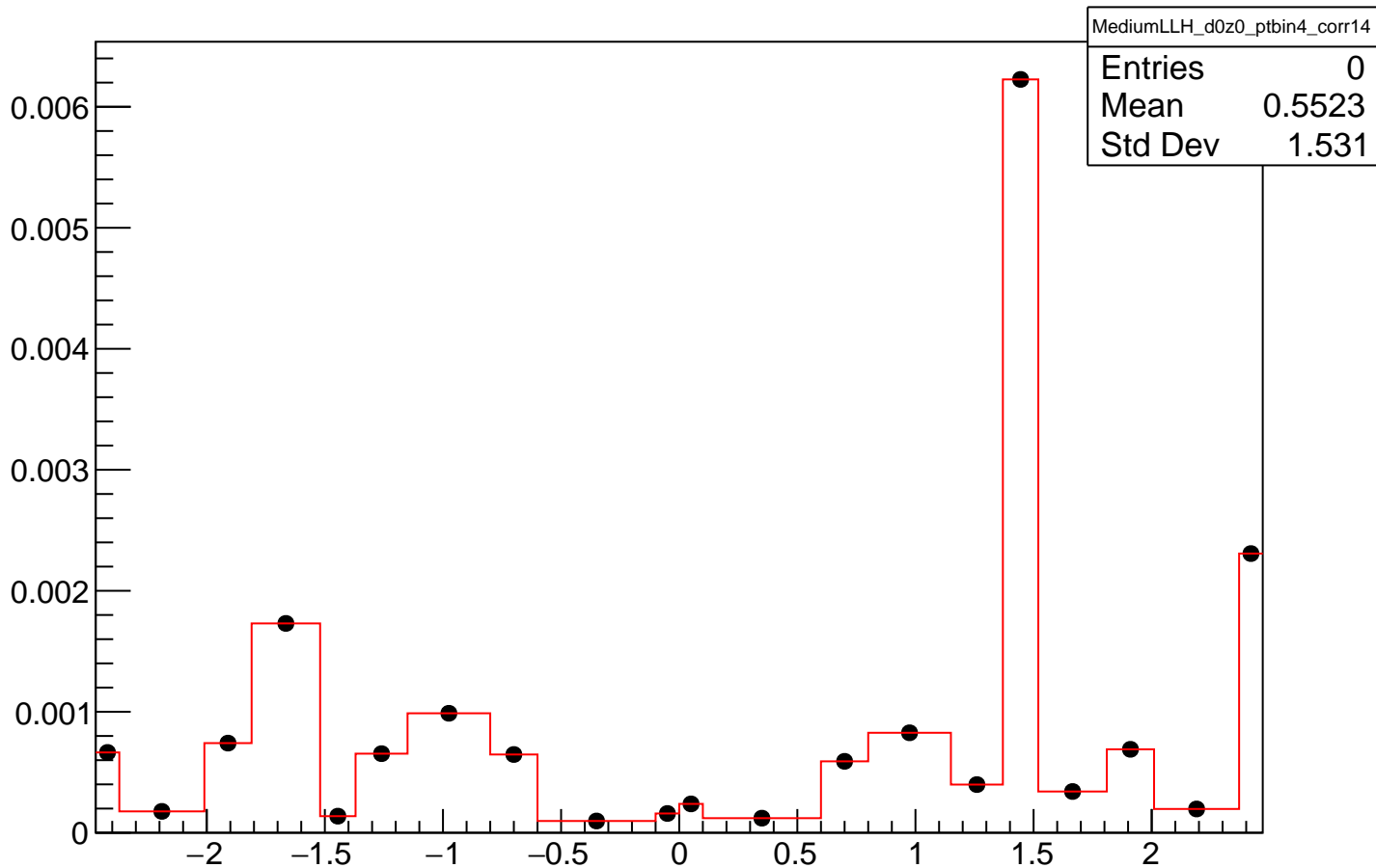
FullSim_MediumLLH_d0z0_corr14



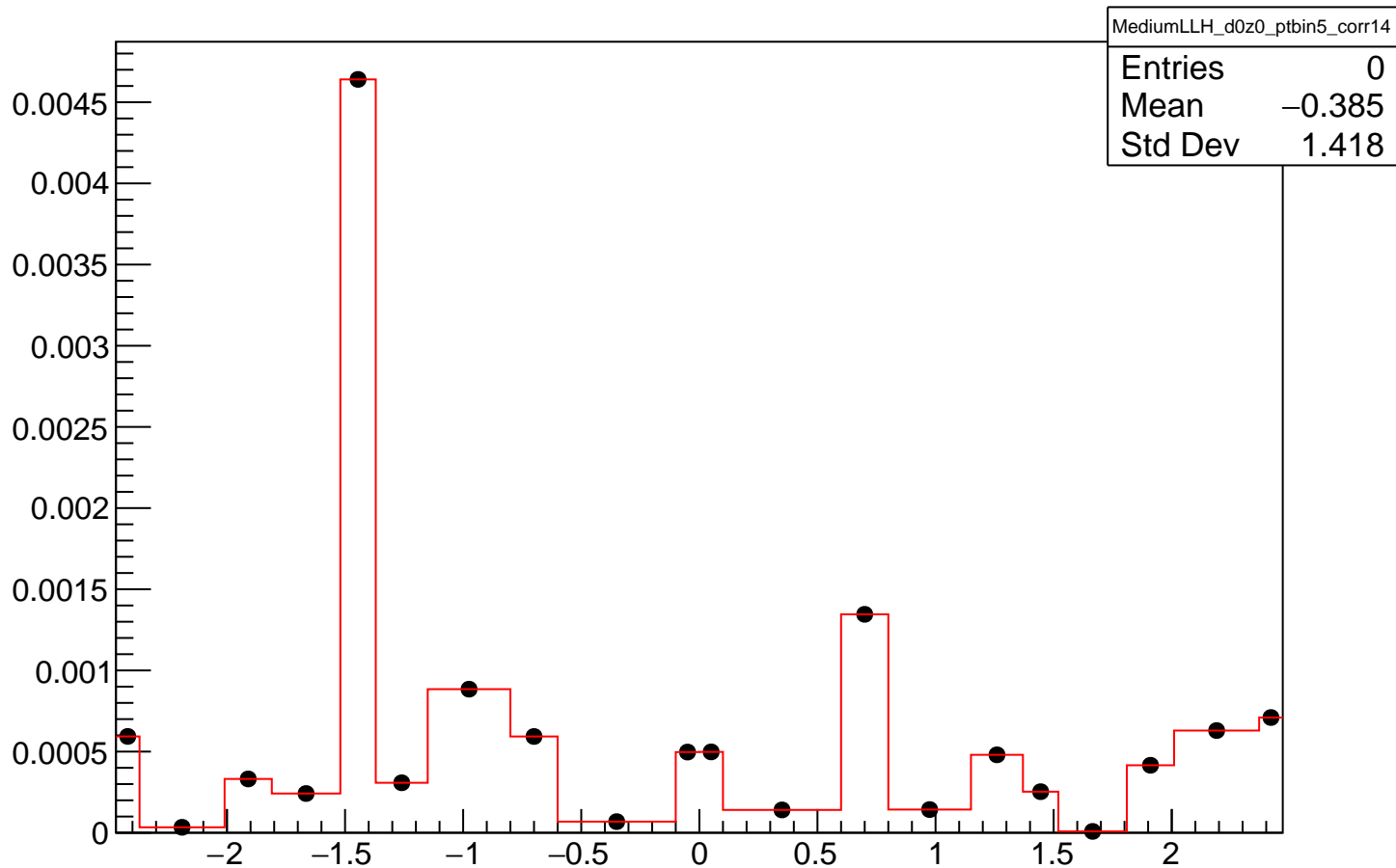
FullSim_MediumLLH_d0z0_corr14



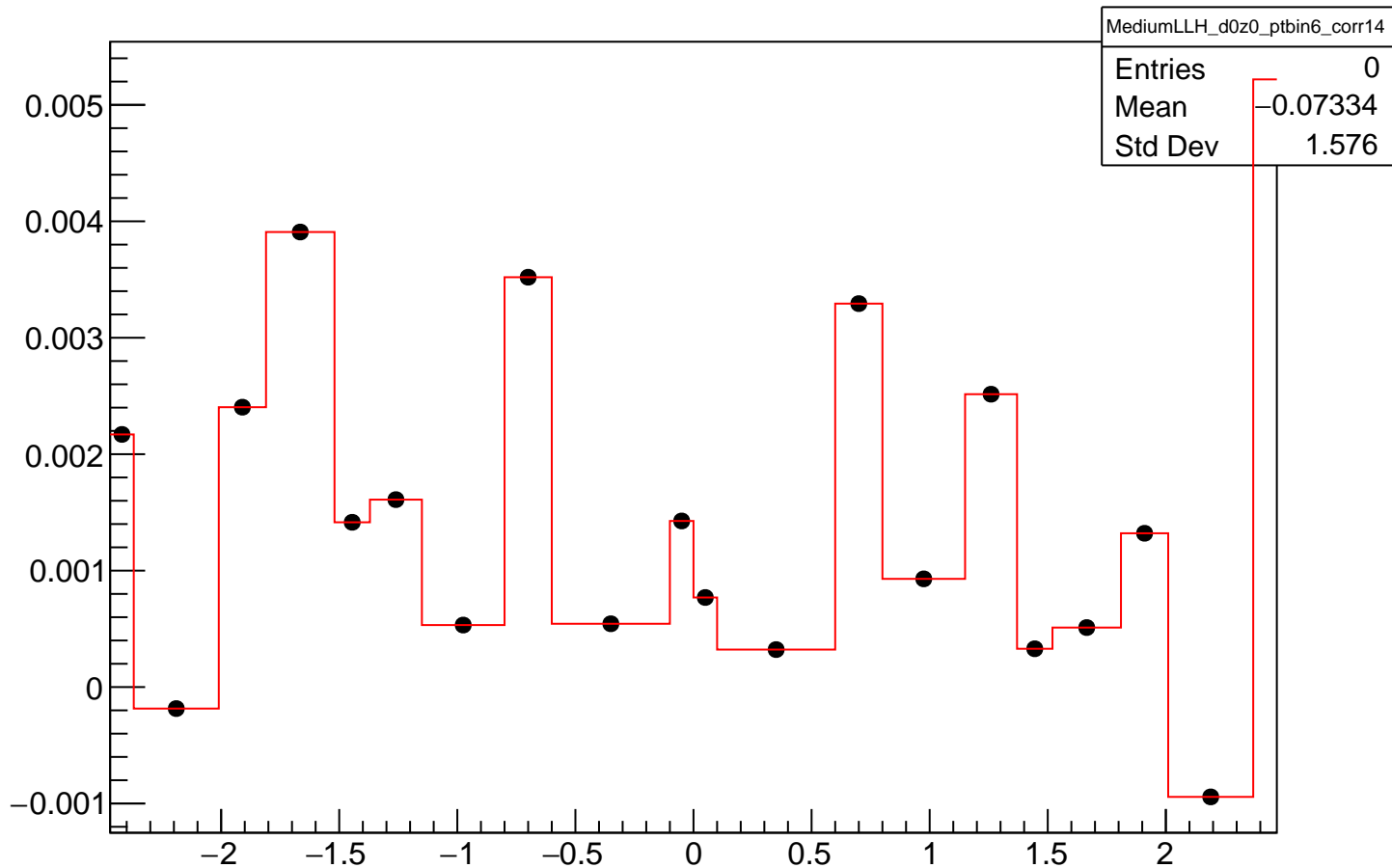
FullSim_MediumLLH_d0z0_corr14



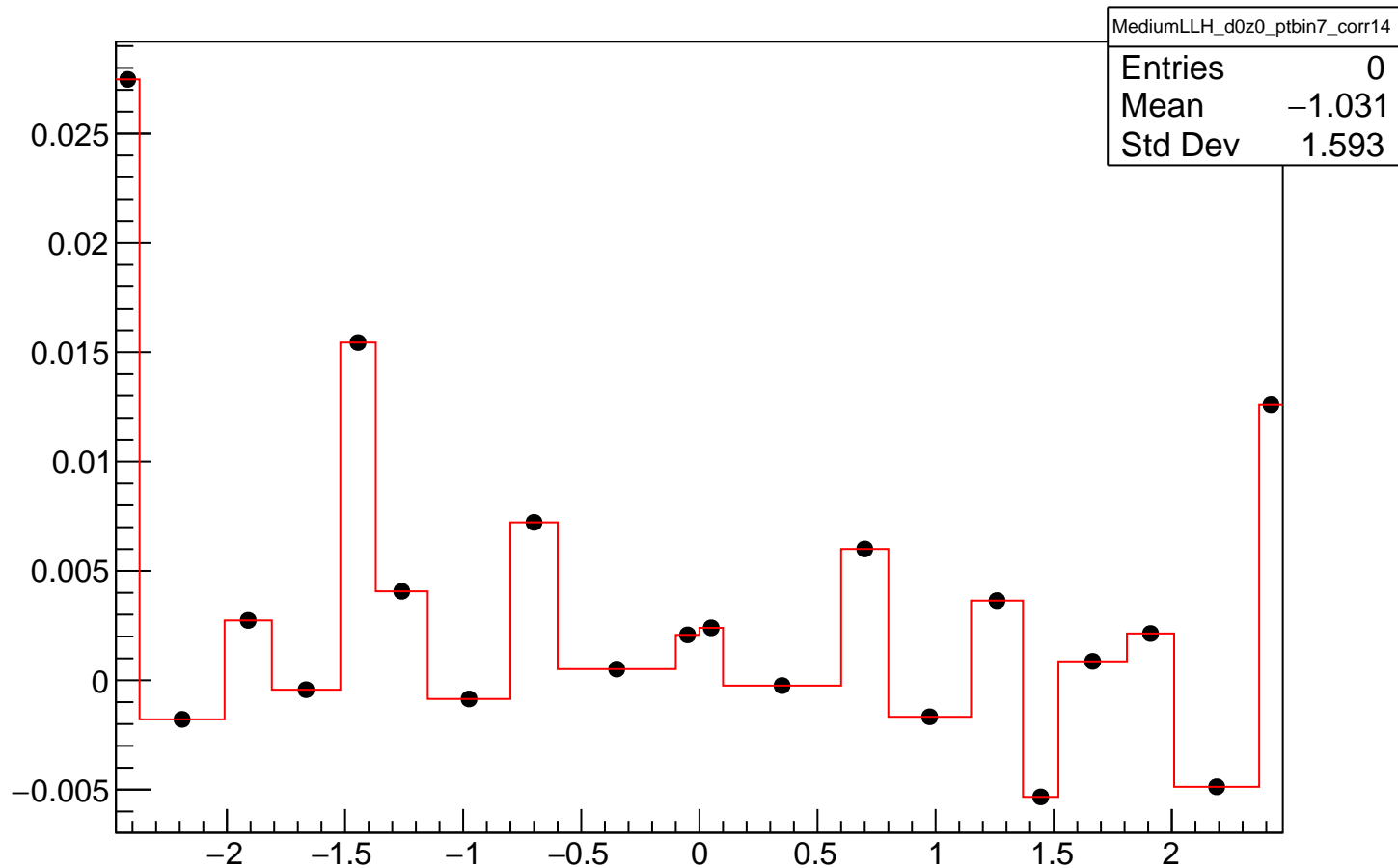
FullSim_MediumLLH_d0z0_corr14



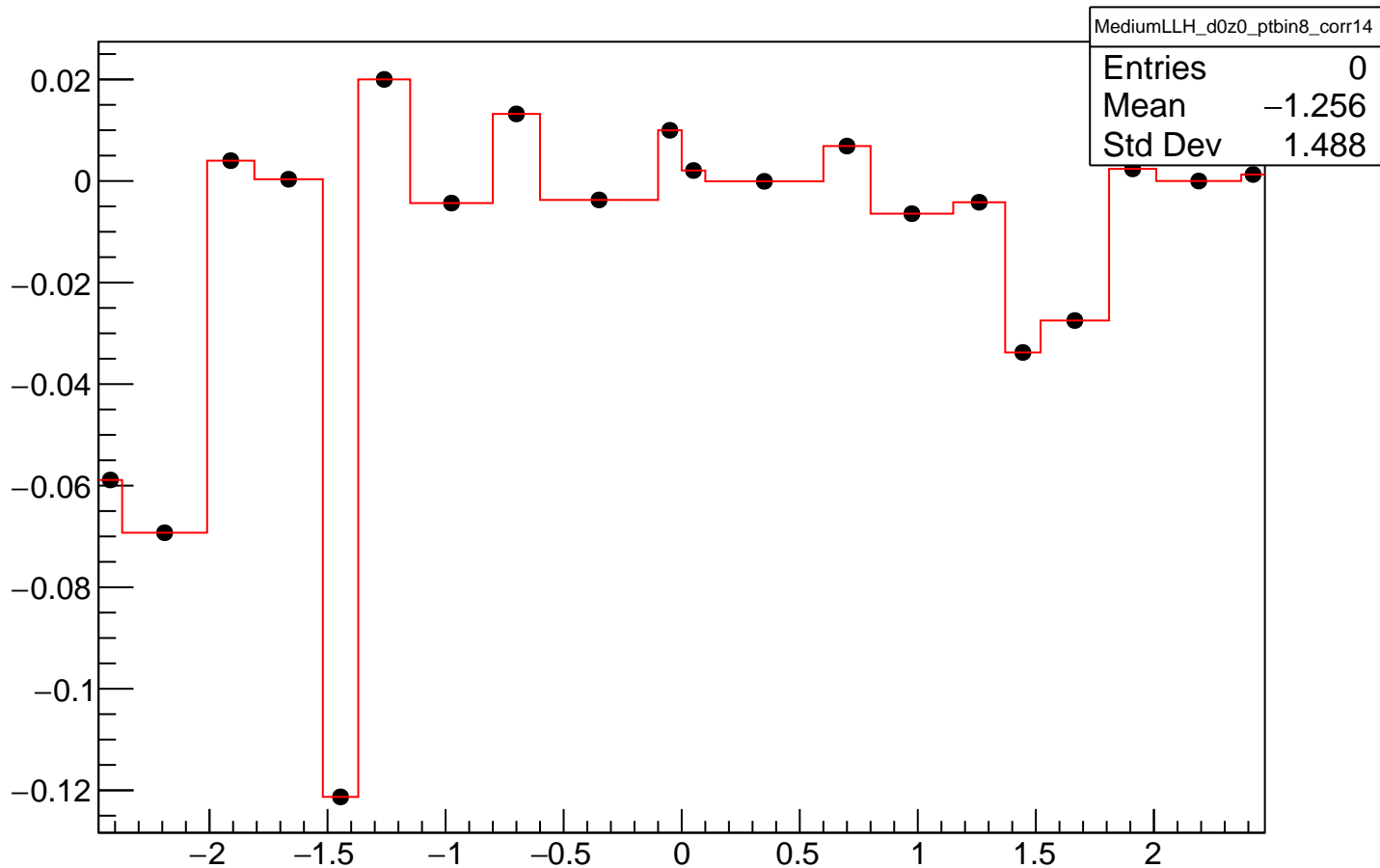
FullSim_MediumLLH_d0z0_corr14



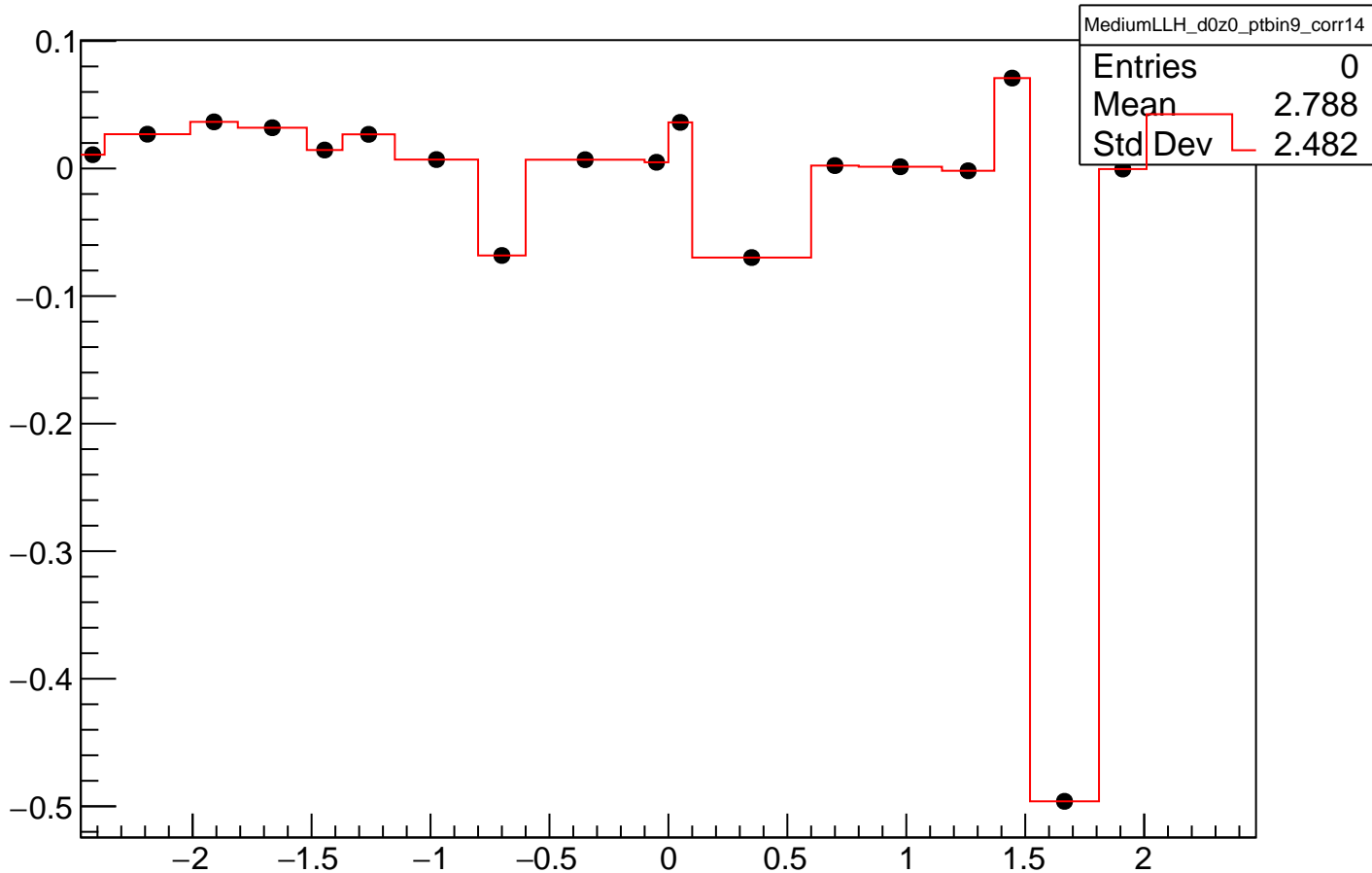
FullSim_MediumLLH_d0z0_corr14



FullSim_MediumLLH_d0z0_corr14



FullSim_MediumLLH_d0z0_corr14



FullSim_MediumLLH_d0z0_corr14

