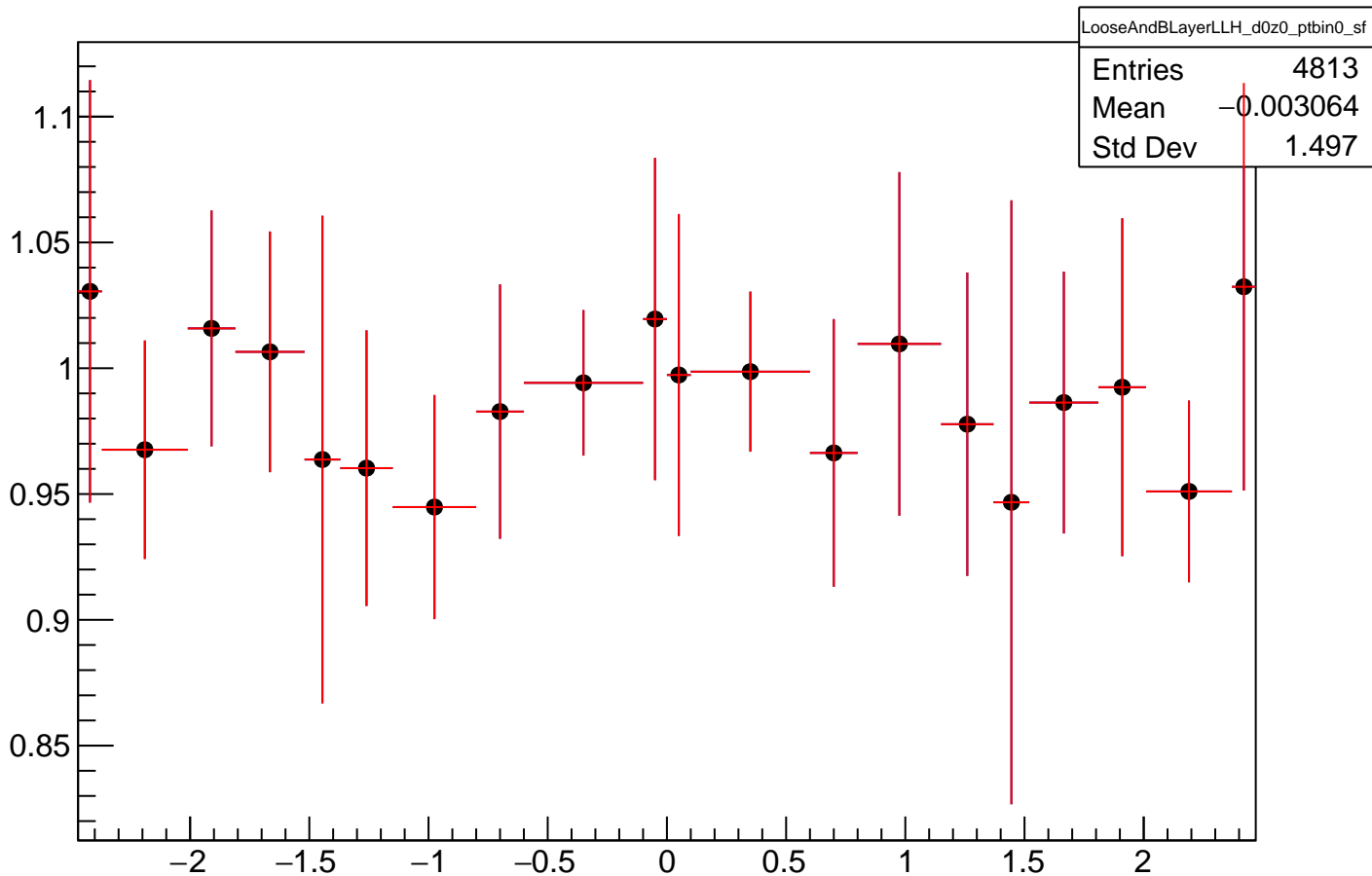
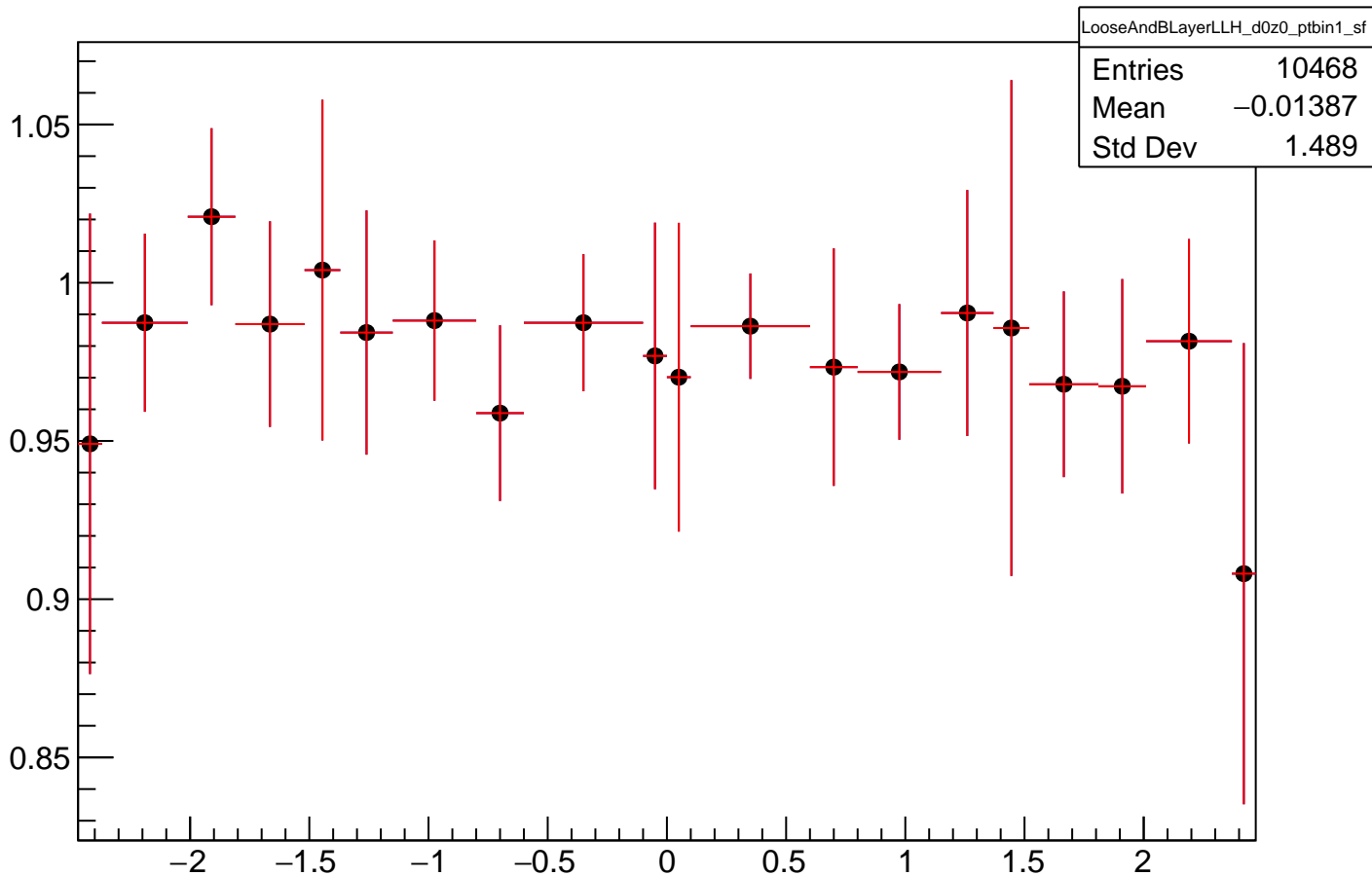


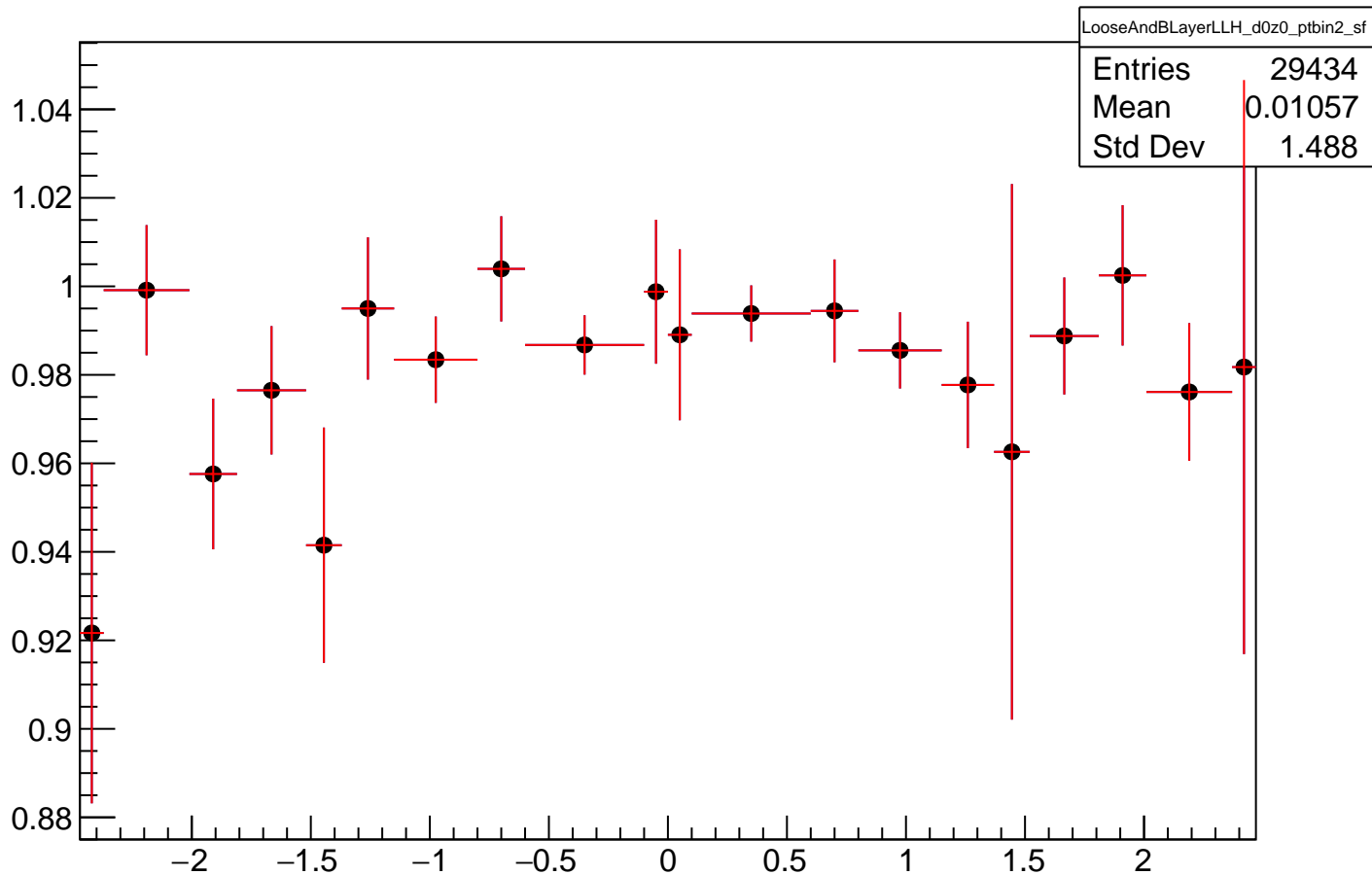
# FullSim\_LooseLLH\_d0z0\_sf



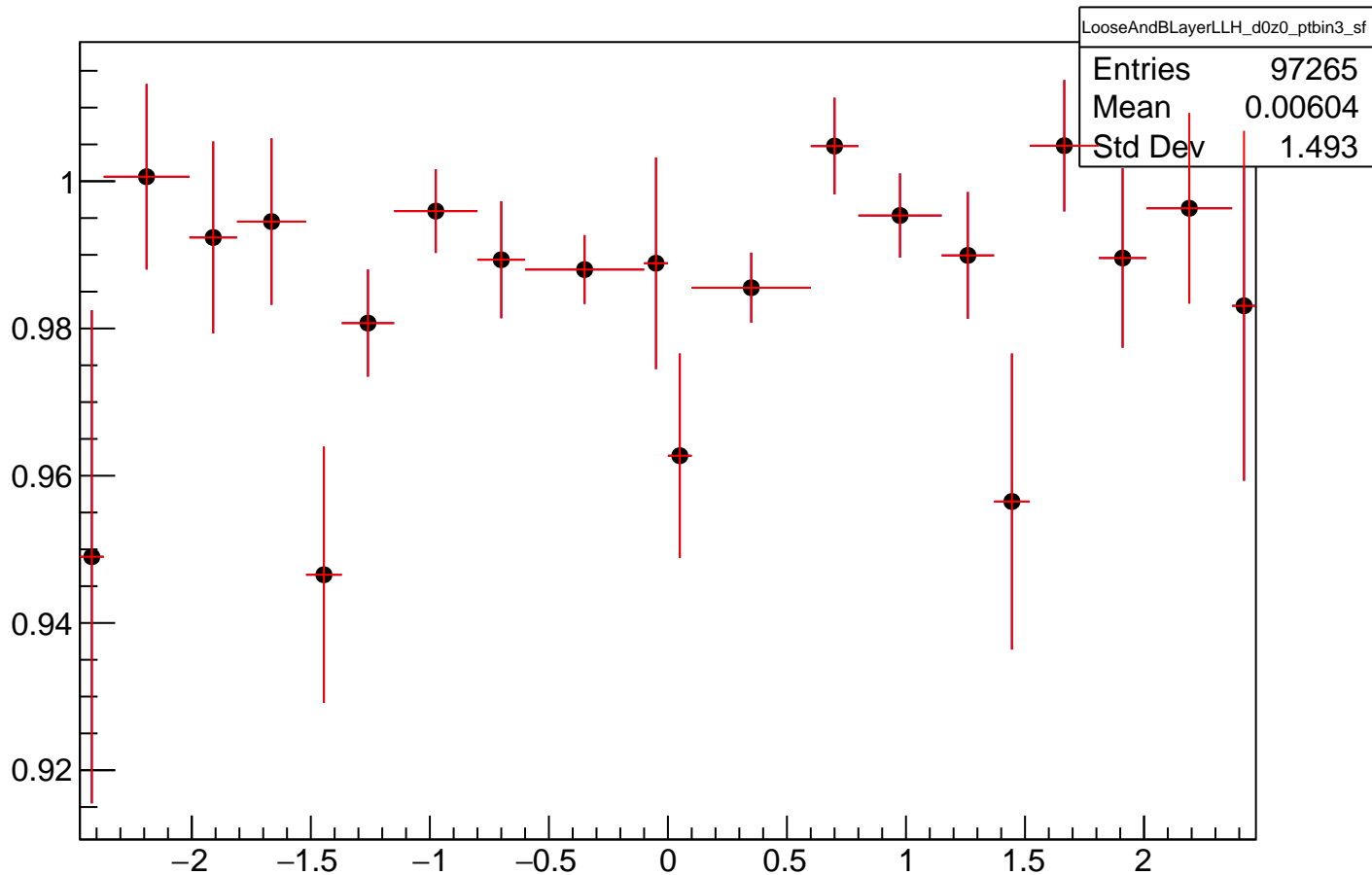
# FullSim\_LooseLLH\_d0z0\_sf



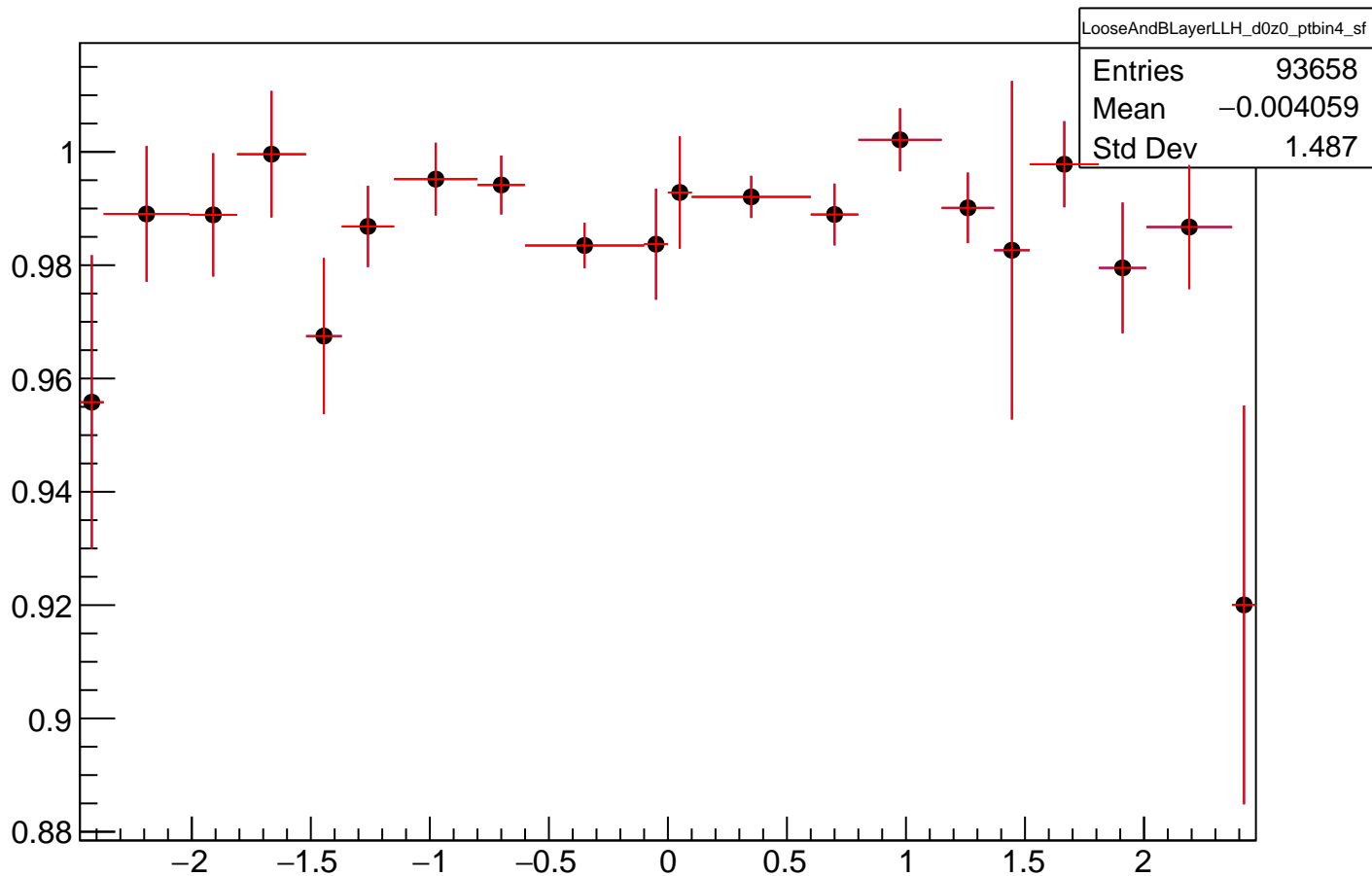
# FullSim\_LooseLLH\_d0z0\_sf



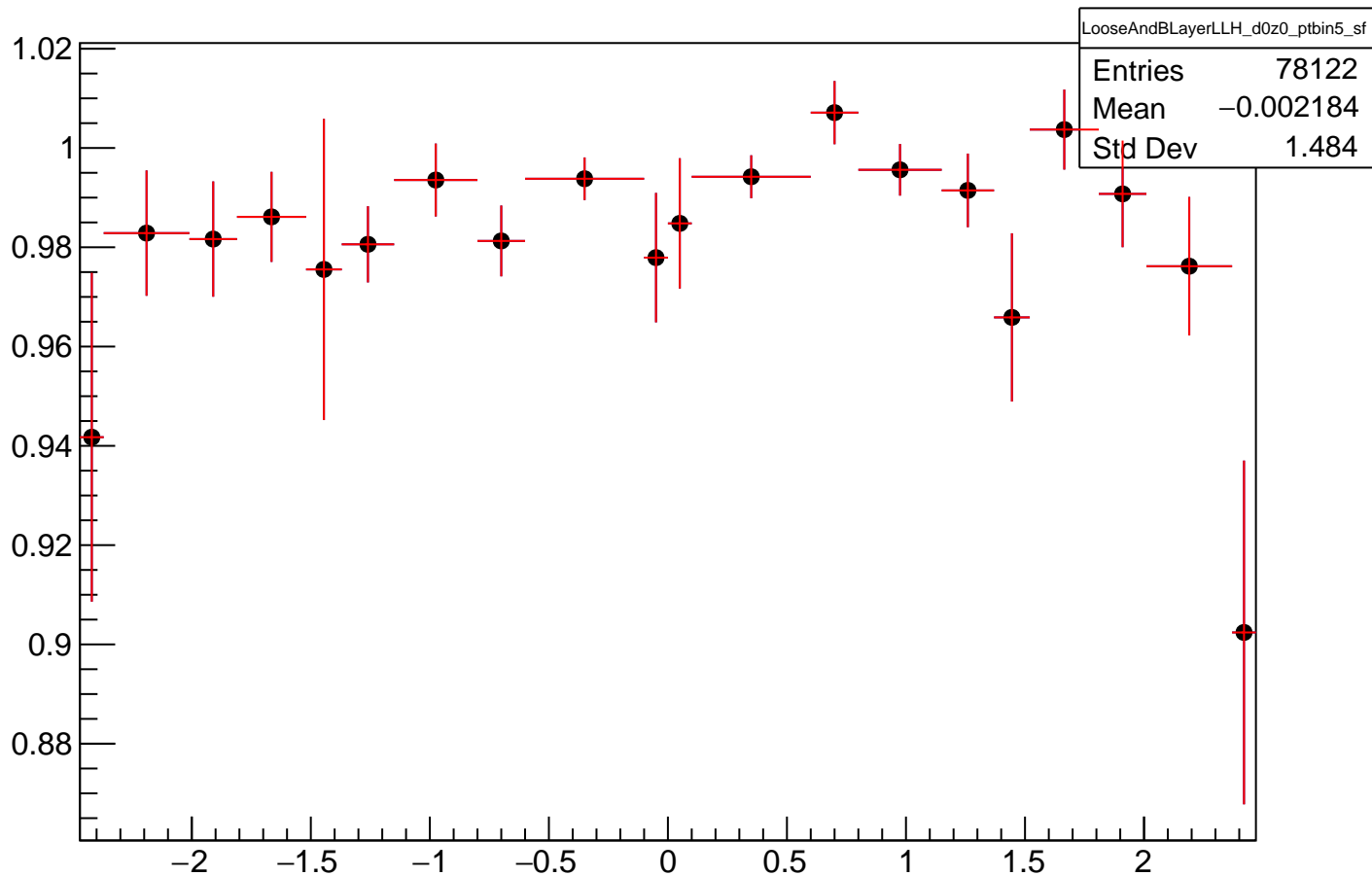
# FullSim\_LooseLLH\_d0z0\_sf



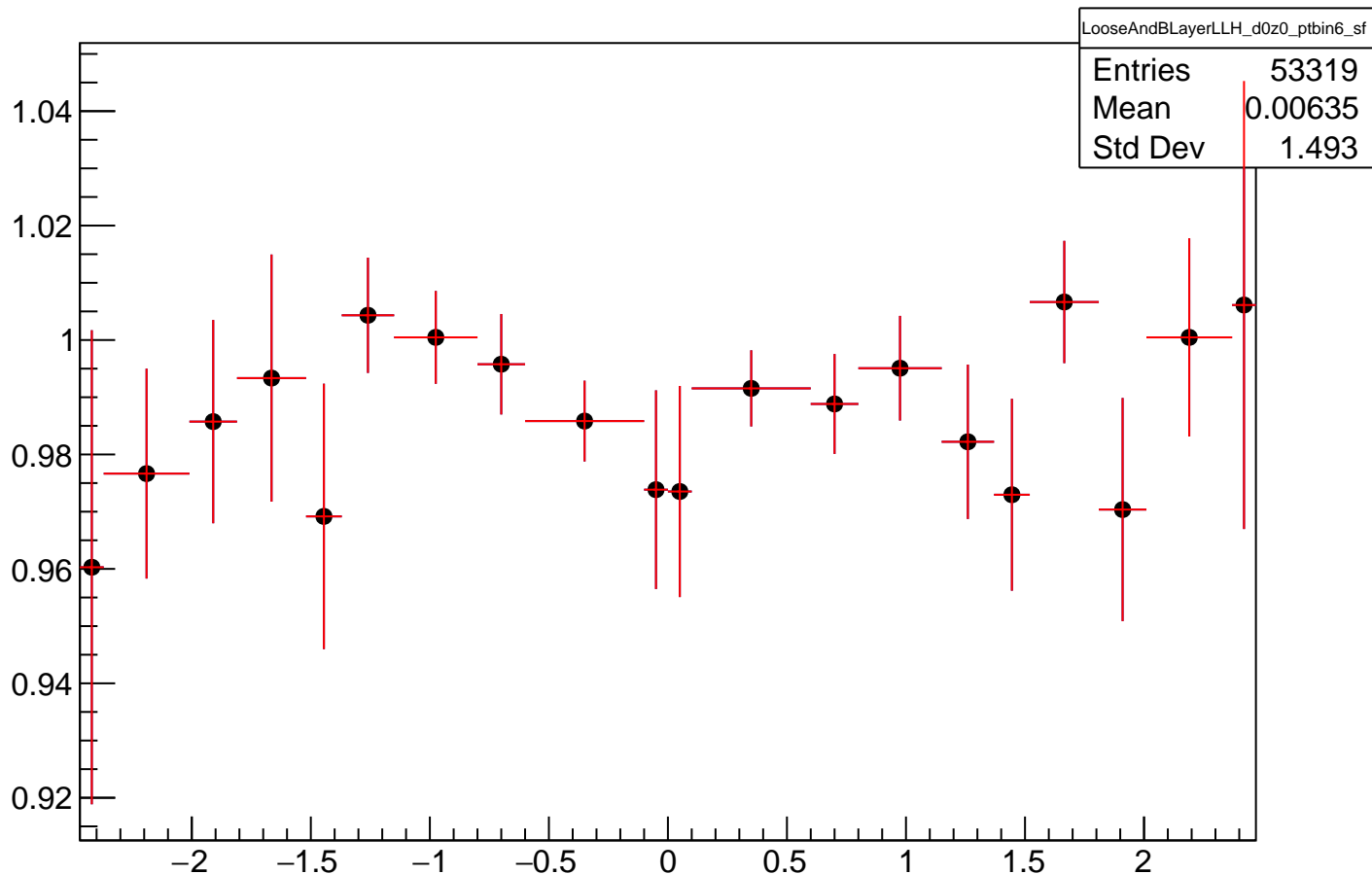
# FullSim\_LooseLLH\_d0z0\_sf



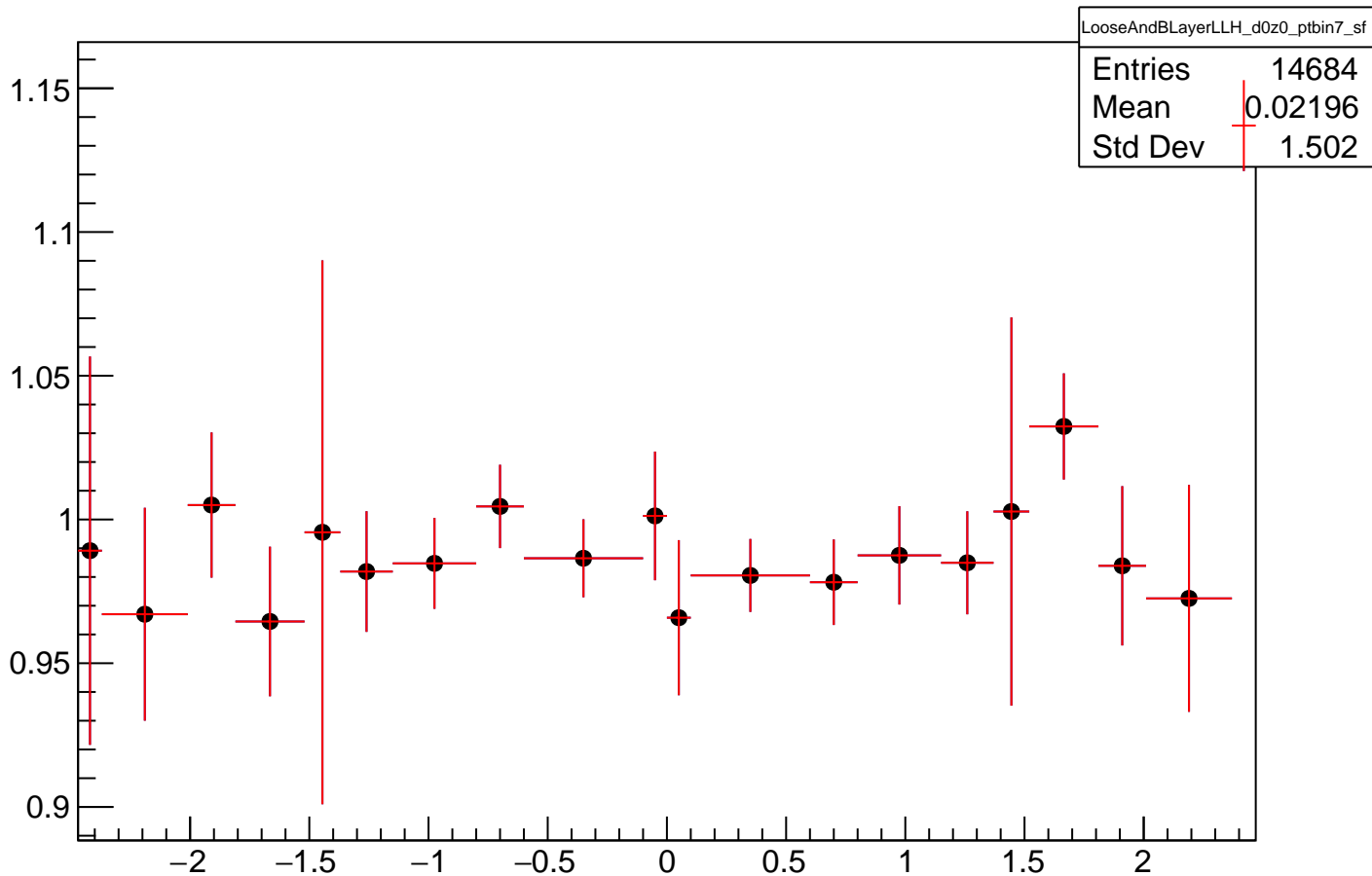
# FullSim\_LooseLLH\_d0z0\_sf



# FullSim\_LooseLLH\_d0z0\_sf

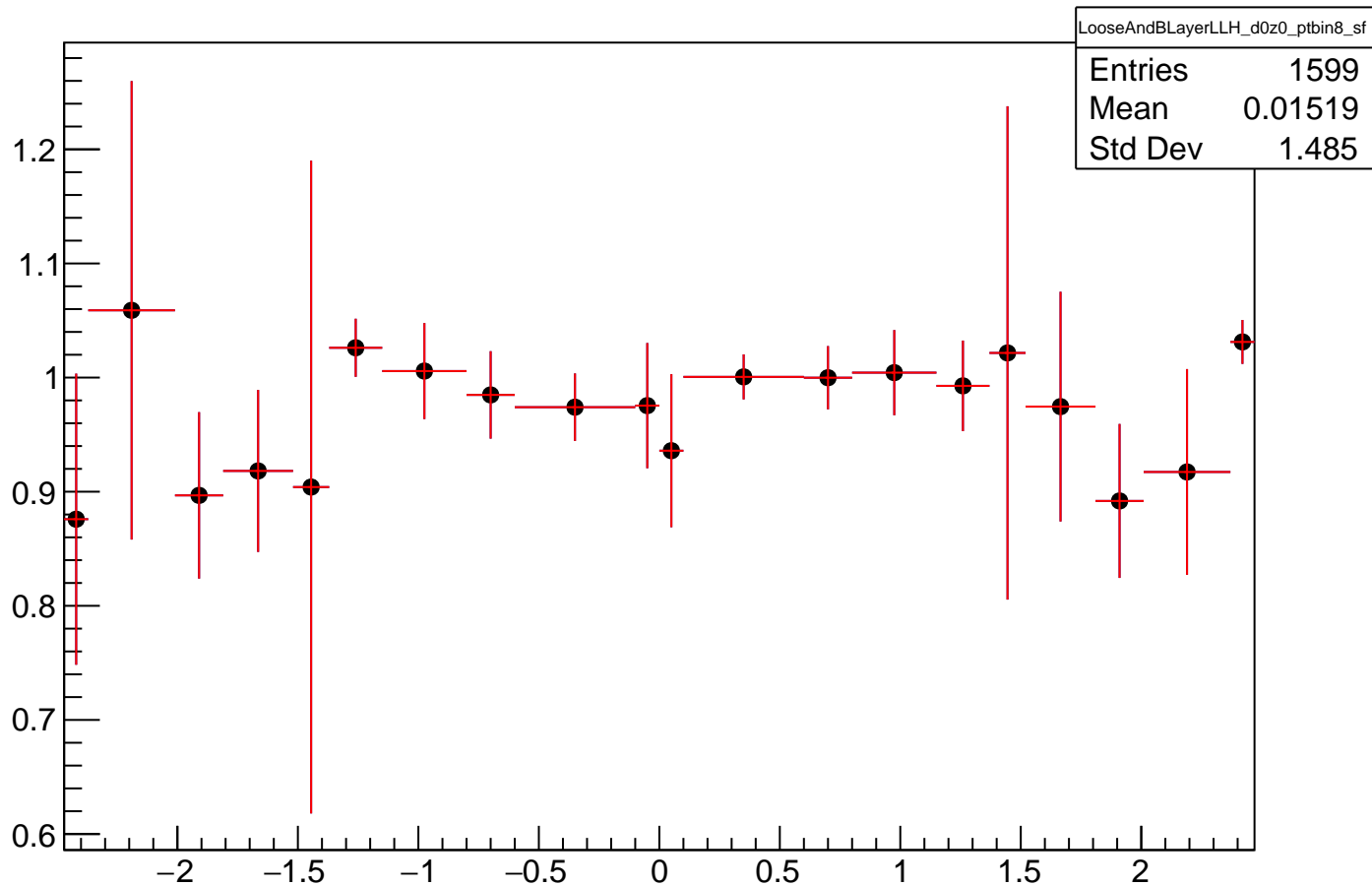


# FullSim\_LooseLLH\_d0z0\_sf

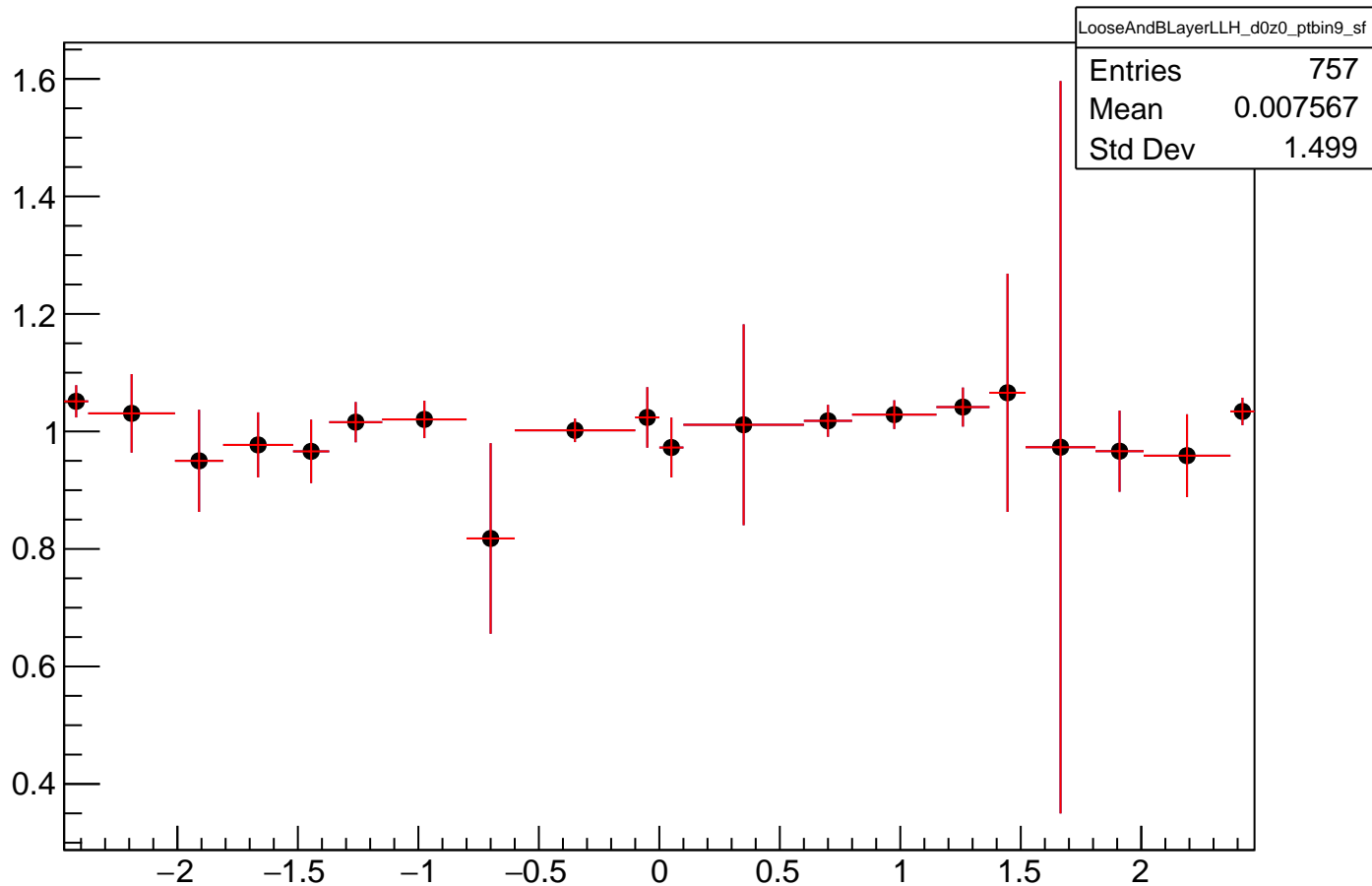




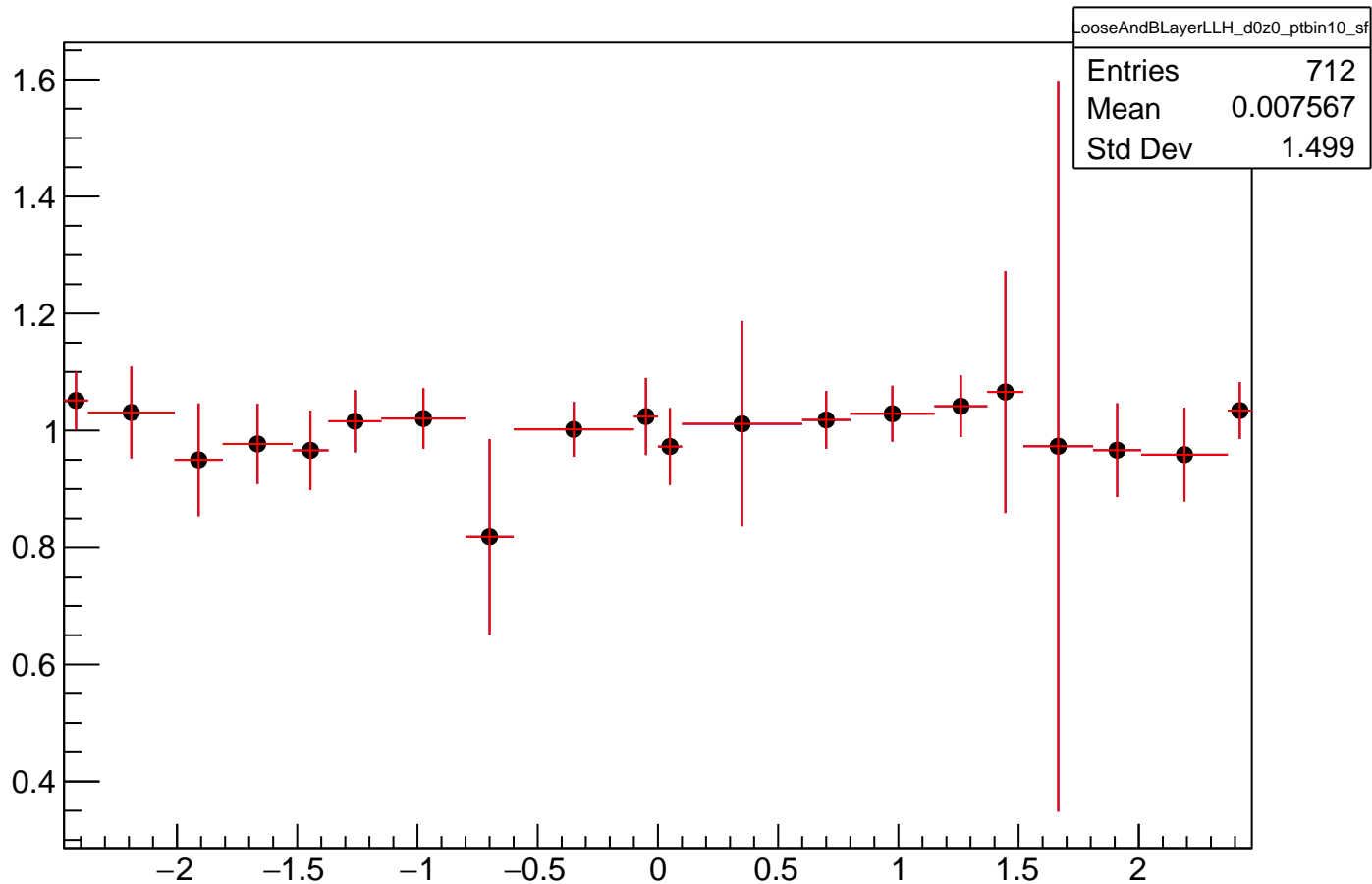
# FullSim\_LooseLLH\_d0z0\_sf



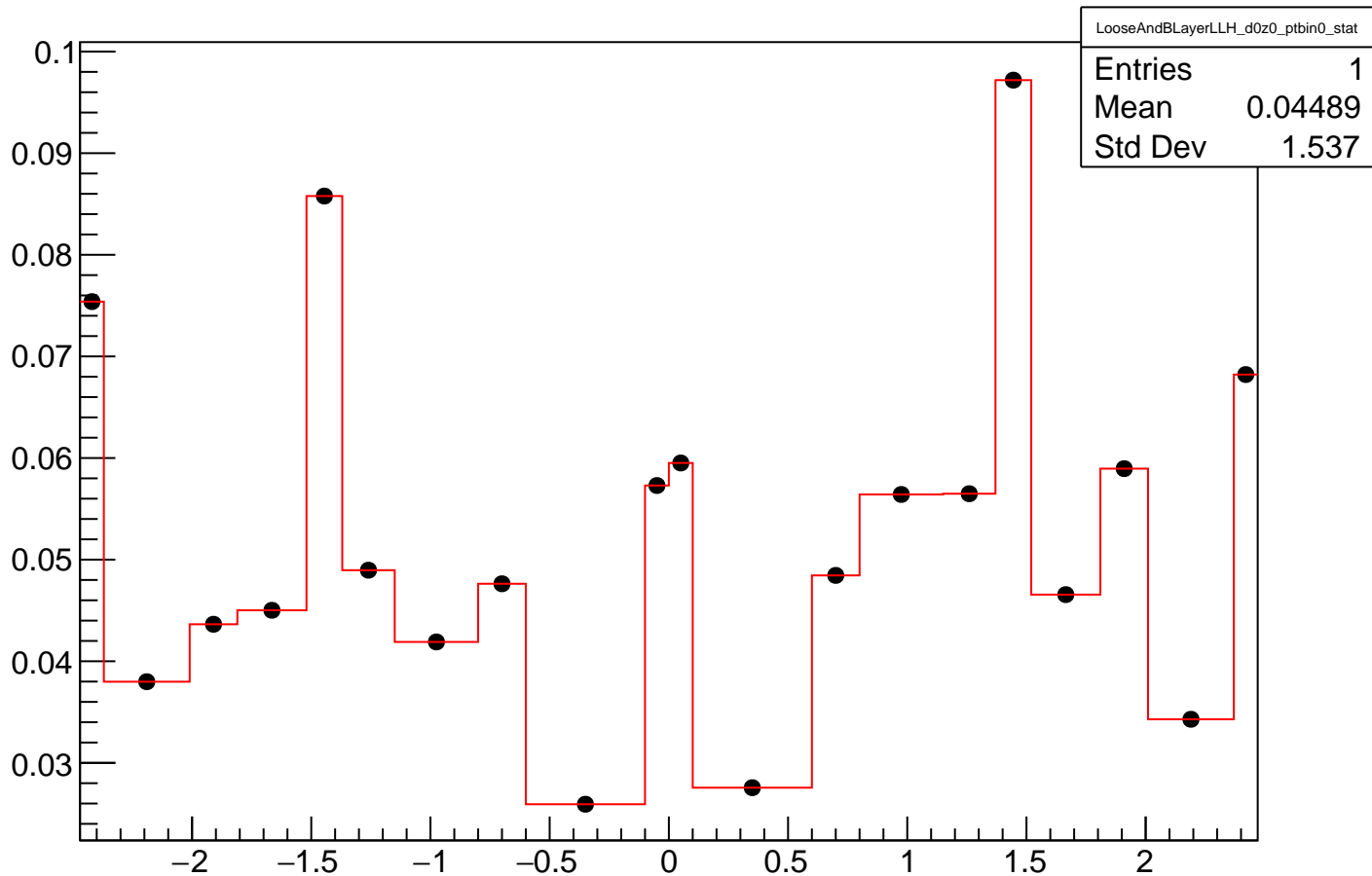
# FullSim\_LooseLLH\_d0z0\_sf



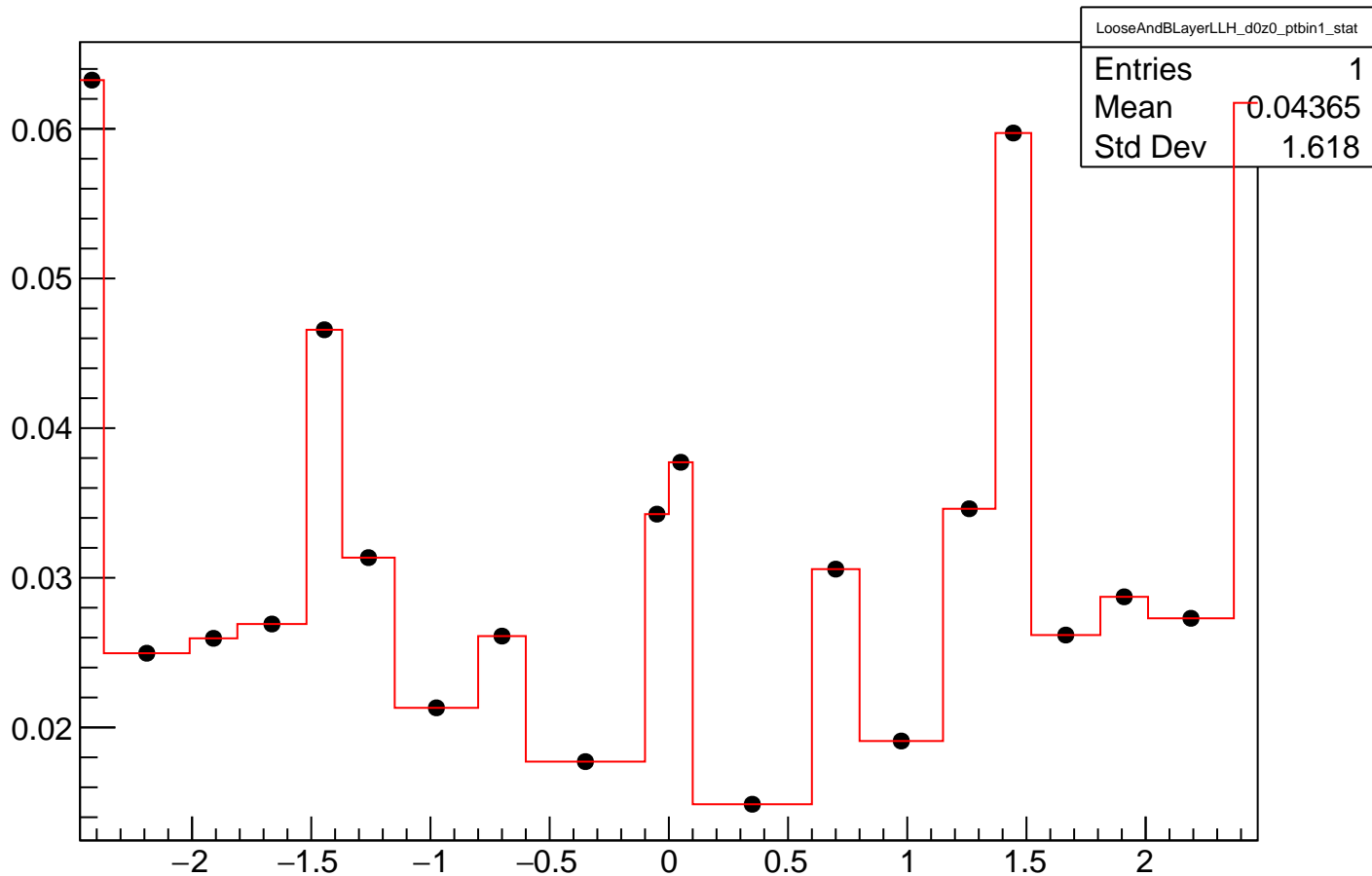
# FullSim\_LooseLLH\_d0z0\_sf



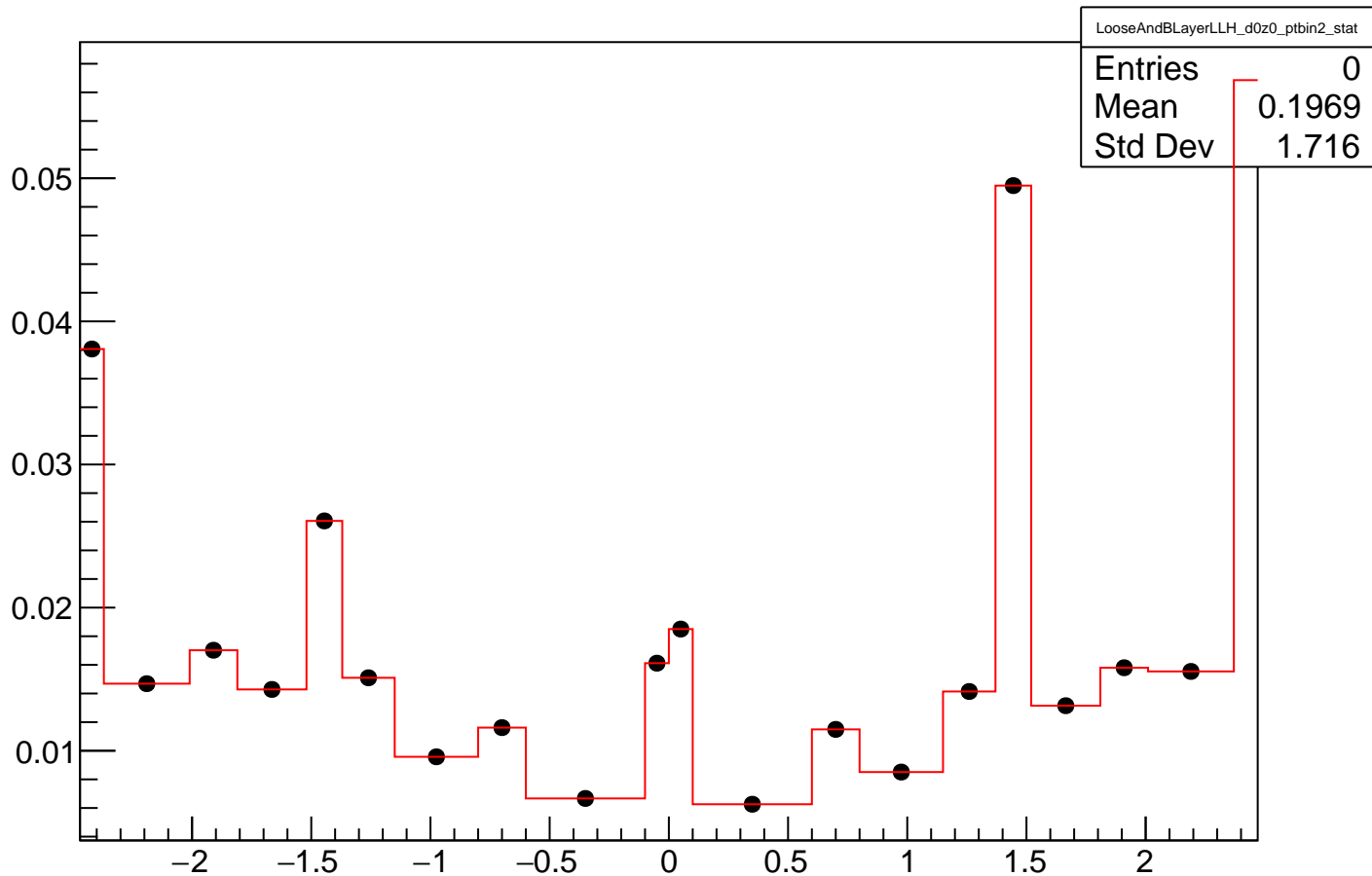
# FullSim\_LooseLLH\_d0z0\_stat



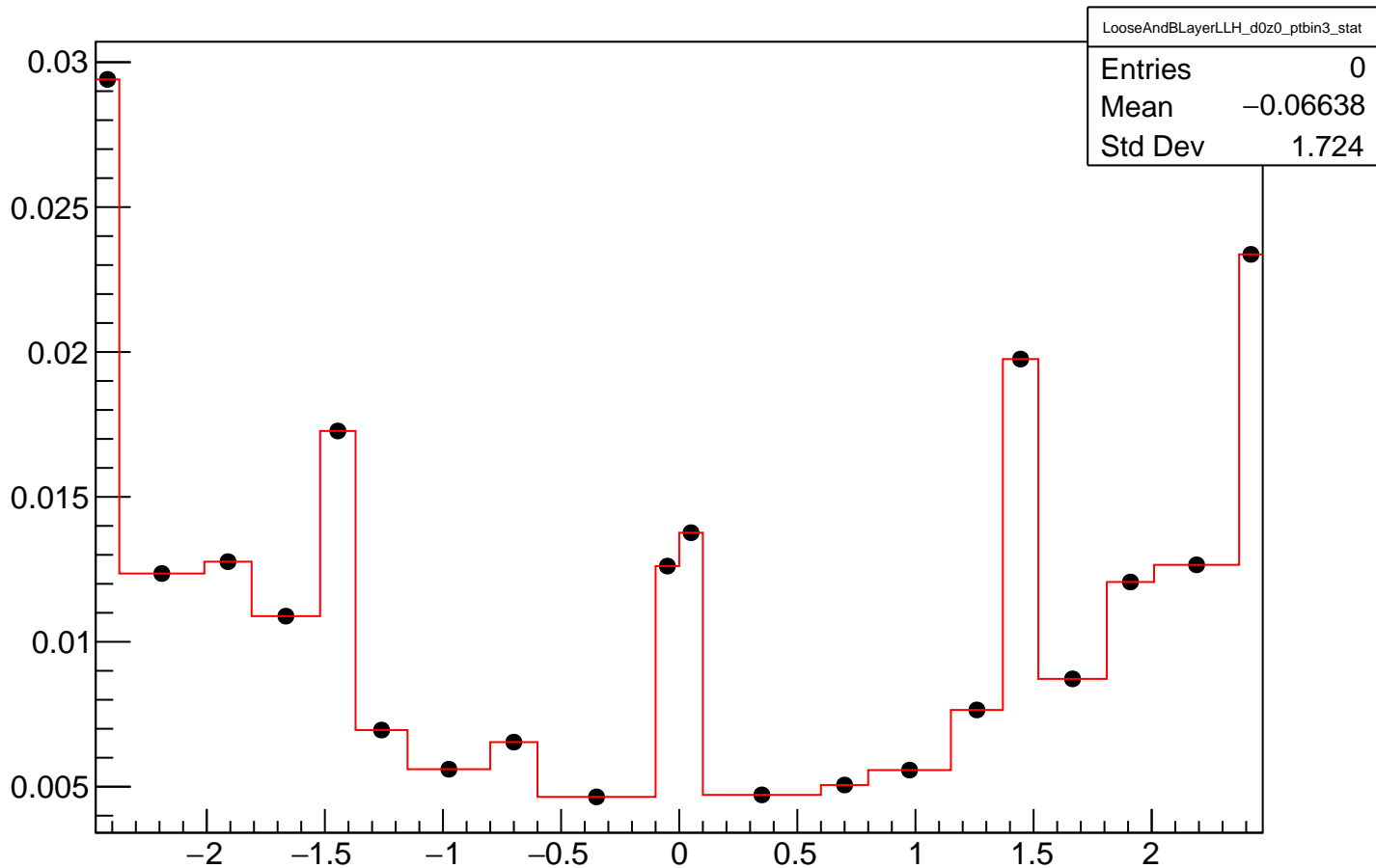
# FullSim\_LooseLLH\_d0z0\_stat



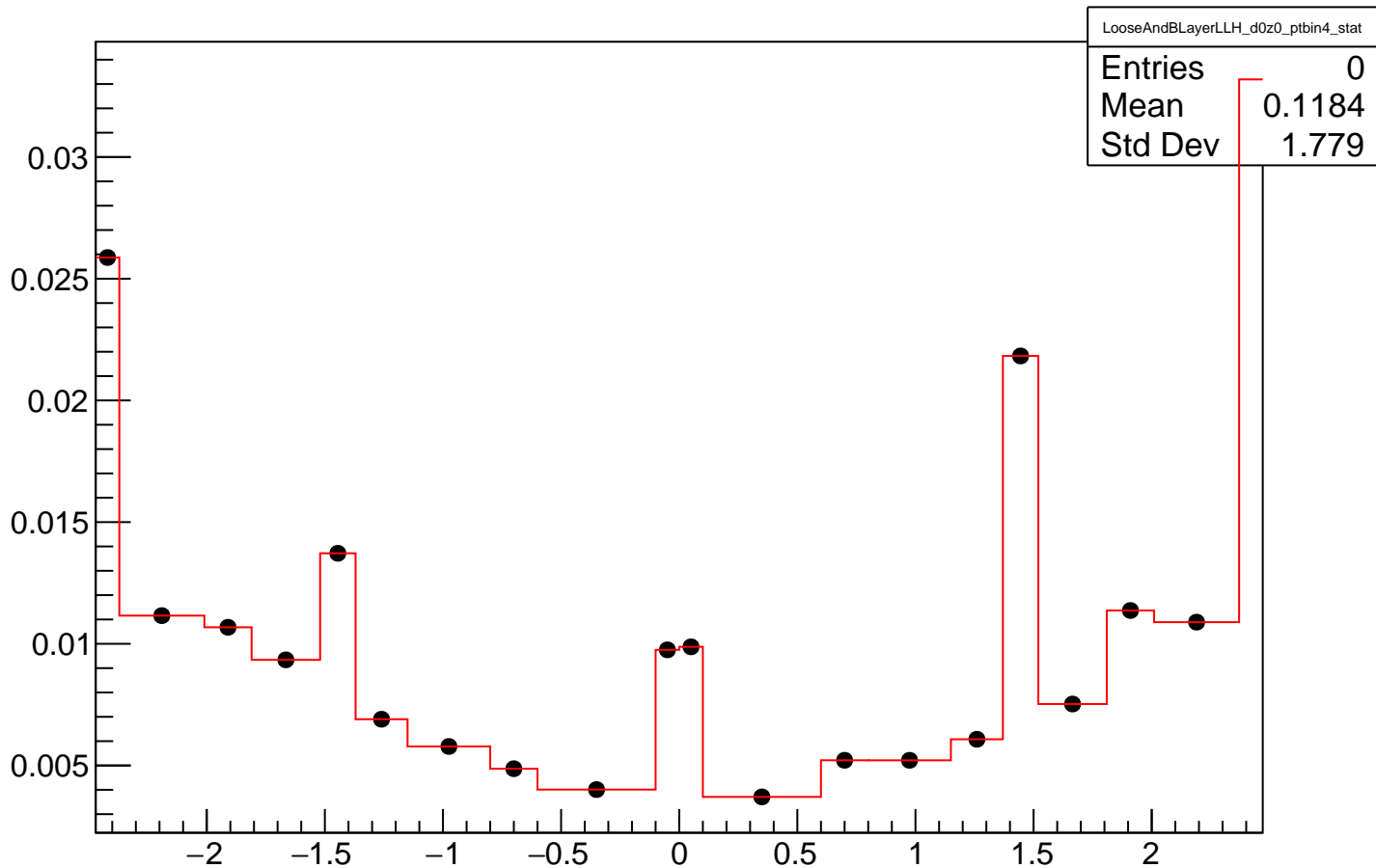
# FullSim\_LooseLLH\_d0z0\_stat



# FullSim\_LooseLLH\_d0z0\_stat

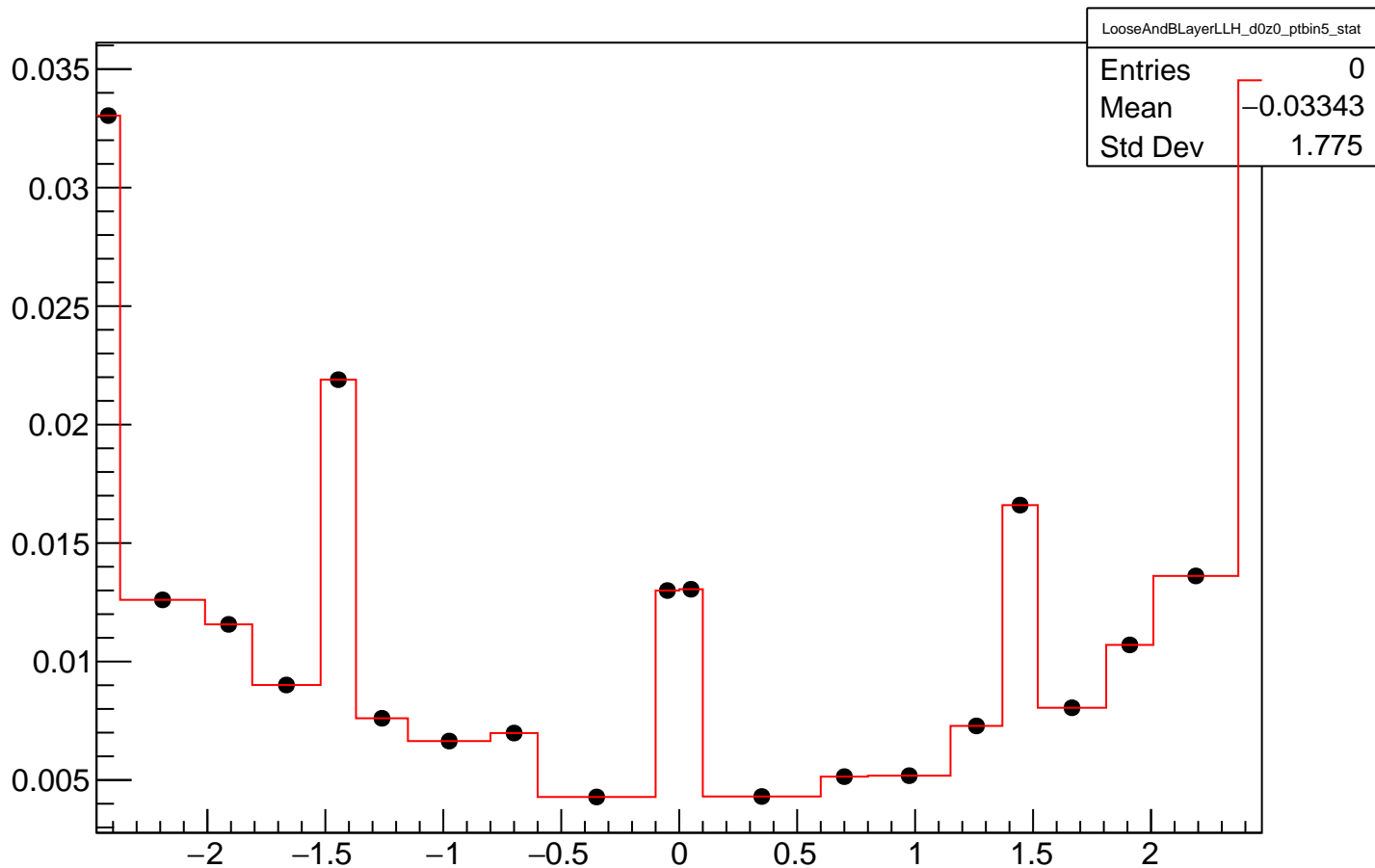


# FullSim\_LooseLLH\_d0z0\_stat

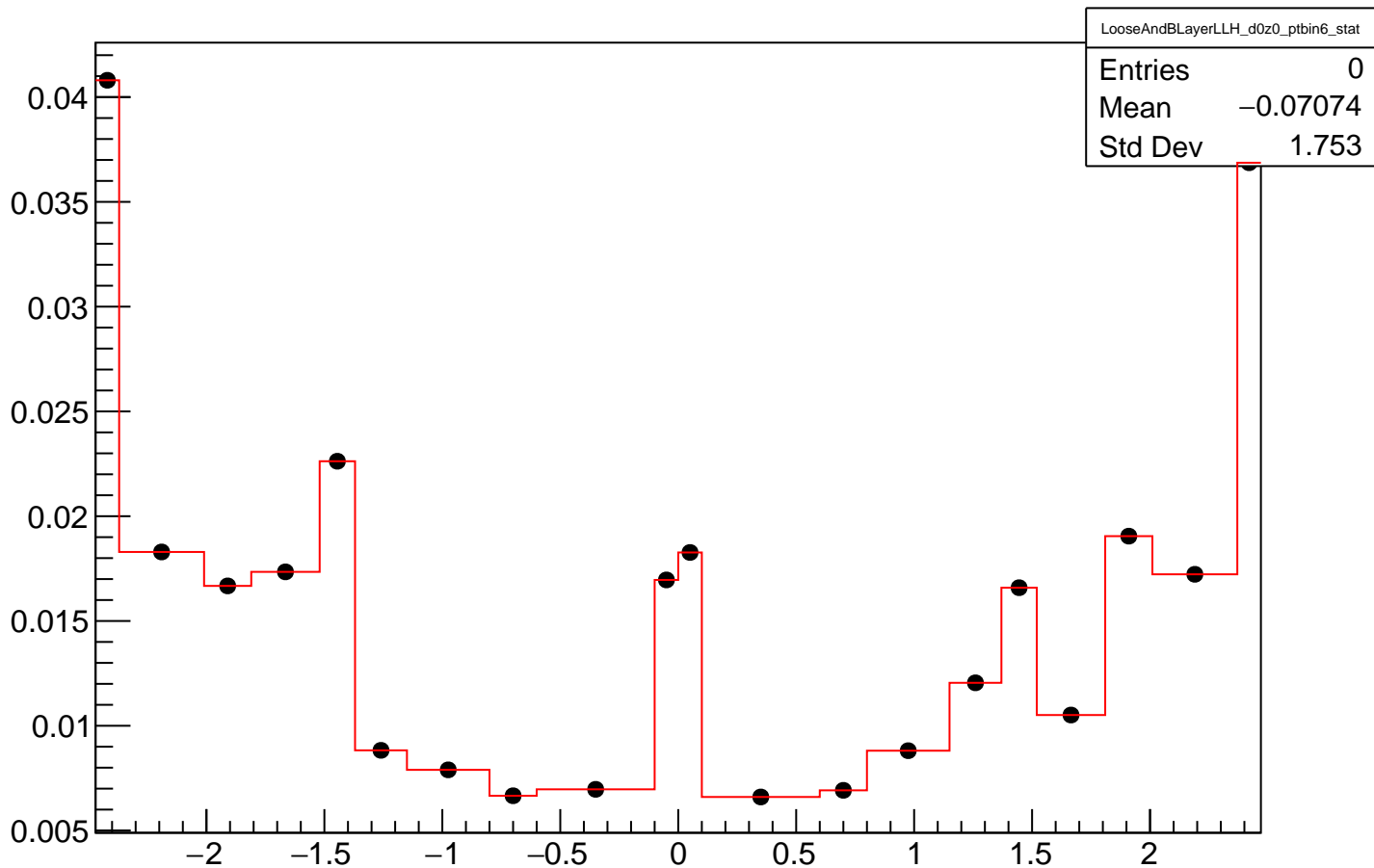




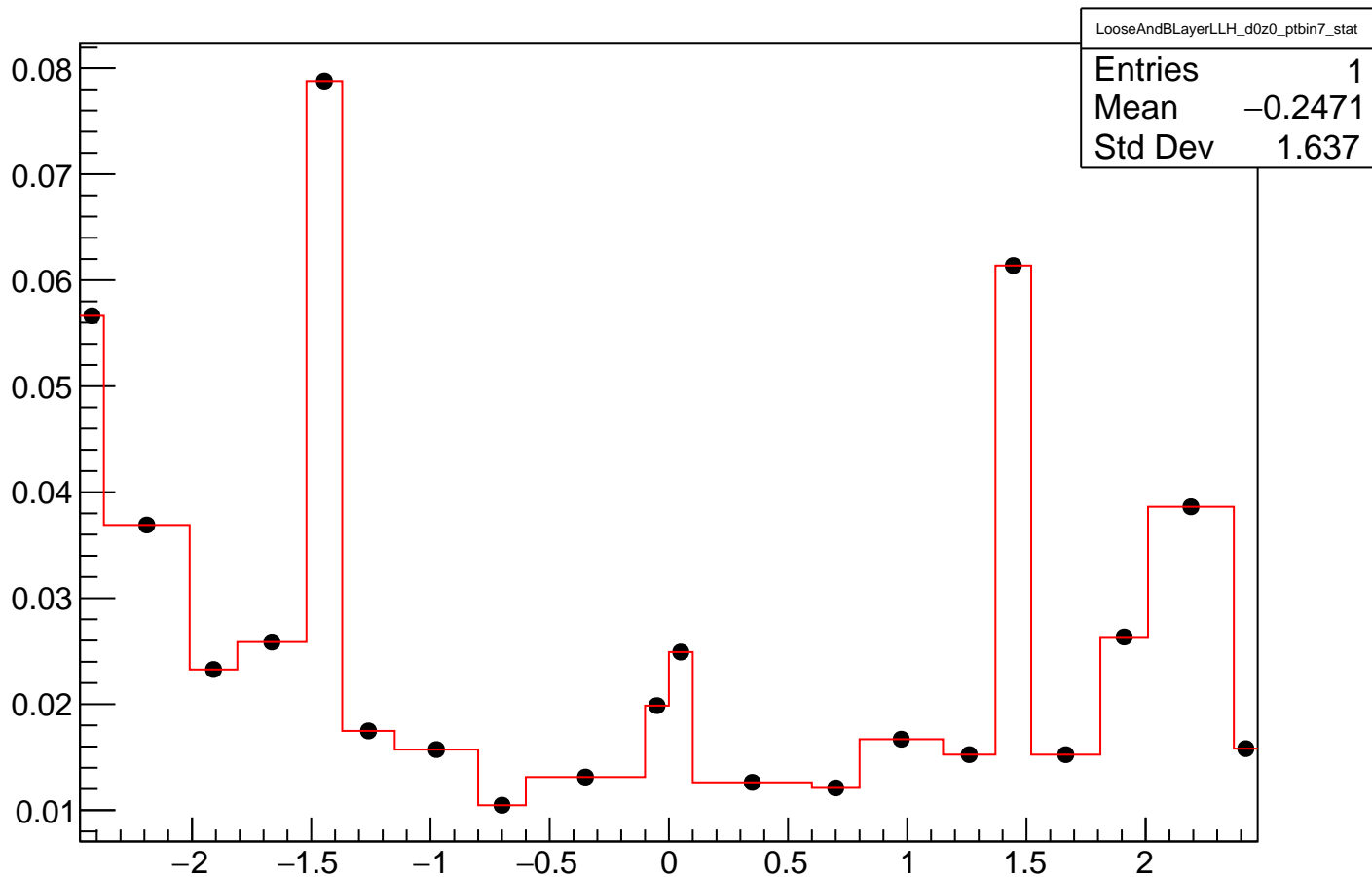
# FullSim\_LooseLLH\_d0z0\_stat



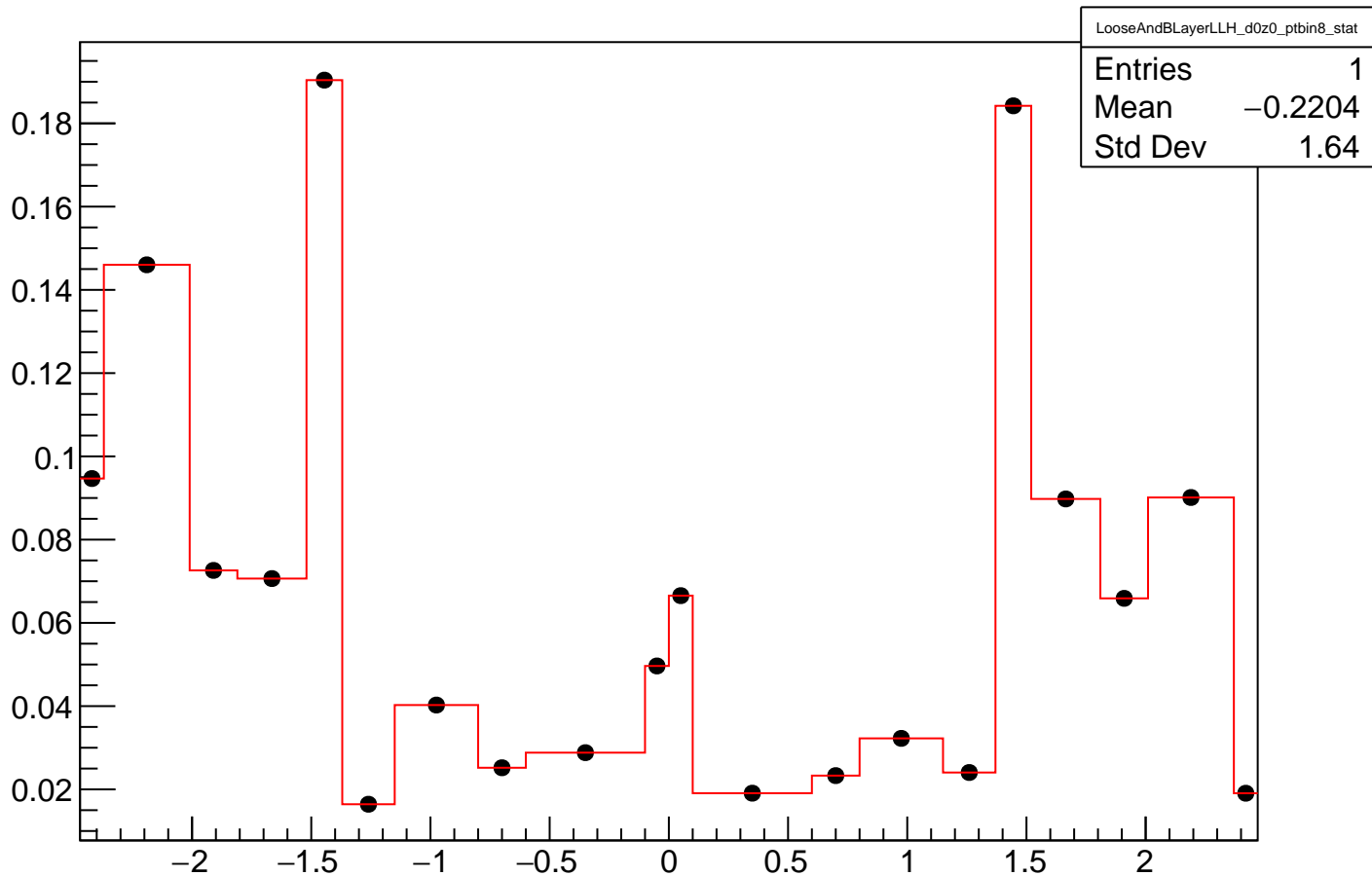
# FullSim\_LooseLLH\_d0z0\_stat



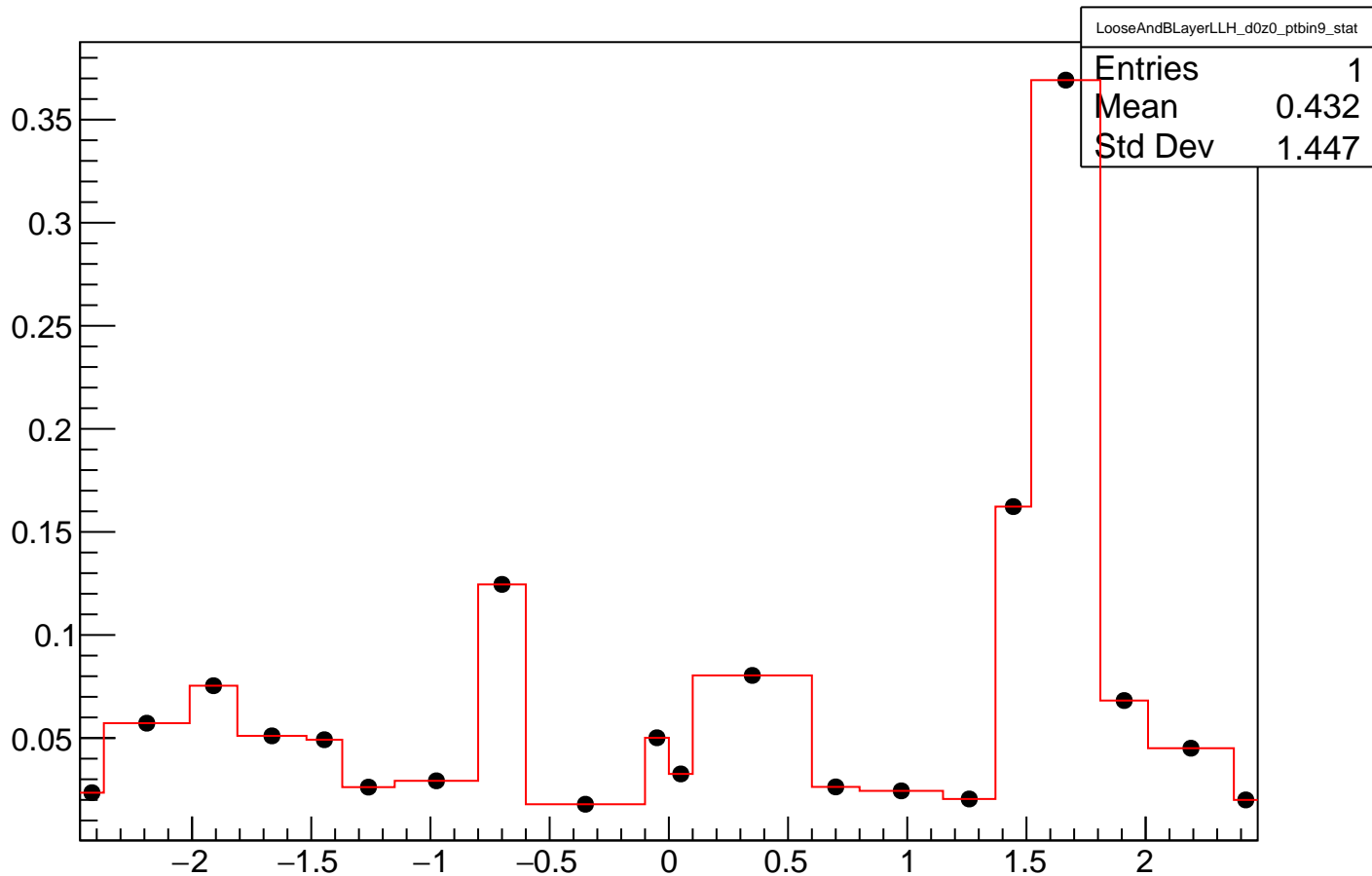
# FullSim\_LooseLLH\_d0z0\_stat



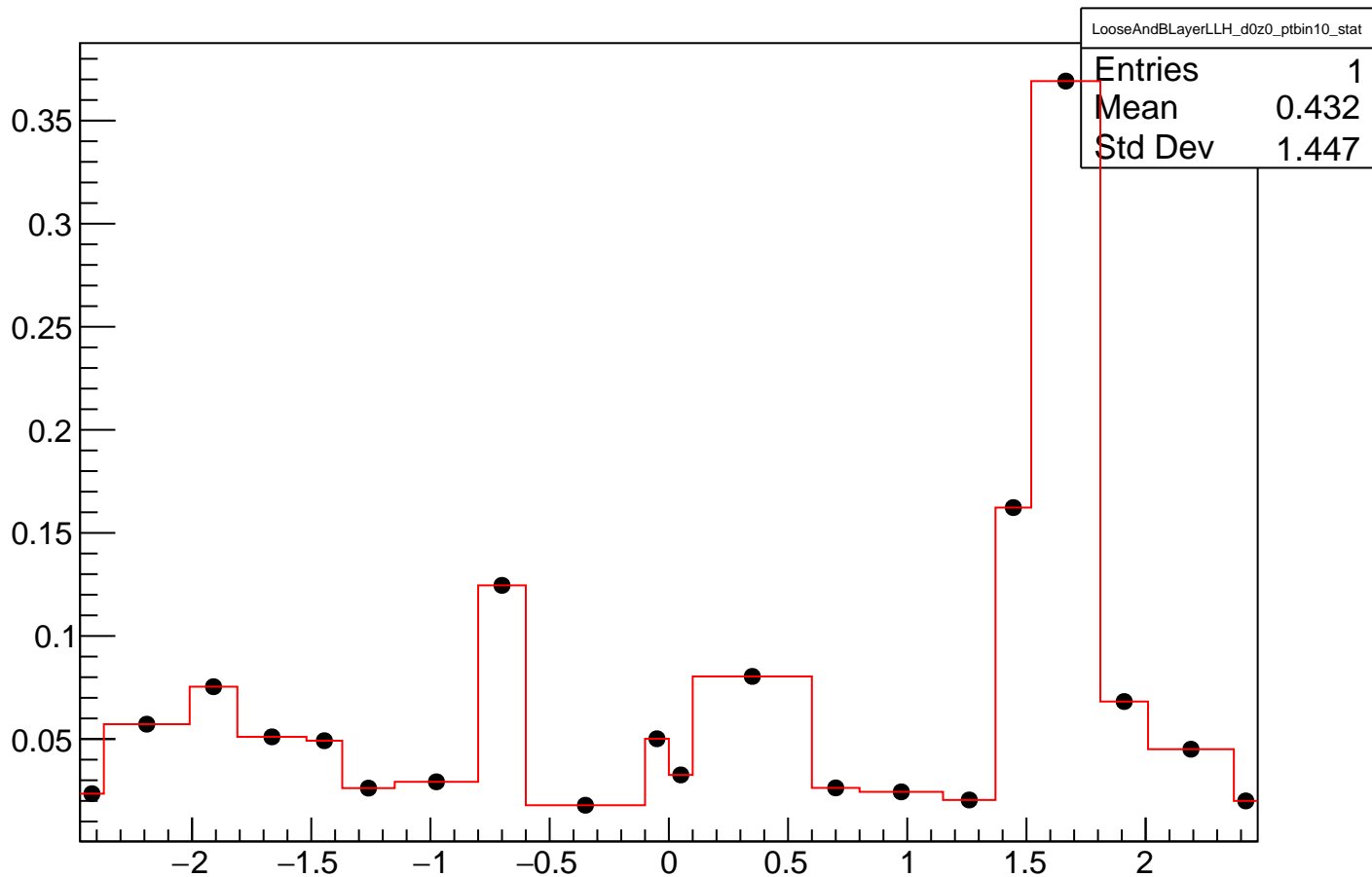
# FullSim\_LooseLLH\_d0z0\_stat



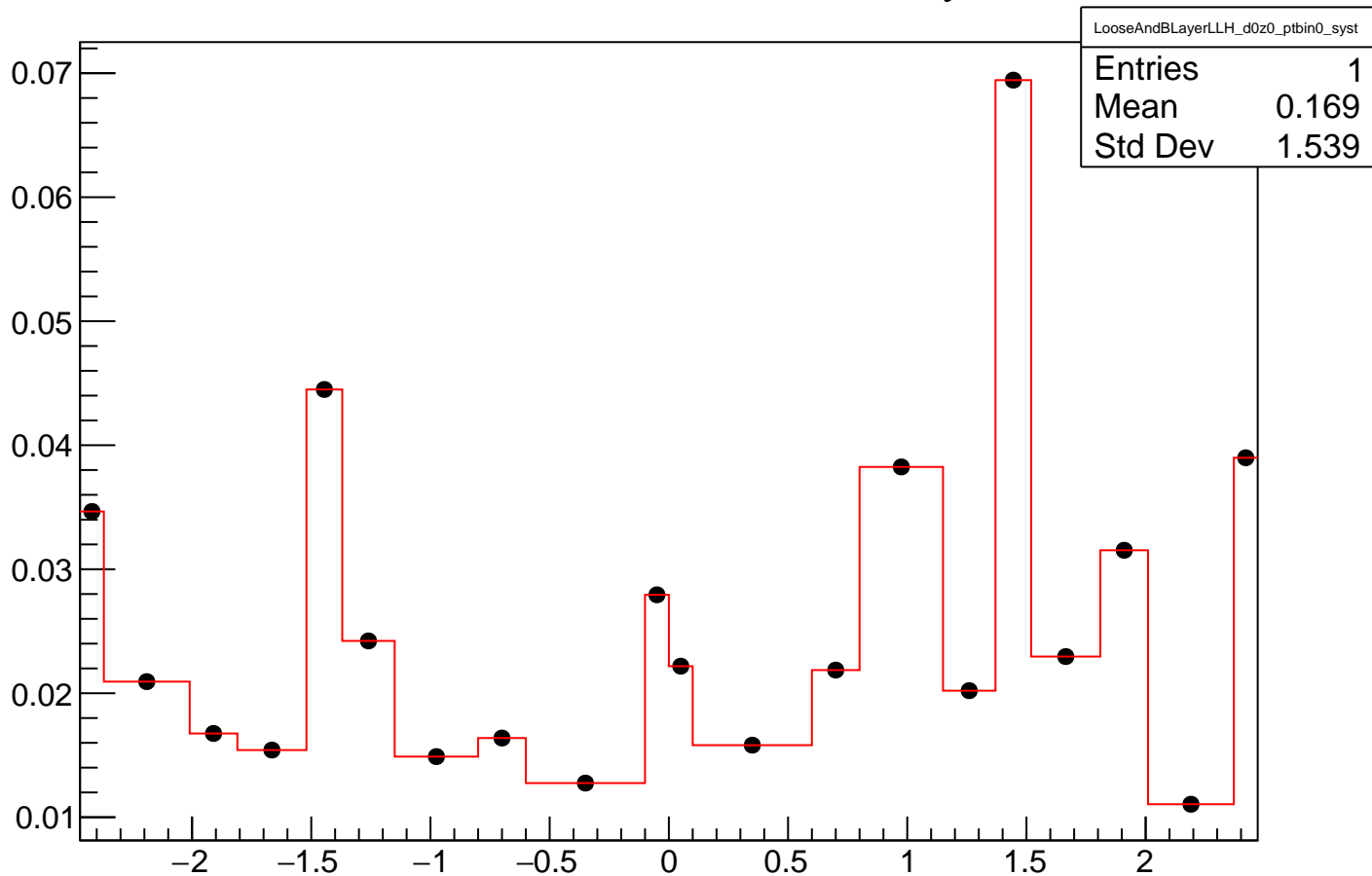
# FullSim\_LooseLLH\_d0z0\_stat



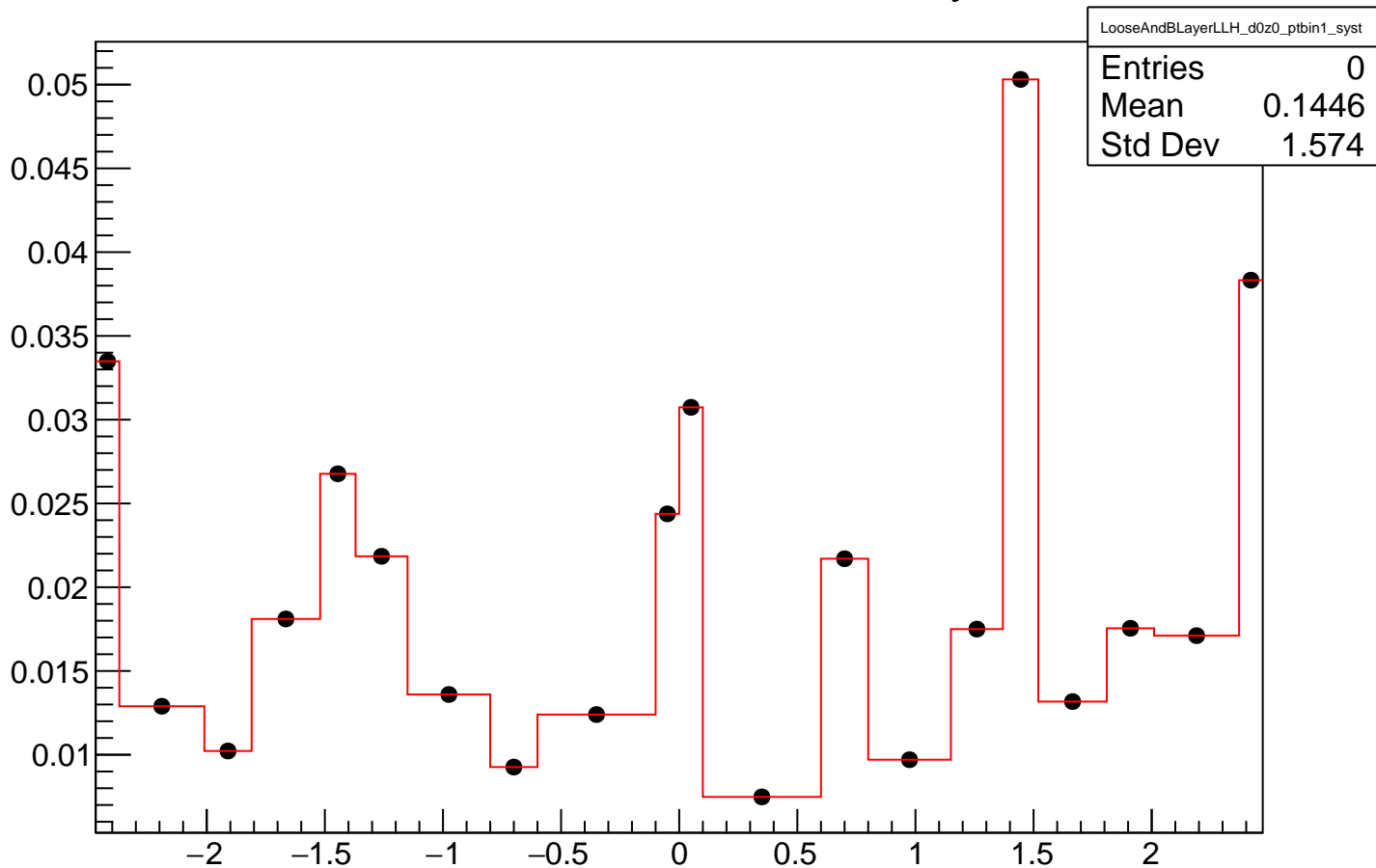
# FullSim\_LooseLLH\_d0z0\_stat



# FullSim\_LooseLLH\_d0z0\_sys

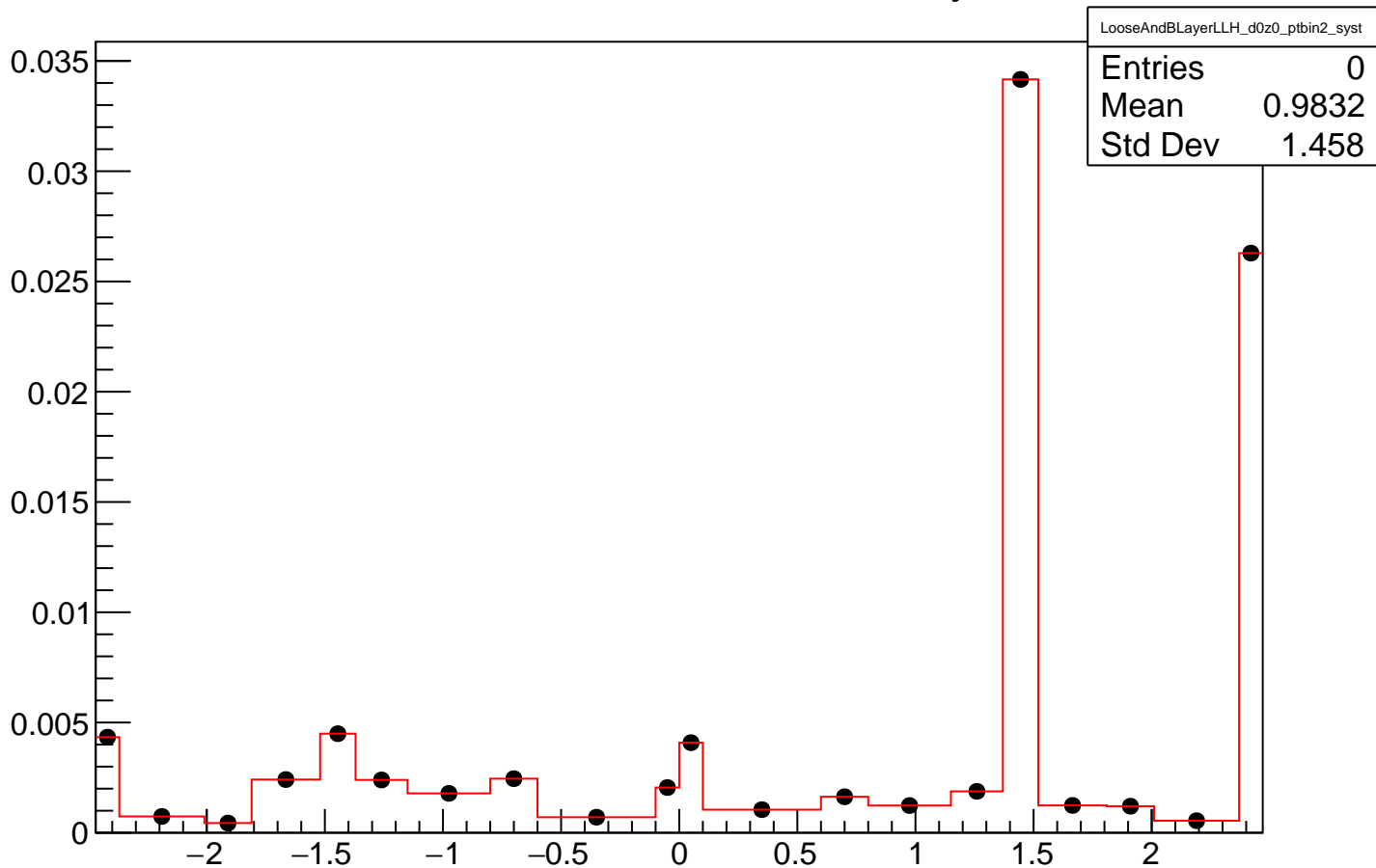


# FullSim\_LooseLLH\_d0z0\_sys

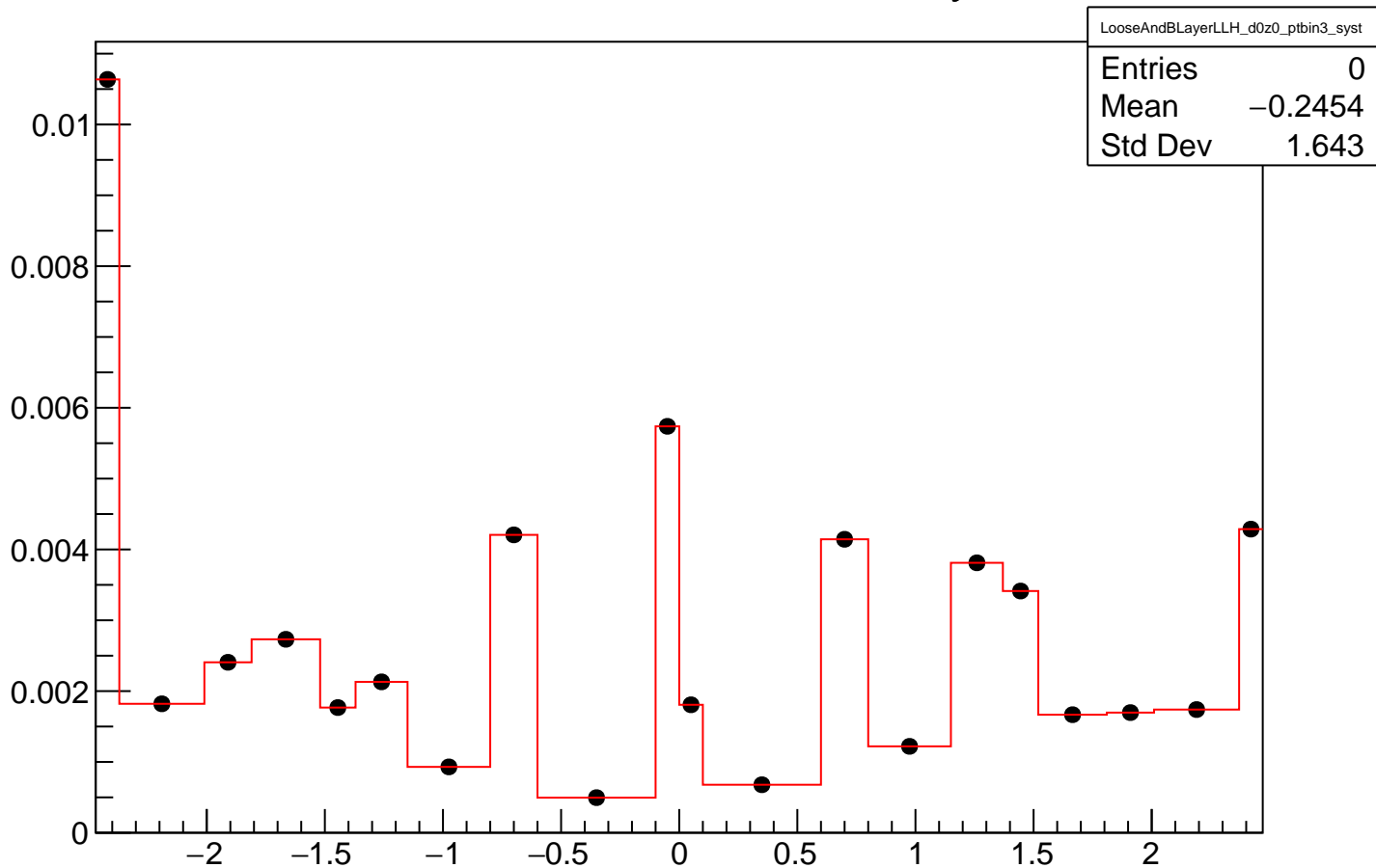




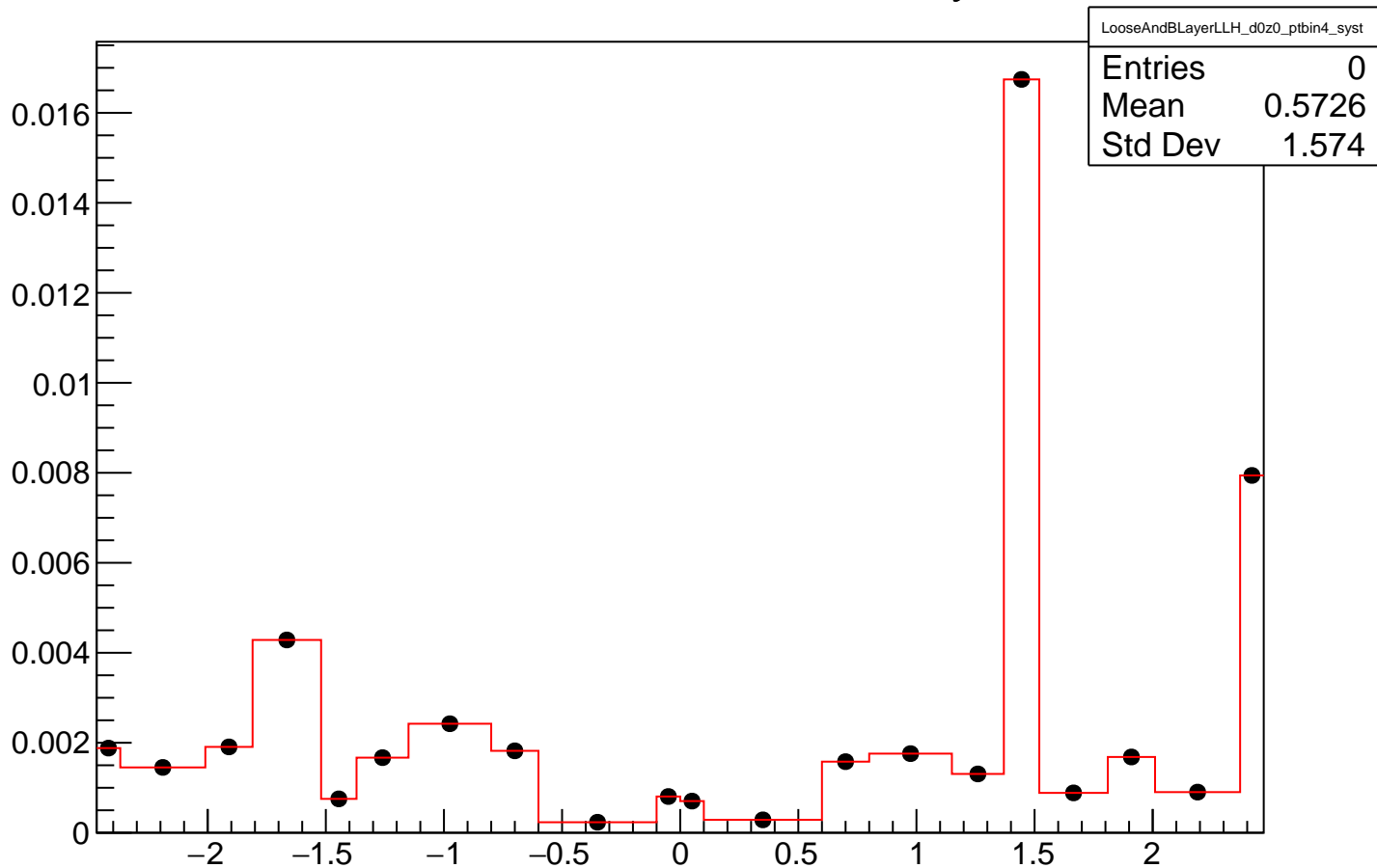
# FullSim\_LooseLLH\_d0z0\_sys



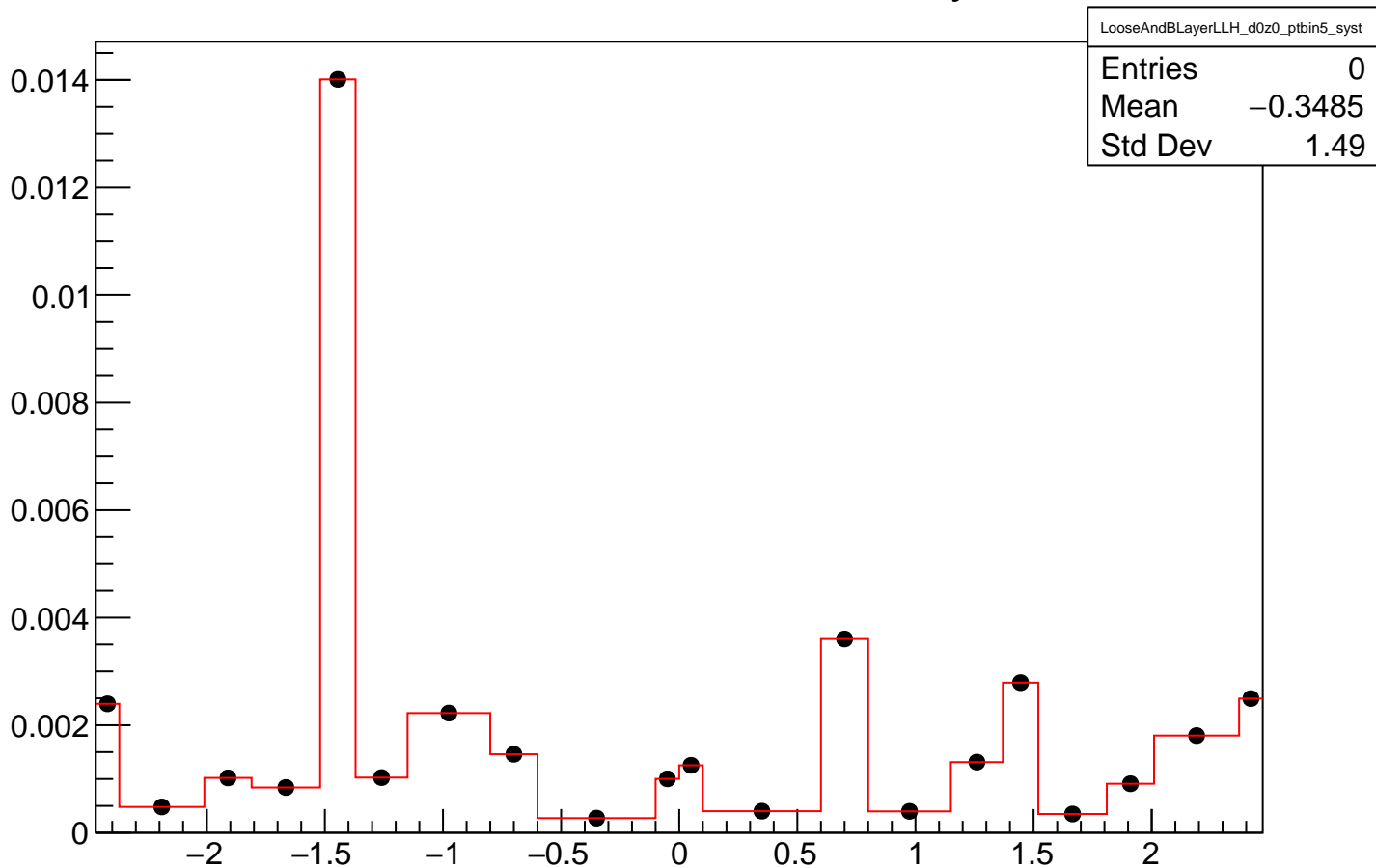
# FullSim\_LooseLLH\_d0z0\_sys



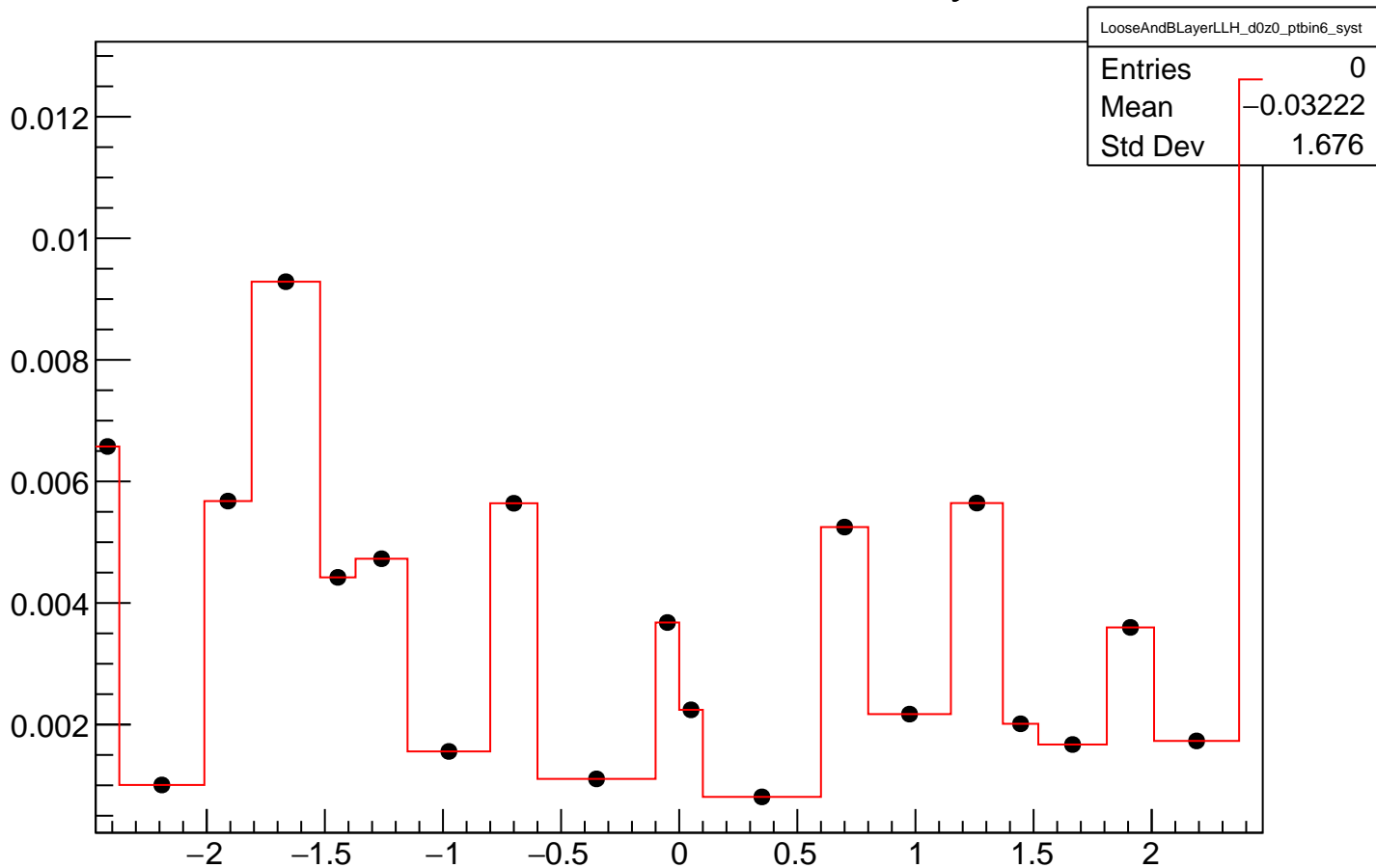
# FullSim\_LooseLLH\_d0z0\_sys



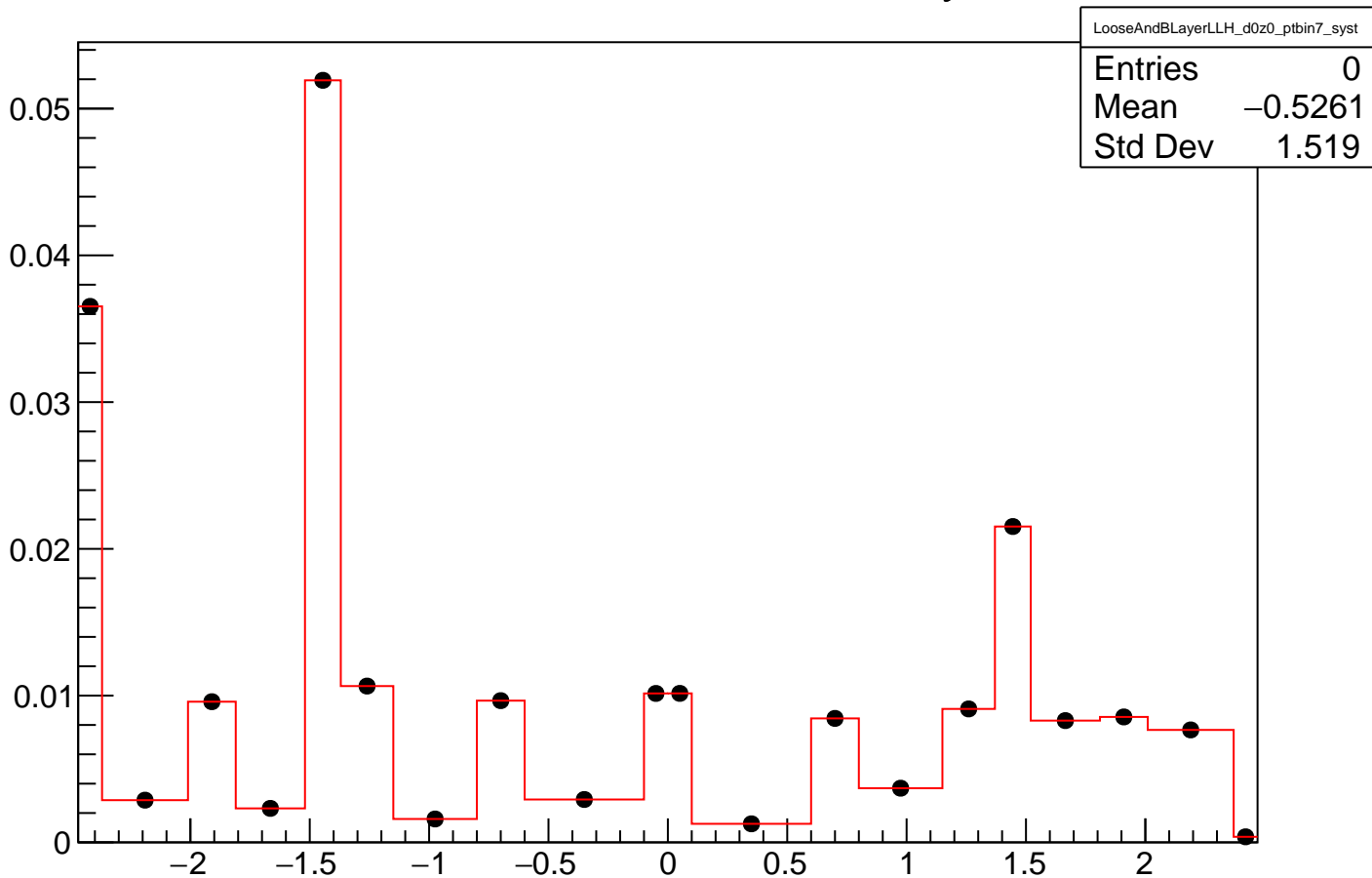
# FullSim\_LooseLLH\_d0z0\_sys



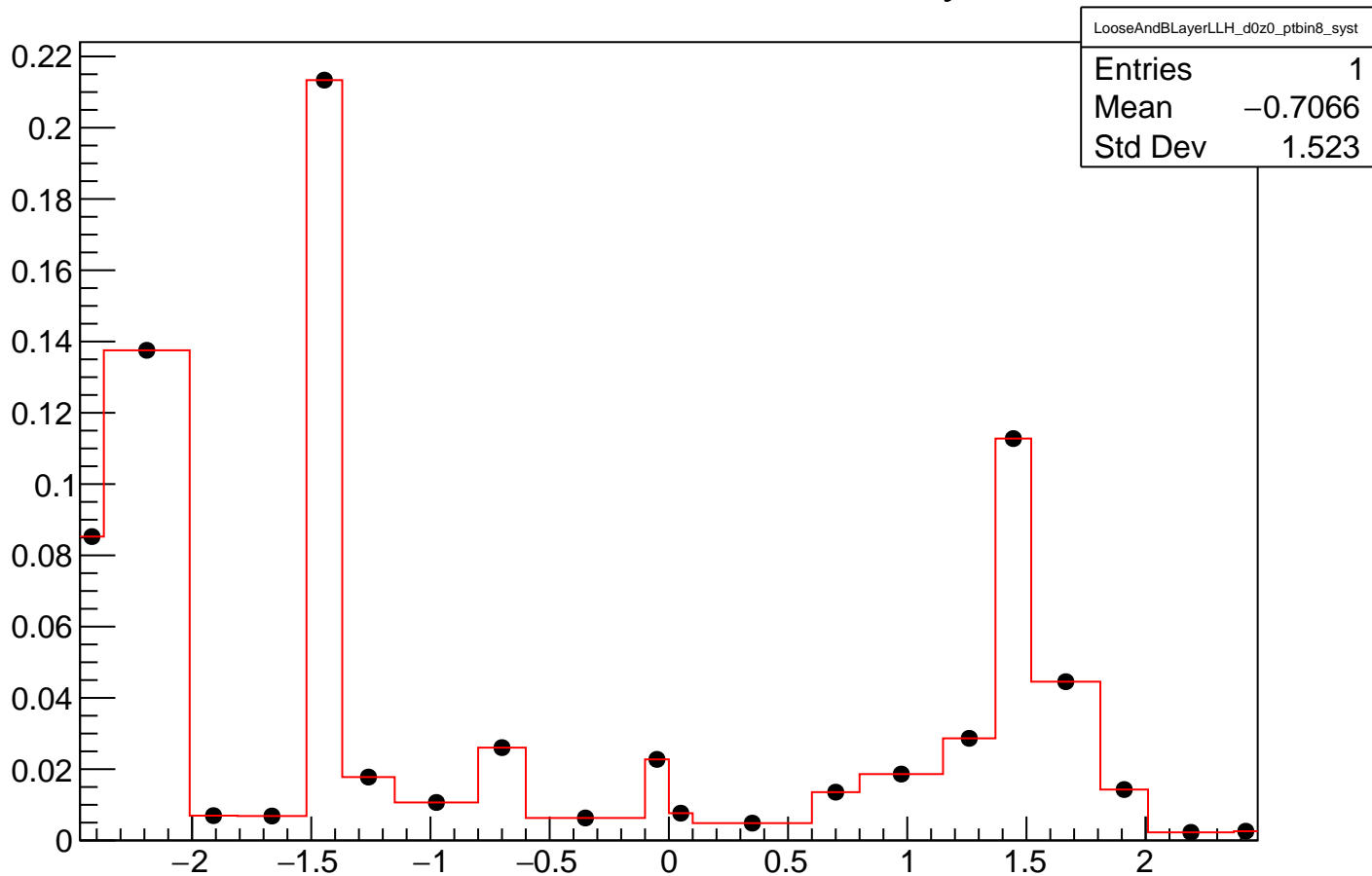
# FullSim\_LooseLLH\_d0z0\_sys



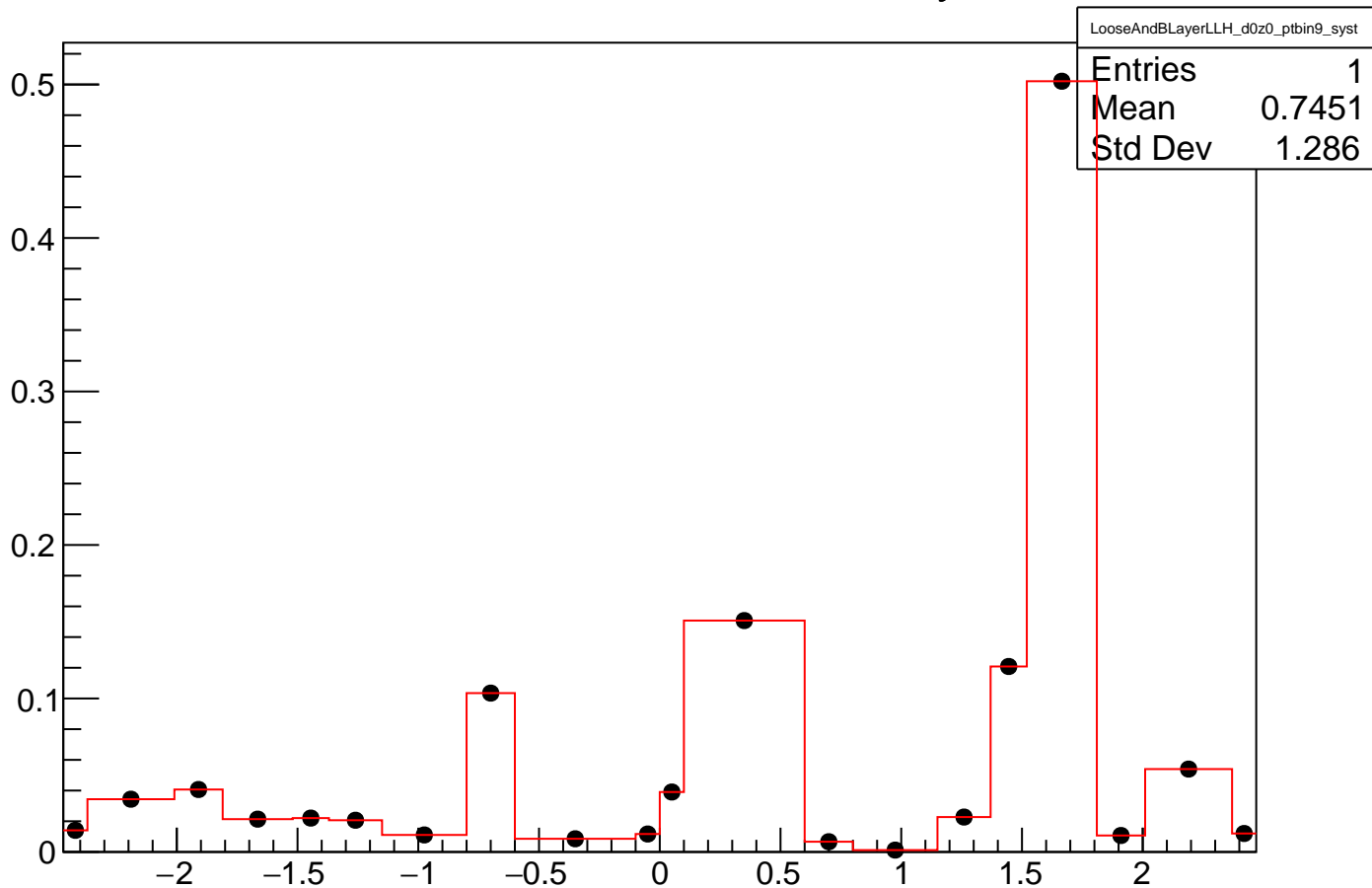
# FullSim\_LooseLLH\_d0z0\_sys



# FullSim\_LooseLLH\_d0z0\_sys

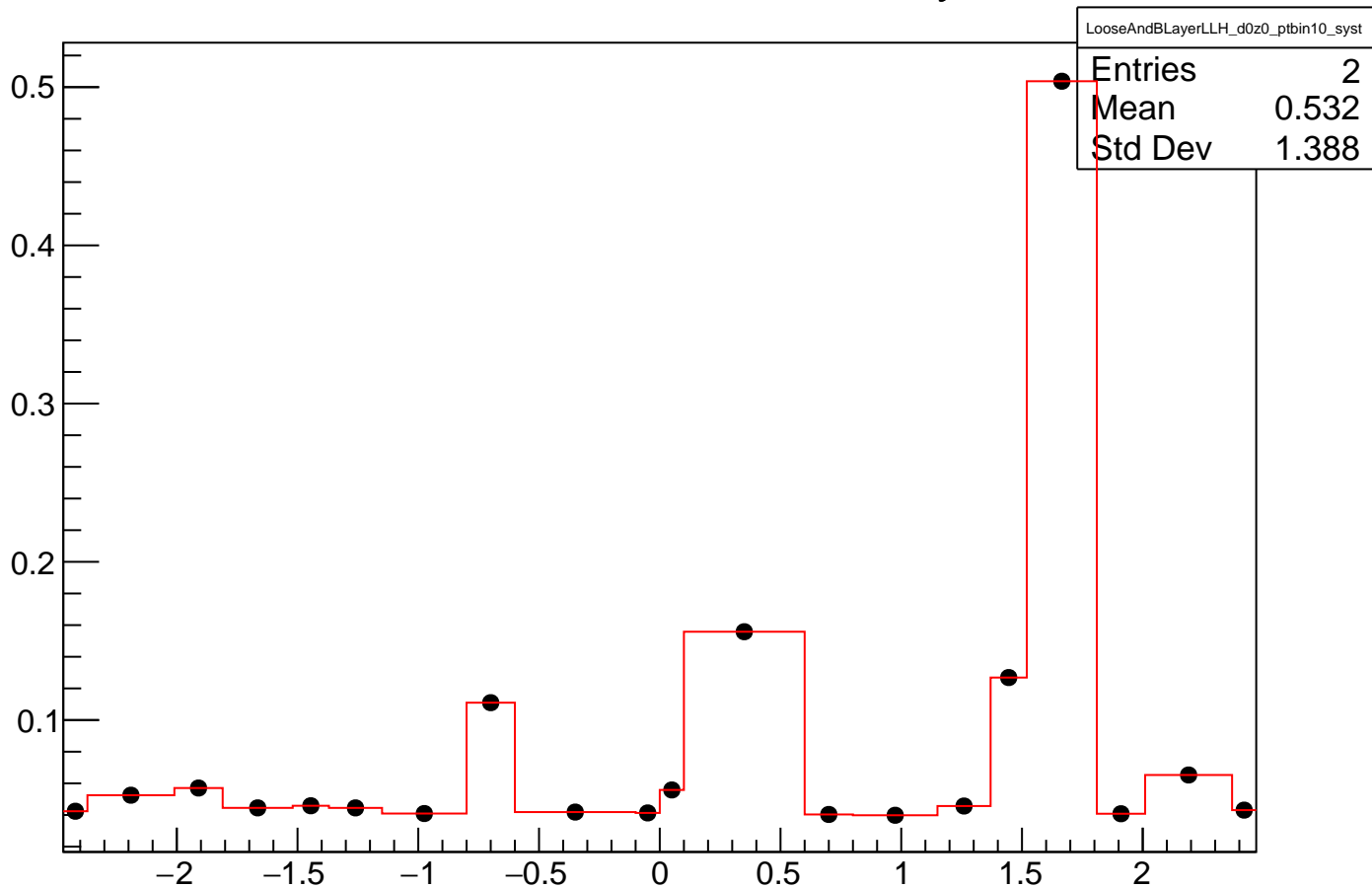


# FullSim\_LooseLLH\_d0z0\_sys

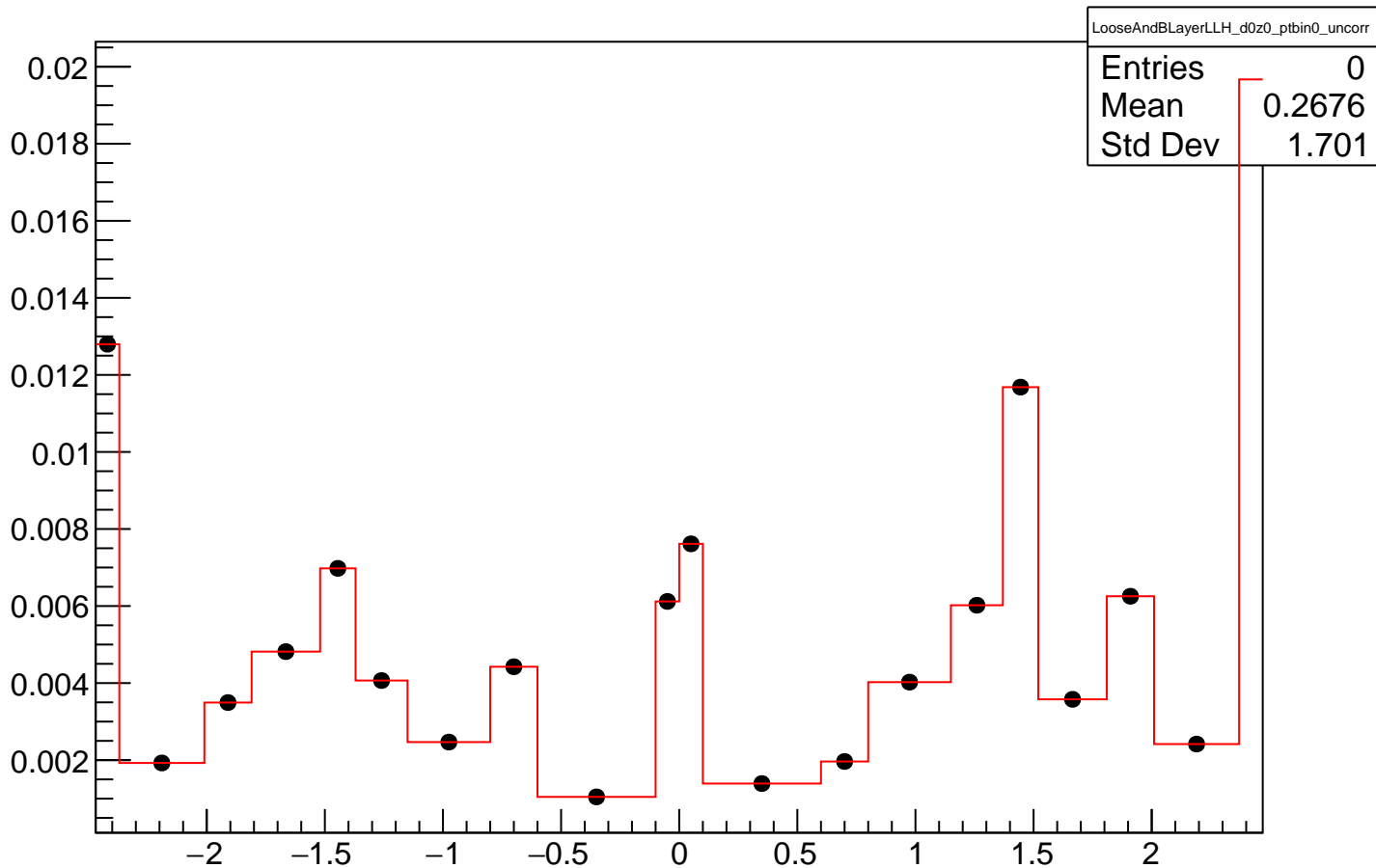




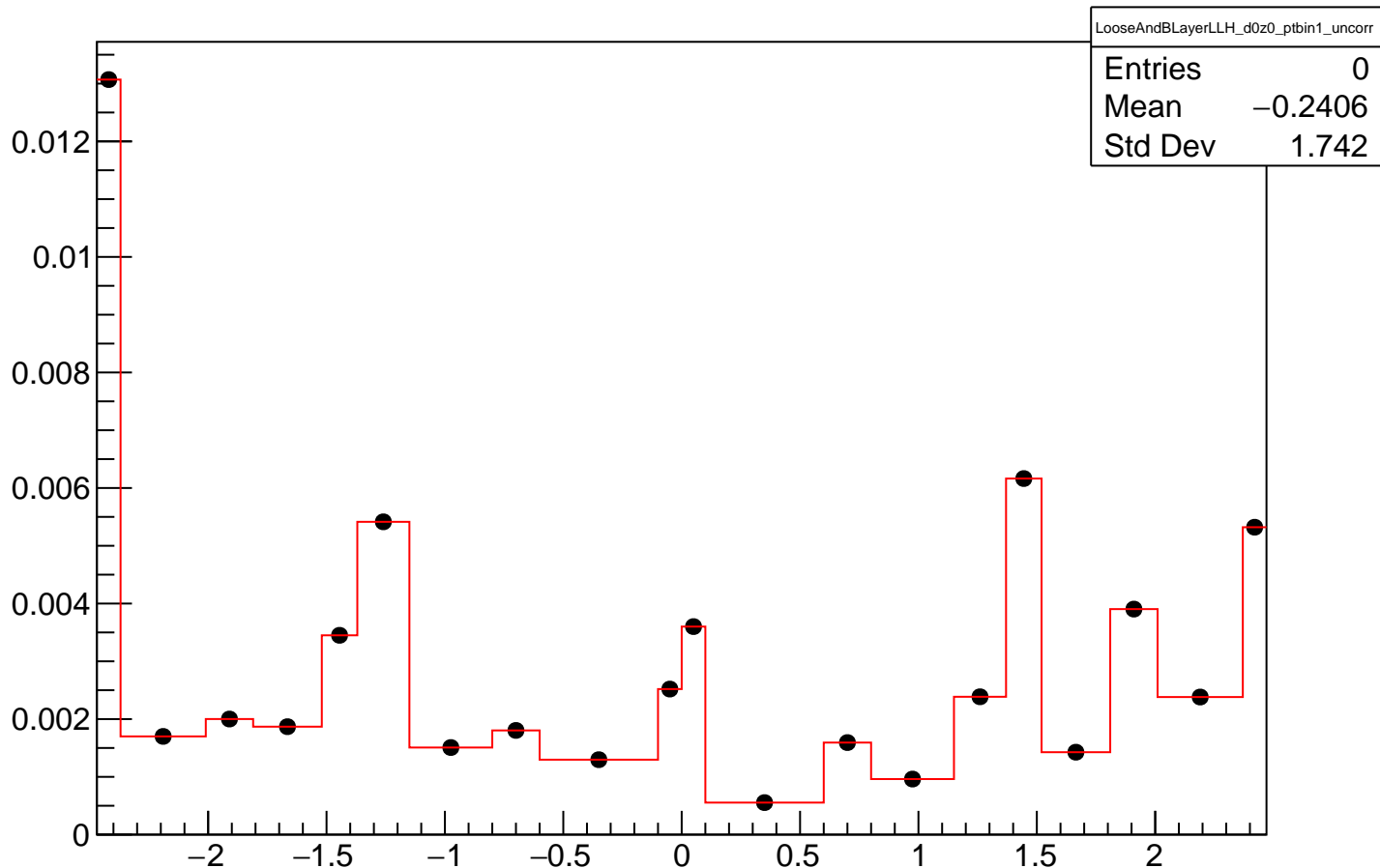
# FullSim\_LooseLLH\_d0z0\_sys



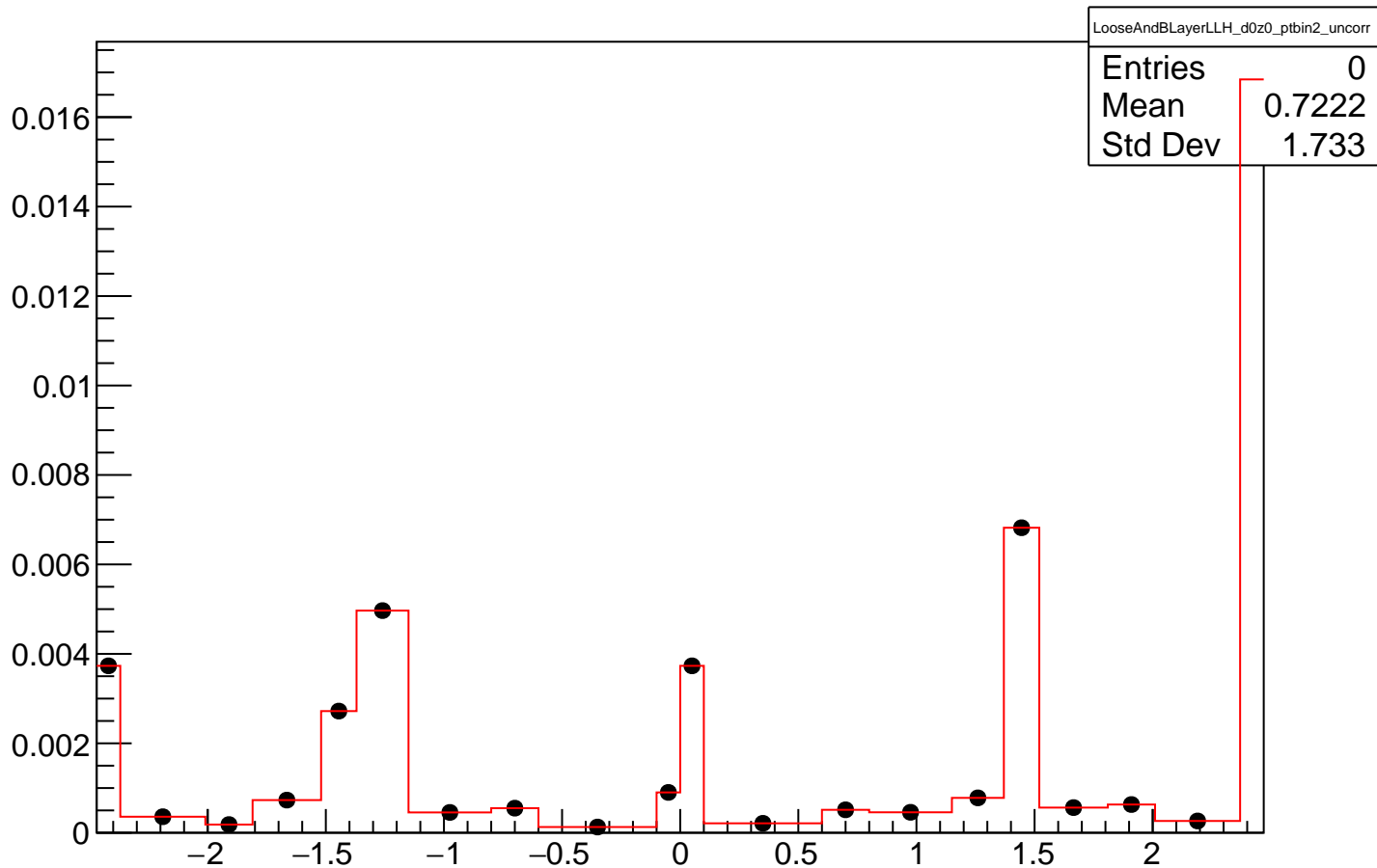
# FullSim\_LooseLLH\_d0z0\_uncorr



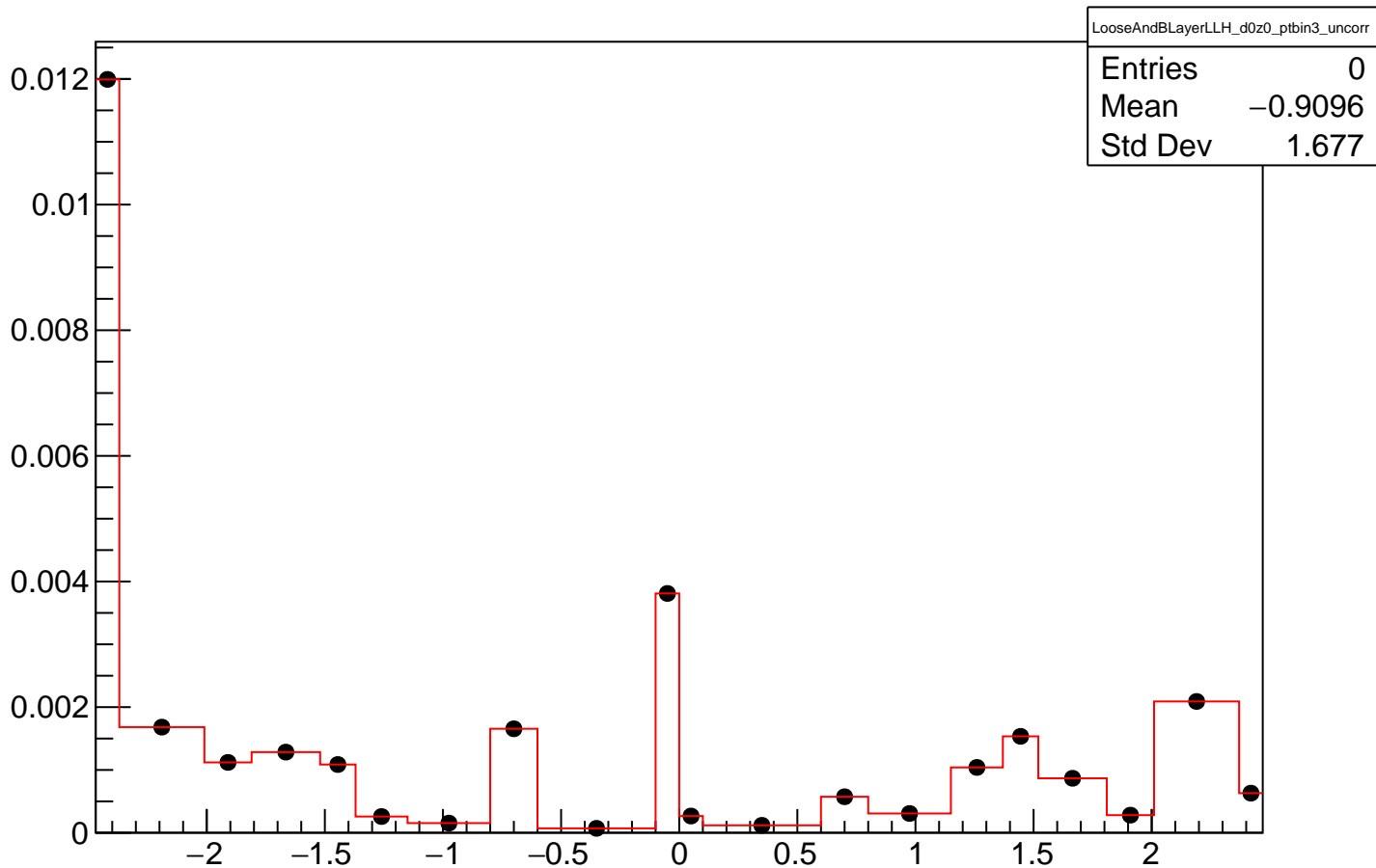
# FullSim\_LooseLLH\_d0z0\_uncorr



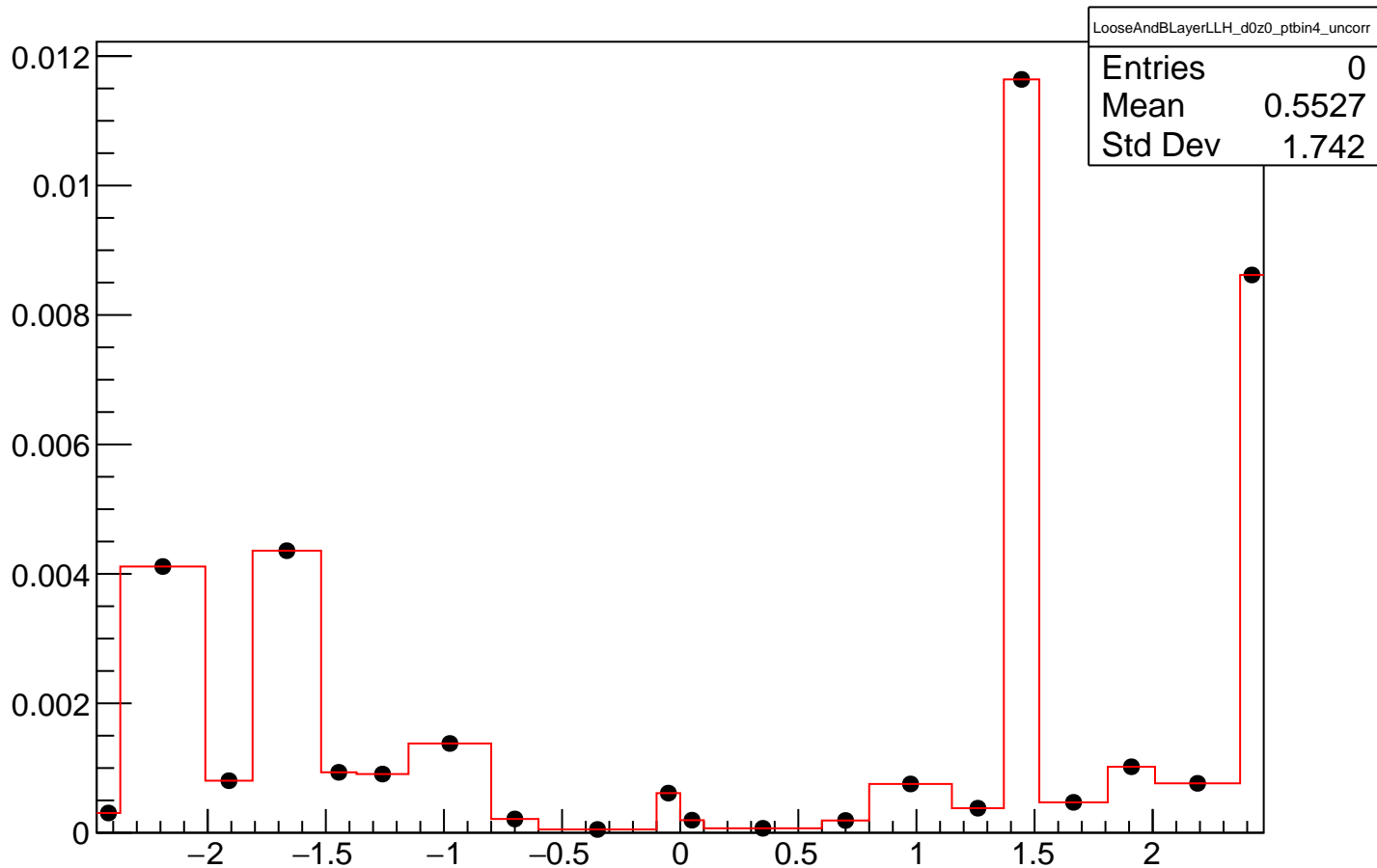
# FullSim\_LooseLLH\_d0z0\_uncorr



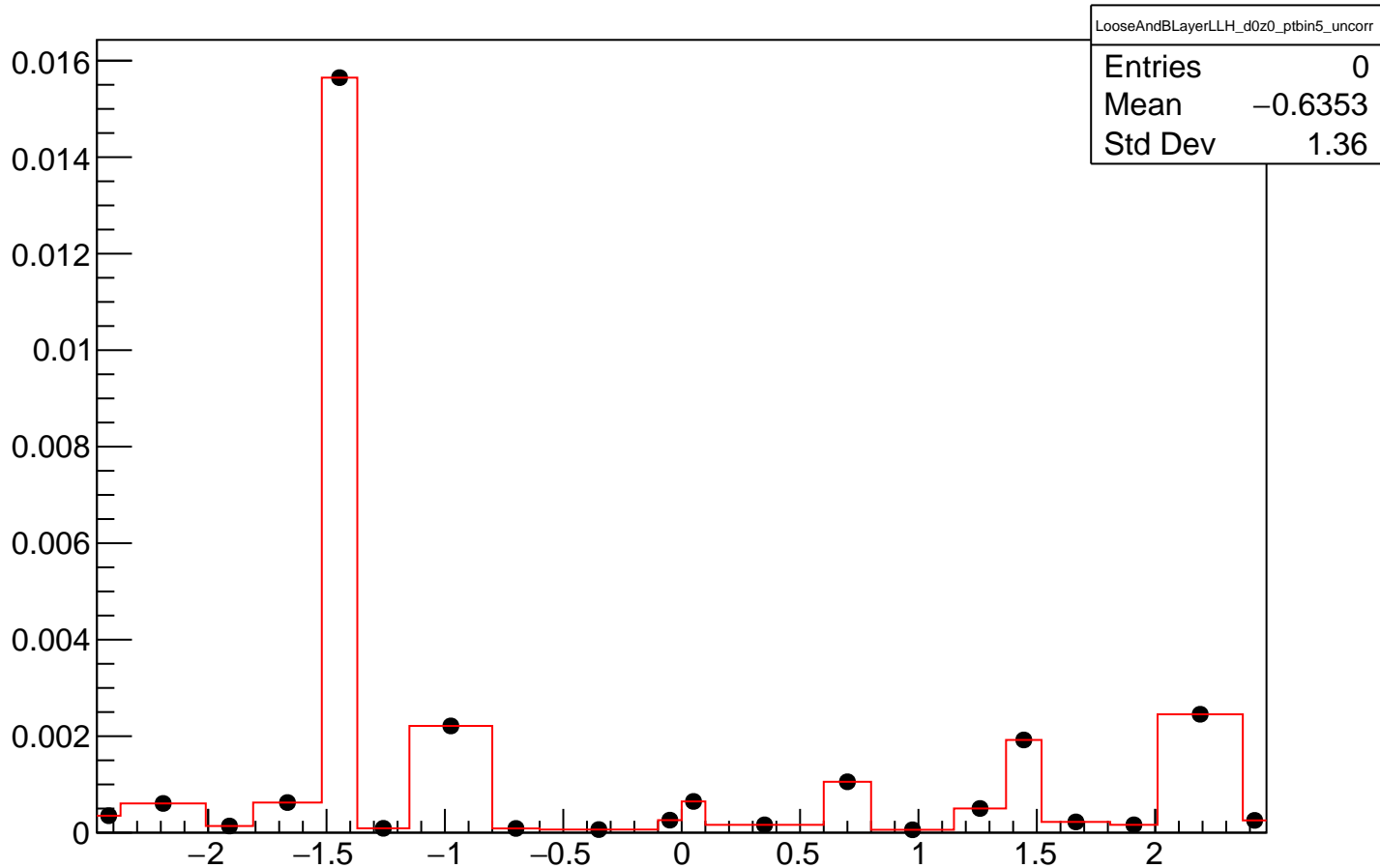
# FullSim\_LooseLLH\_d0z0\_uncorr



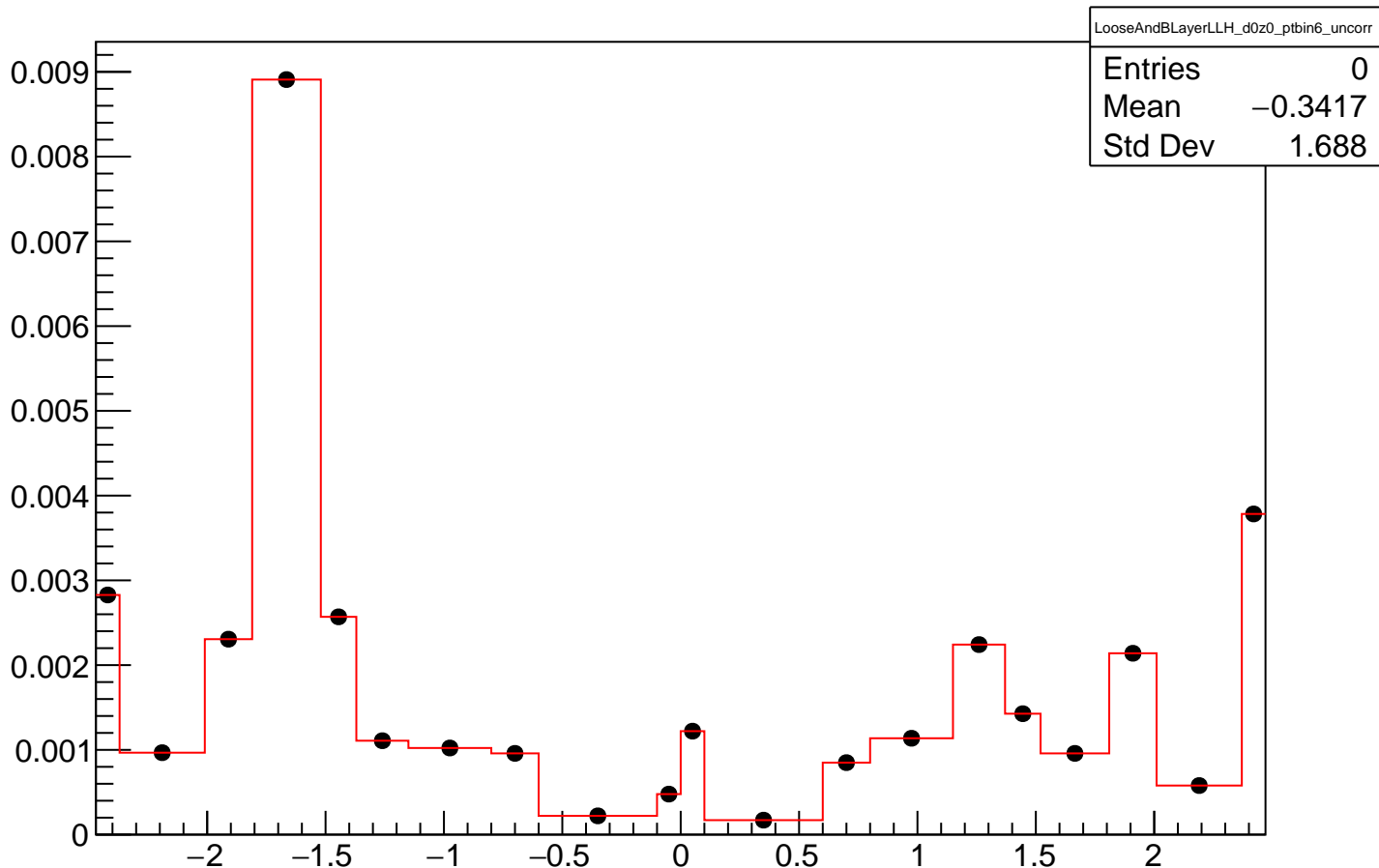
# FullSim\_LooseLLH\_d0z0\_uncorr



# FullSim\_LooseLLH\_d0z0\_uncorr

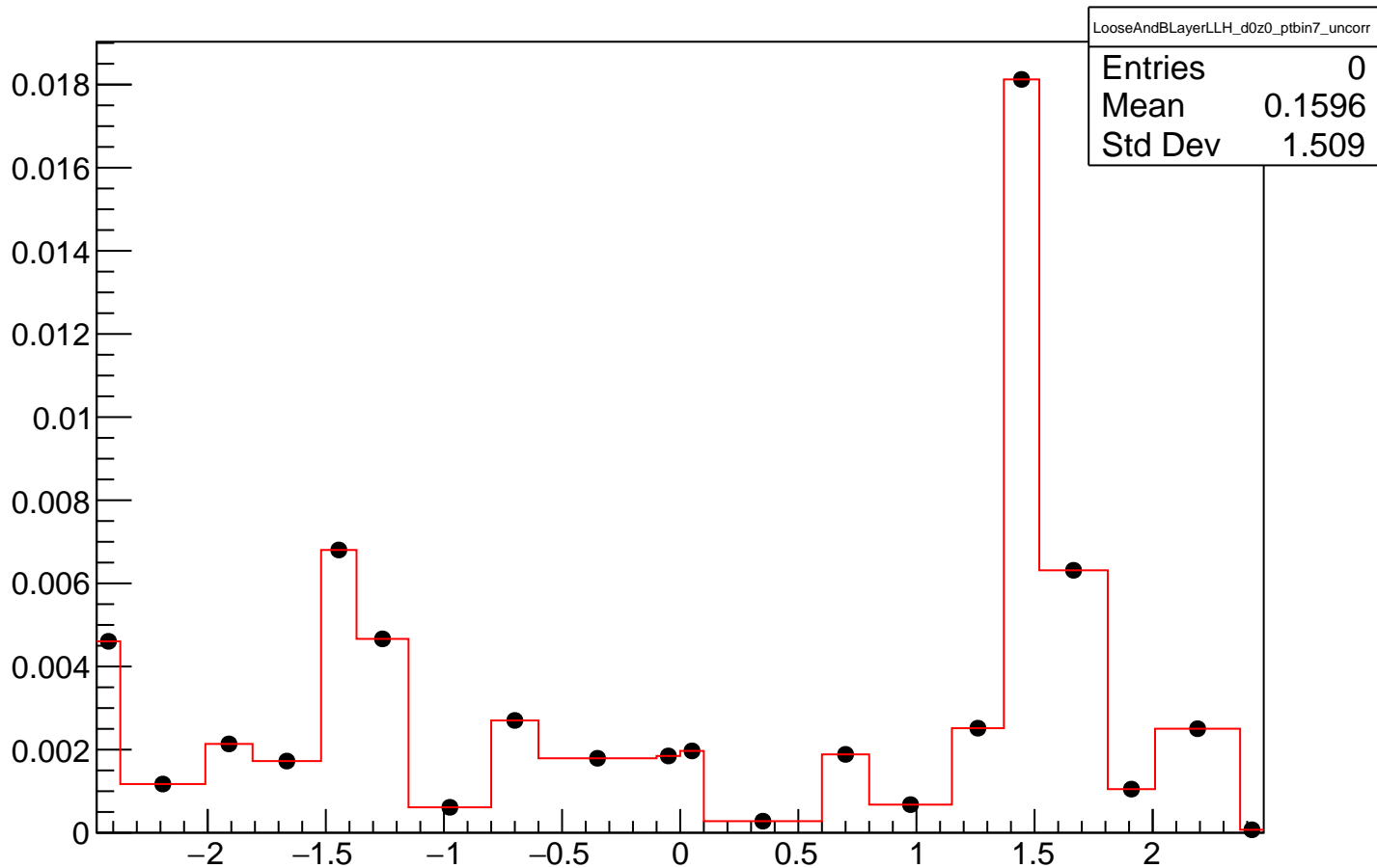


# FullSim\_LooseLLH\_d0z0\_uncorr

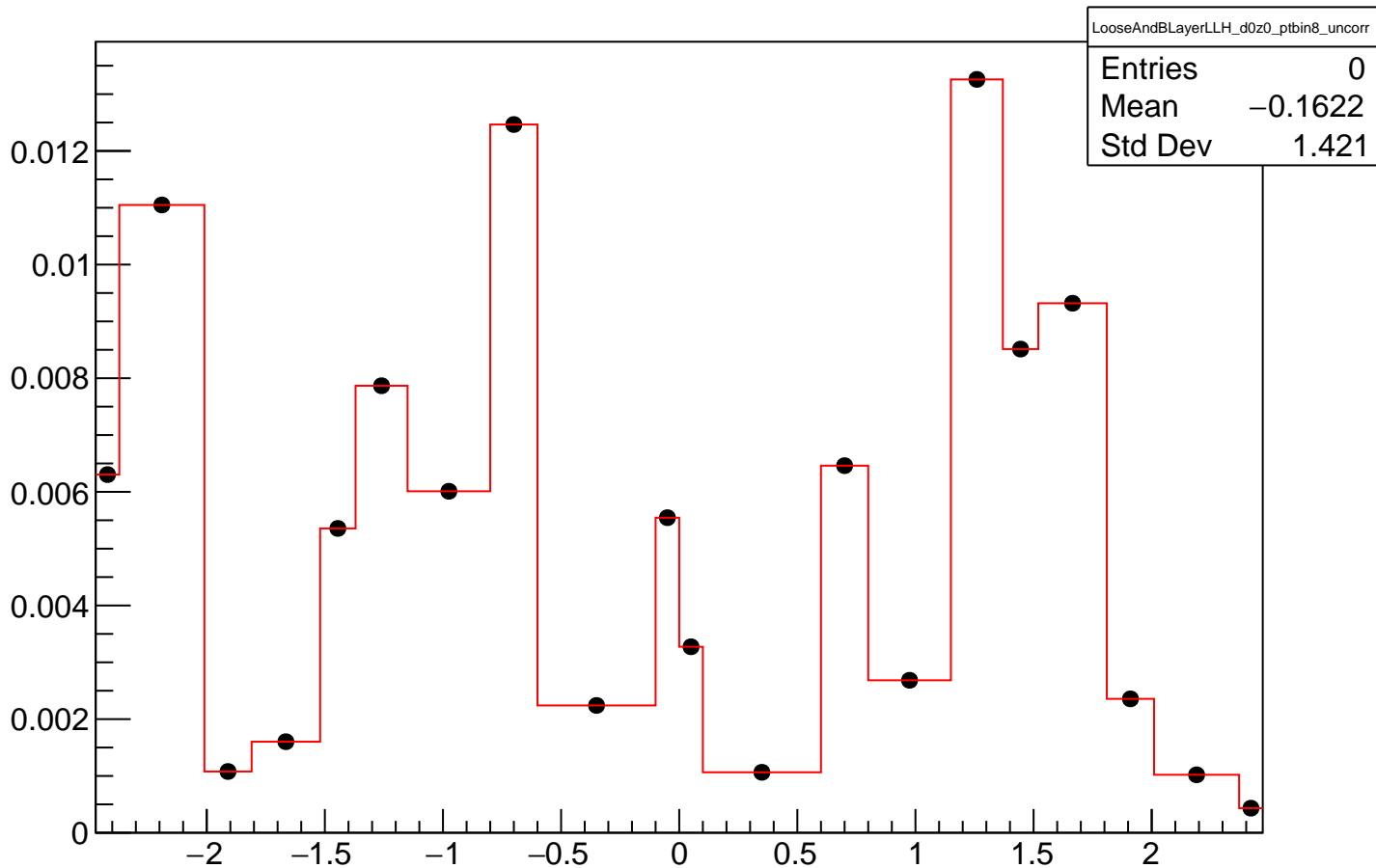




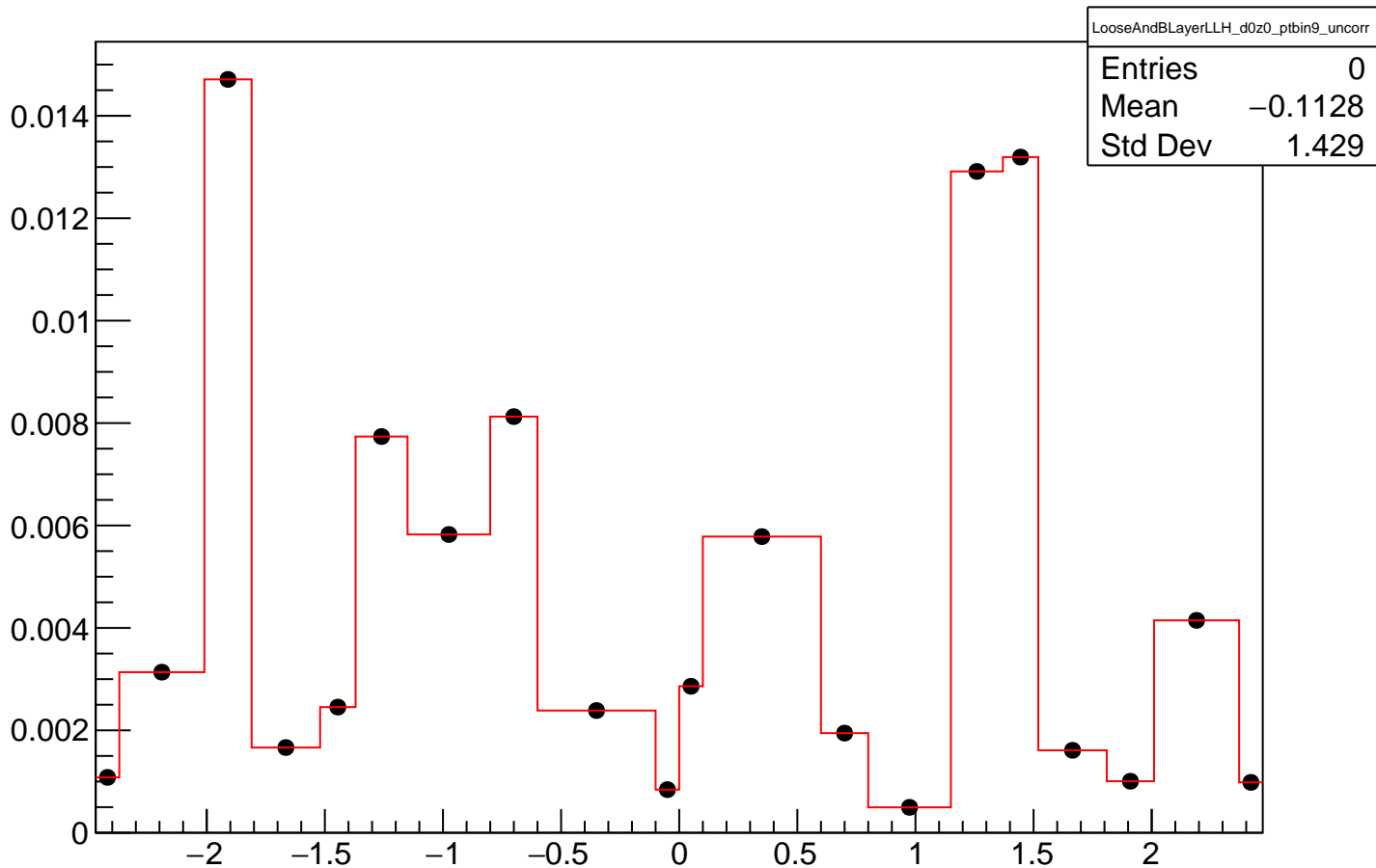
# FullSim\_LooseLLH\_d0z0\_uncorr



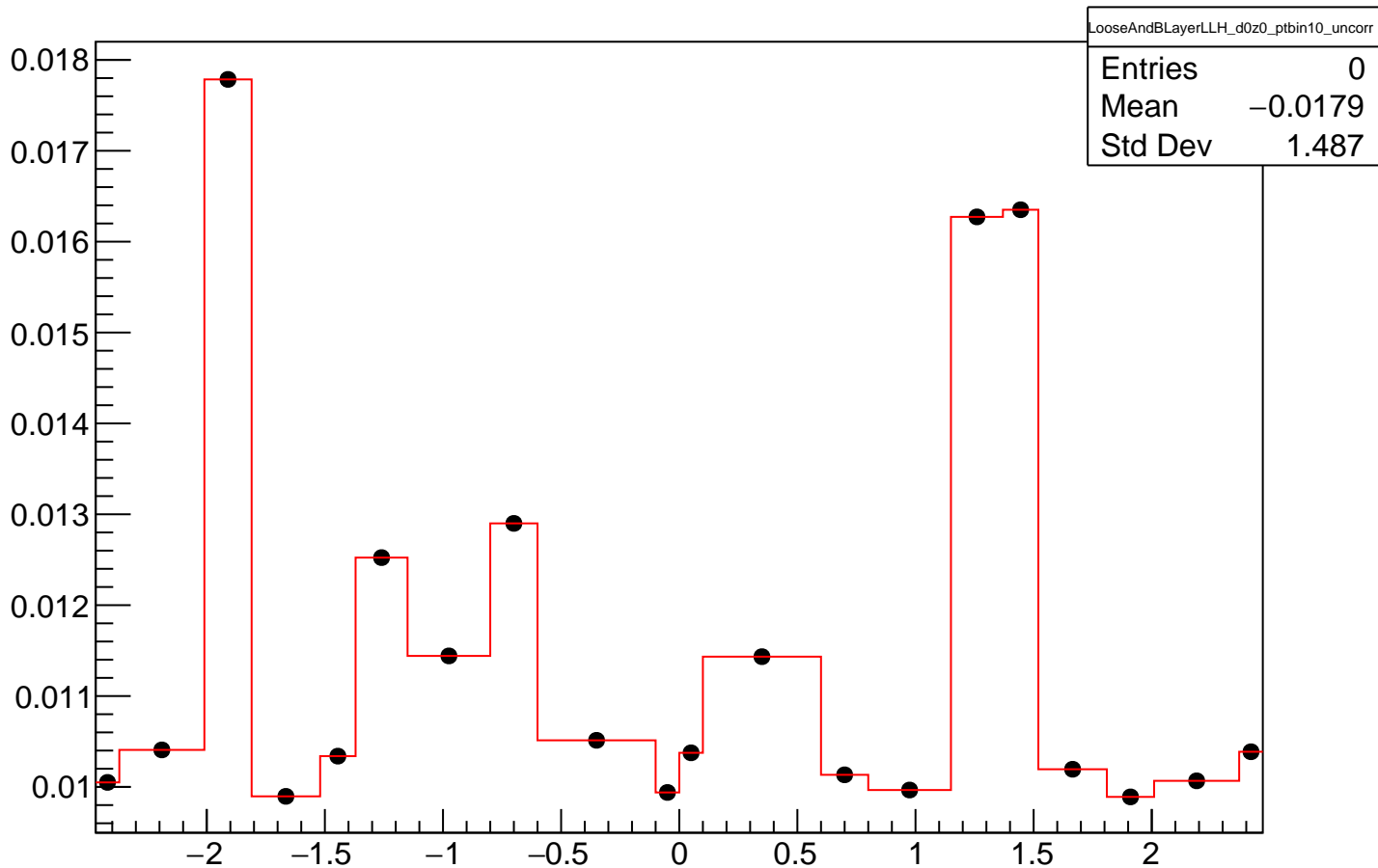
# FullSim\_LooseLLH\_d0z0\_uncorr



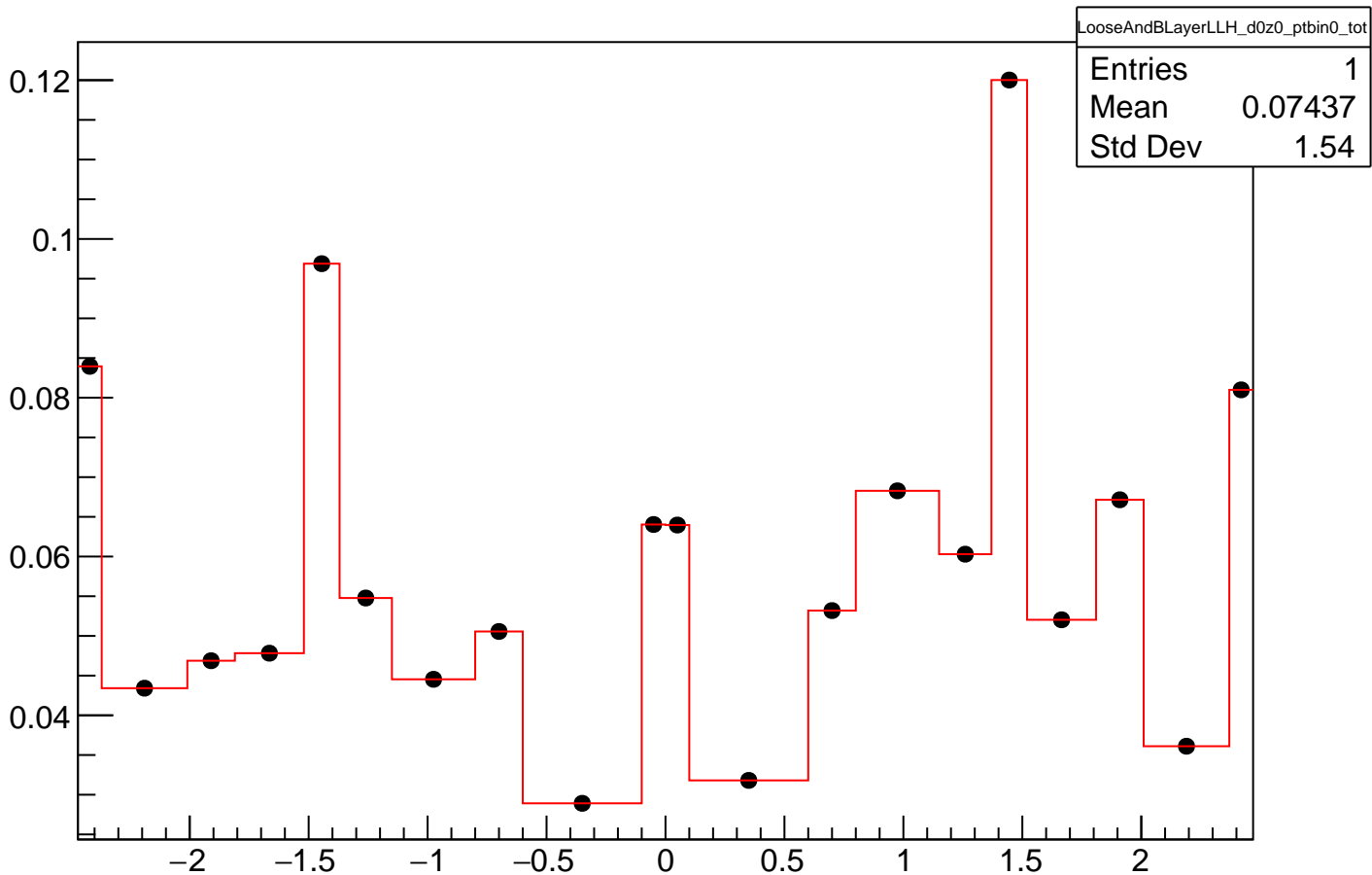
# FullSim\_LooseLLH\_d0z0\_uncorr



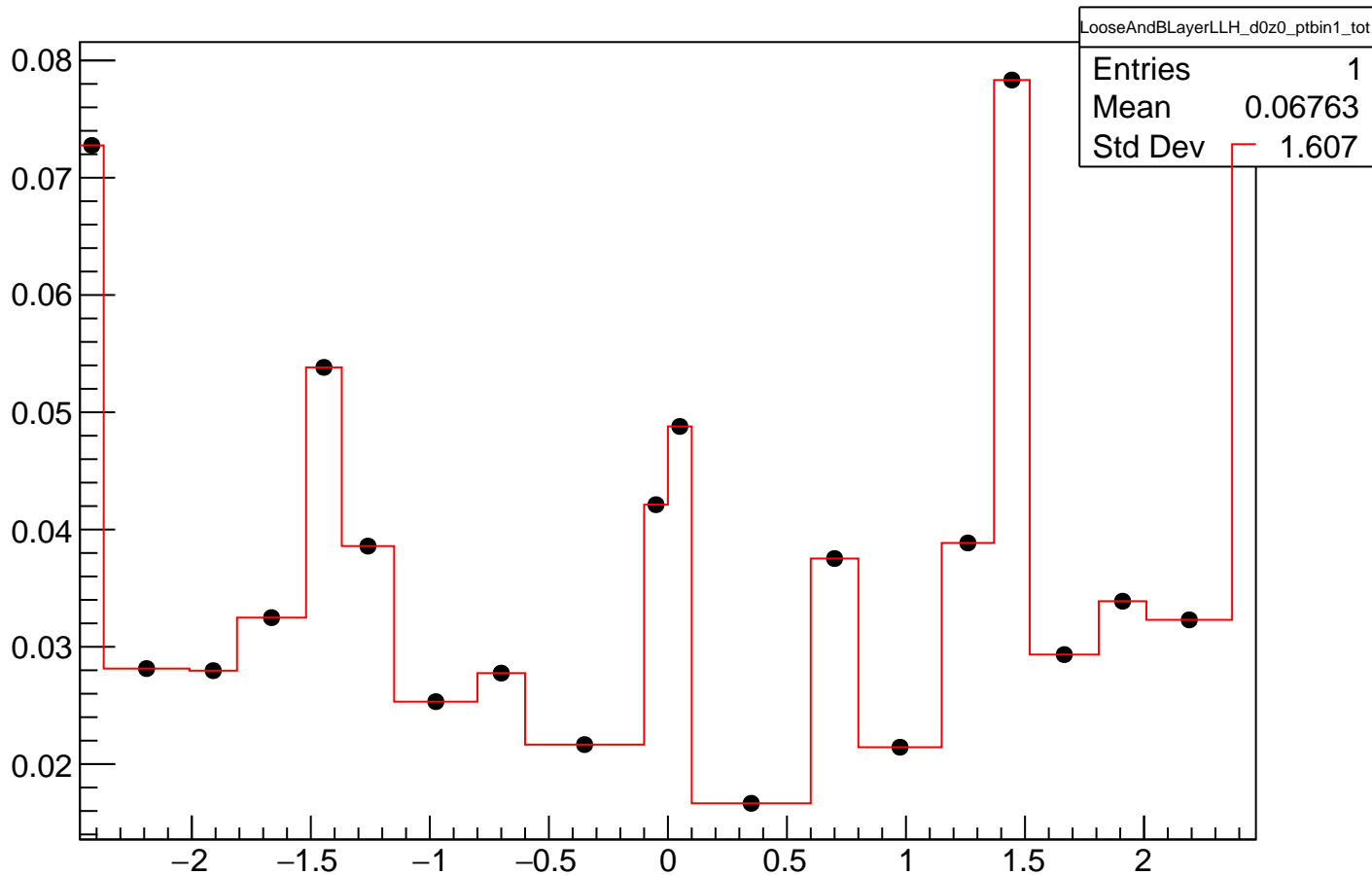
# FullSim\_LooseLLH\_d0z0\_uncorr



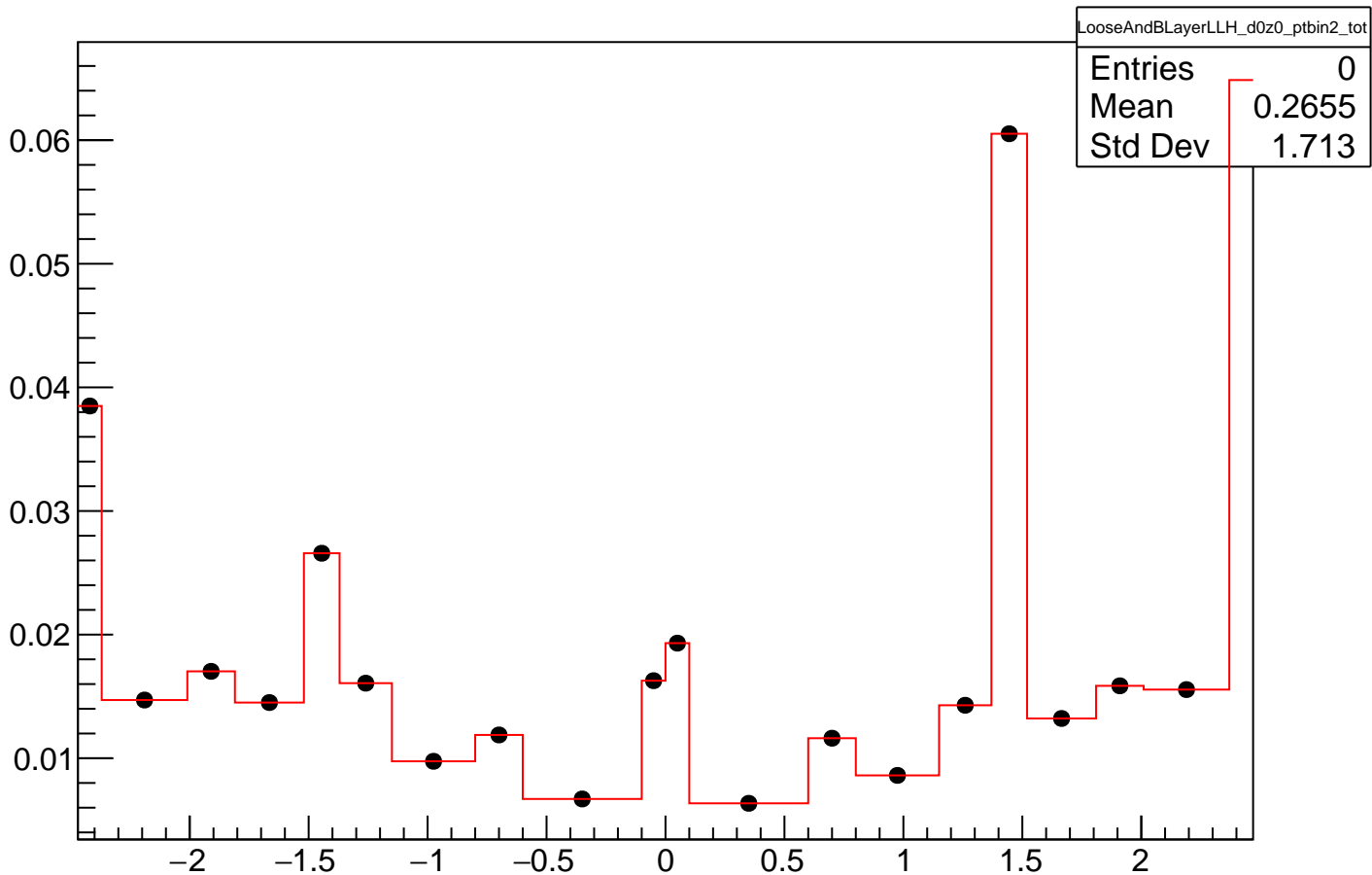
# FullSim\_LooseLLH\_d0z0\_tot



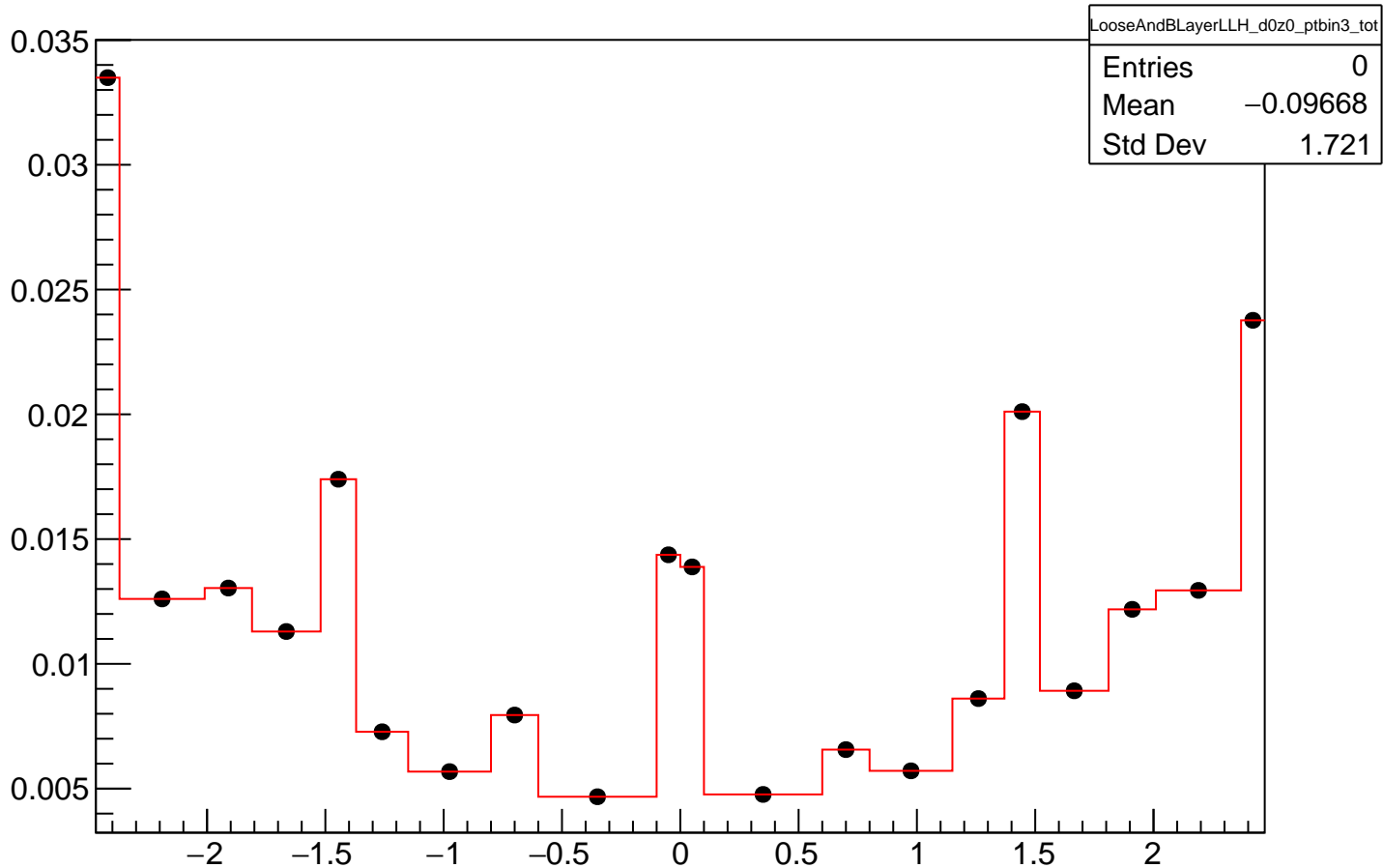
# FullSim\_LooseLLH\_d0z0\_tot



# FullSim\_LooseLLH\_d0z0\_tot

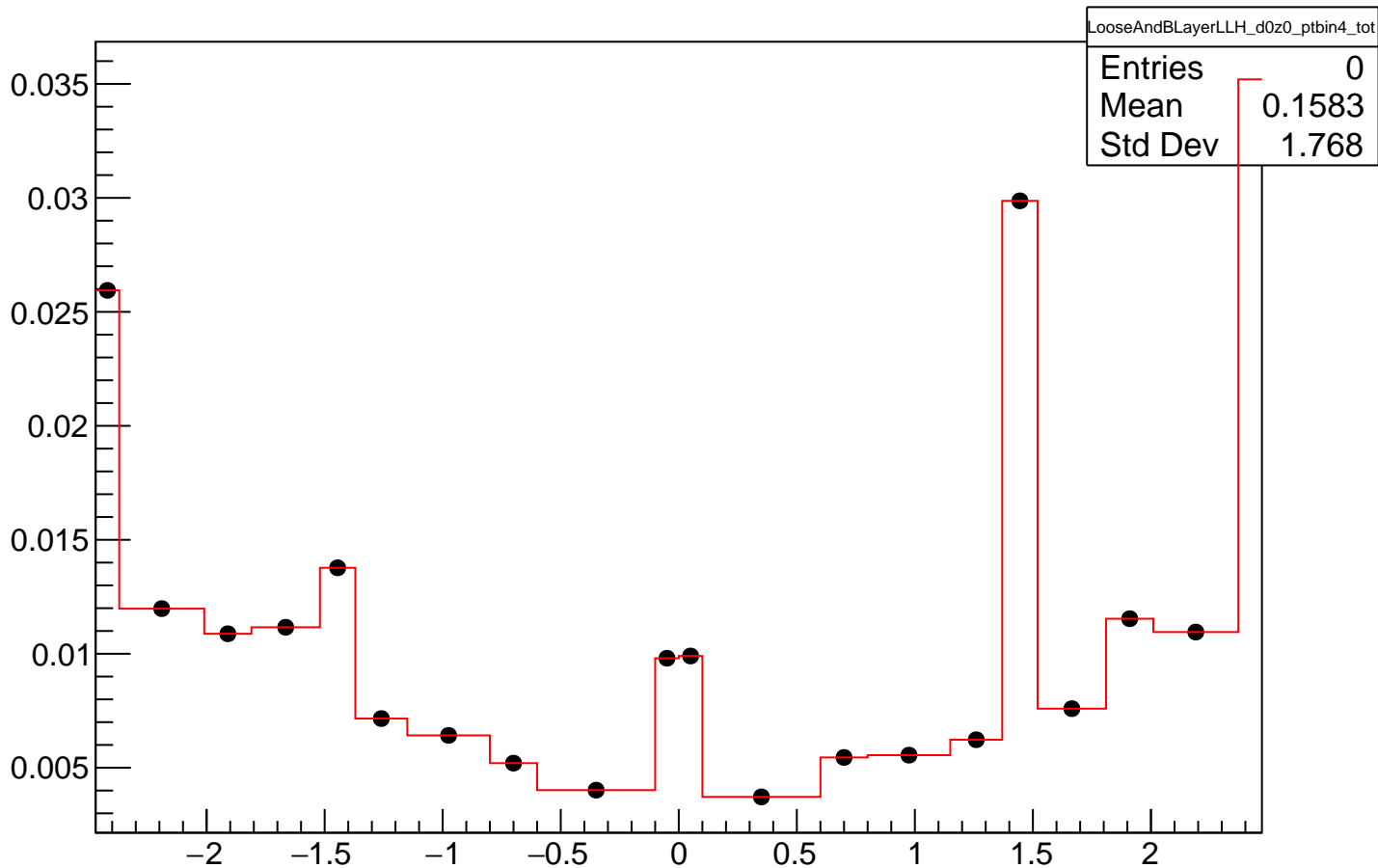


# FullSim\_LooseLLH\_d0z0\_tot

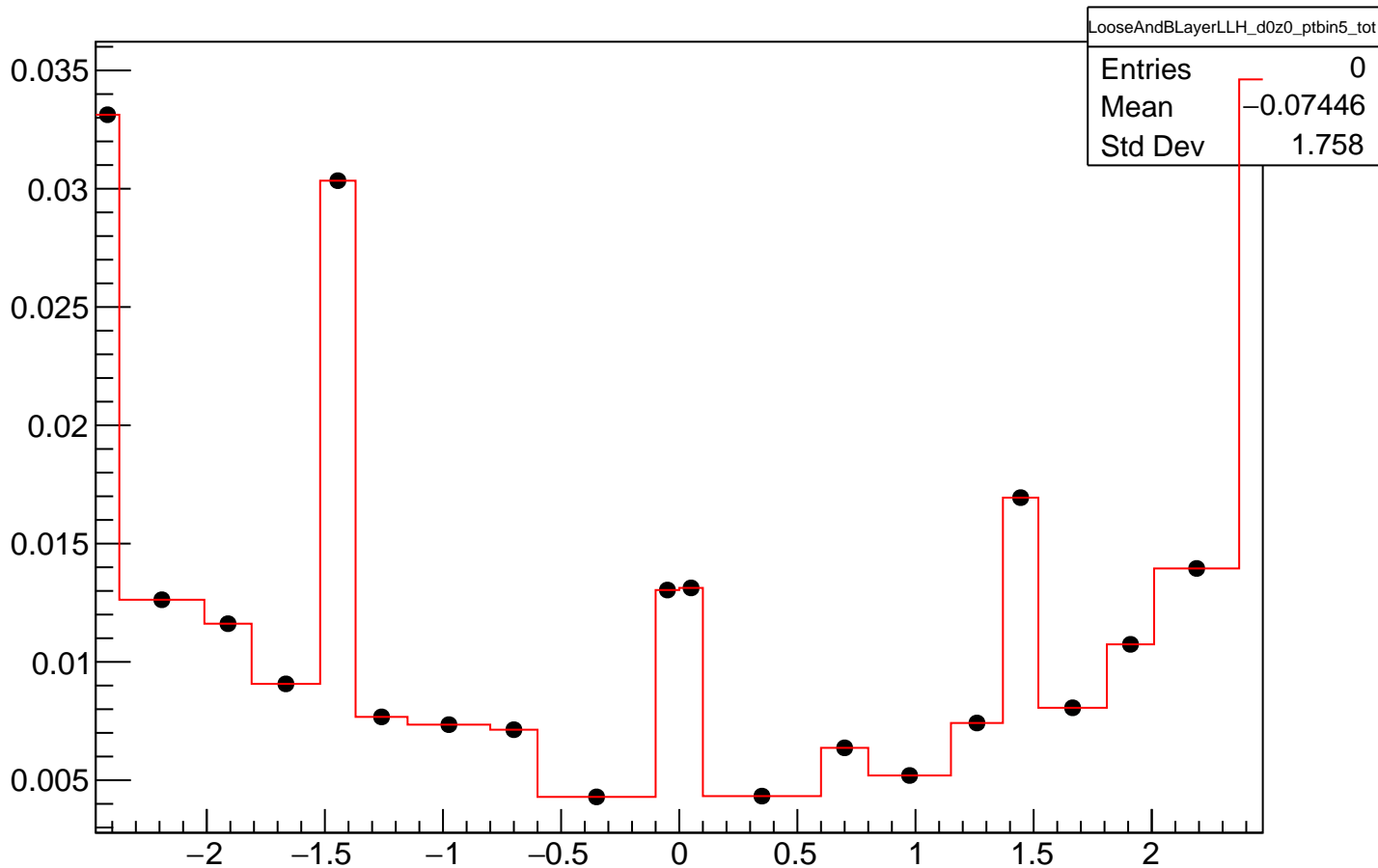




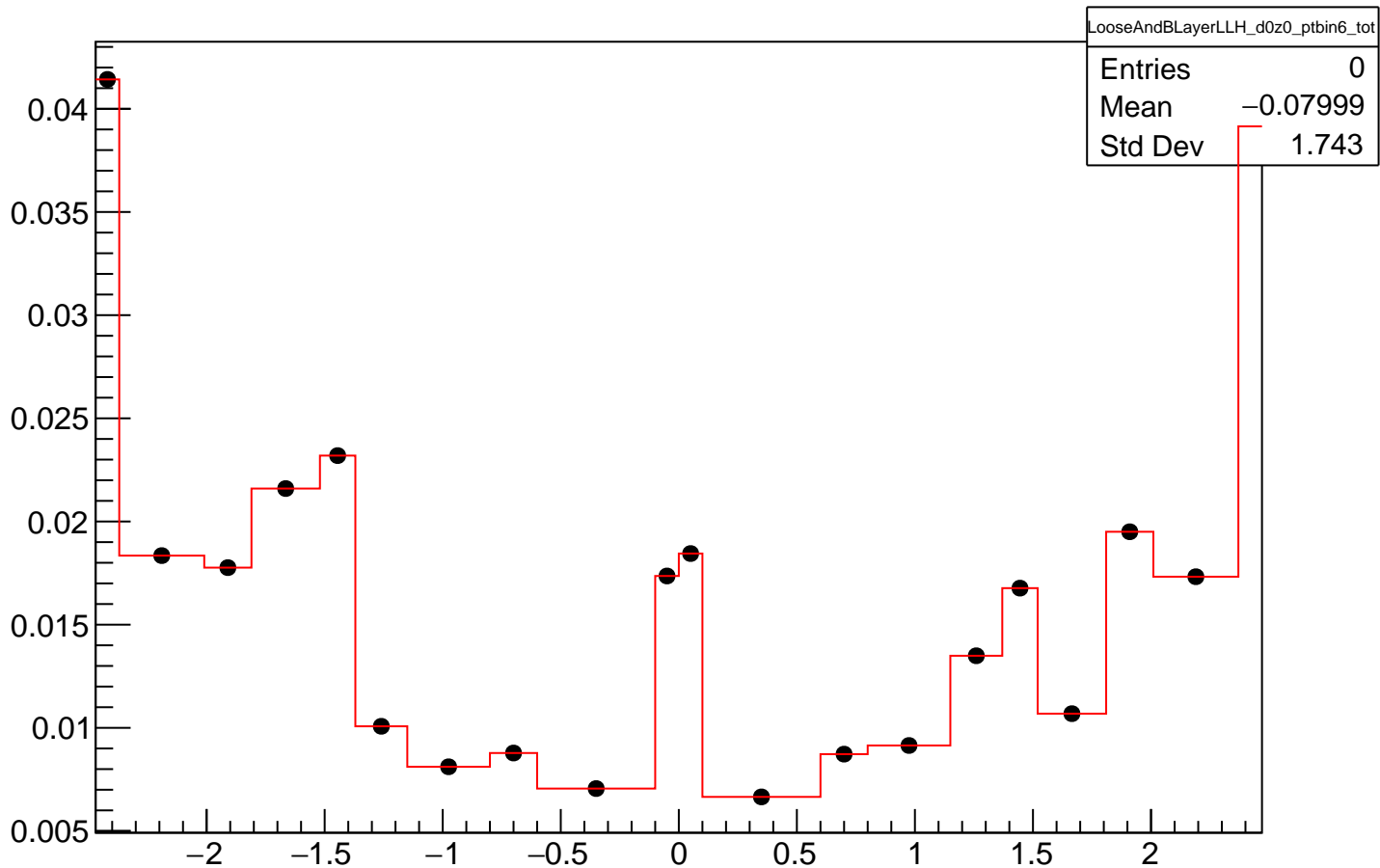
# FullSim\_LooseLLH\_d0z0\_tot



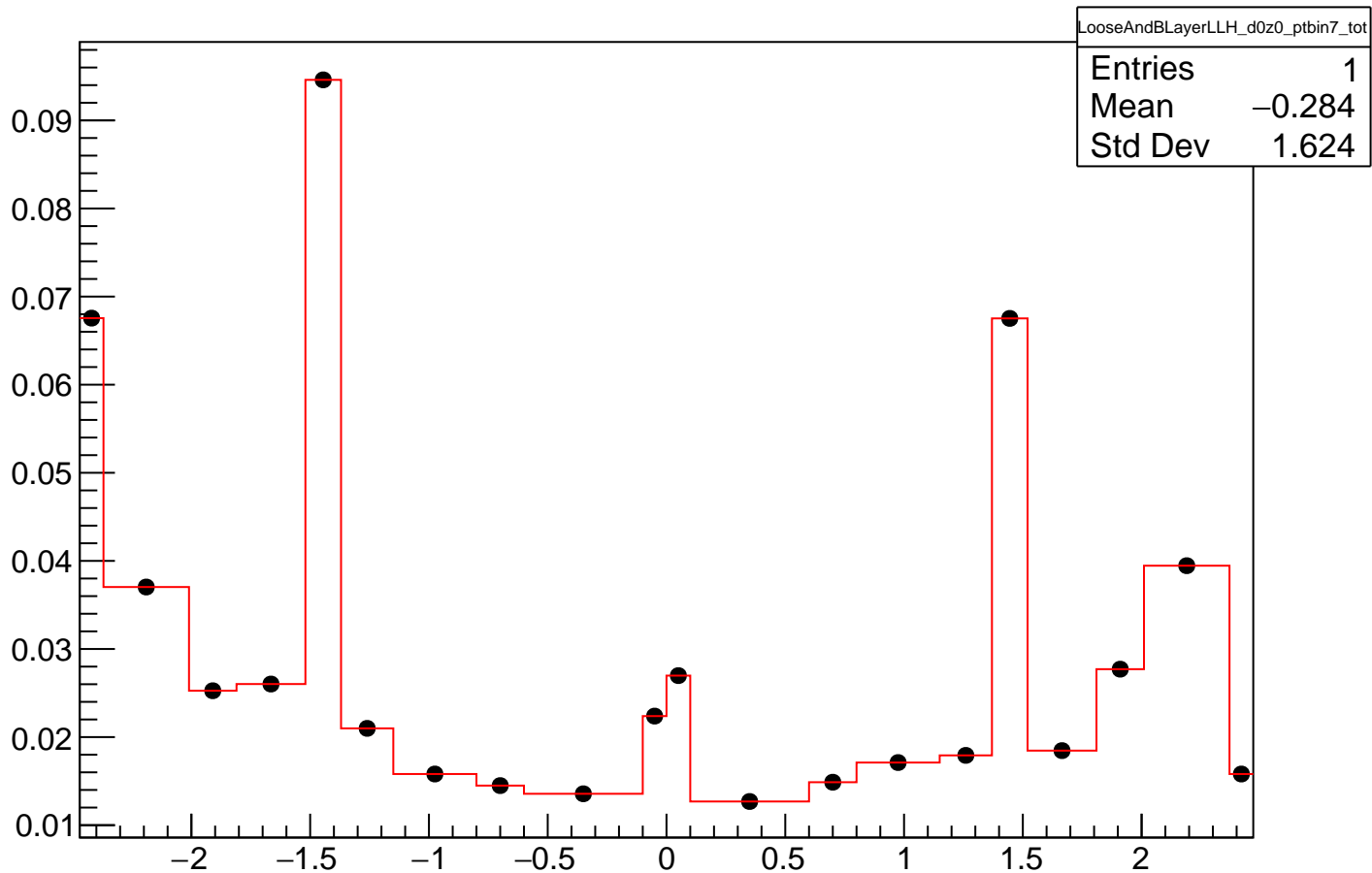
# FullSim\_LooseLLH\_d0z0\_tot



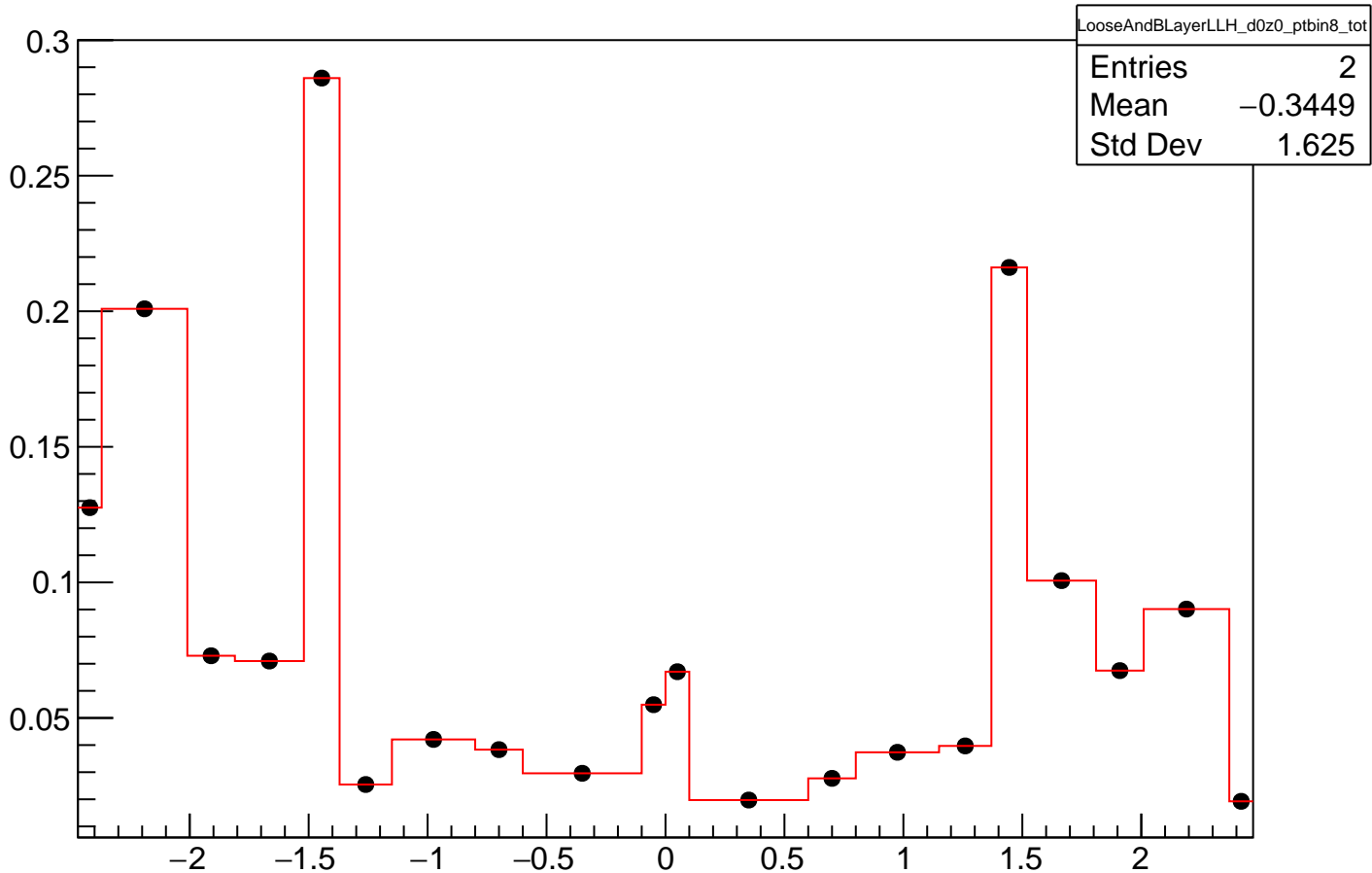
# FullSim\_LooseLLH\_d0z0\_tot



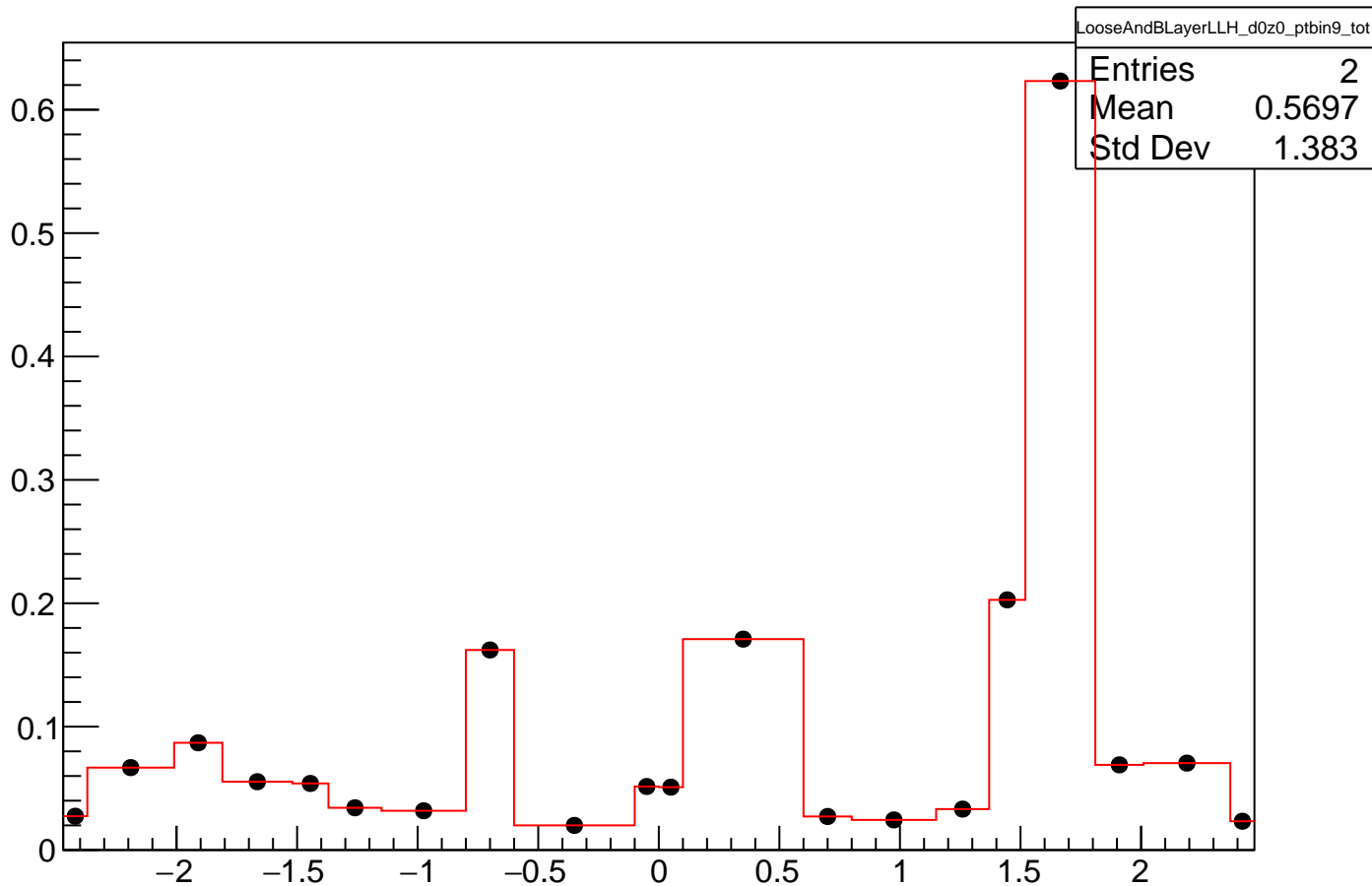
# FullSim\_LooseLLH\_d0z0\_tot



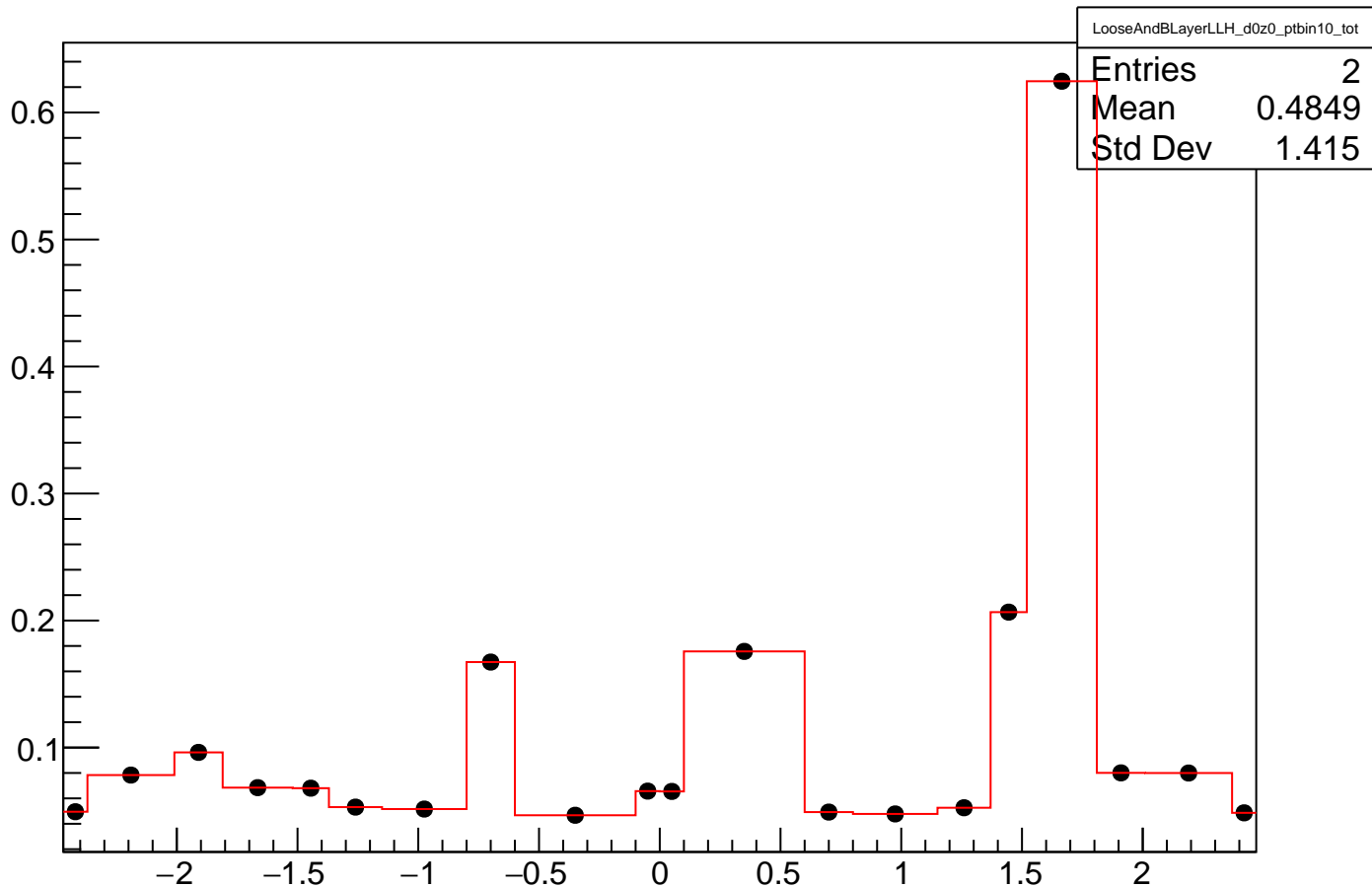
# FullSim\_LooseLLH\_d0z0\_tot



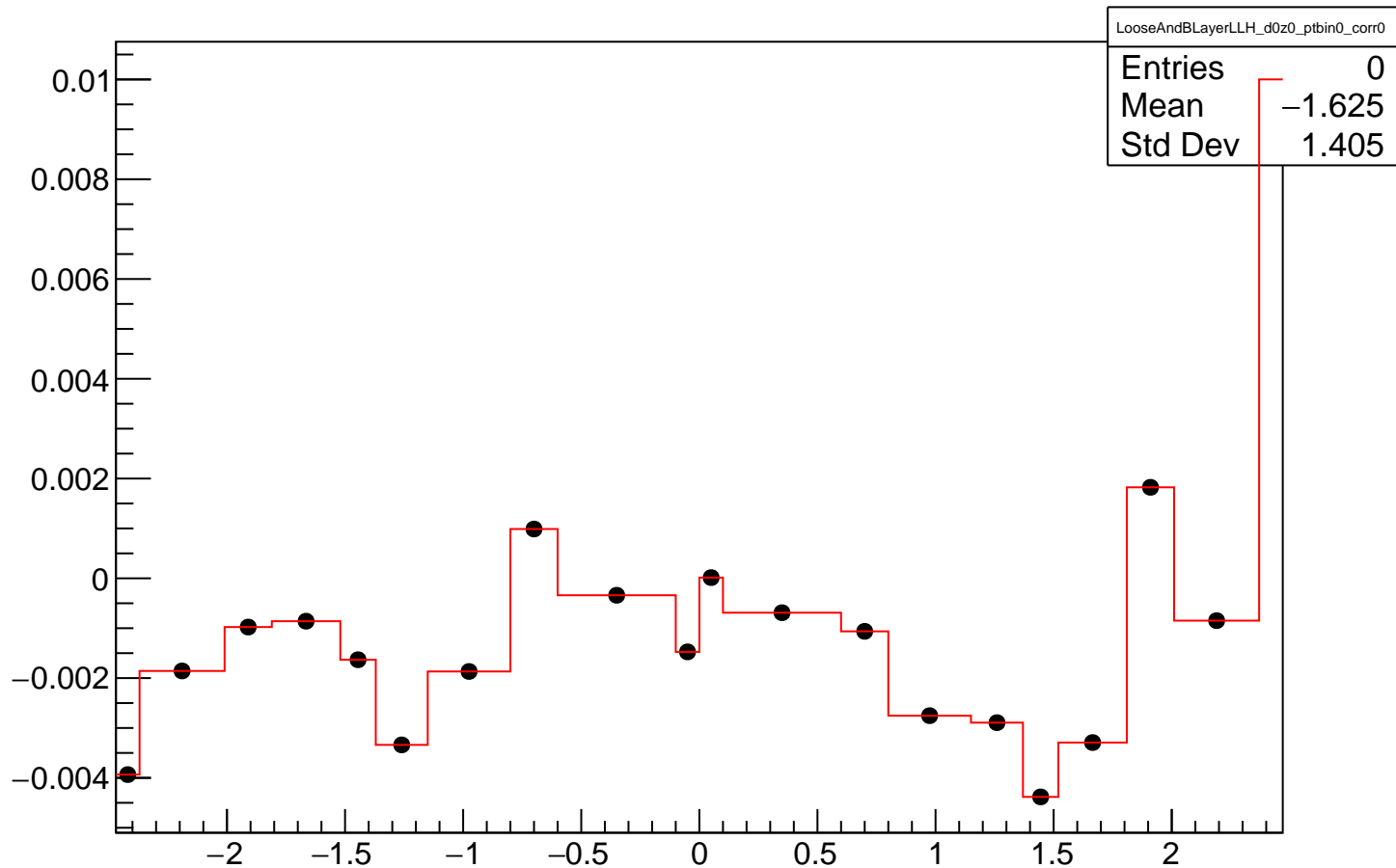
# FullSim\_LooseLLH\_d0z0\_tot



# FullSim\_LooseLLH\_d0z0\_tot

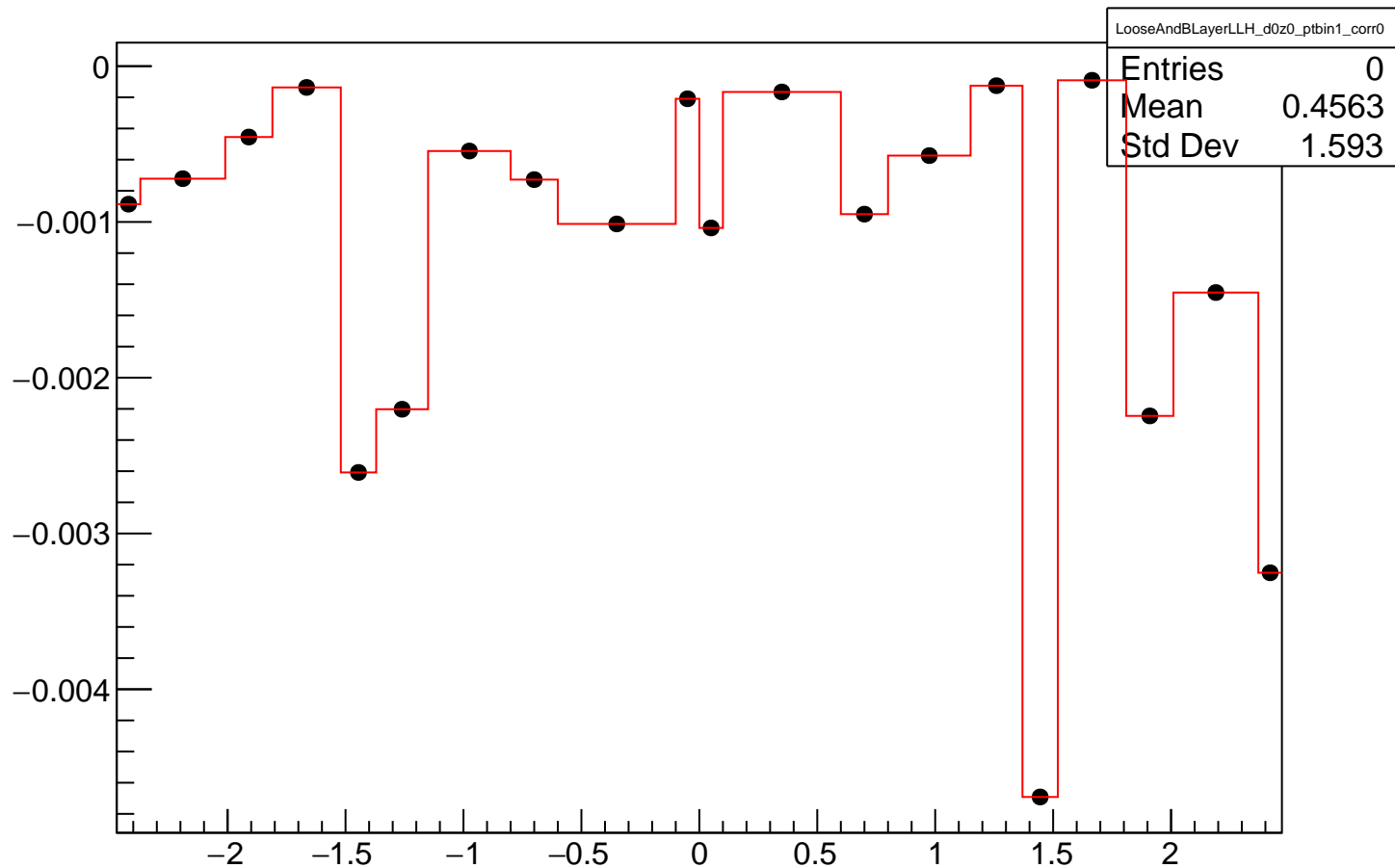


# FullSim\_LooseLLH\_d0z0\_corr0

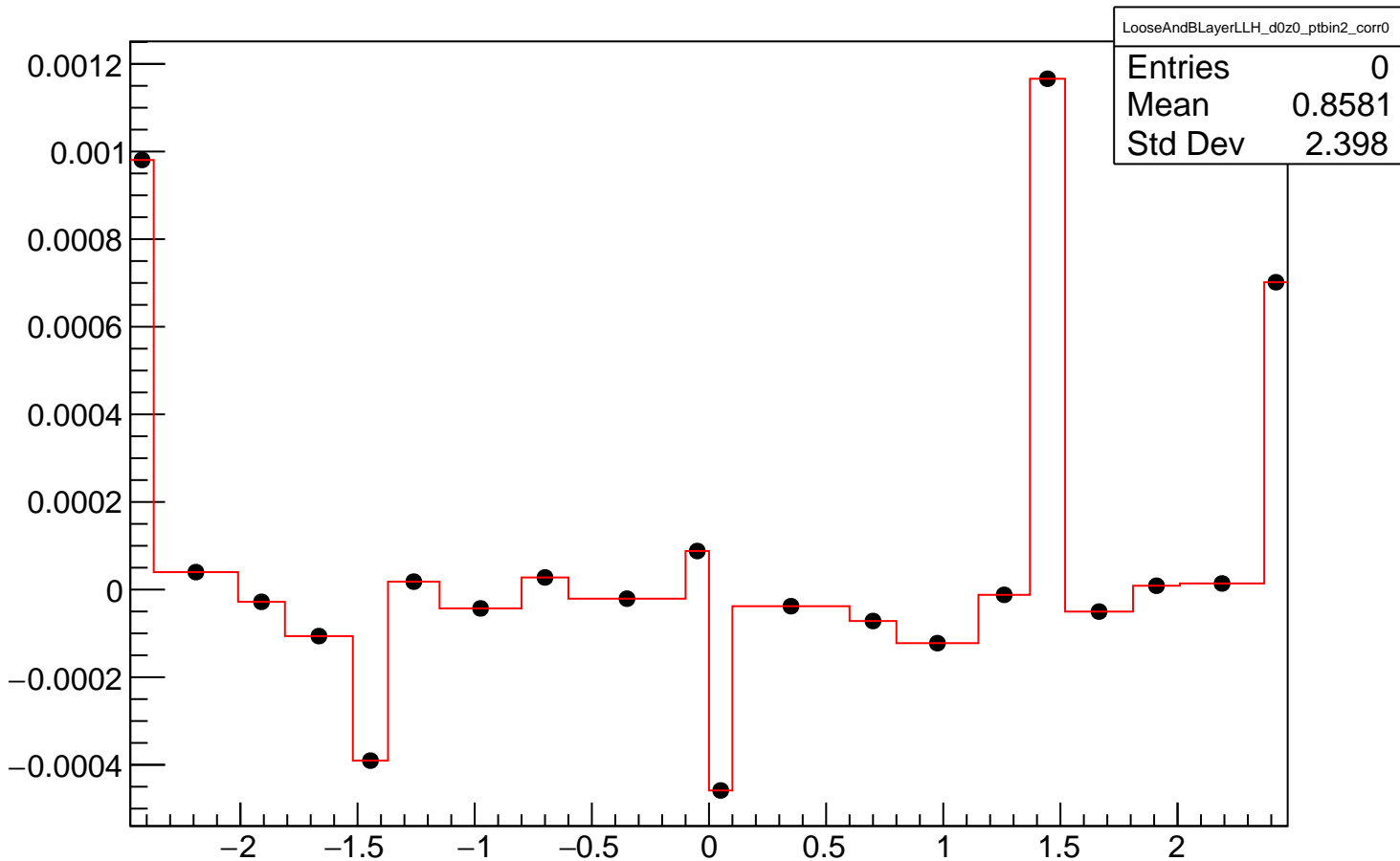




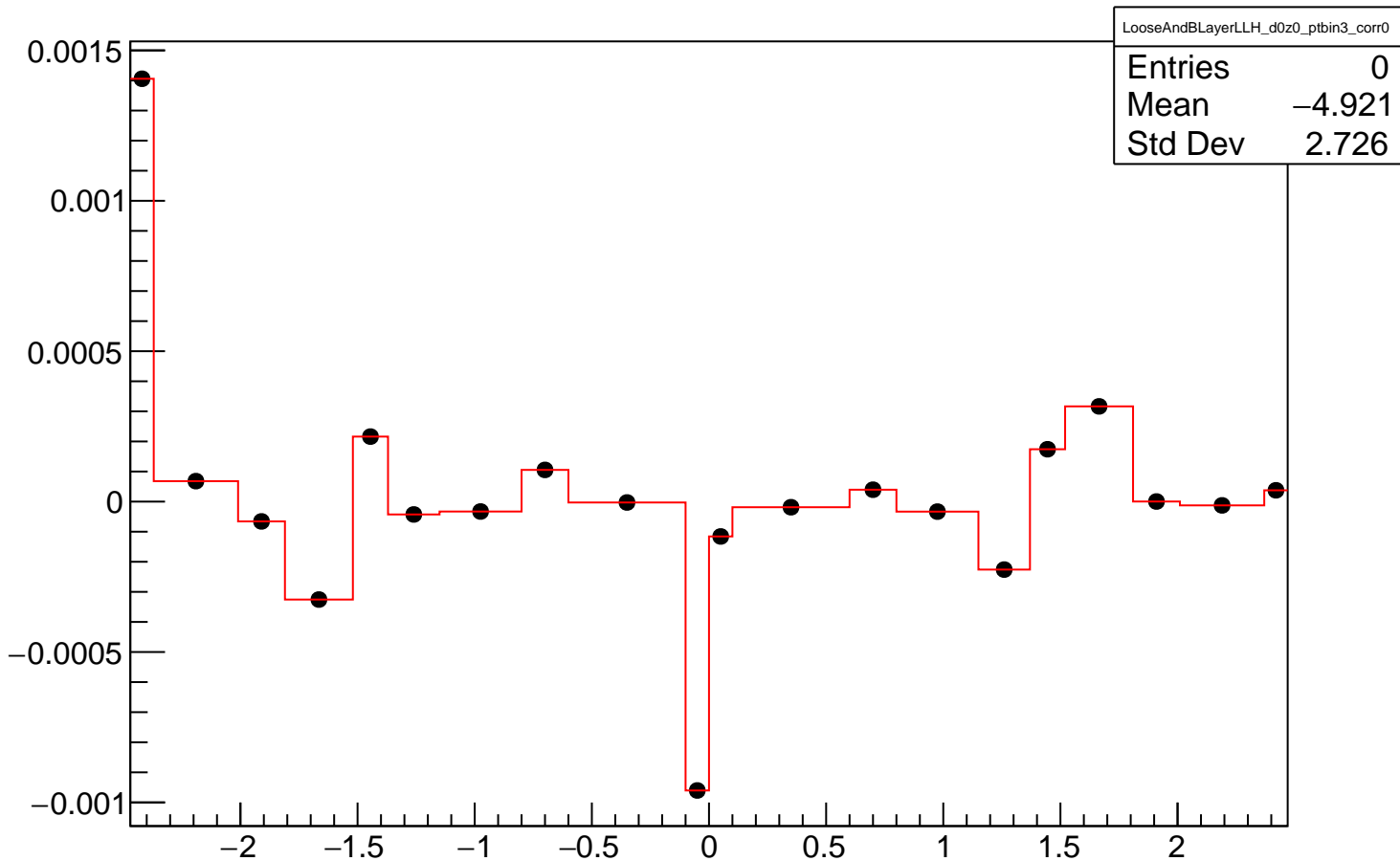
# FullSim\_LooseLLH\_d0z0\_corr0



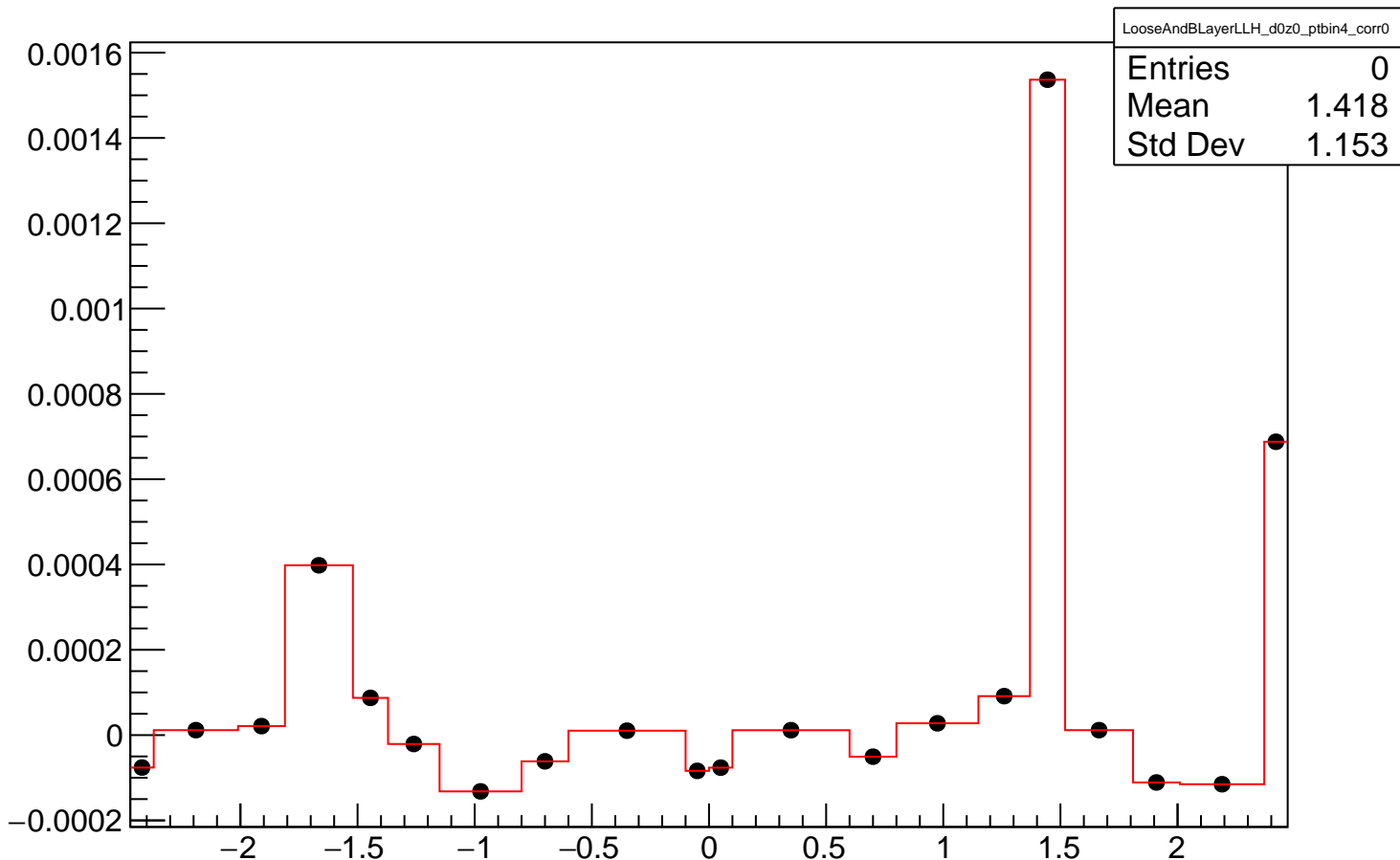
# FullSim\_LooseLLH\_d0z0\_corr0



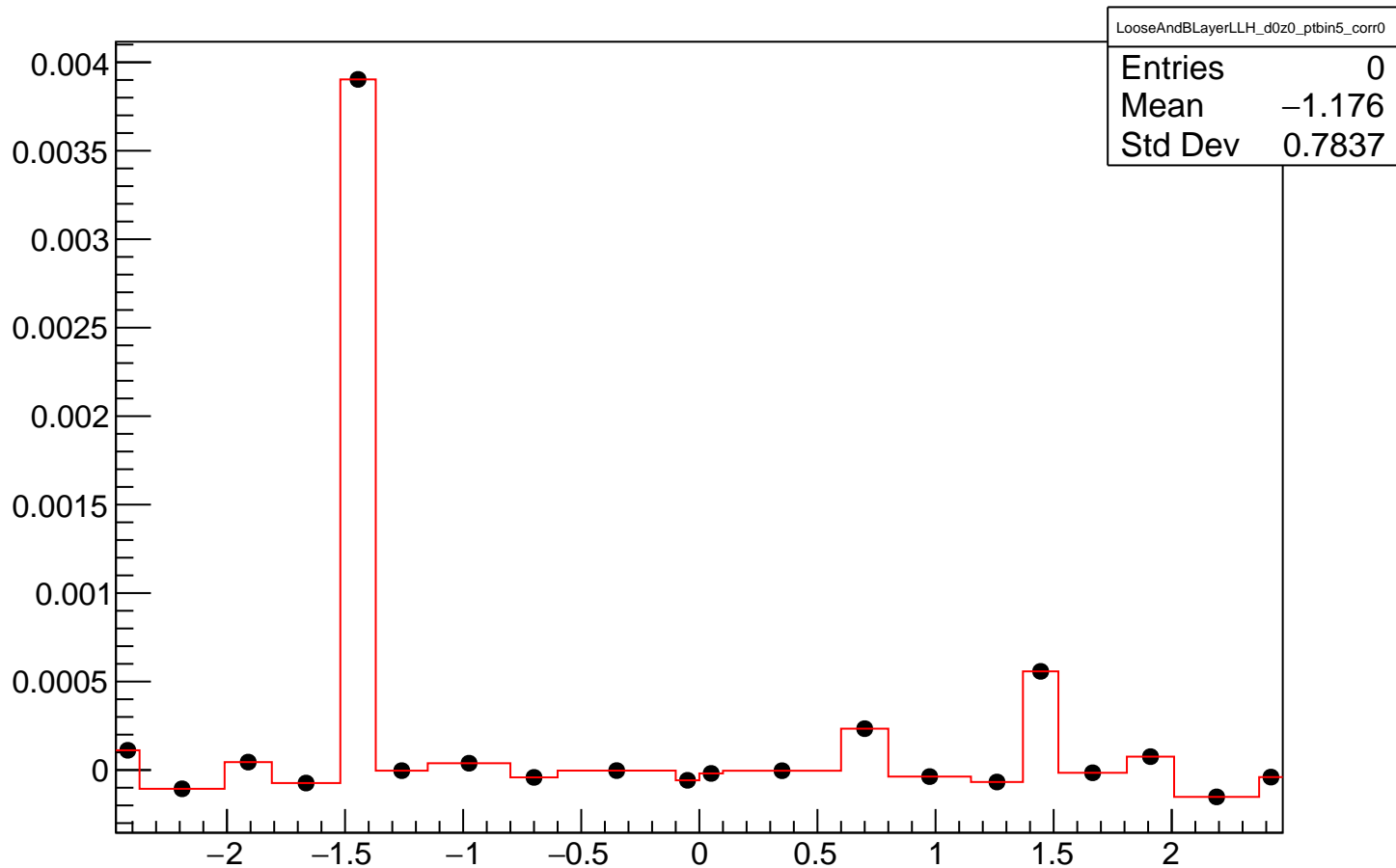
# FullSim\_LooseLLH\_d0z0\_corr0



# FullSim\_LooseLLH\_d0z0\_corr0

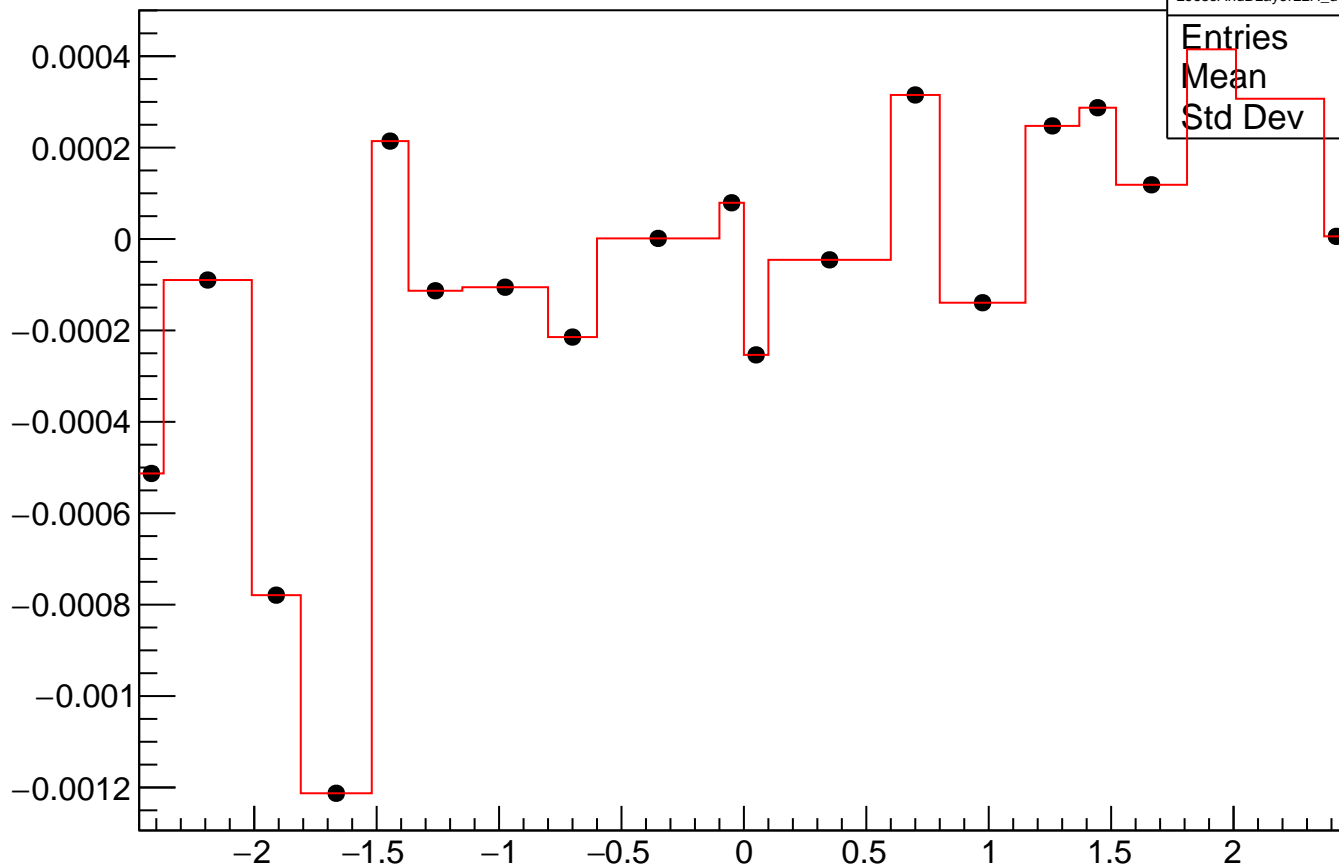


# FullSim\_LooseLLH\_d0z0\_corr0

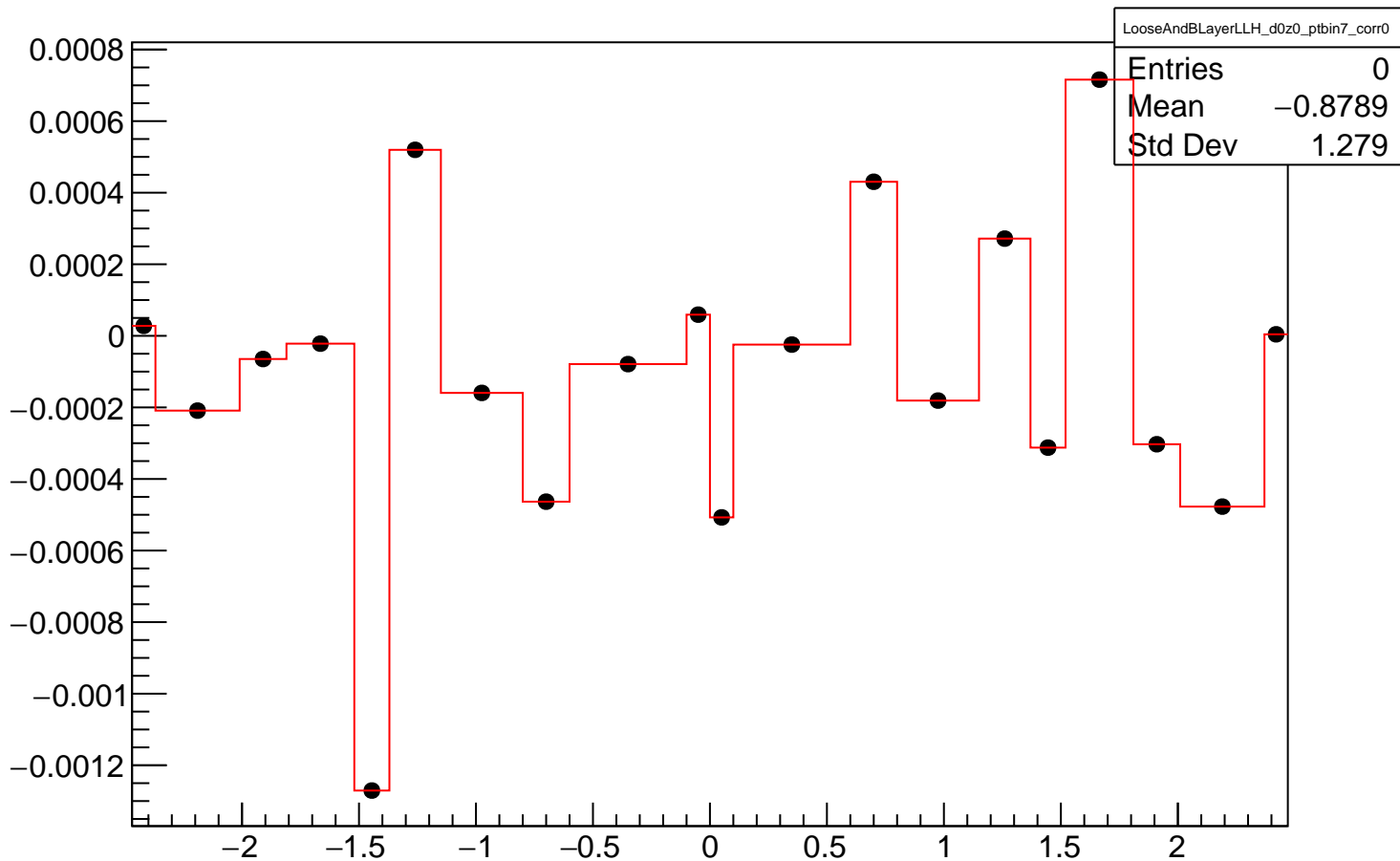


# FullSim\_LooseLLH\_d0z0\_corr0

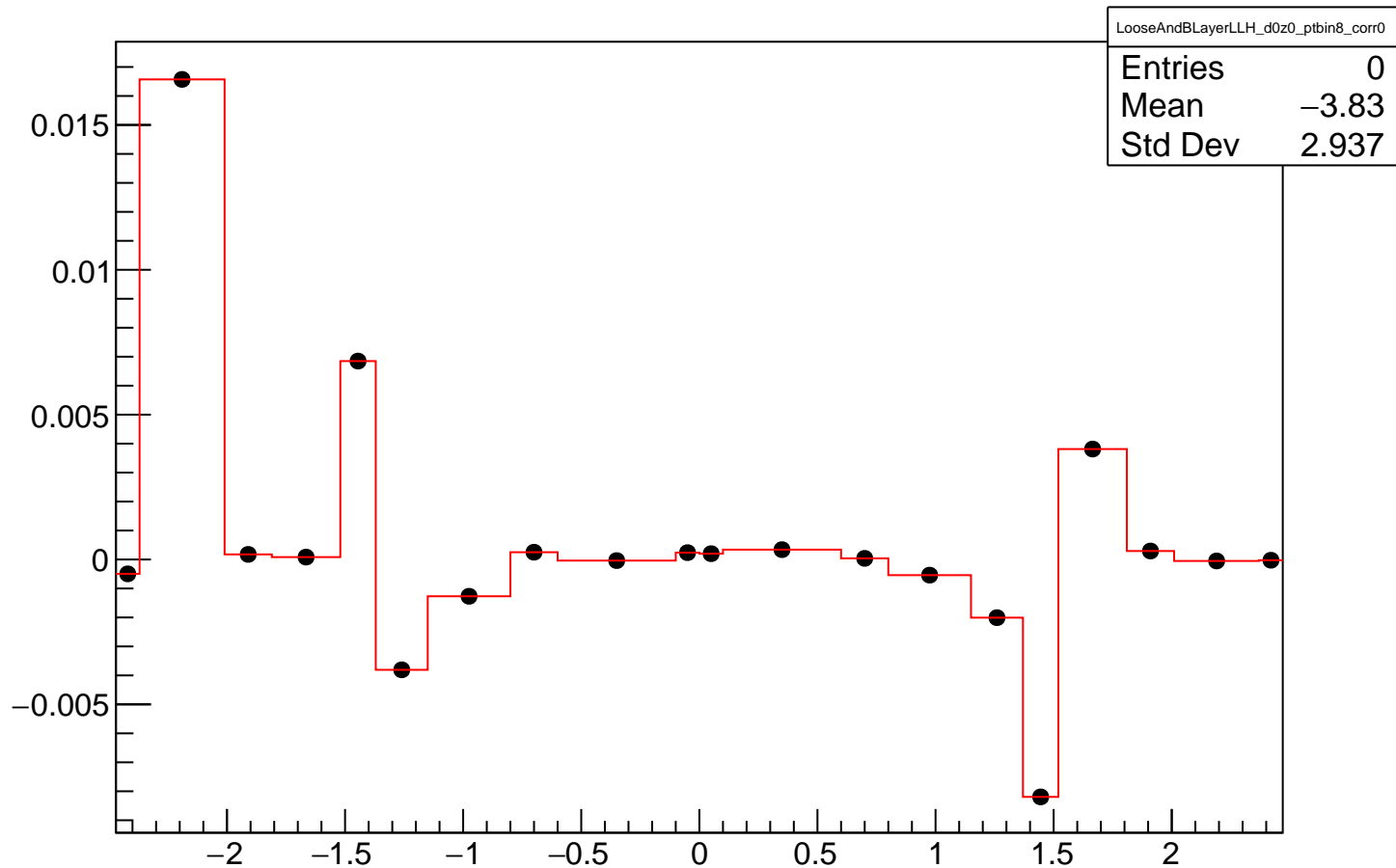
LooseAndBLayerLLH_d0z0_ptbin6_corr0	
Entries	0
Mean	-5.076
Std Dev	4.714



# FullSim\_LooseLLH\_d0z0\_corr0

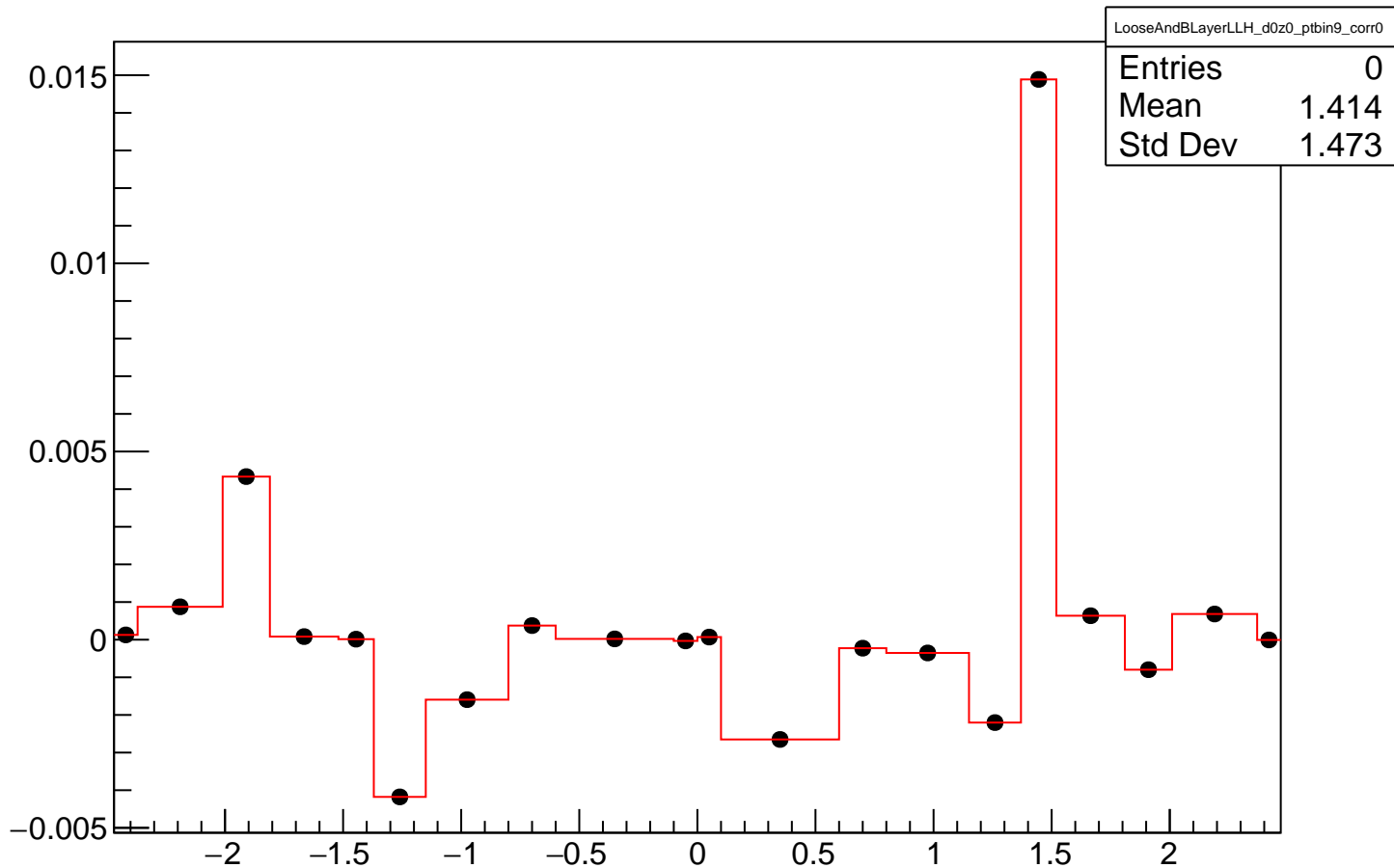


# FullSim\_LooseLLH\_d0z0\_corr0

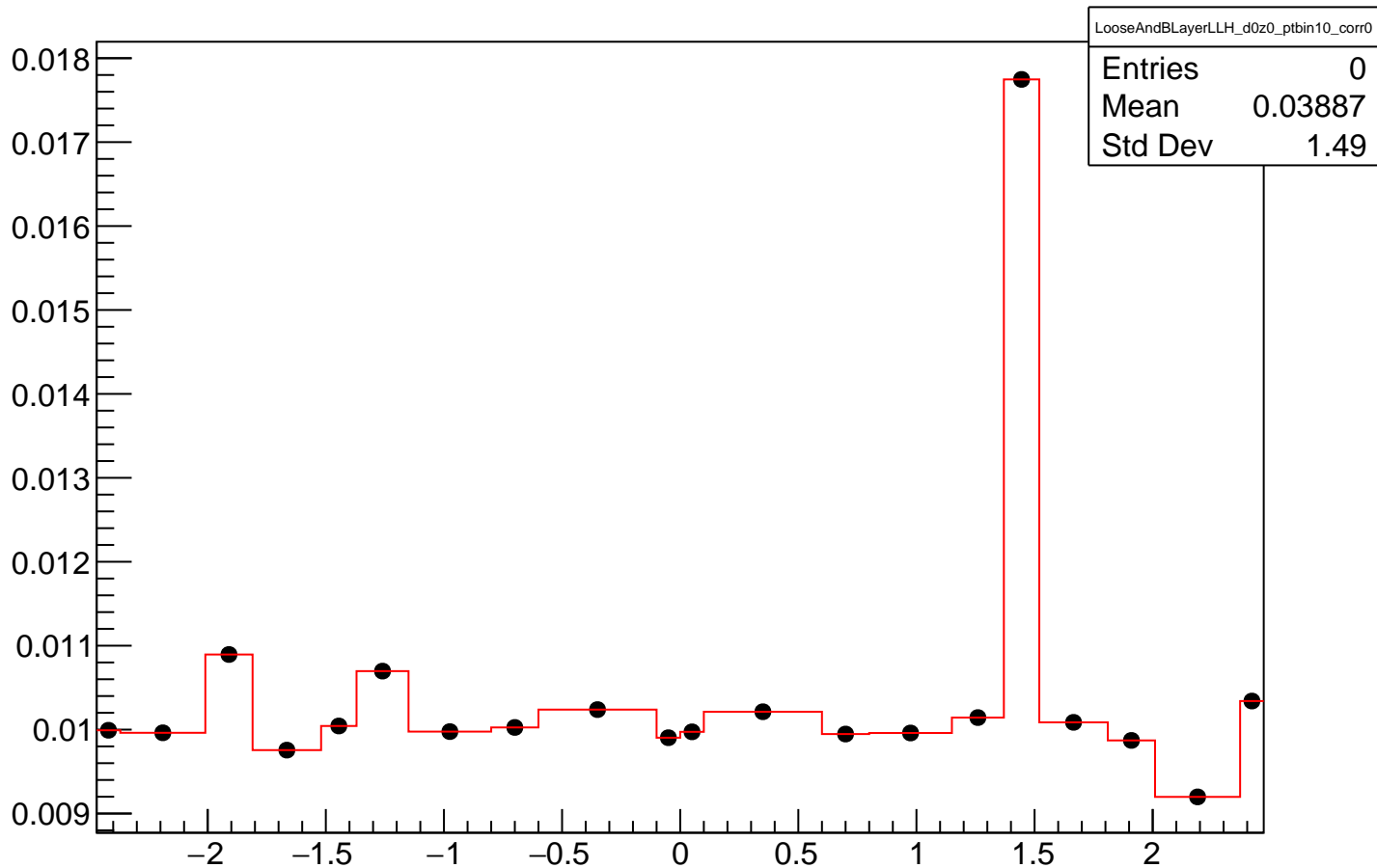




# FullSim\_LooseLLH\_d0z0\_corr0



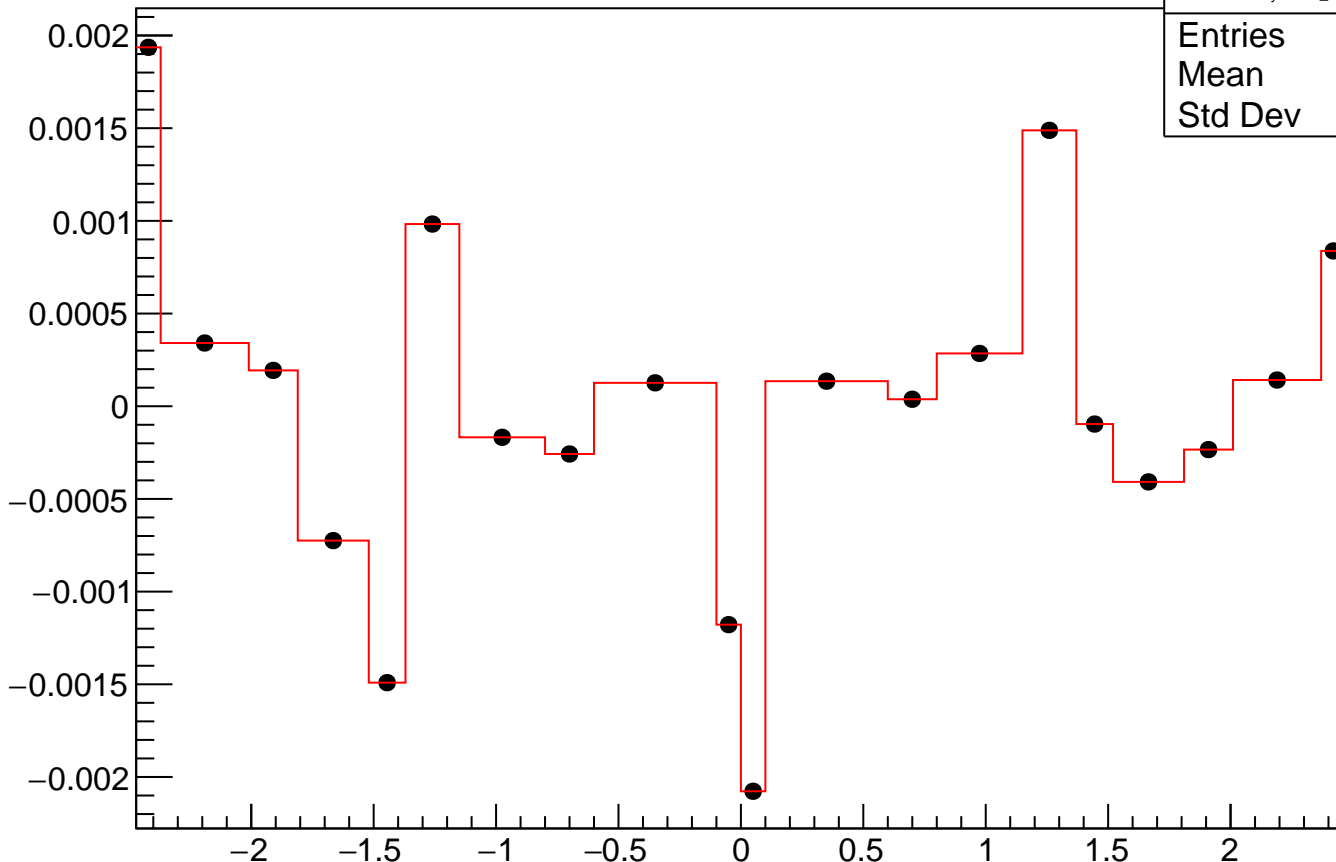
# FullSim\_LooseLLH\_d0z0\_corr0



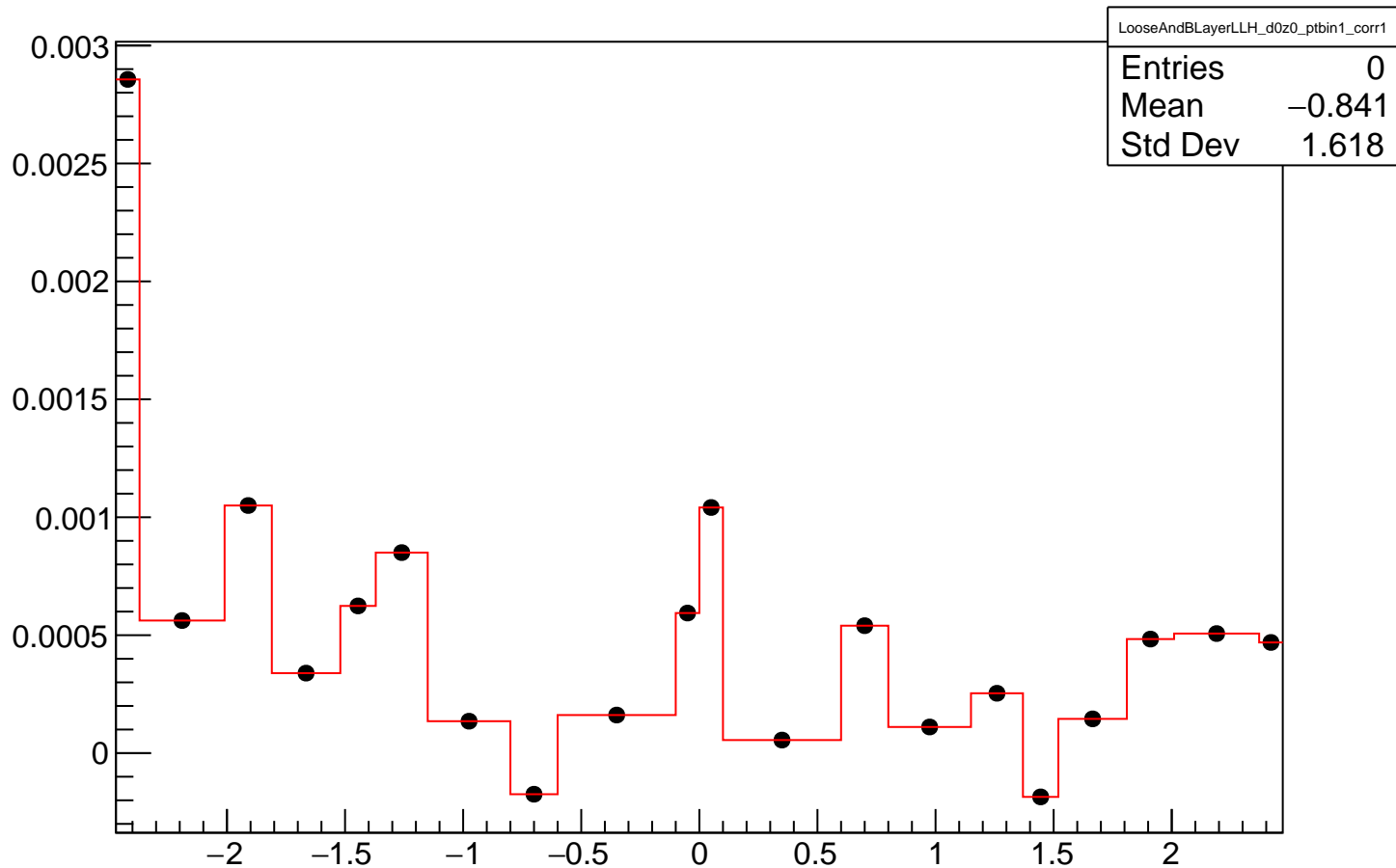
# FullSim\_LooseLLH\_d0z0\_corr1

LooseAndBLayerLLH\_d0z0\_ptbin0\_corr1

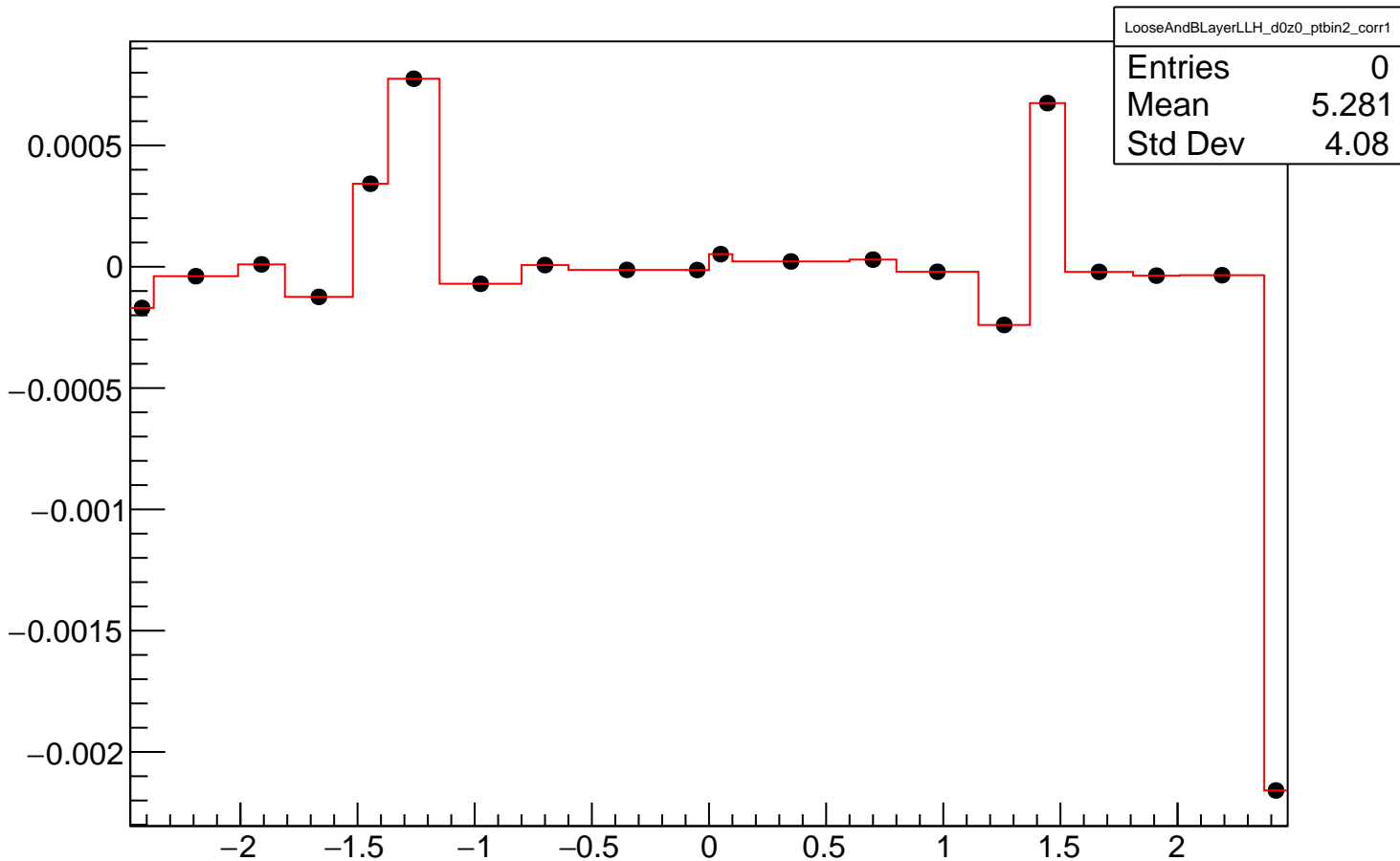
Entries	0
Mean	0.9641
Std Dev	11.12



# FullSim\_LooseLLH\_d0z0\_corr1



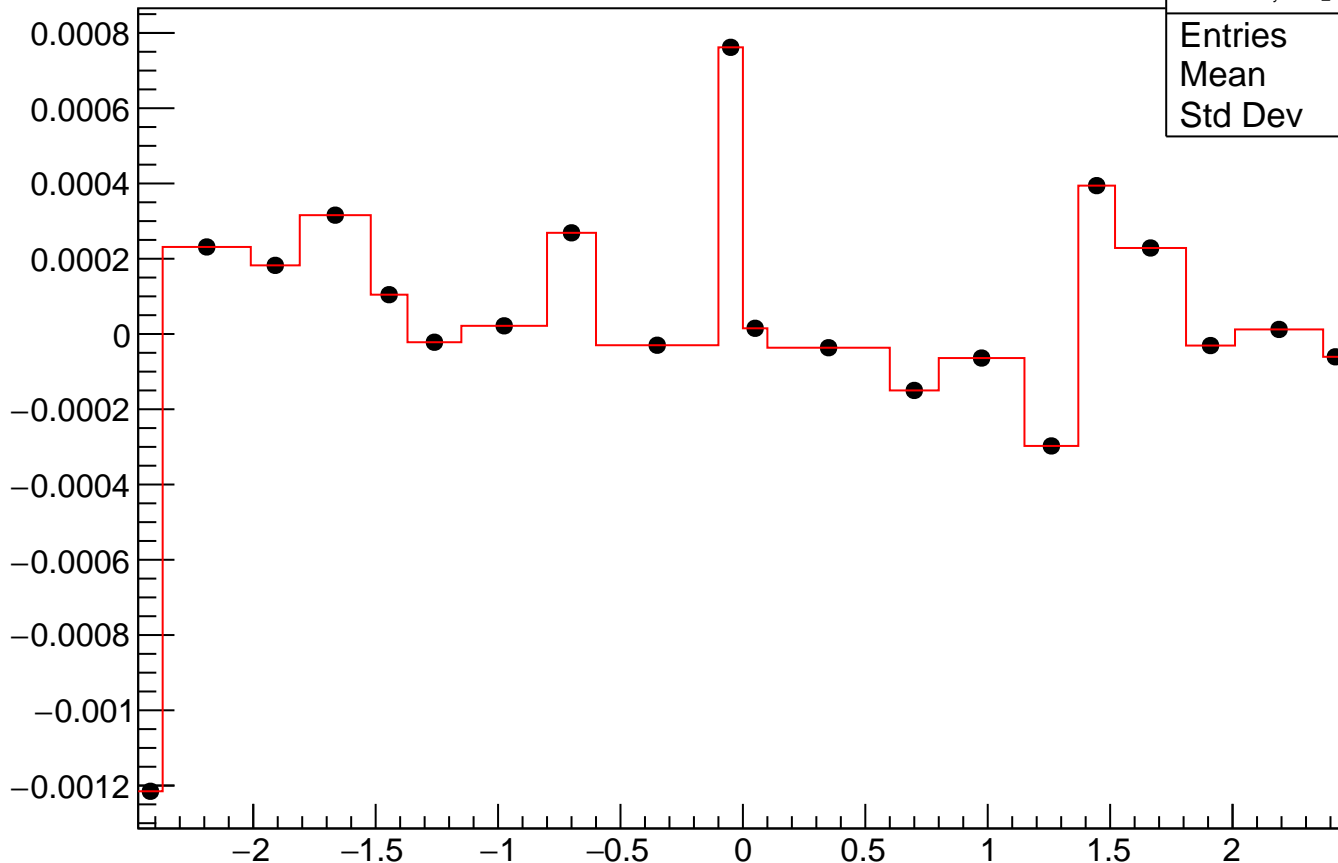
# FullSim\_LooseLLH\_d0z0\_corr1



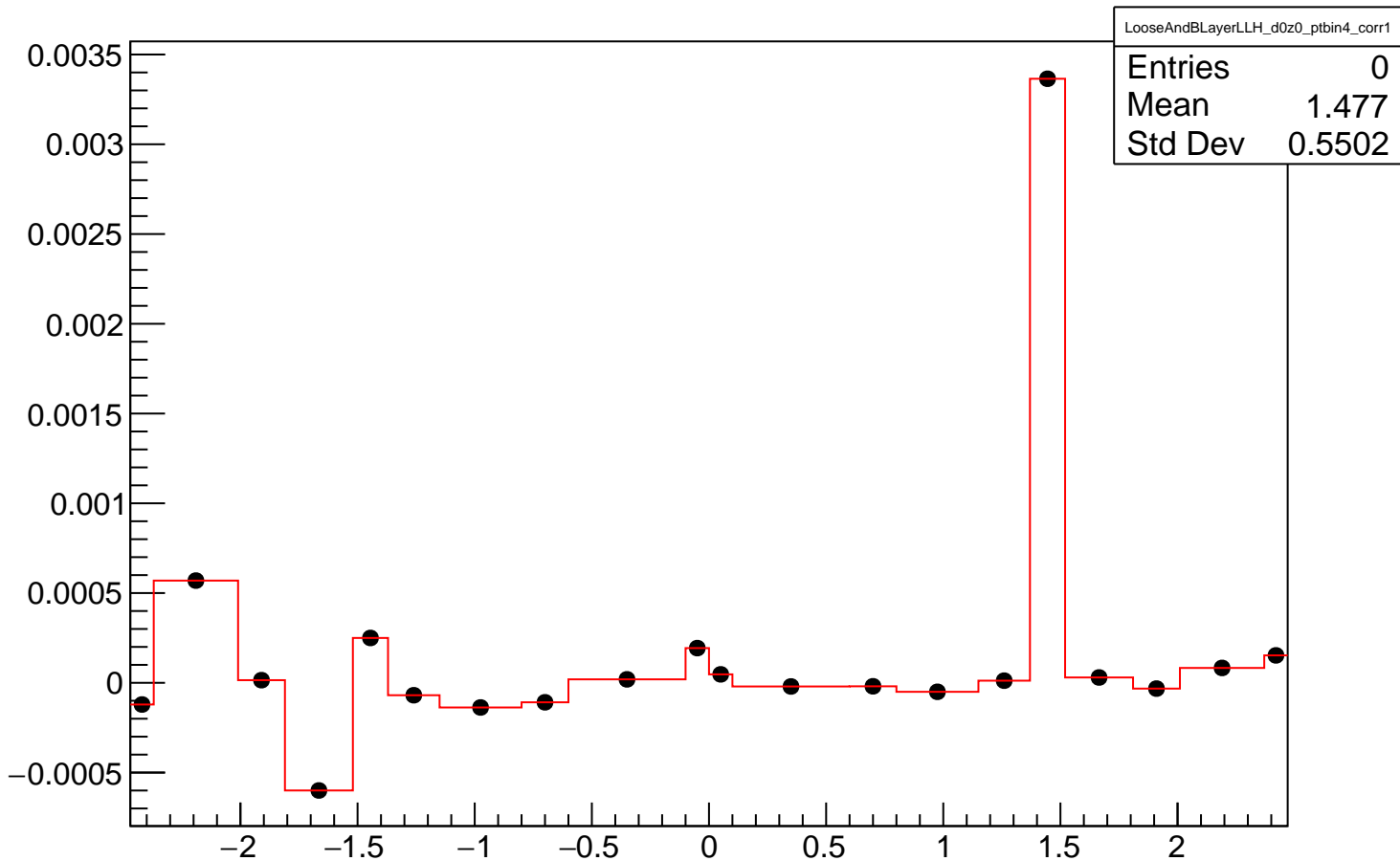
# FullSim\_LooseLLH\_d0z0\_corr1

LooseAndBLayerLLH\_d0z0\_ptbin3\_corr1

Entries	0
Mean	2.248
Std Dev	3.304

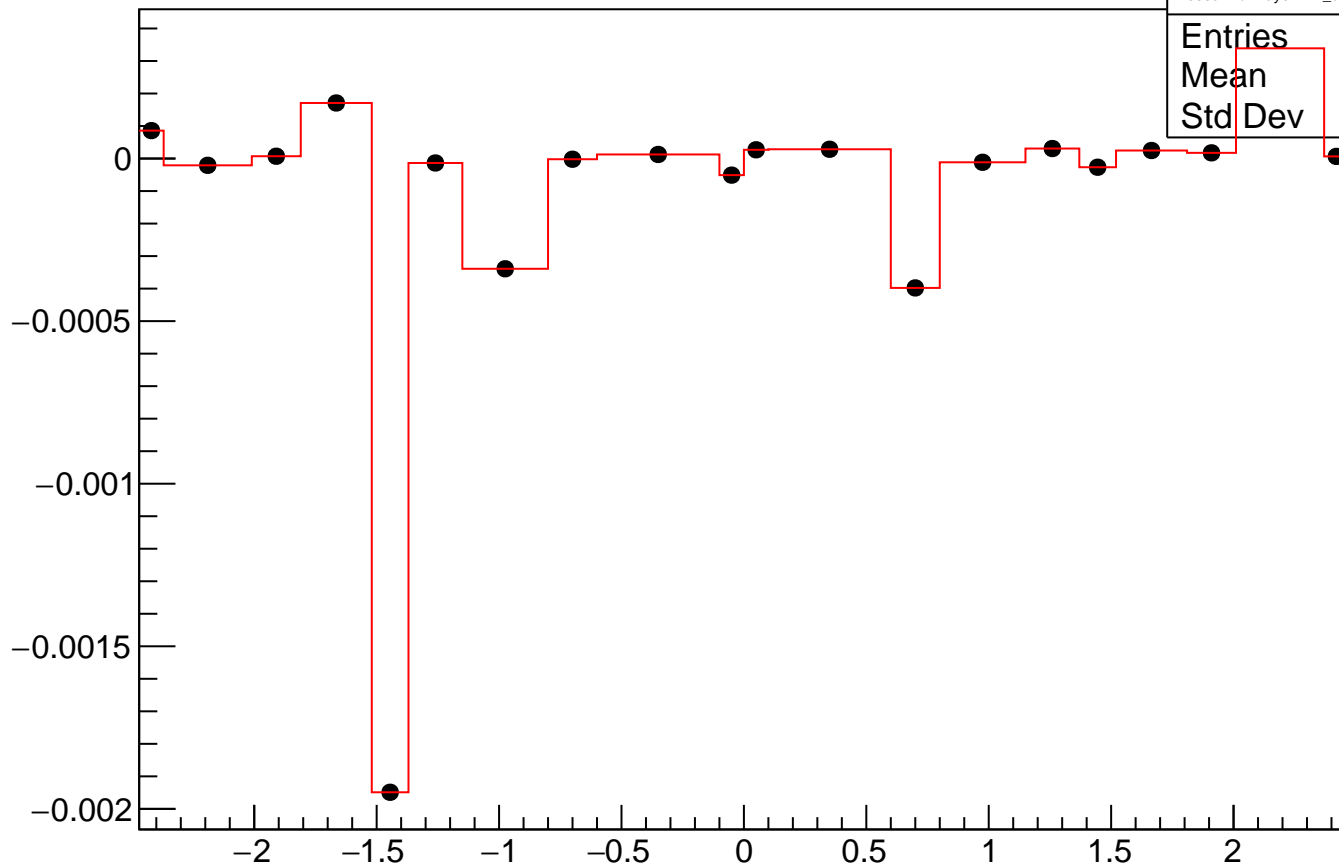


# FullSim\_LooseLLH\_d0z0\_corr1



# FullSim\_LooseLLH\_d0z0\_corr1

LooseAndBLayerLLH_d0z0_ptbin5_corr1	
Entries	0
Mean	-1.58
Std Dev	1.251

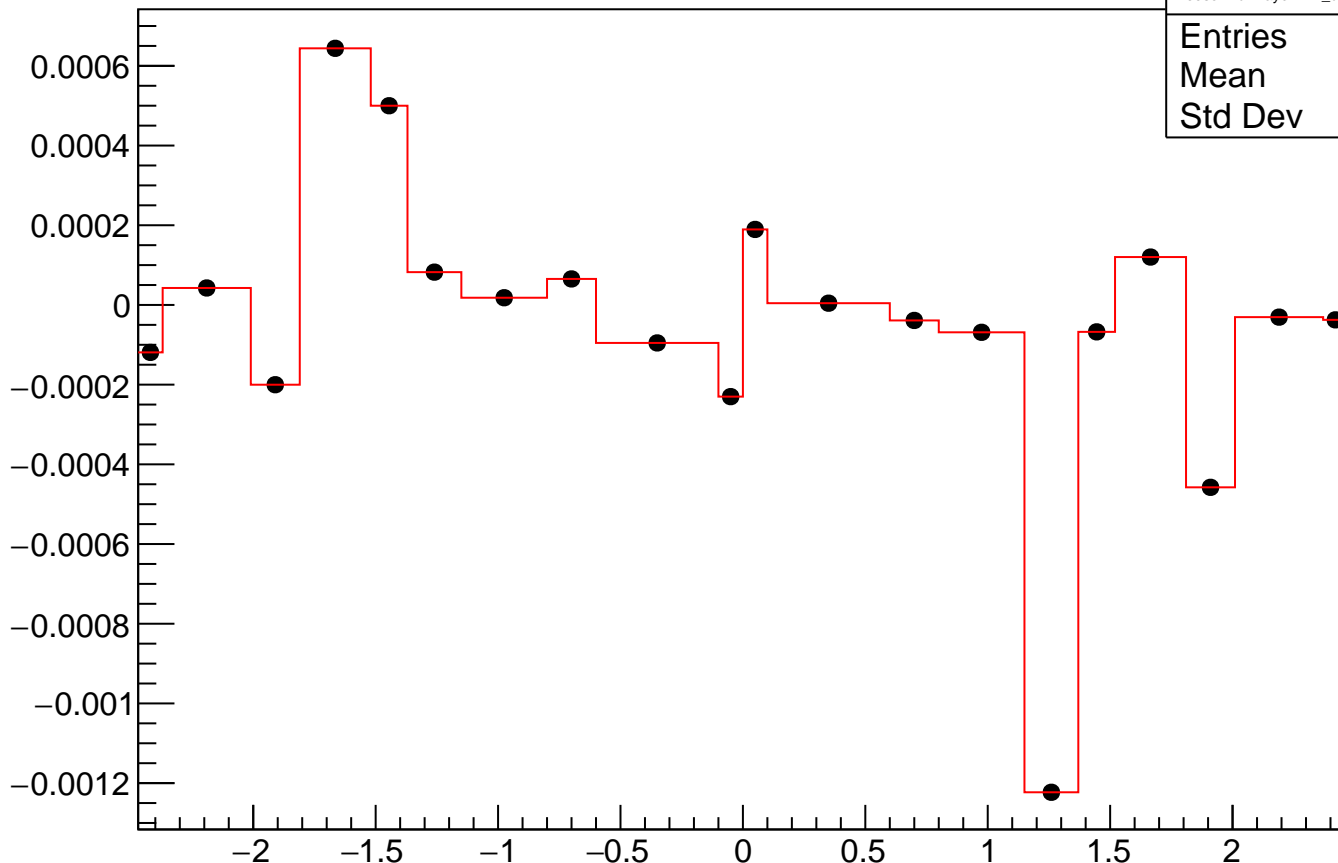




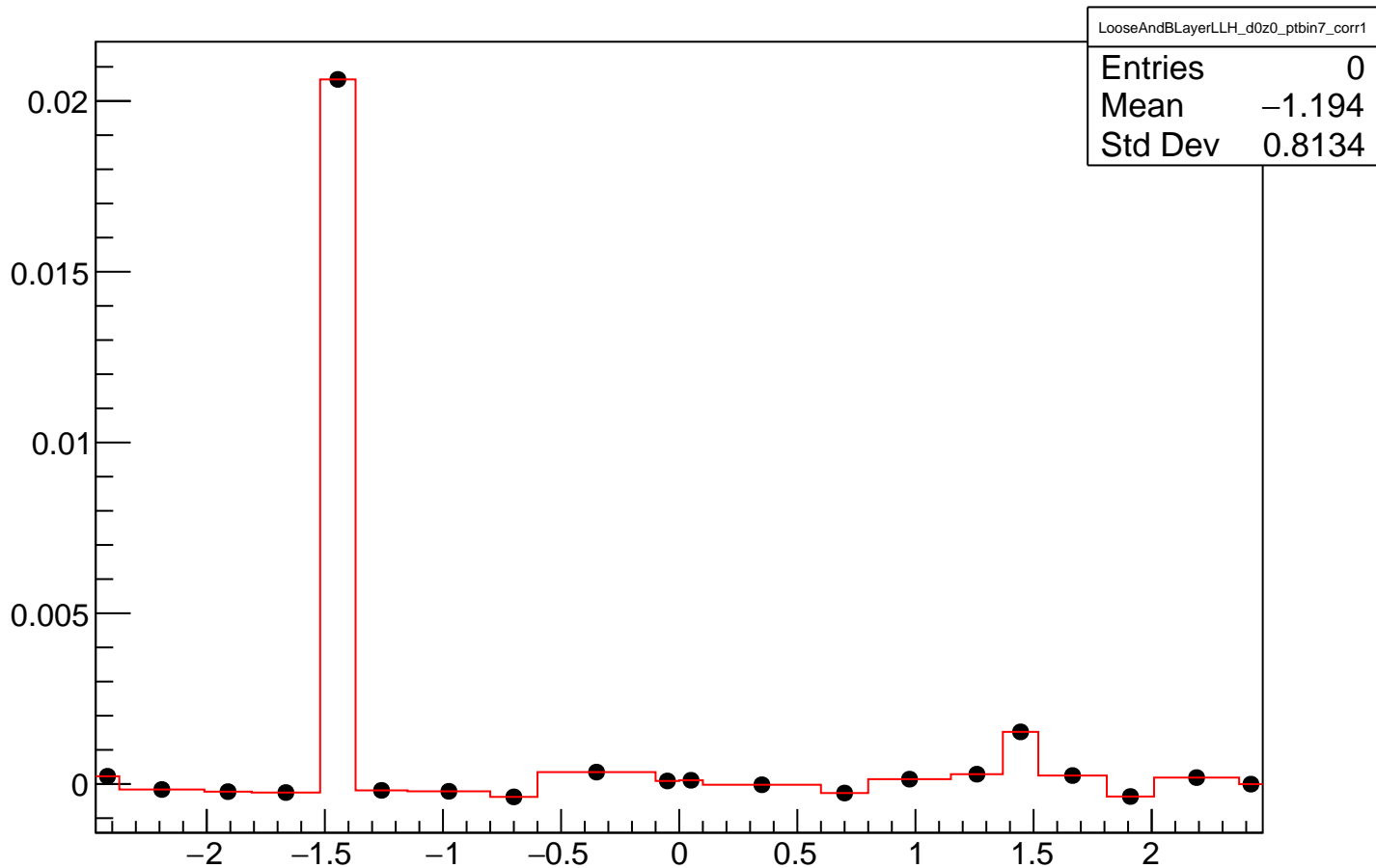
# FullSim\_LooseLLH\_d0z0\_corr1

LooseAndBLayerLLH\_d0z0\_ptbin6\_corr1

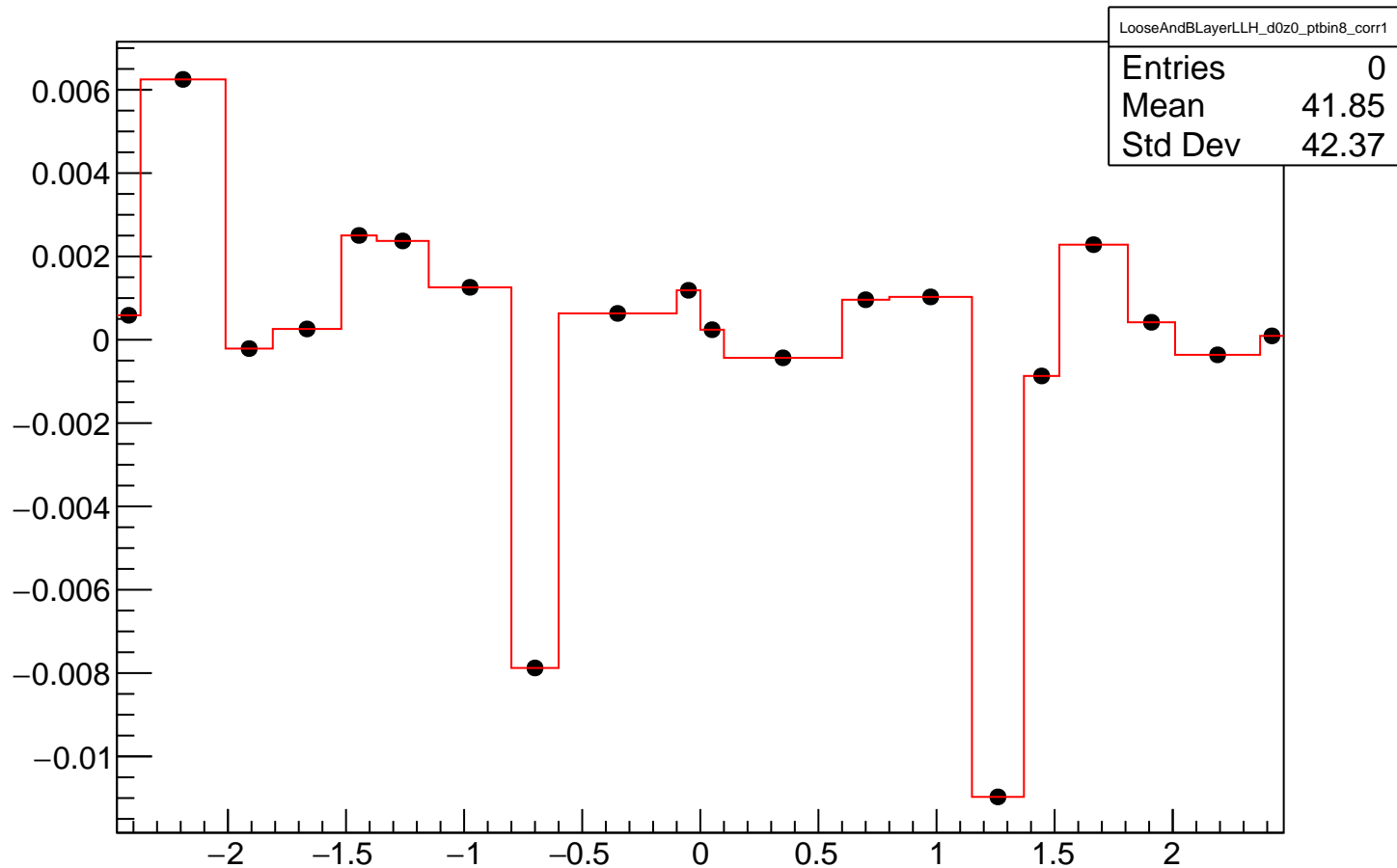
Entries	0
Mean	4.316
Std Dev	4.038



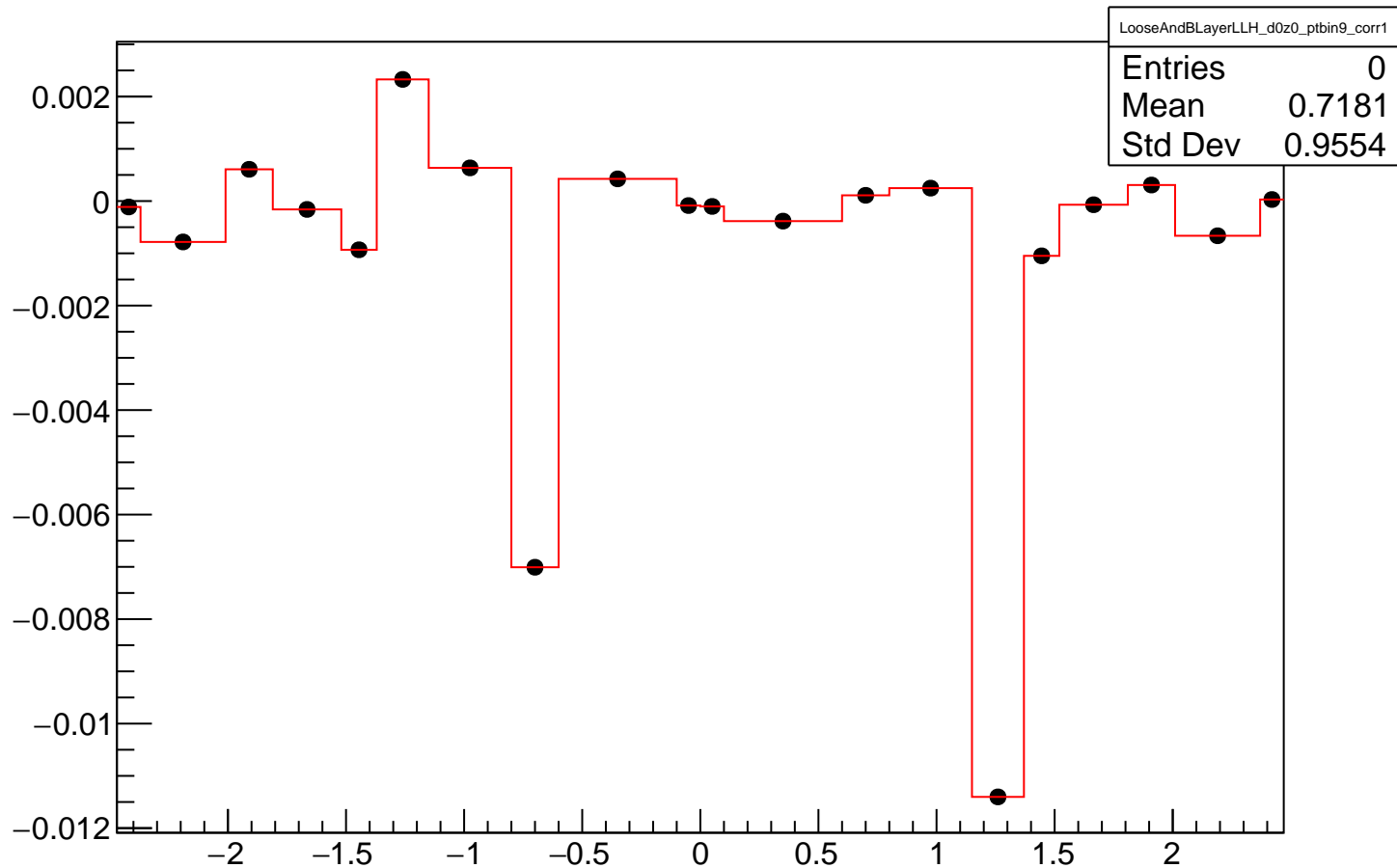
# FullSim\_LooseLLH\_d0z0\_corr1



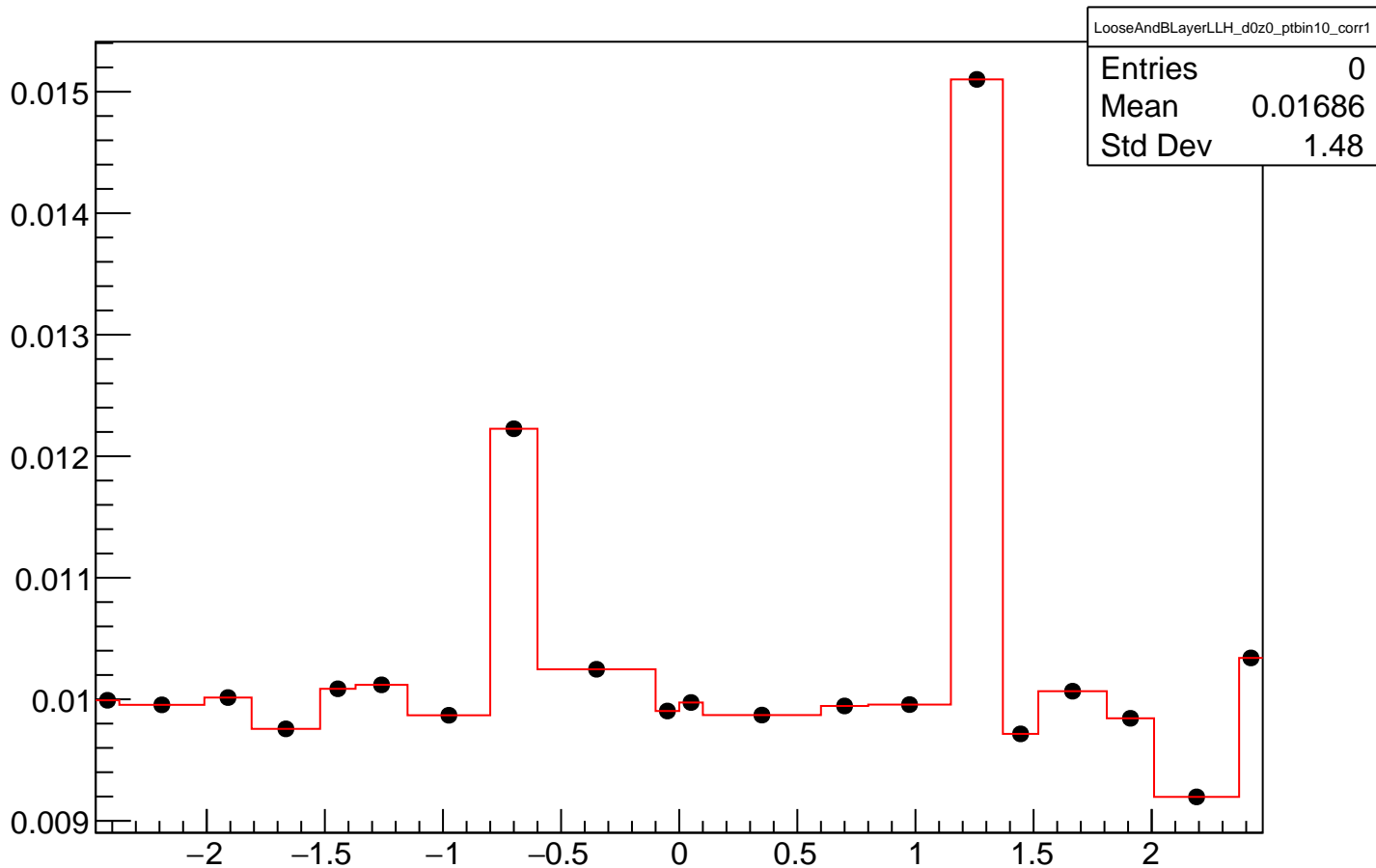
# FullSim\_LooseLLH\_d0z0\_corr1



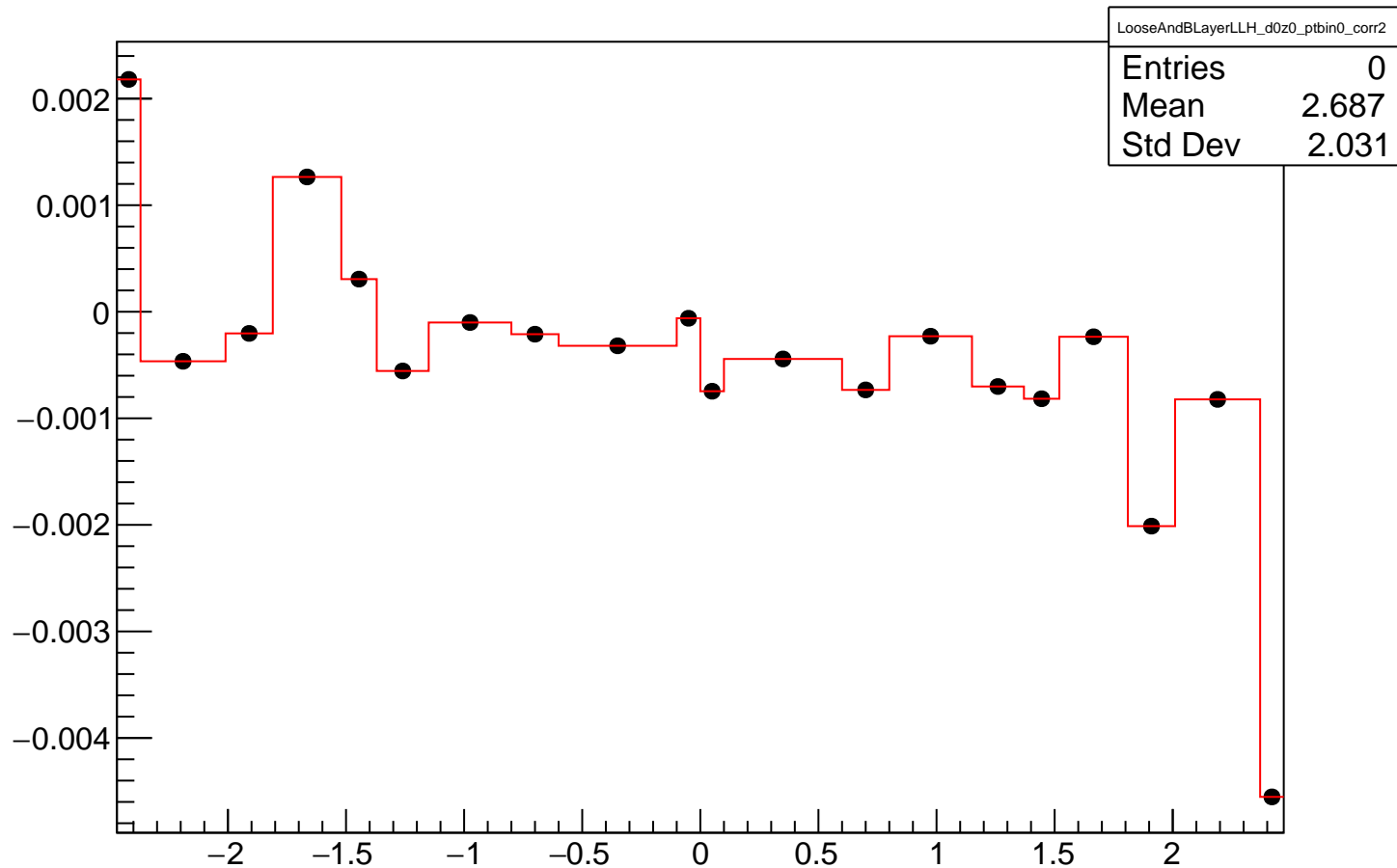
# FullSim\_LooseLLH\_d0z0\_corr1



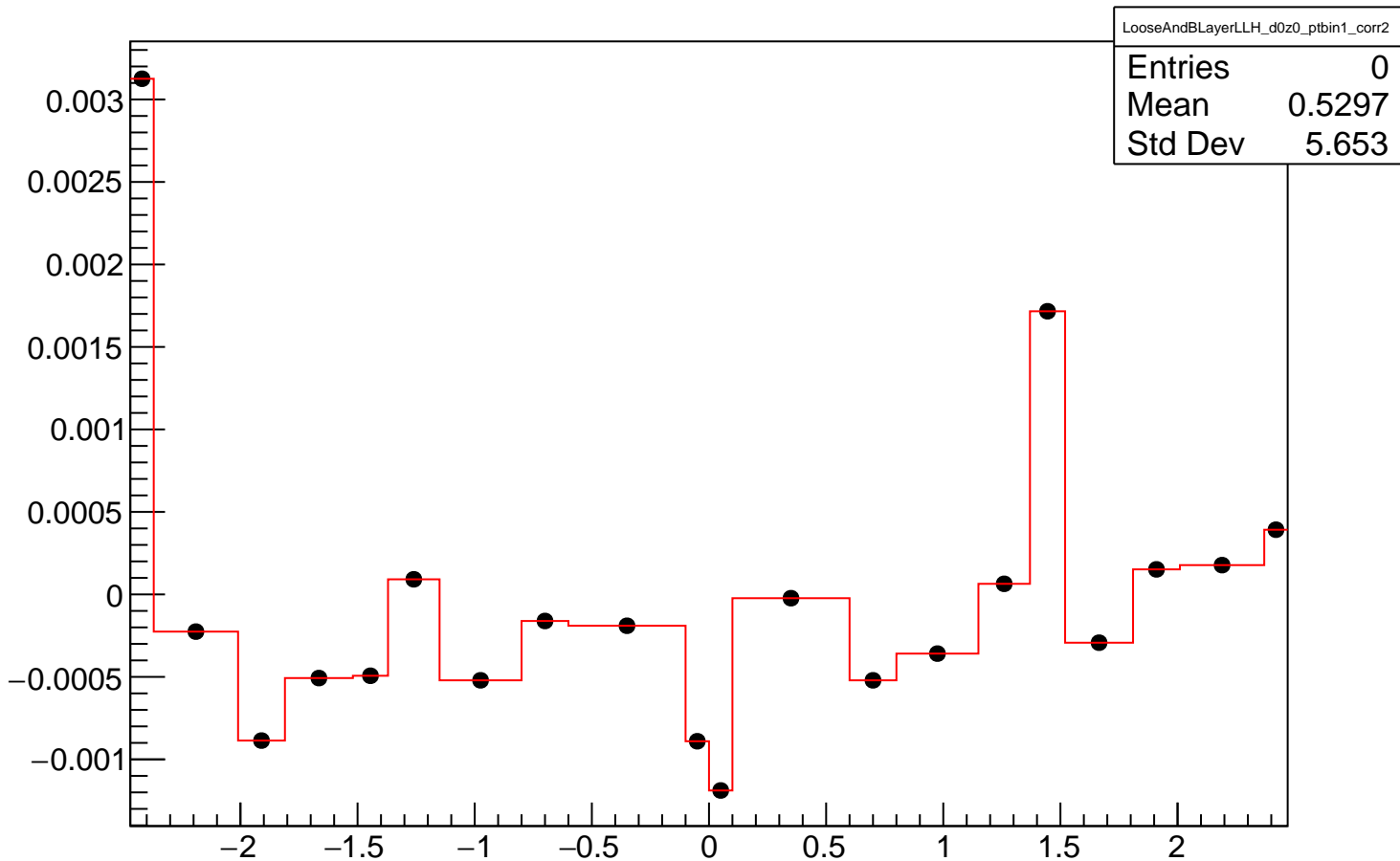
# FullSim\_LooseLLH\_d0z0\_corr1



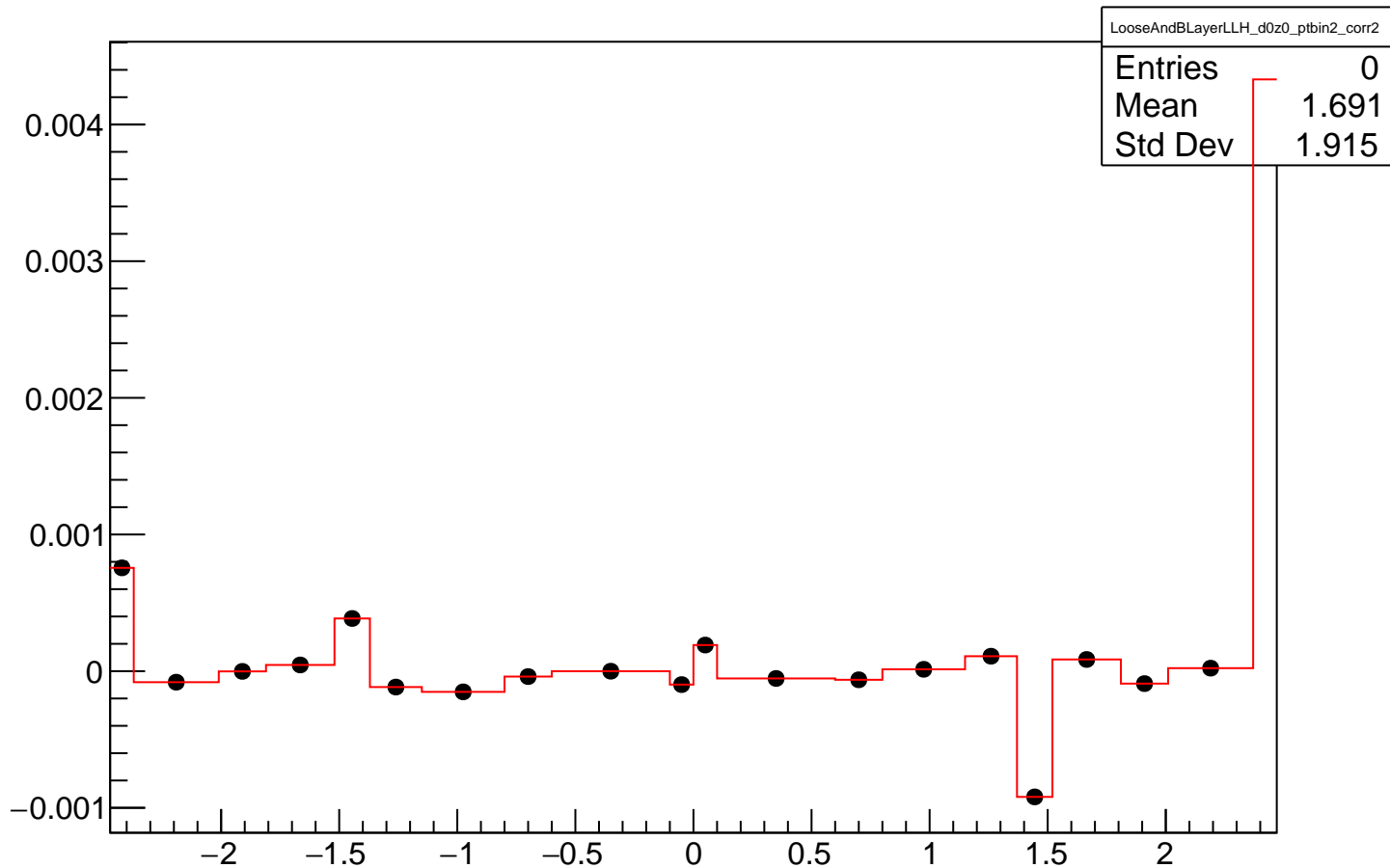
# FullSim\_LooseLLH\_d0z0\_corr2



# FullSim\_LooseLLH\_d0z0\_corr2

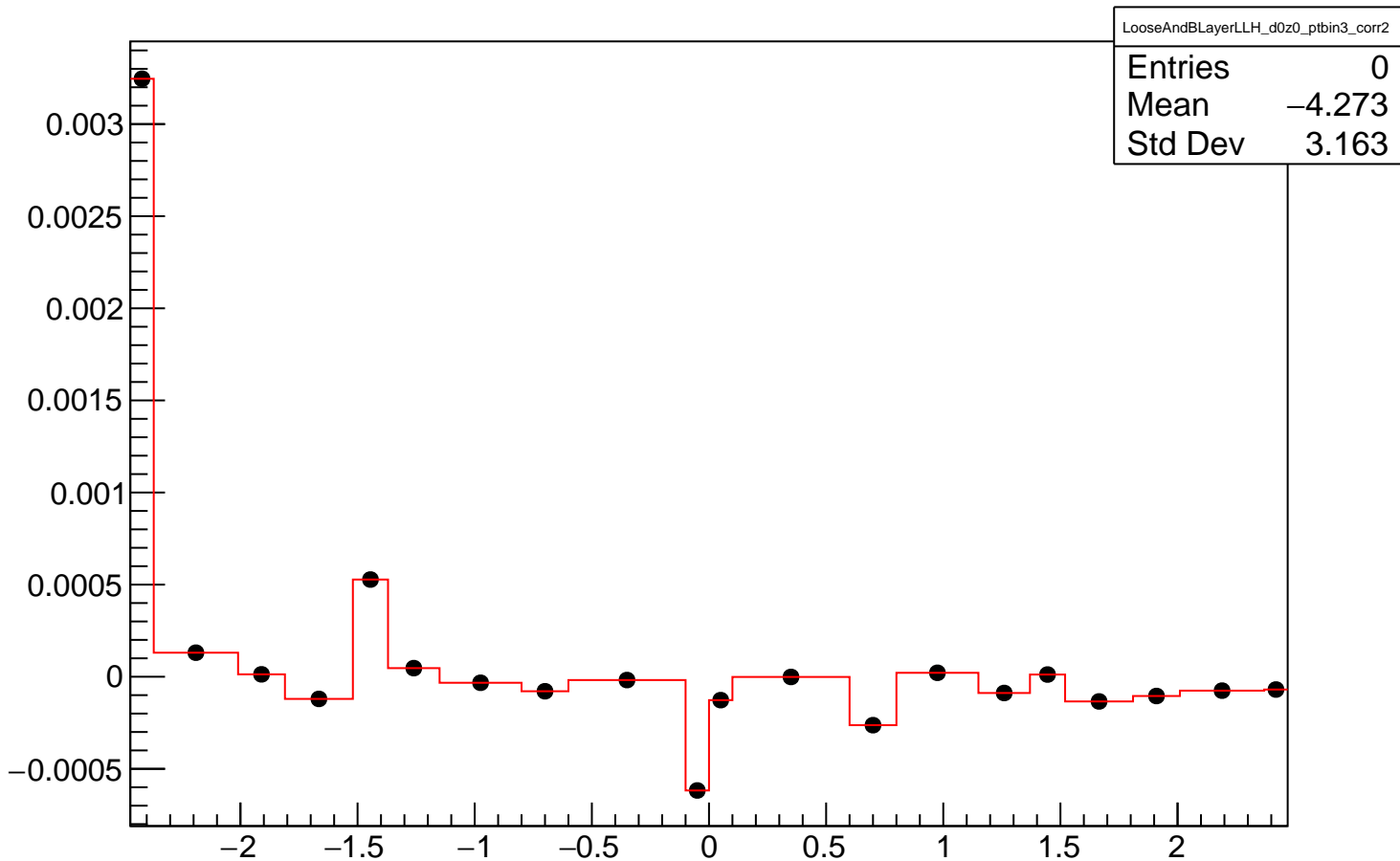


# FullSim\_LooseLLH\_d0z0\_corr2

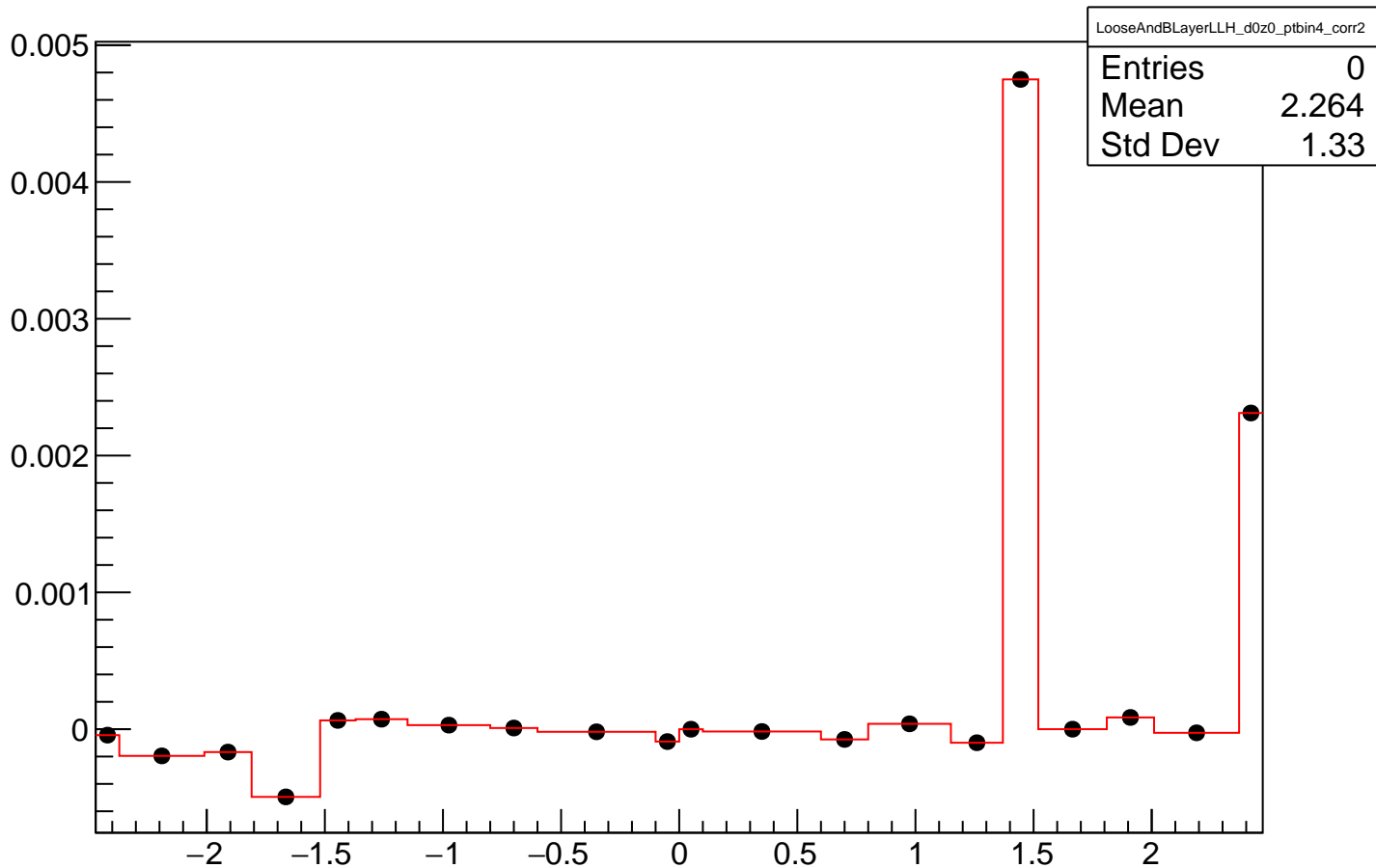




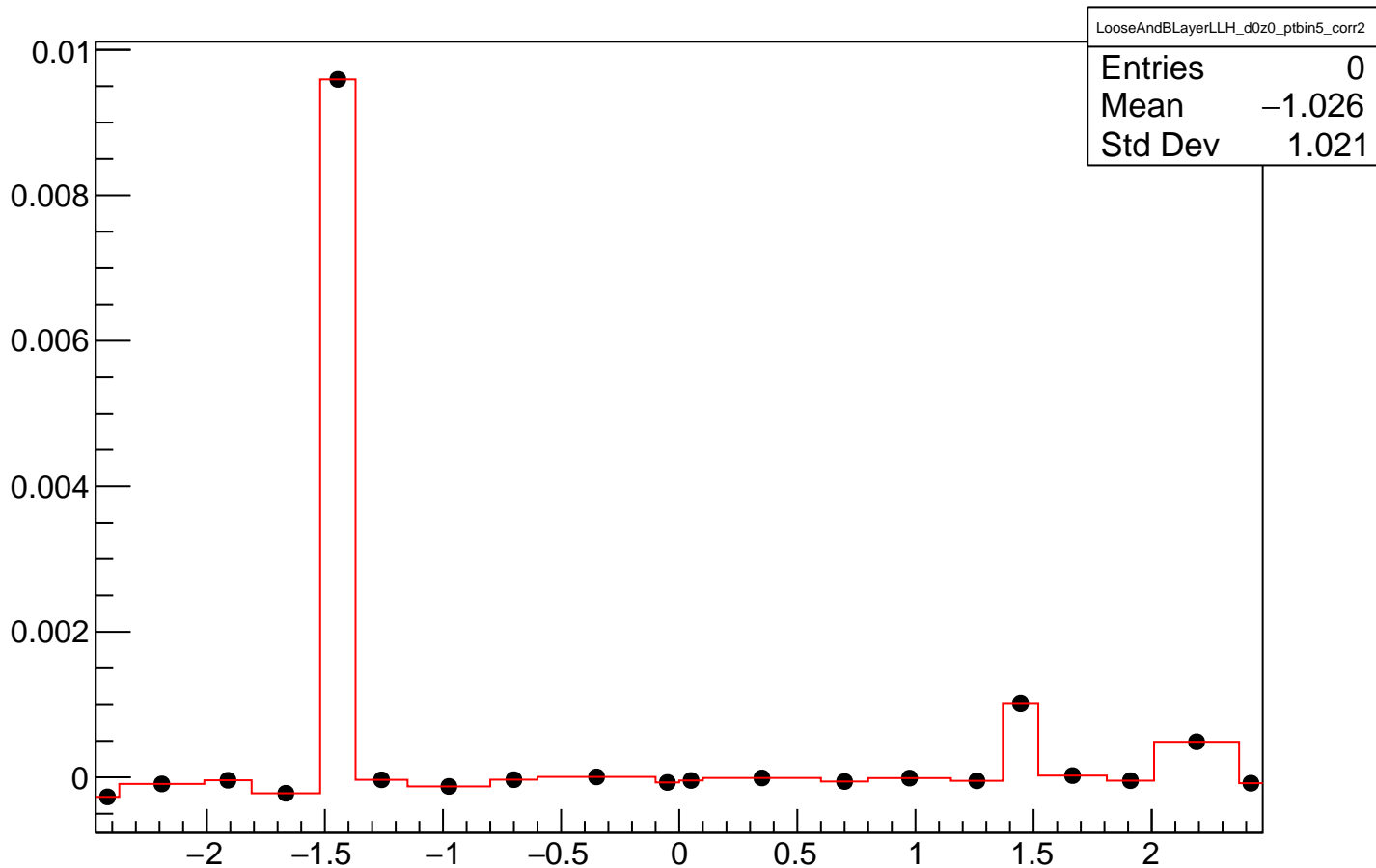
# FullSim\_LooseLLH\_d0z0\_corr2



# FullSim\_LooseLLH\_d0z0\_corr2



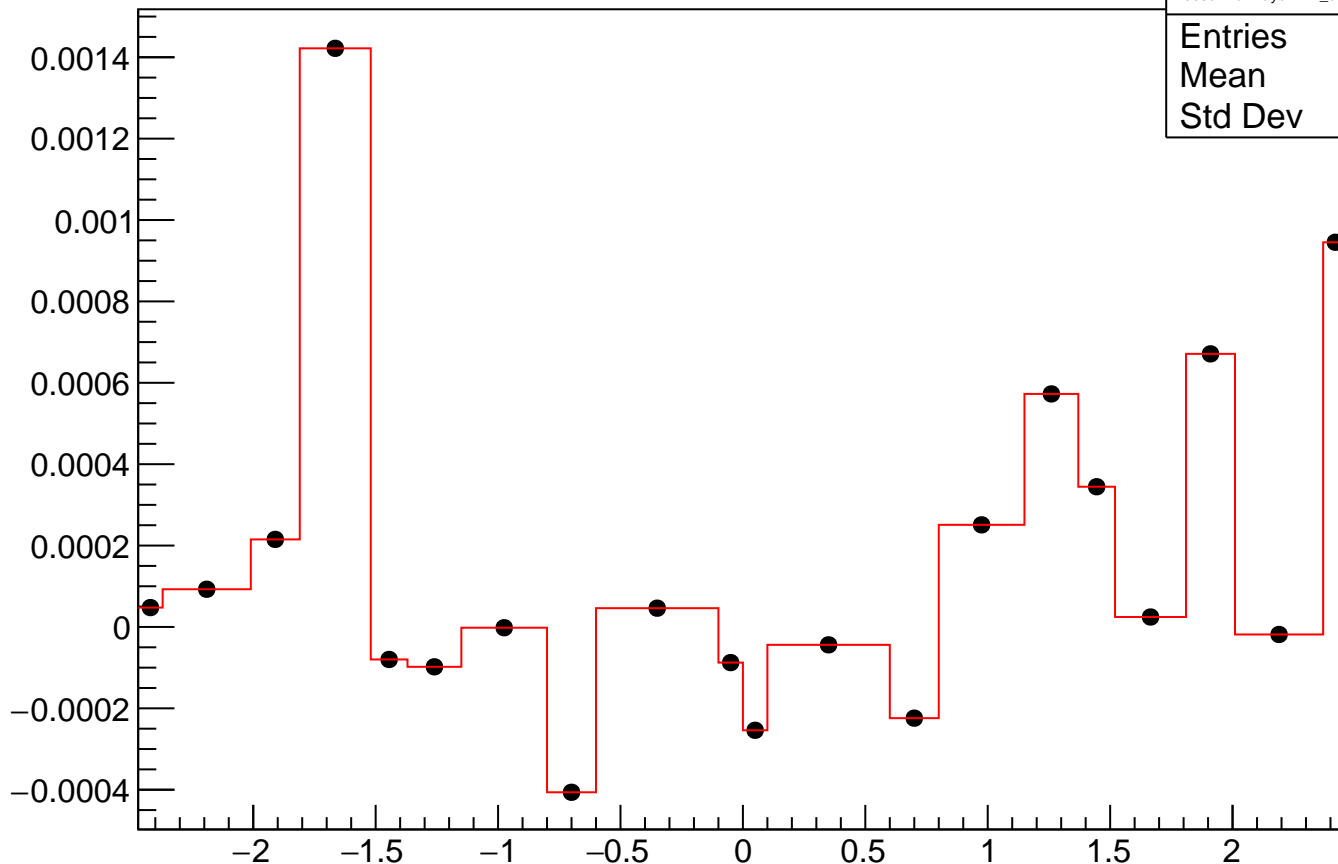
# FullSim\_LooseLLH\_d0z0\_corr2



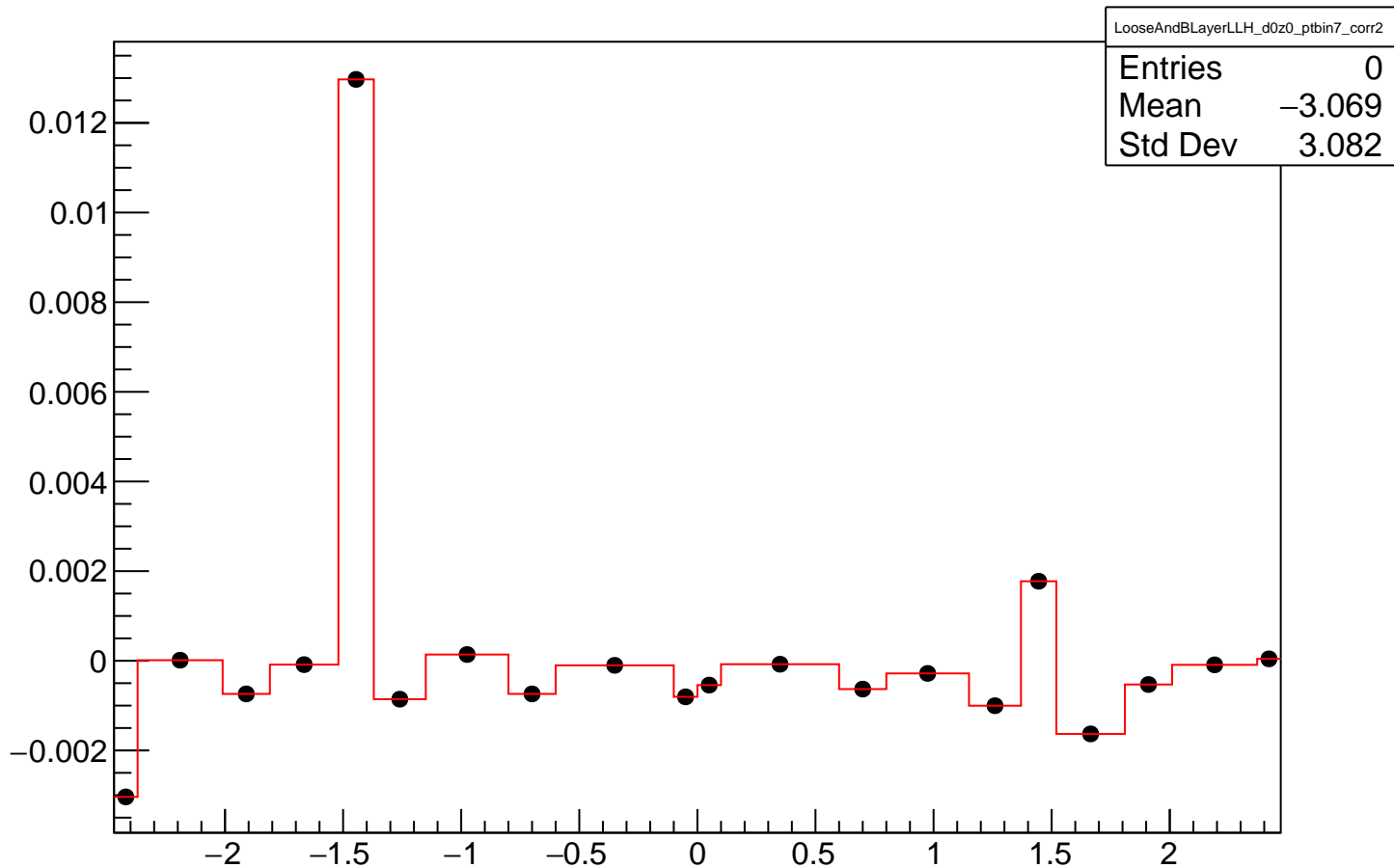
# FullSim\_LooseLLH\_d0z0\_corr2

LooseAndBLayerLLH\_d0z0\_ptbin6\_corr2

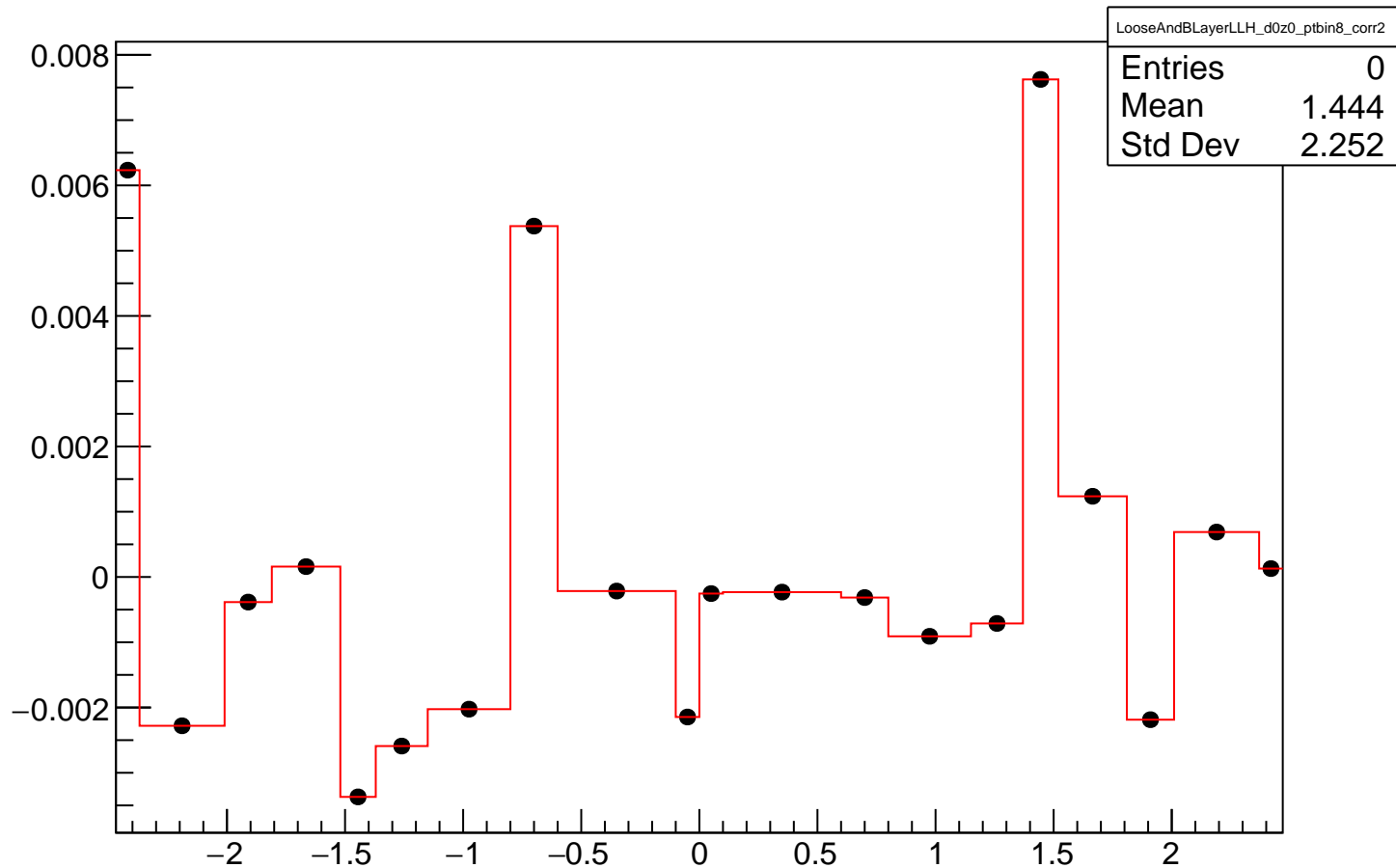
Entries	0
Mean	0.6628
Std Dev	1.961



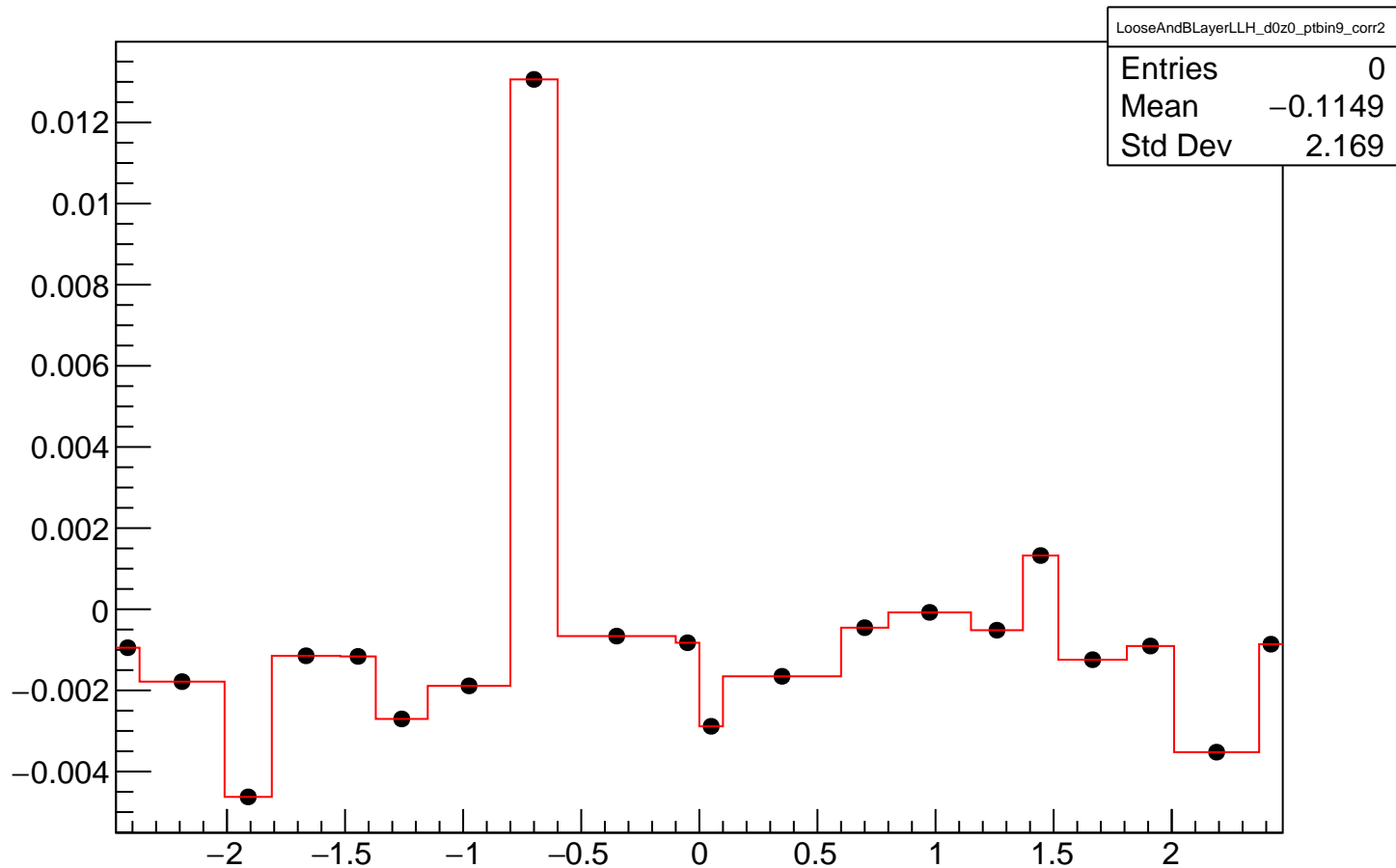
# FullSim\_LooseLLH\_d0z0\_corr2



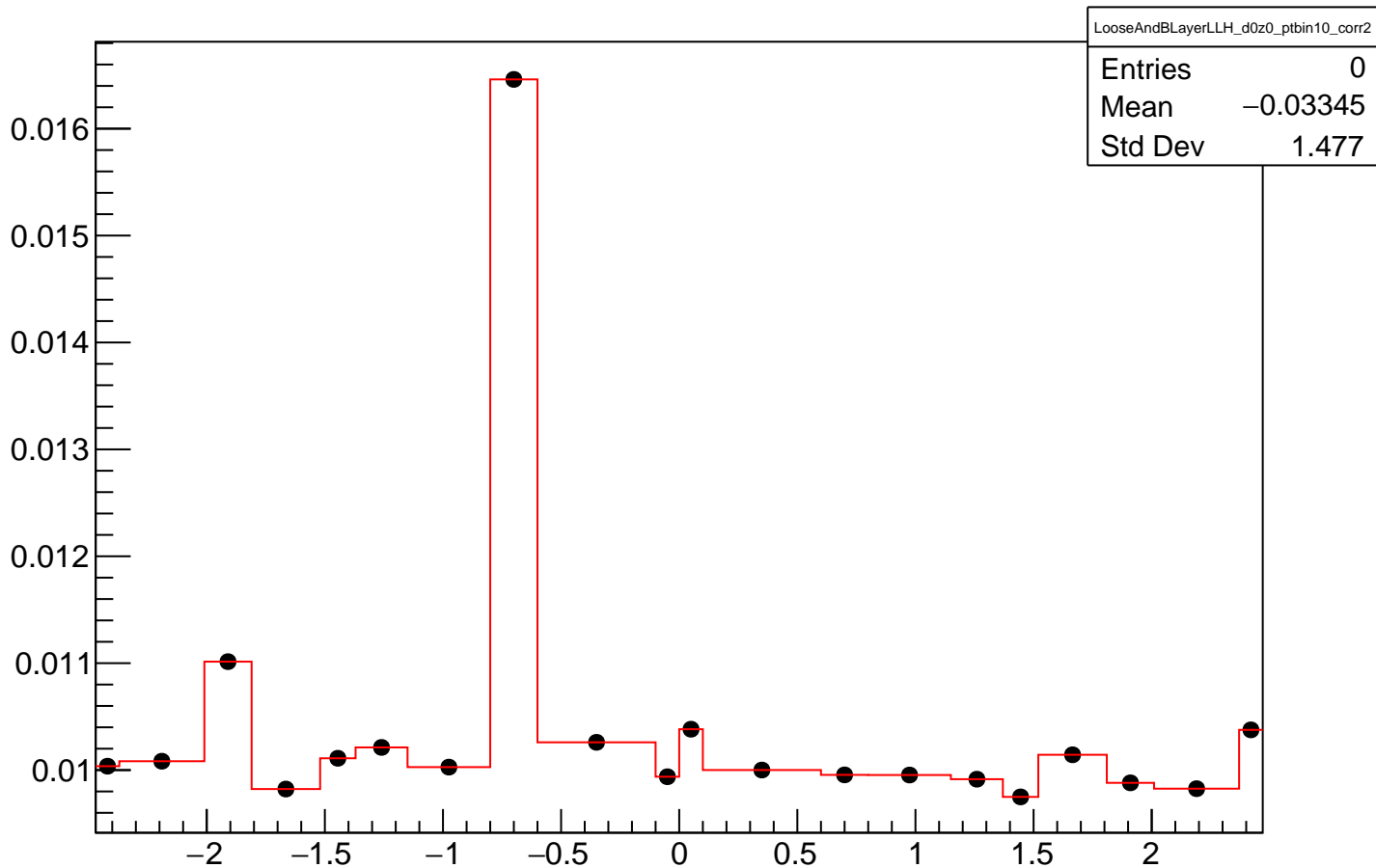
# FullSim\_LooseLLH\_d0z0\_corr2



# FullSim\_LooseLLH\_d0z0\_corr2

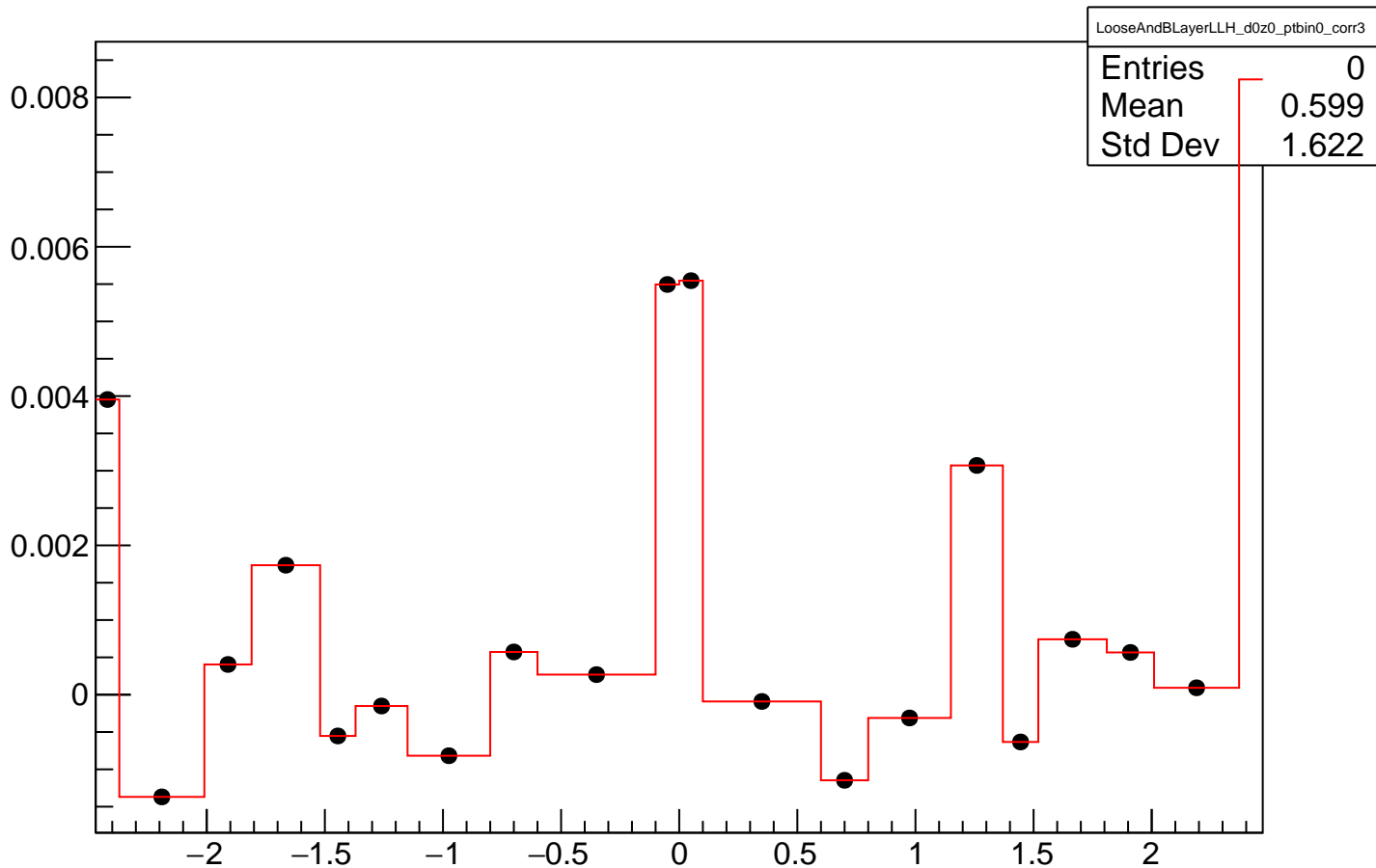


# FullSim\_LooseLLH\_d0z0\_corr2

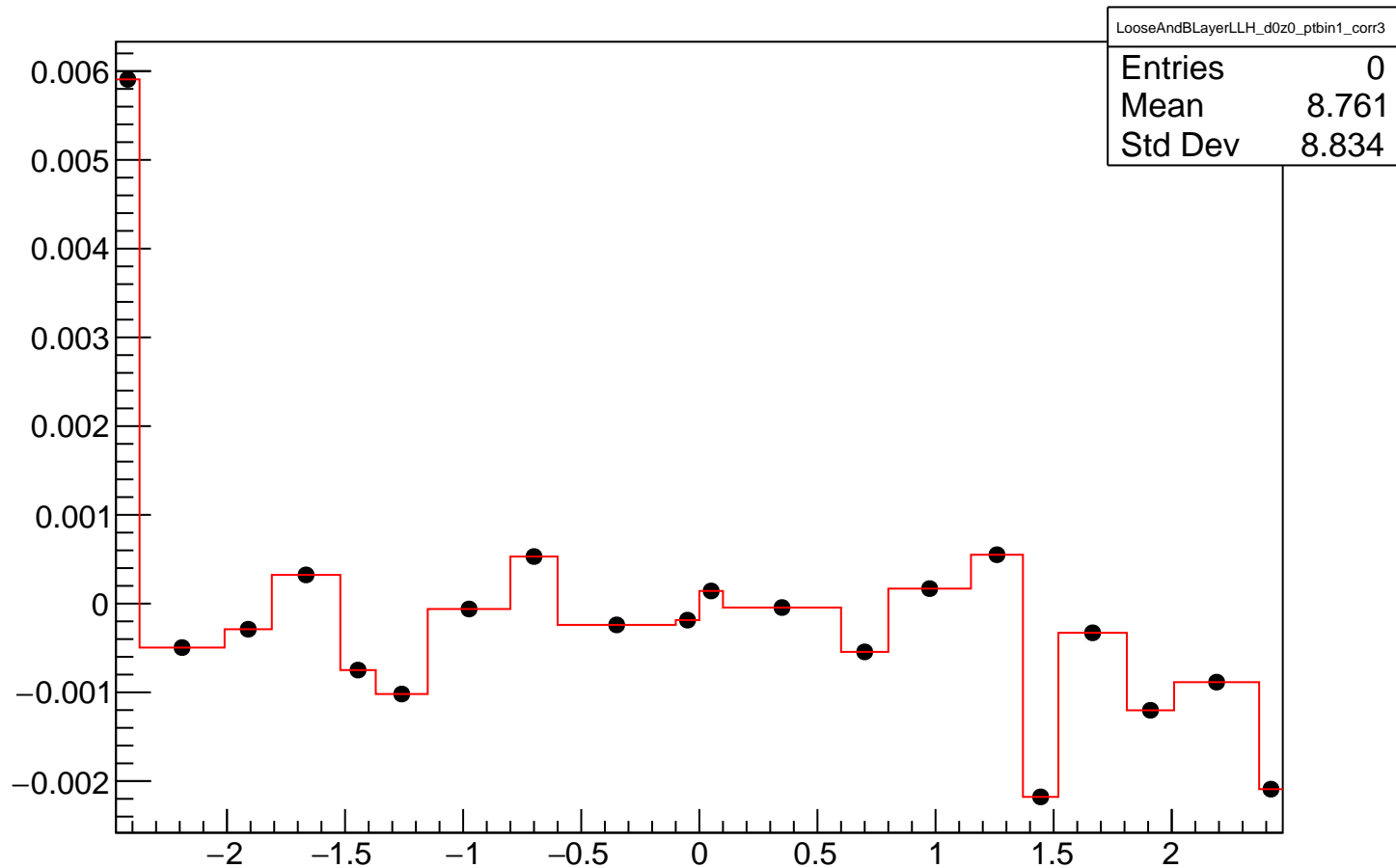




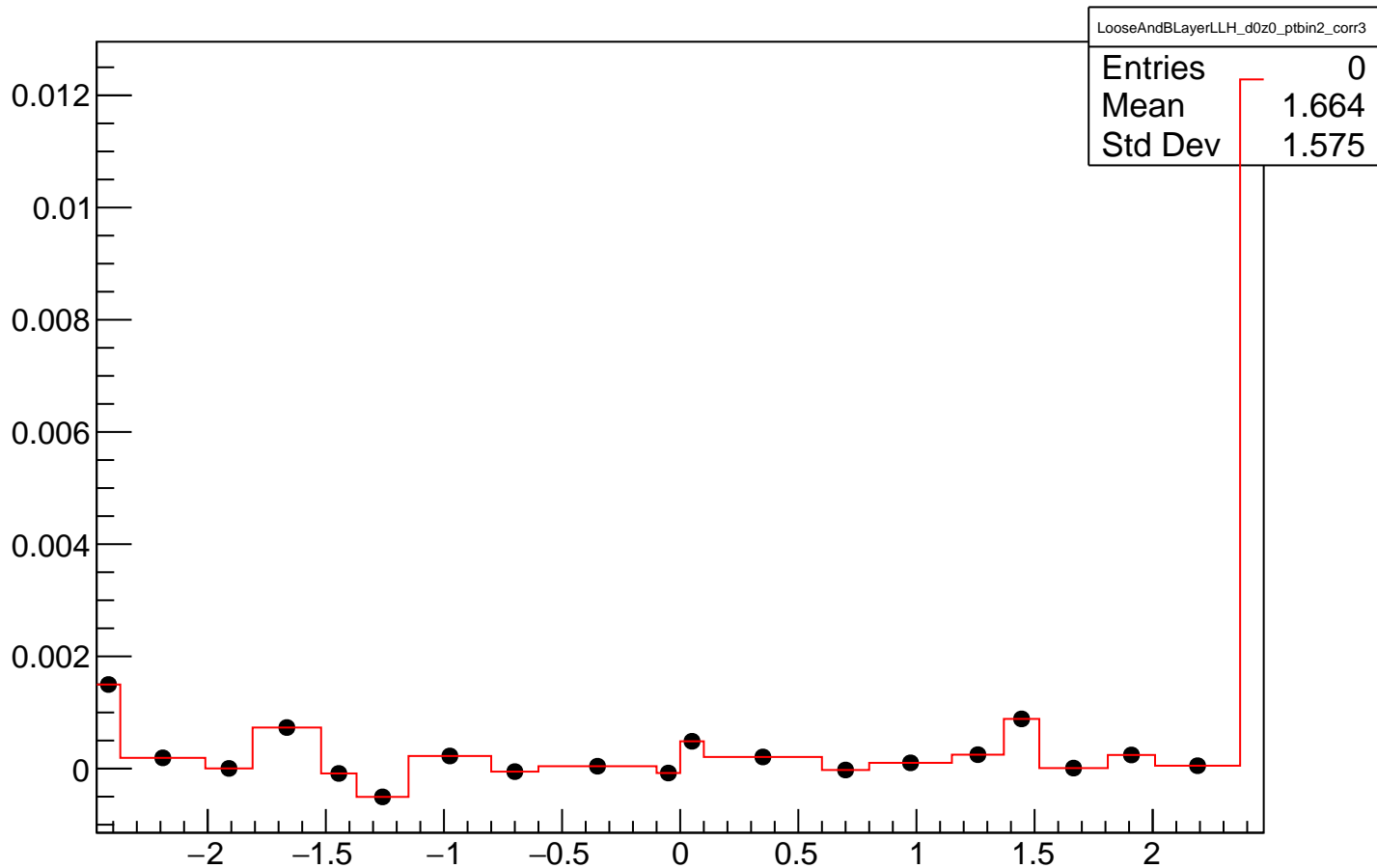
# FullSim\_LooseLLH\_d0z0\_corr3



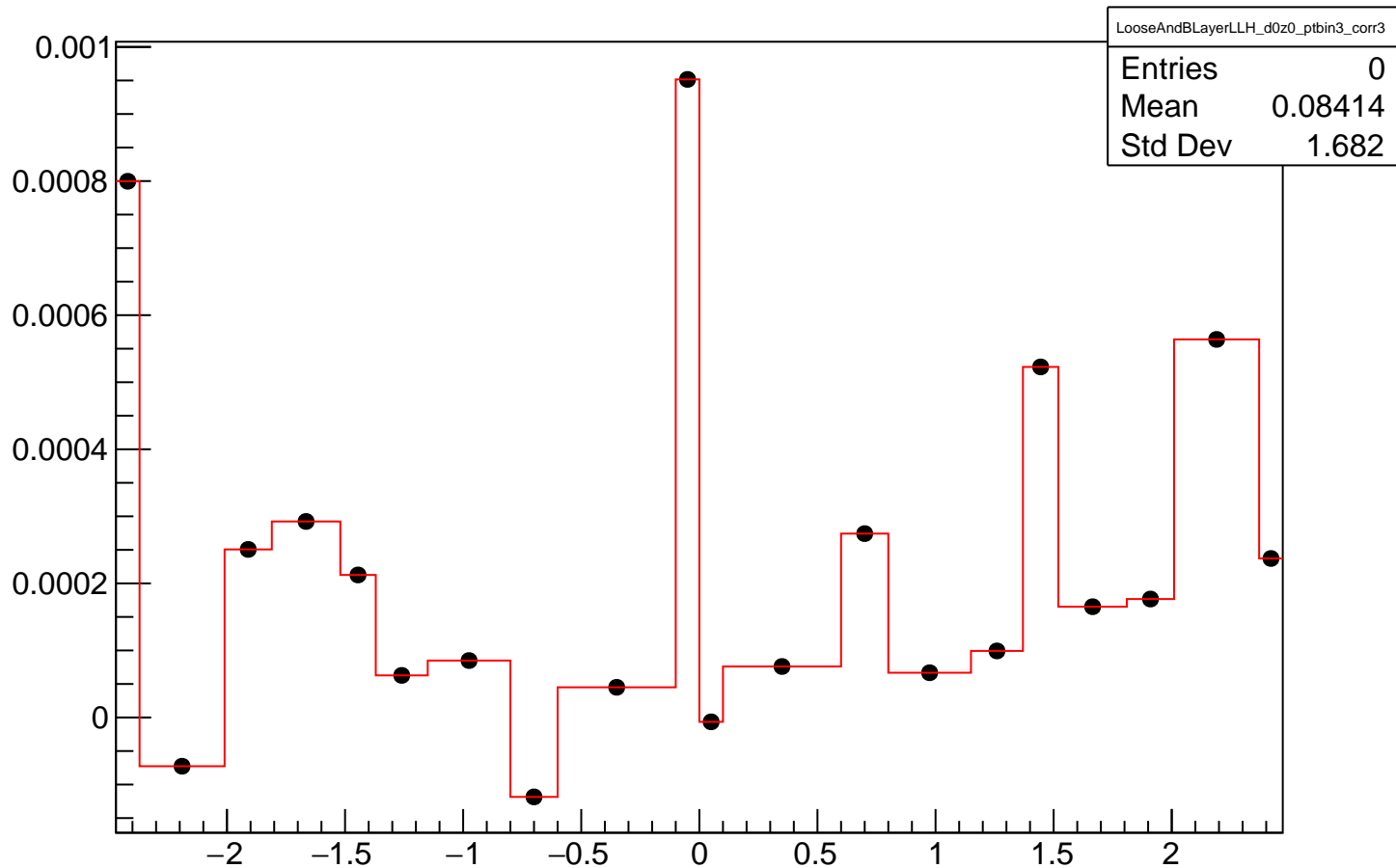
# FullSim\_LooseLLH\_d0z0\_corr3



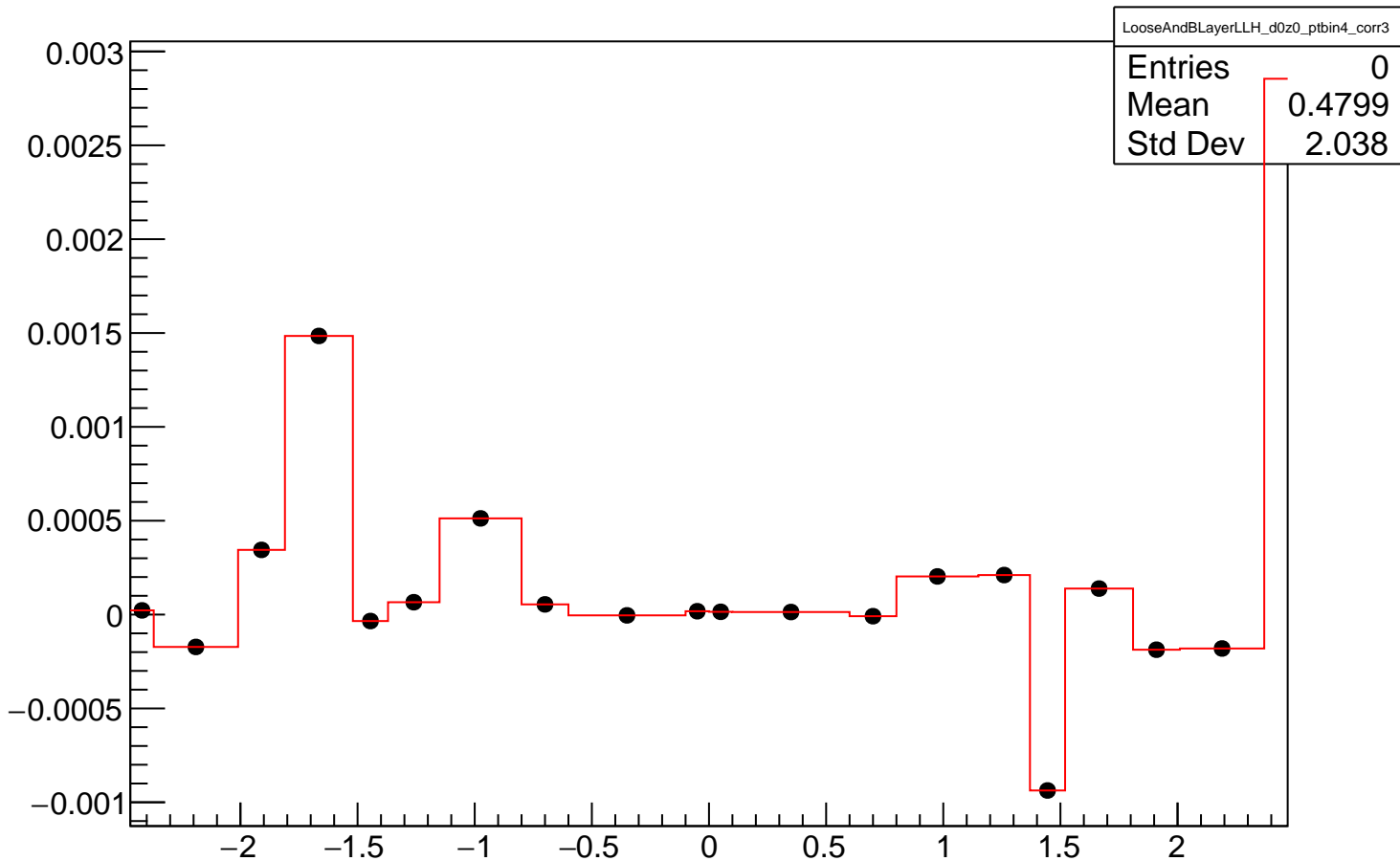
# FullSim\_LooseLLH\_d0z0\_corr3



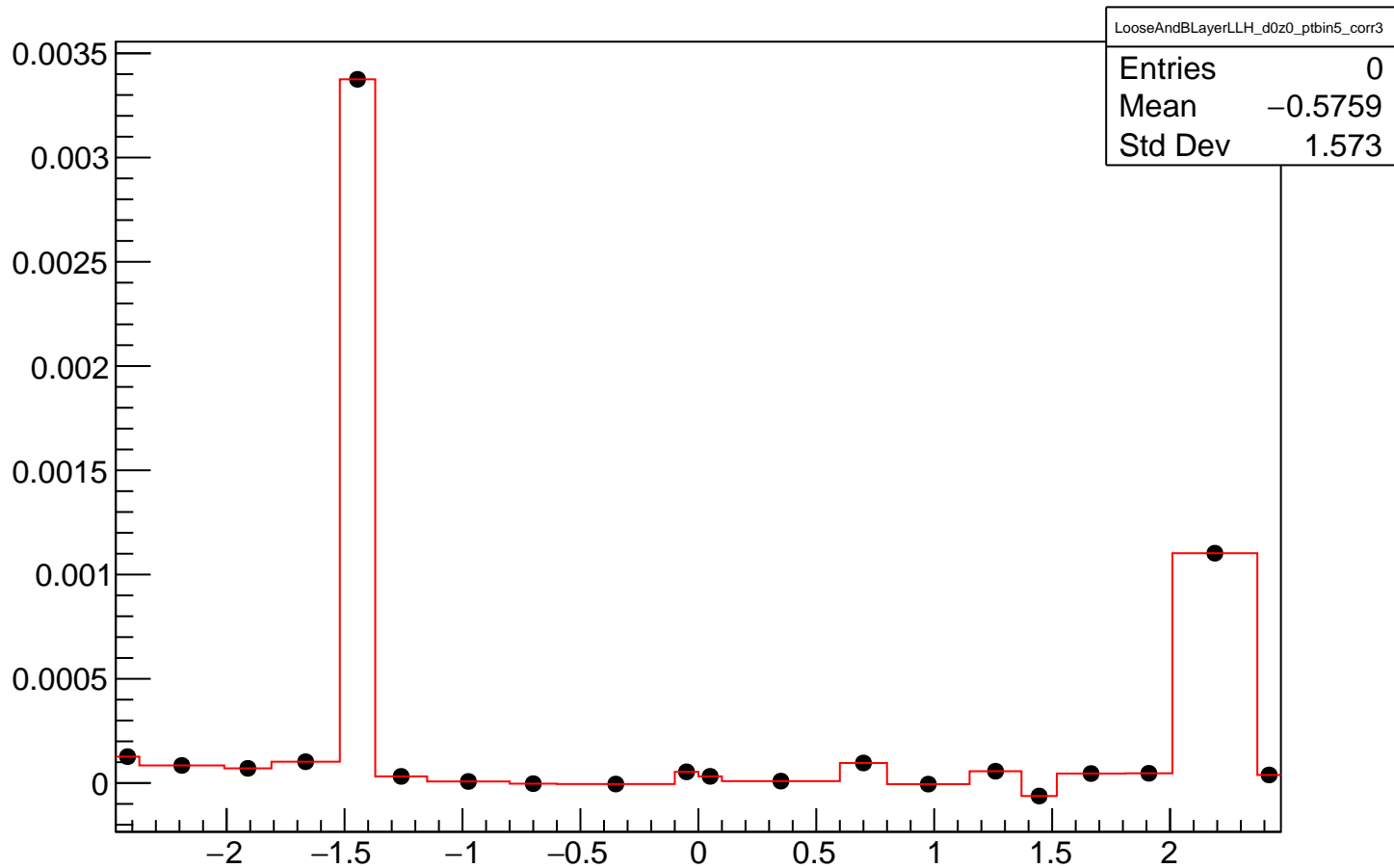
# FullSim\_LooseLLH\_d0z0\_corr3



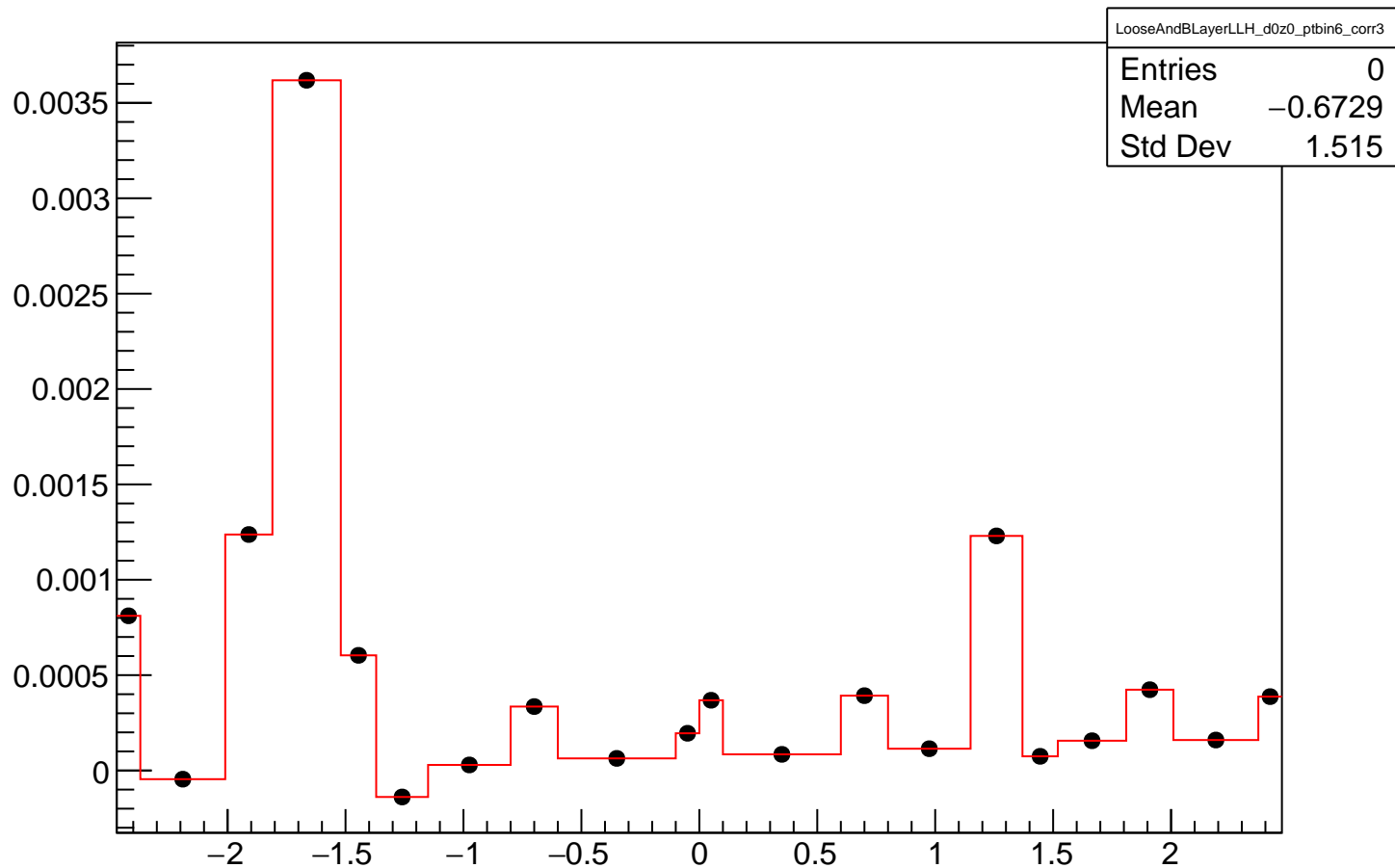
# FullSim\_LooseLLH\_d0z0\_corr3



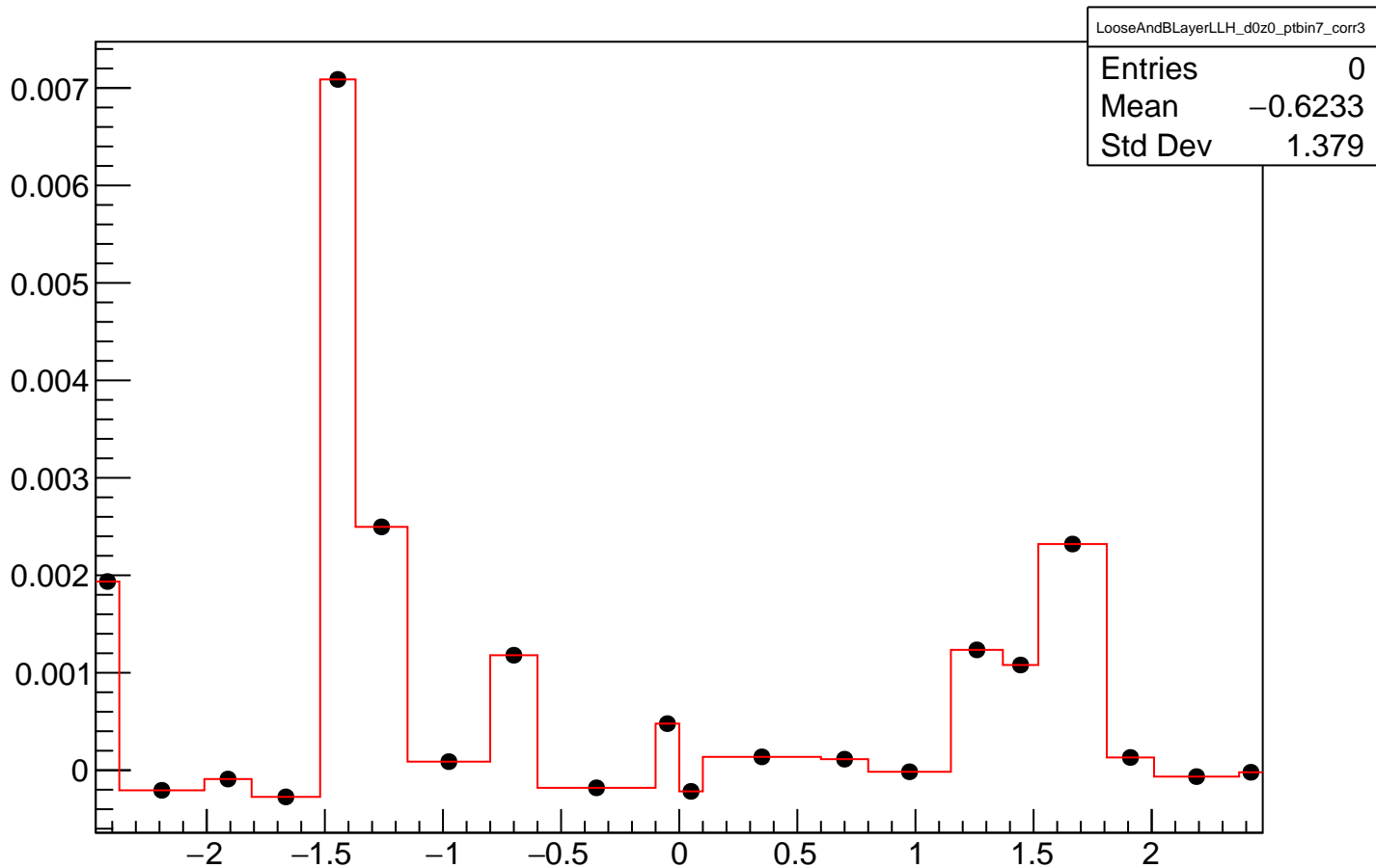
# FullSim\_LooseLLH\_d0z0\_corr3



# FullSim\_LooseLLH\_d0z0\_corr3

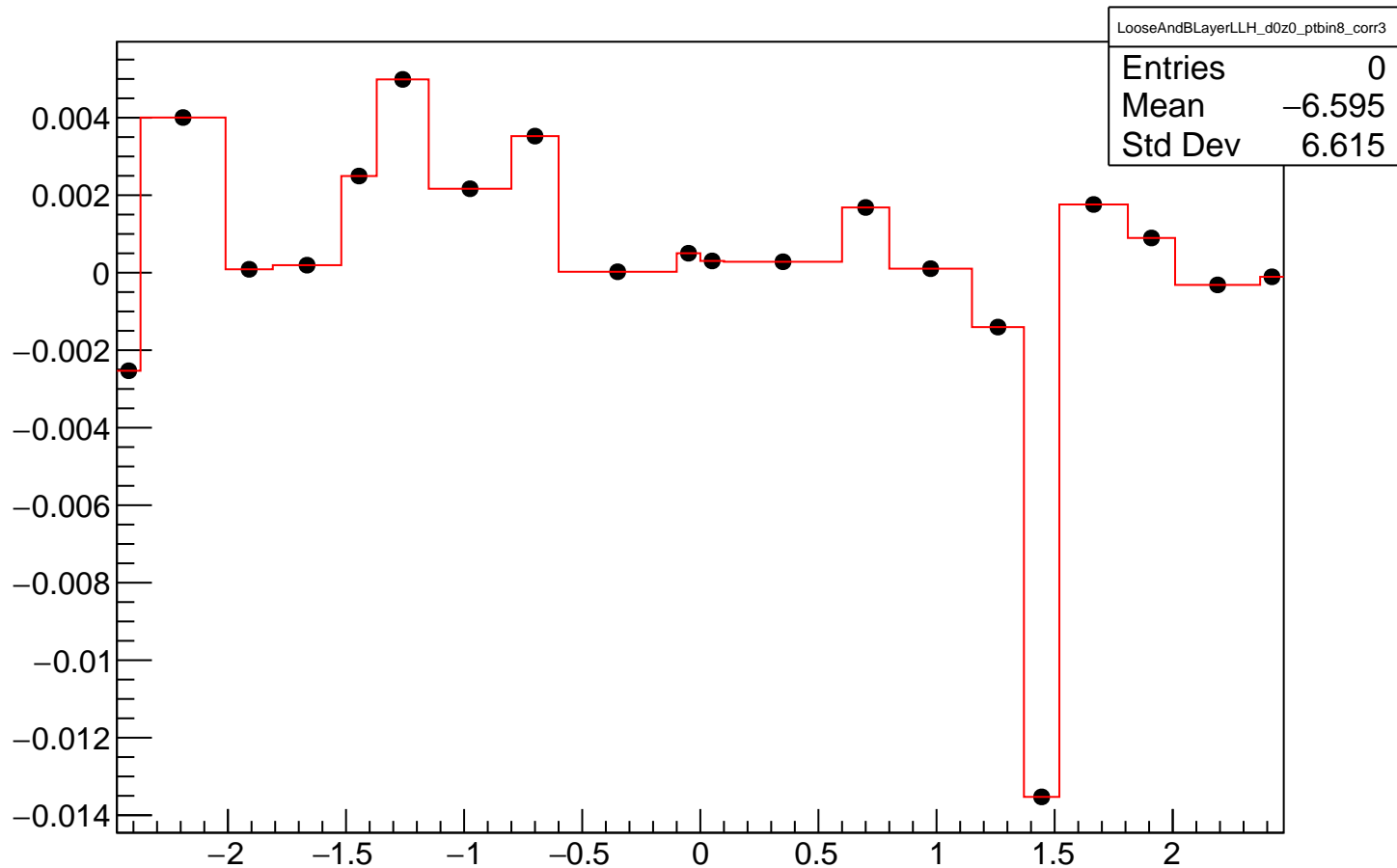


# FullSim\_LooseLLH\_d0z0\_corr3

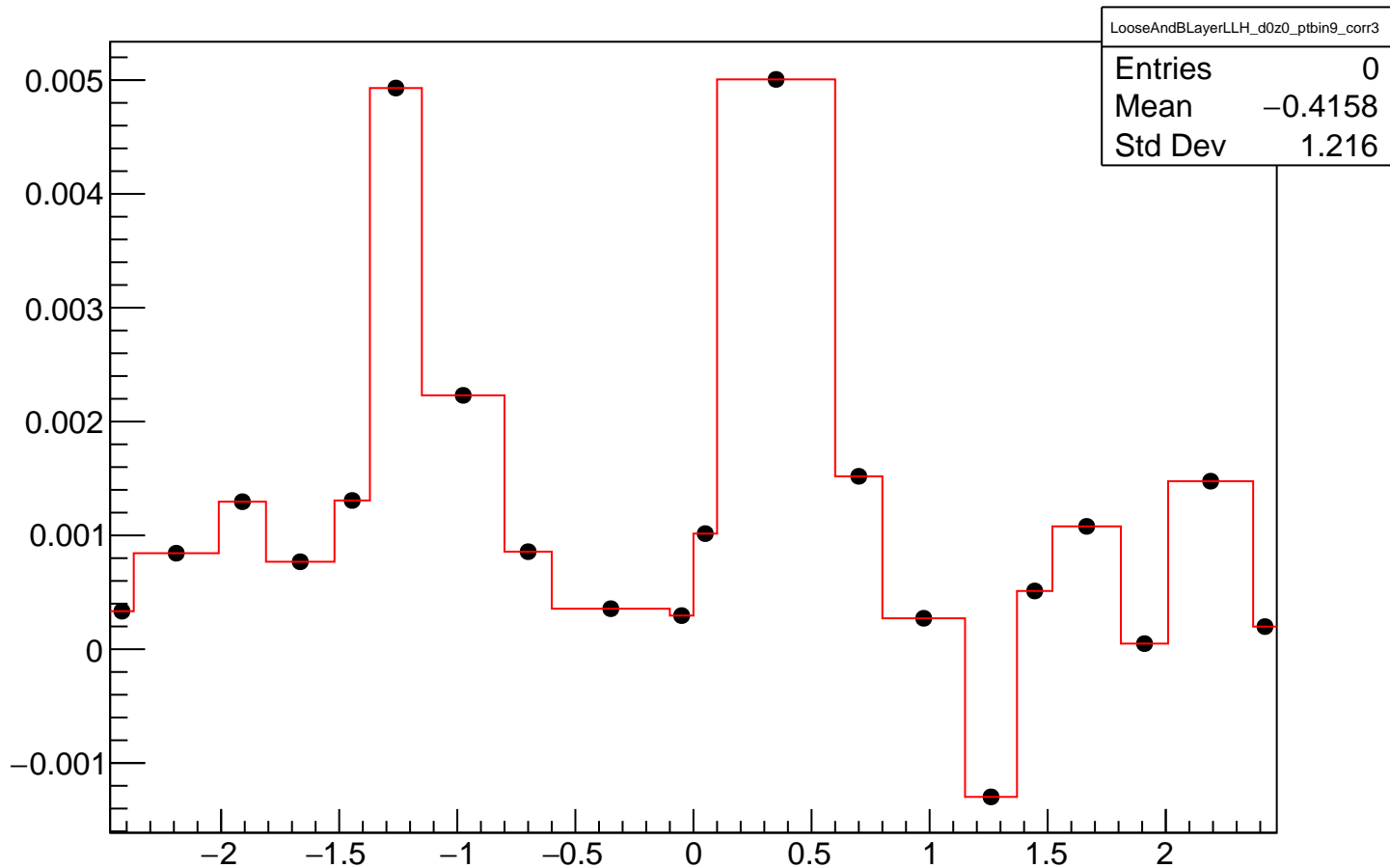




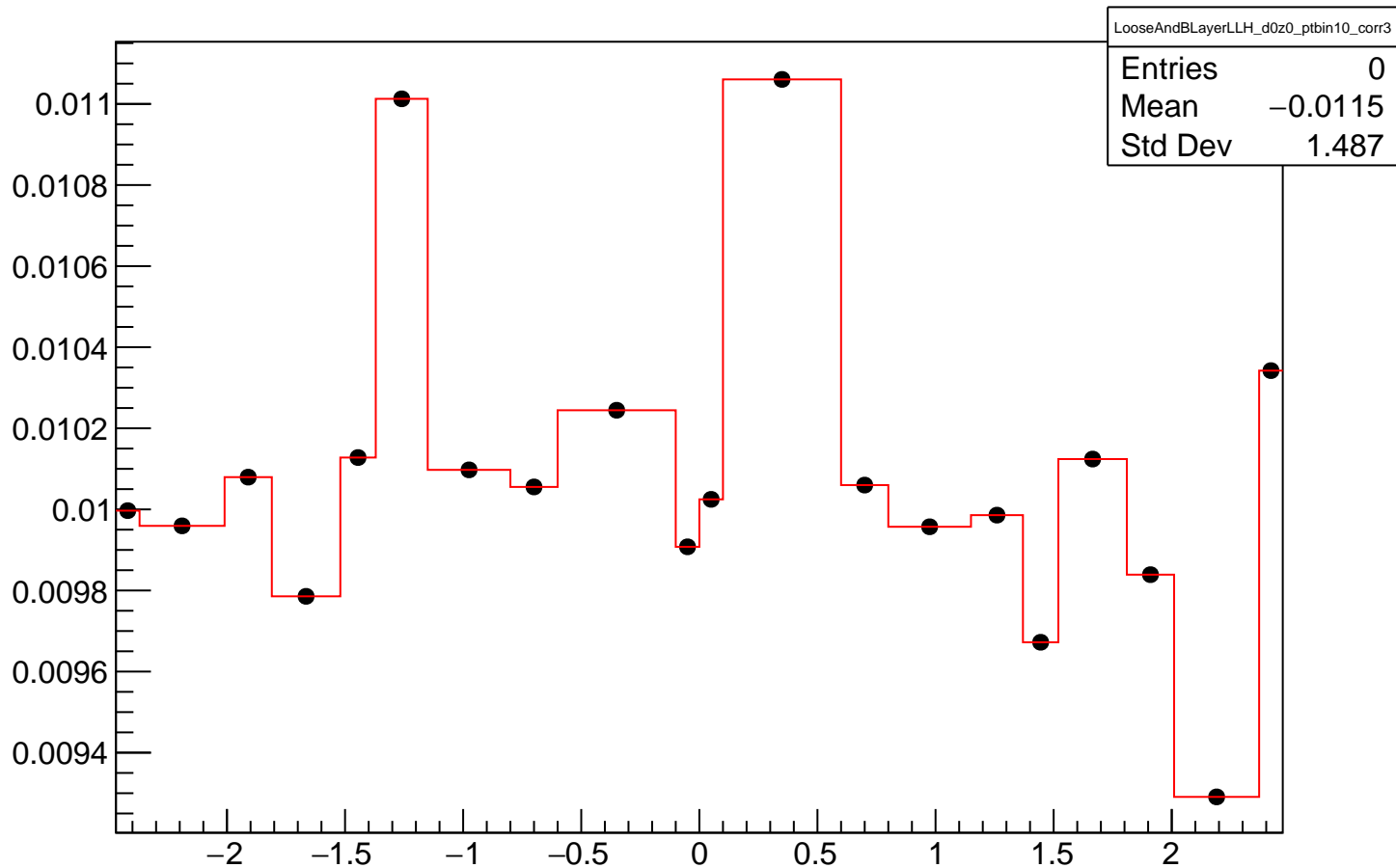
# FullSim\_LooseLLH\_d0z0\_corr3



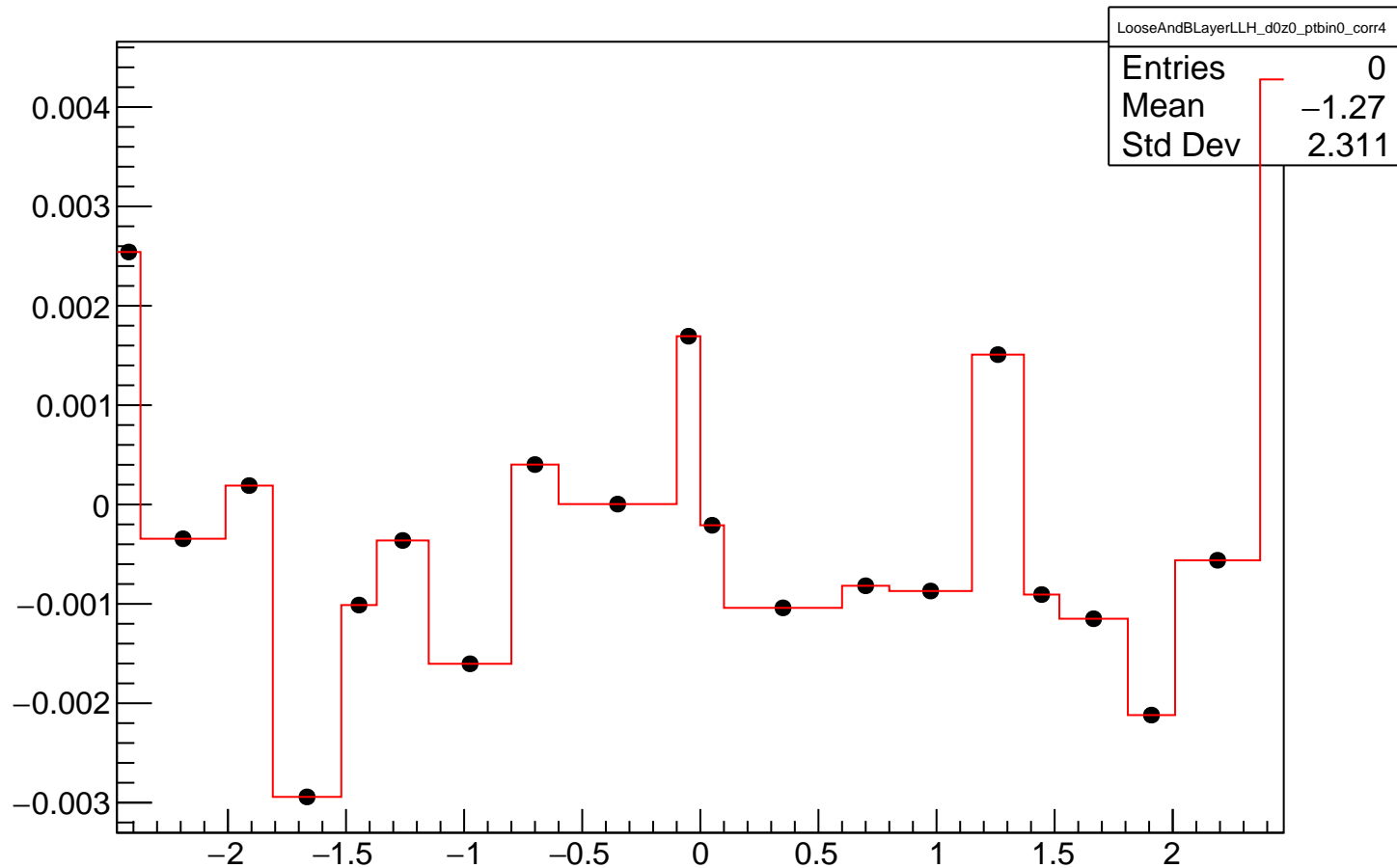
# FullSim\_LooseLLH\_d0z0\_corr3



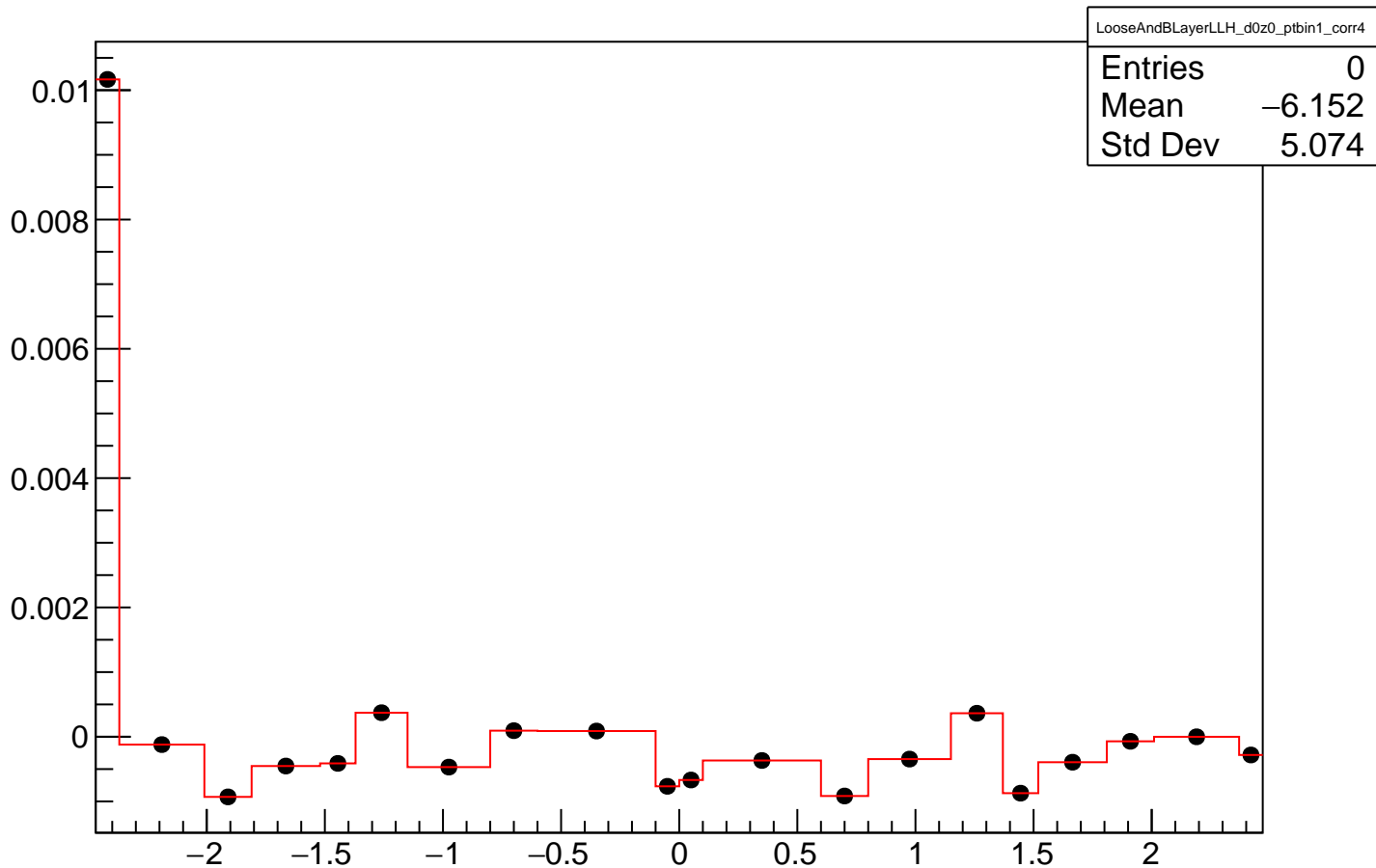
# FullSim\_LooseLLH\_d0z0\_corr3



# FullSim\_LooseLLH\_d0z0\_corr4



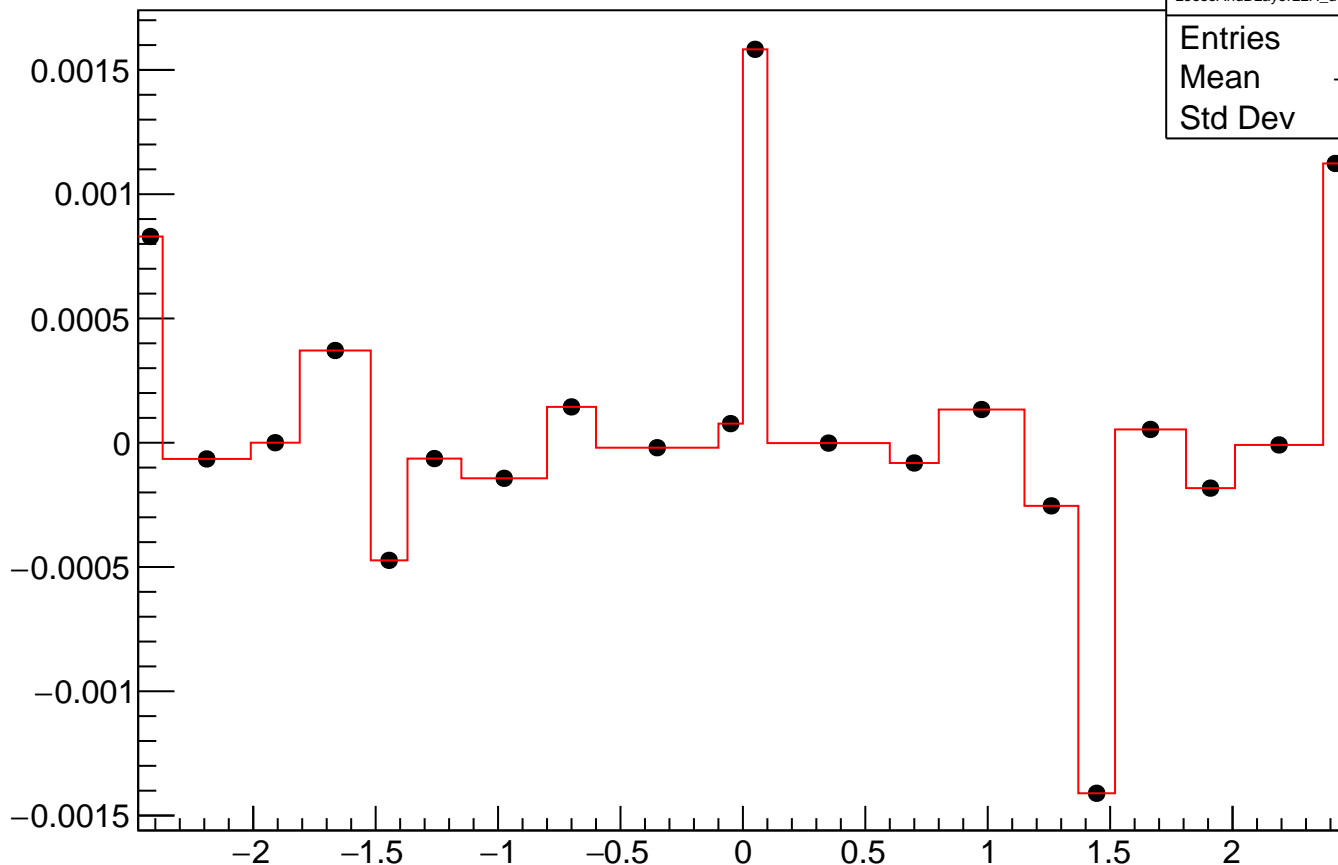
# FullSim\_LooseLLH\_d0z0\_corr4



# FullSim\_LooseLLH\_d0z0\_corr4

LooseAndBLayerLLH\_d0z0\_ptbin2\_corr4

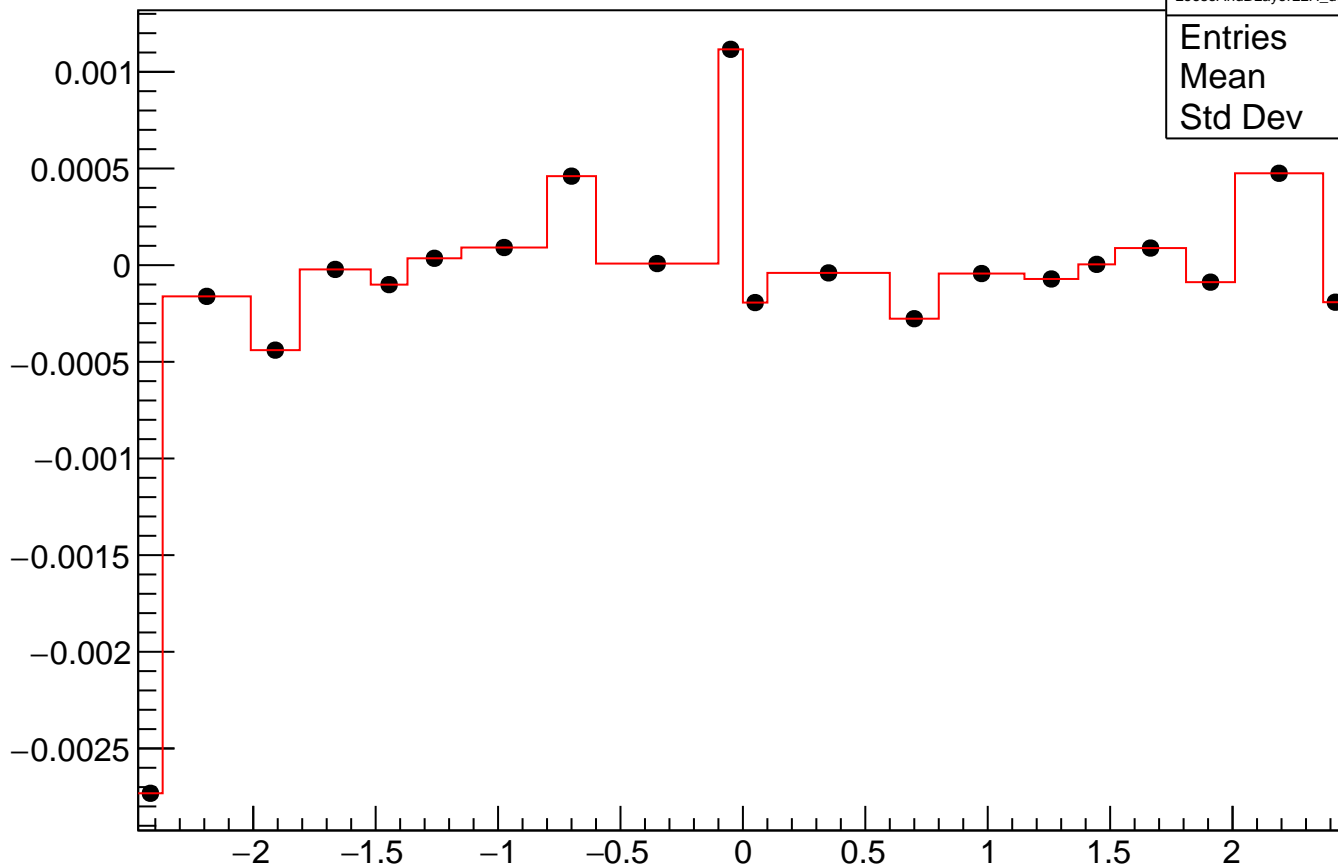
Entries	0
Mean	-0.8958
Std Dev	1.912



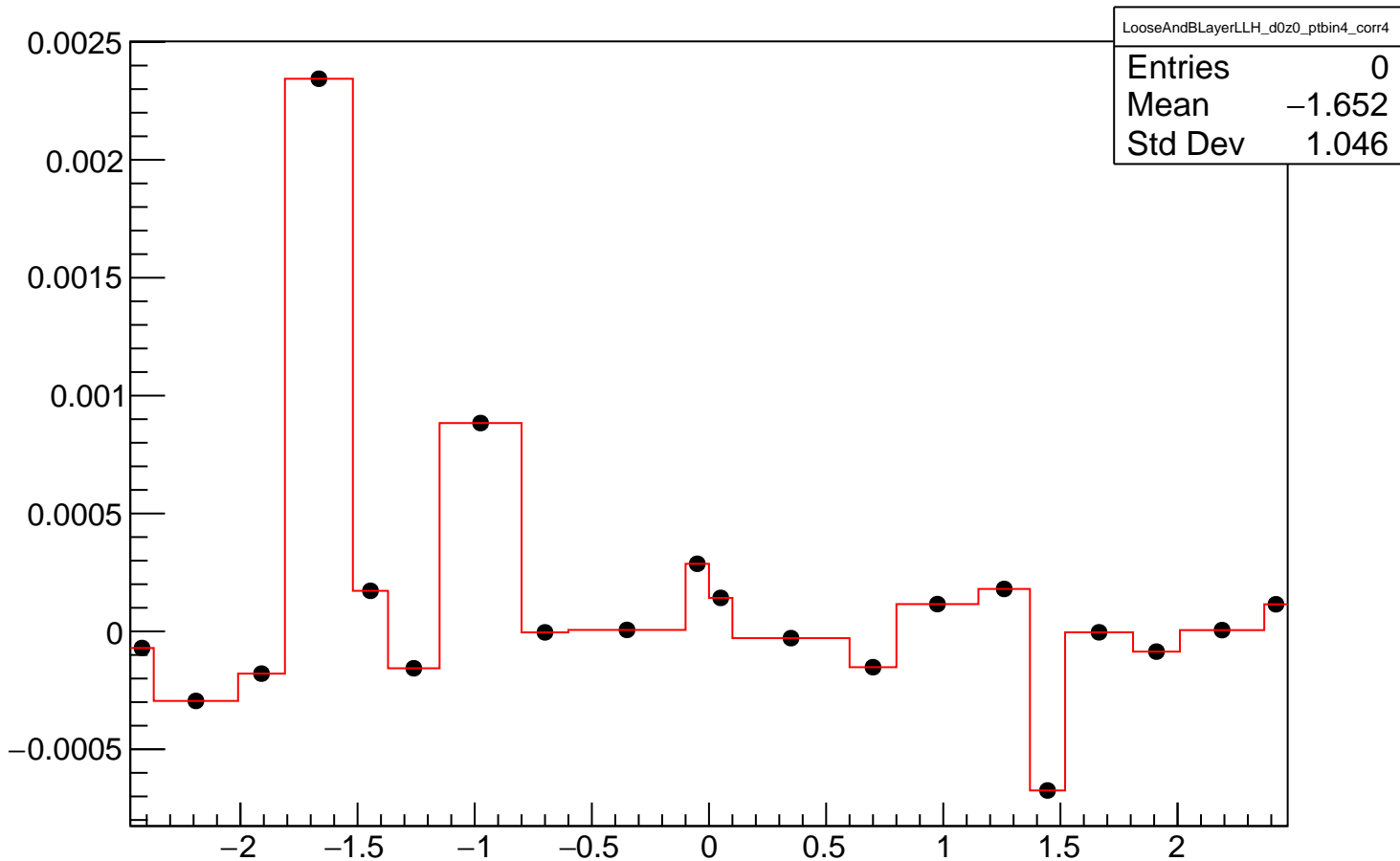
# FullSim\_LooseLLH\_d0z0\_corr4

LooseAndBLayerLLH\_d0z0\_ptbin3\_corr4

Entries	0
Mean	-3.693
Std Dev	2.289



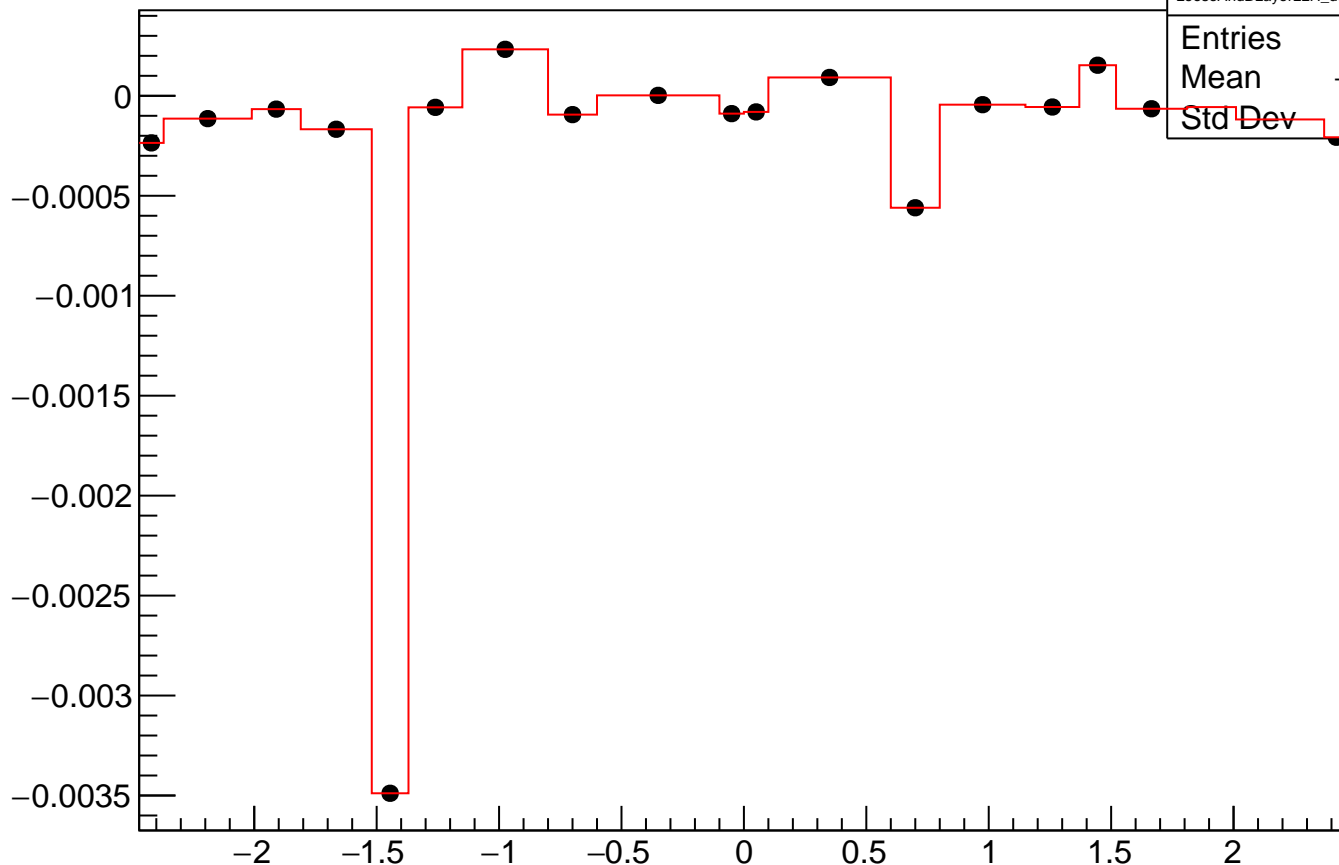
# FullSim\_LooseLLH\_d0z0\_corr4





# FullSim\_LooseLLH\_d0z0\_corr4

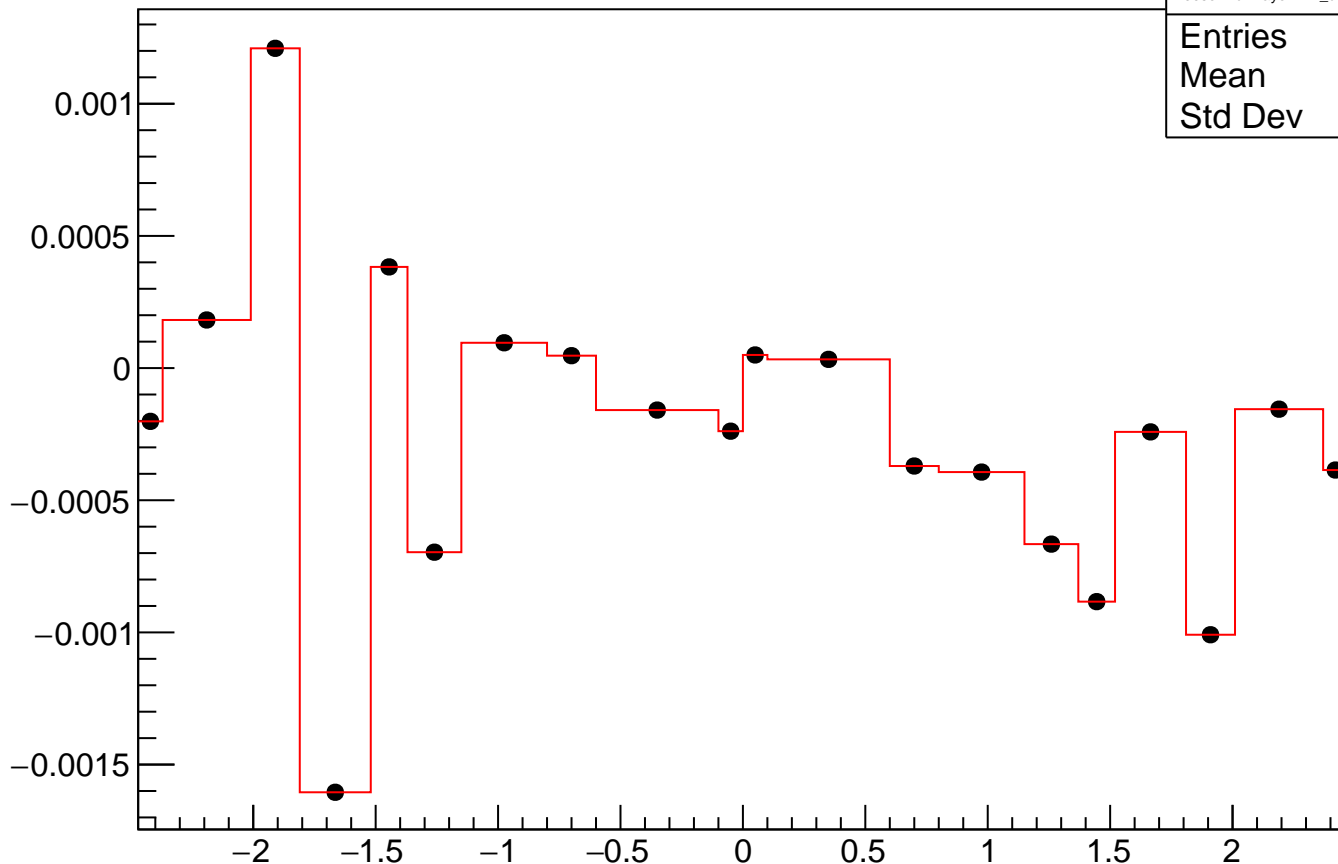
LooseAndBLayerLLH_d0z0_ptbin5_corr4	
Entries	0
Mean	-0.9857
Std Dev	1.197



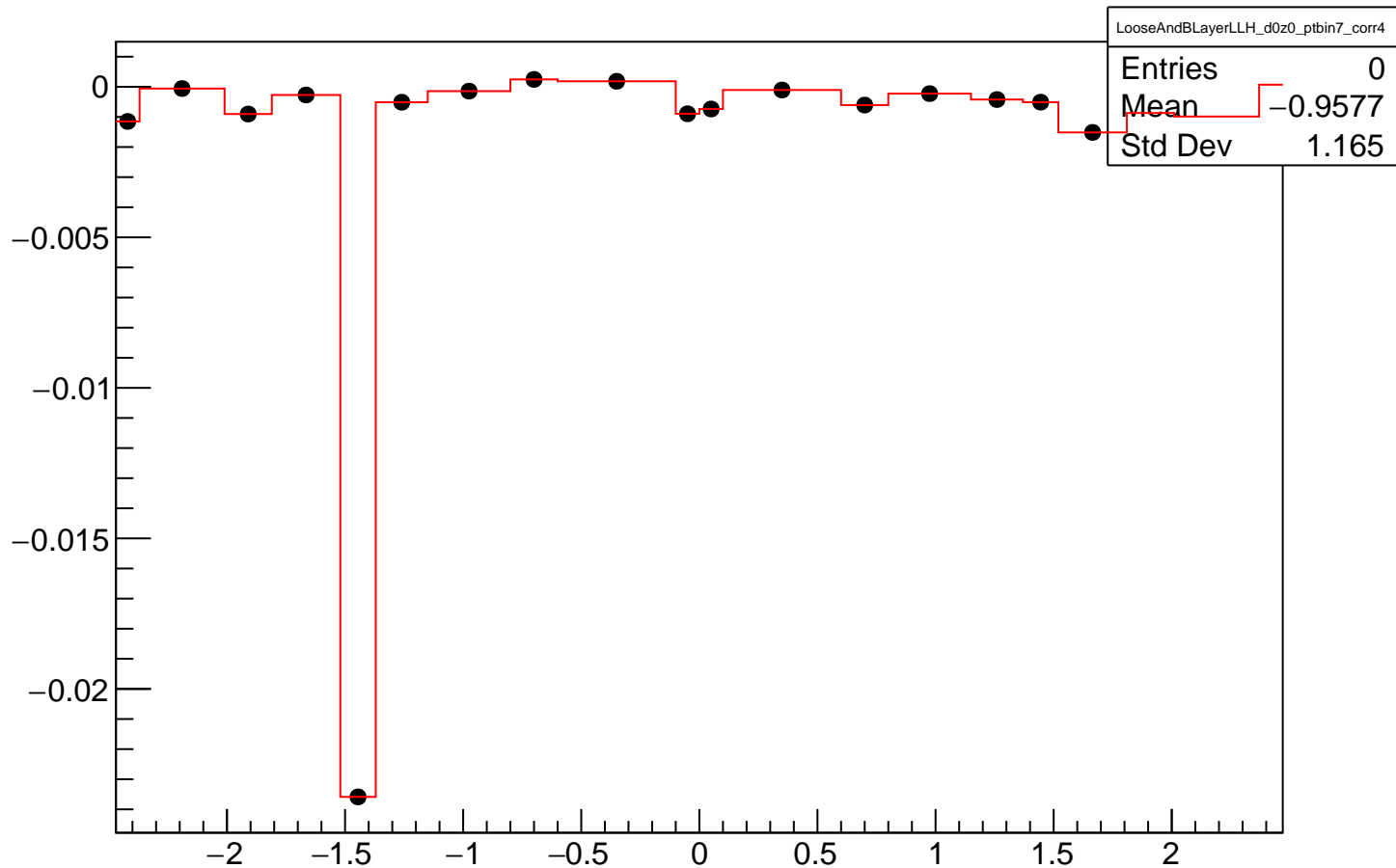
# FullSim\_LooseLLH\_d0z0\_corr4

LooseAndBLayerLLH\_d0z0\_ptbin6\_corr4

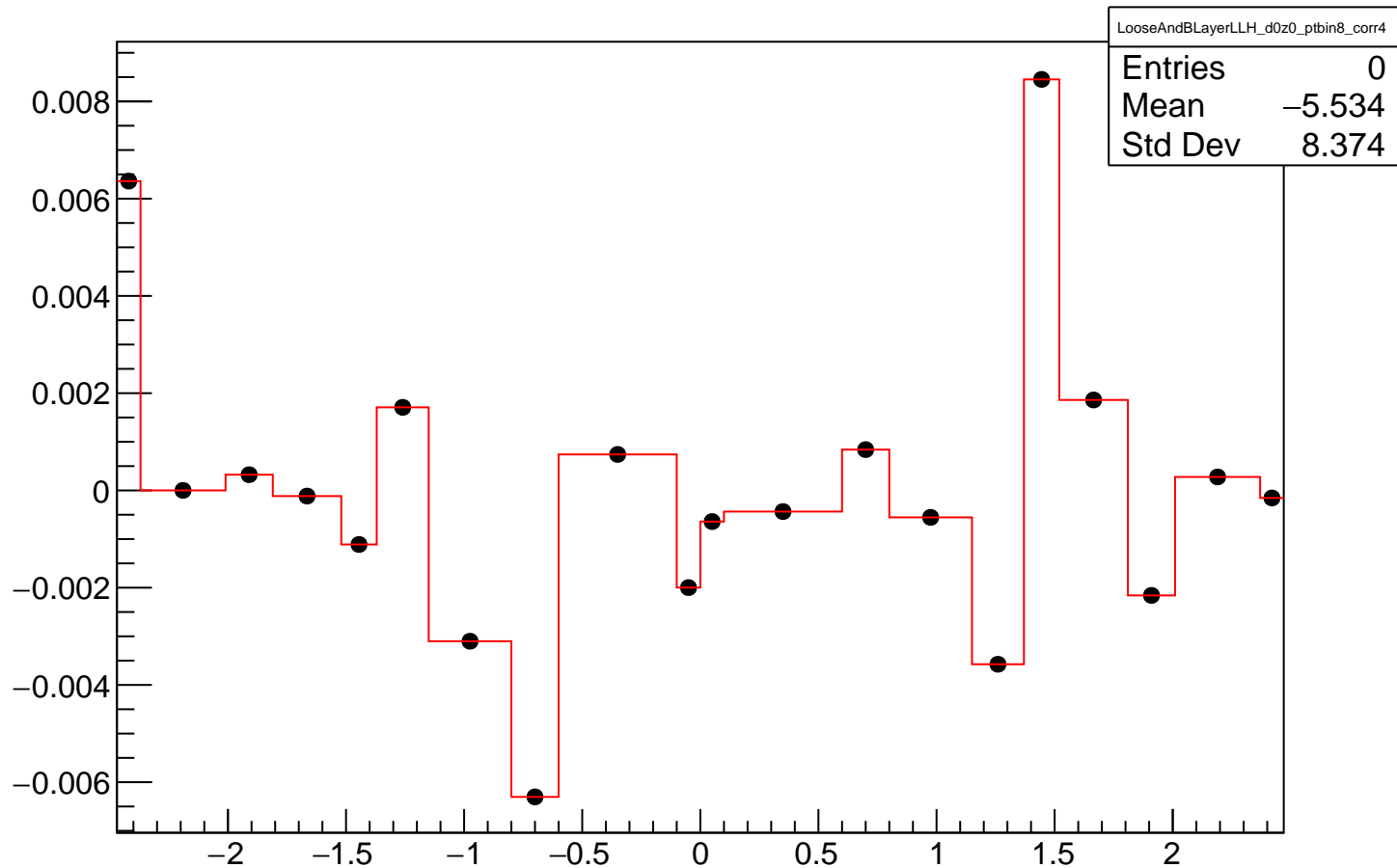
Entries	0
Mean	1.125
Std Dev	1.002



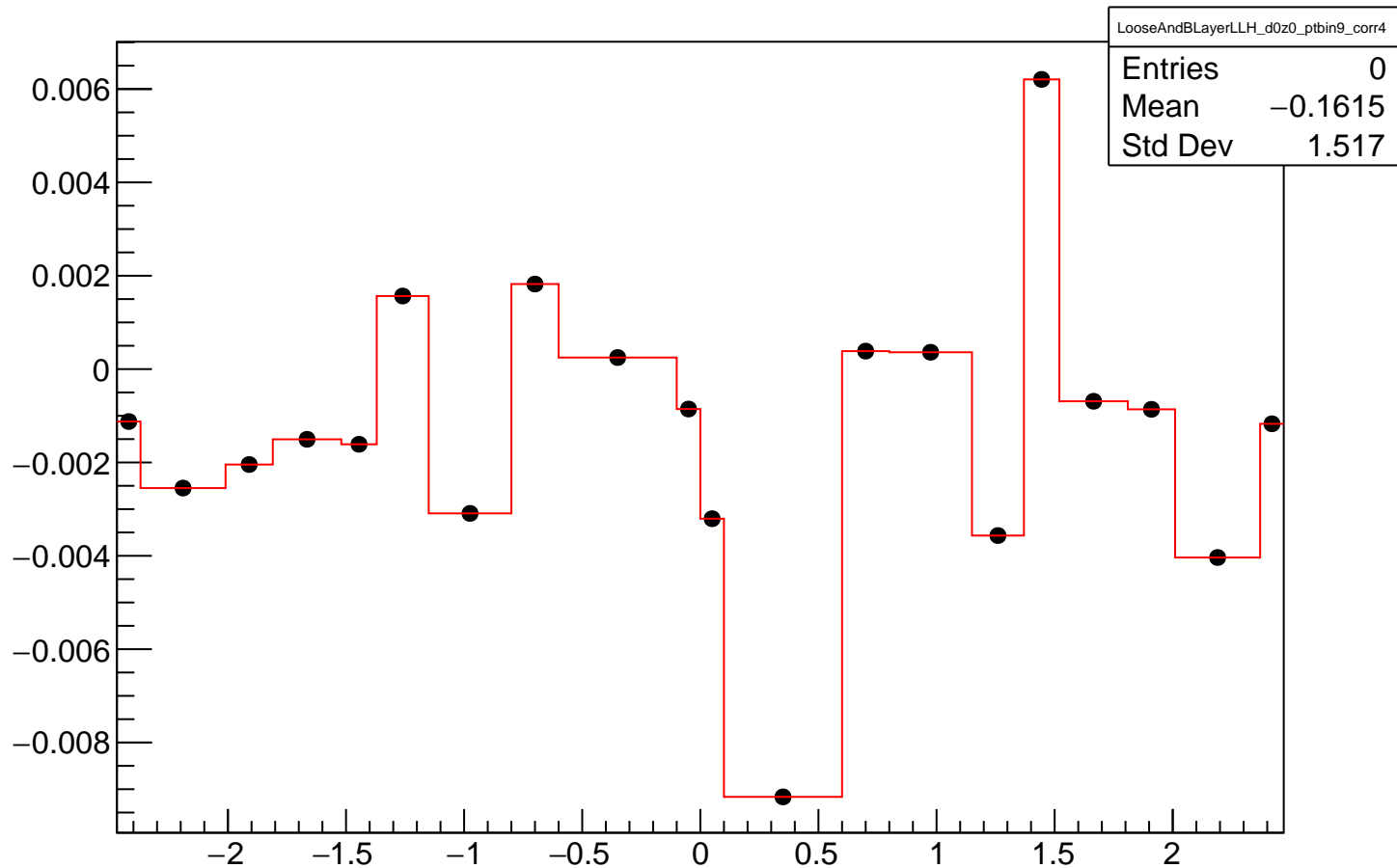
# FullSim\_LooseLLH\_d0z0\_corr4



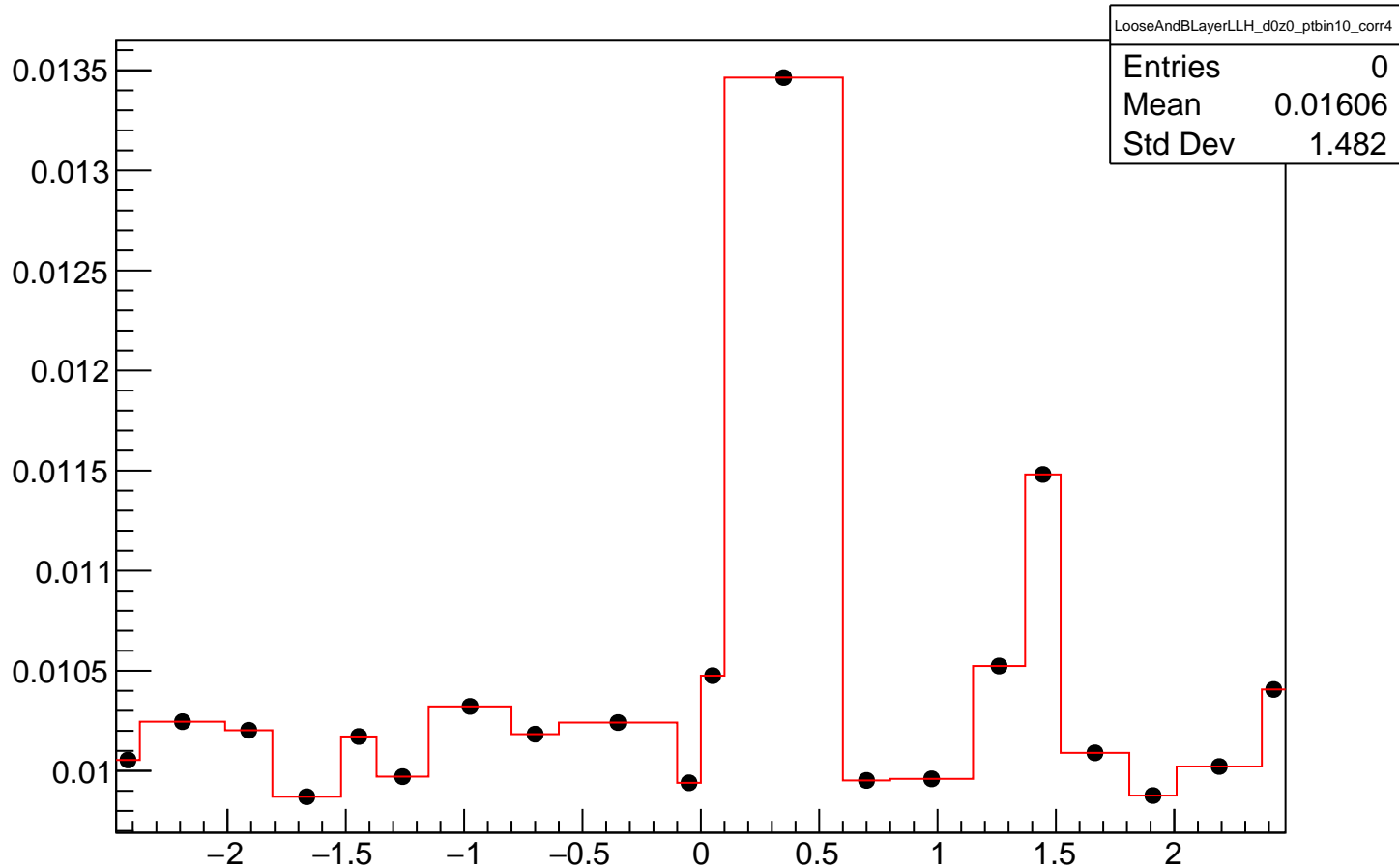
# FullSim\_LooseLLH\_d0z0\_corr4



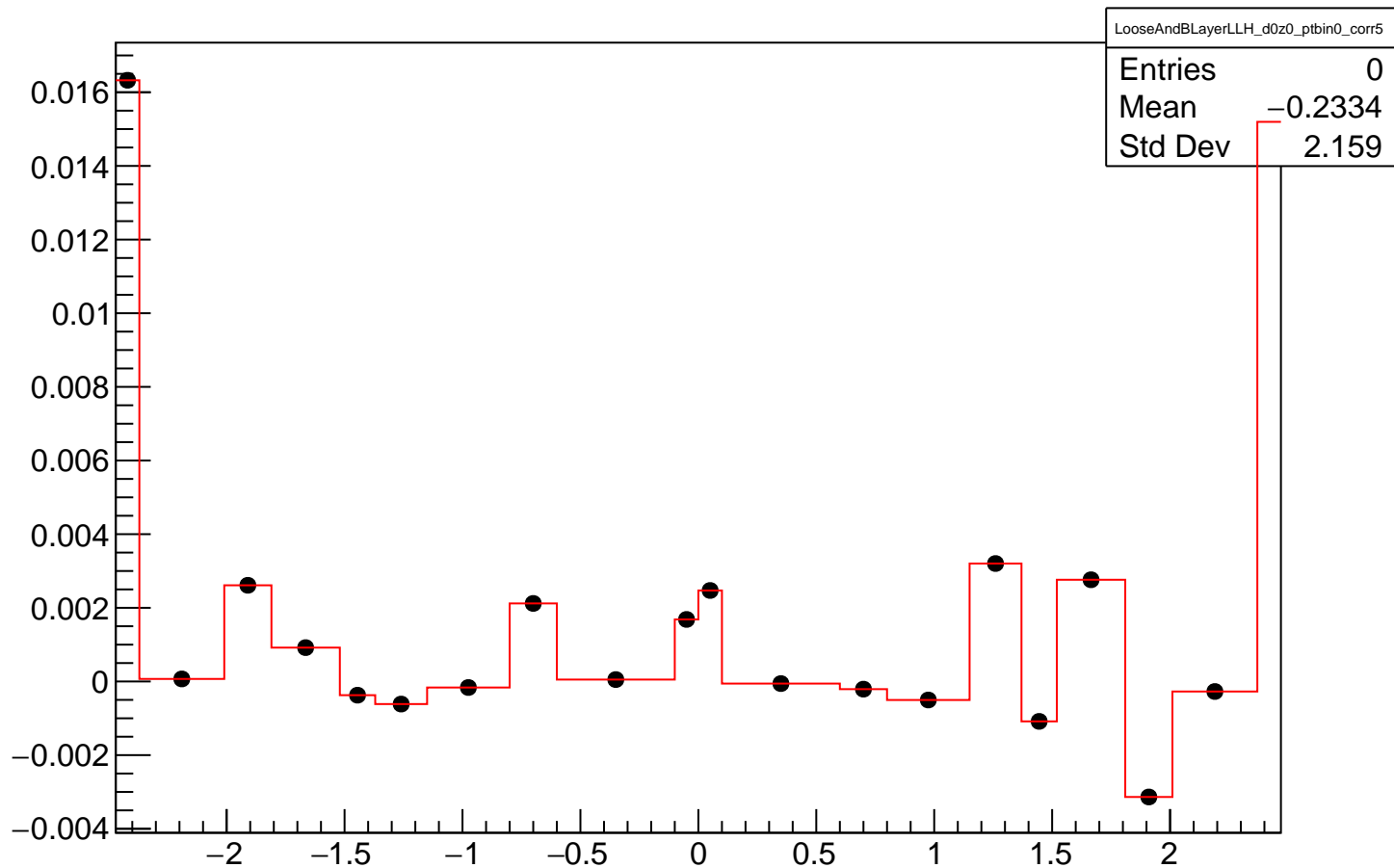
# FullSim\_LooseLLH\_d0z0\_corr4



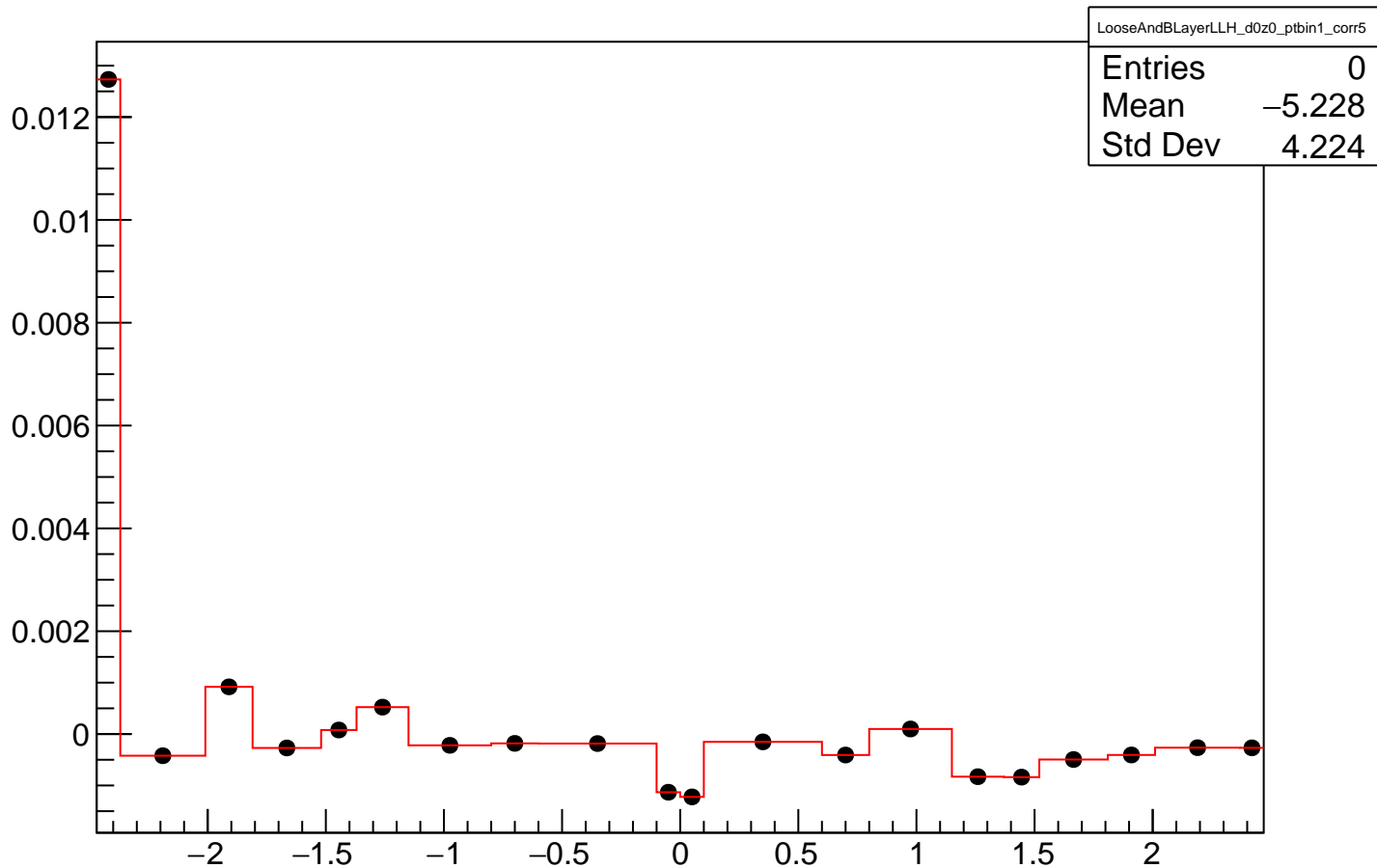
# FullSim\_LooseLLH\_d0z0\_corr4



# FullSim\_LooseLLH\_d0z0\_corr5

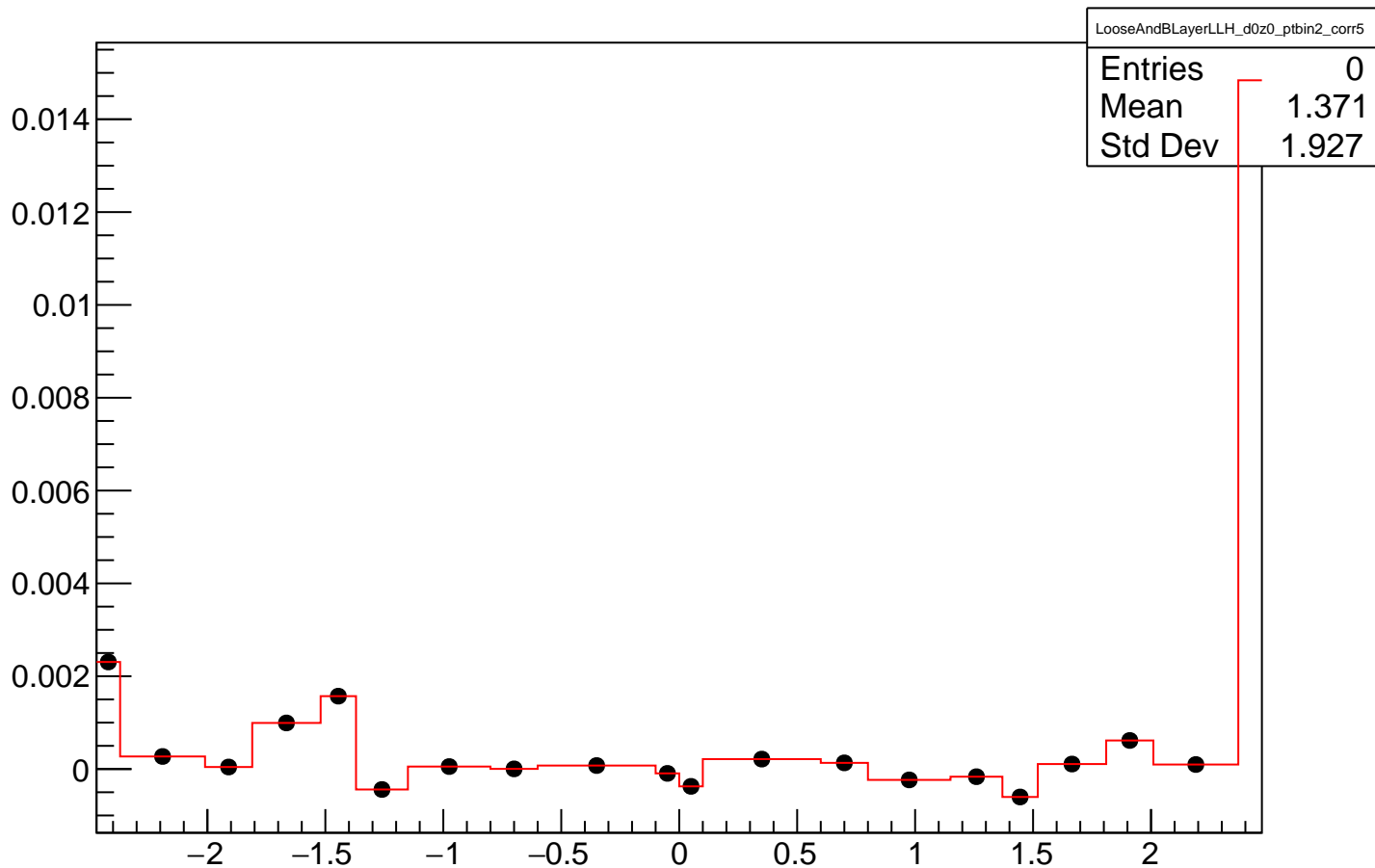


# FullSim\_LooseLLH\_d0z0\_corr5

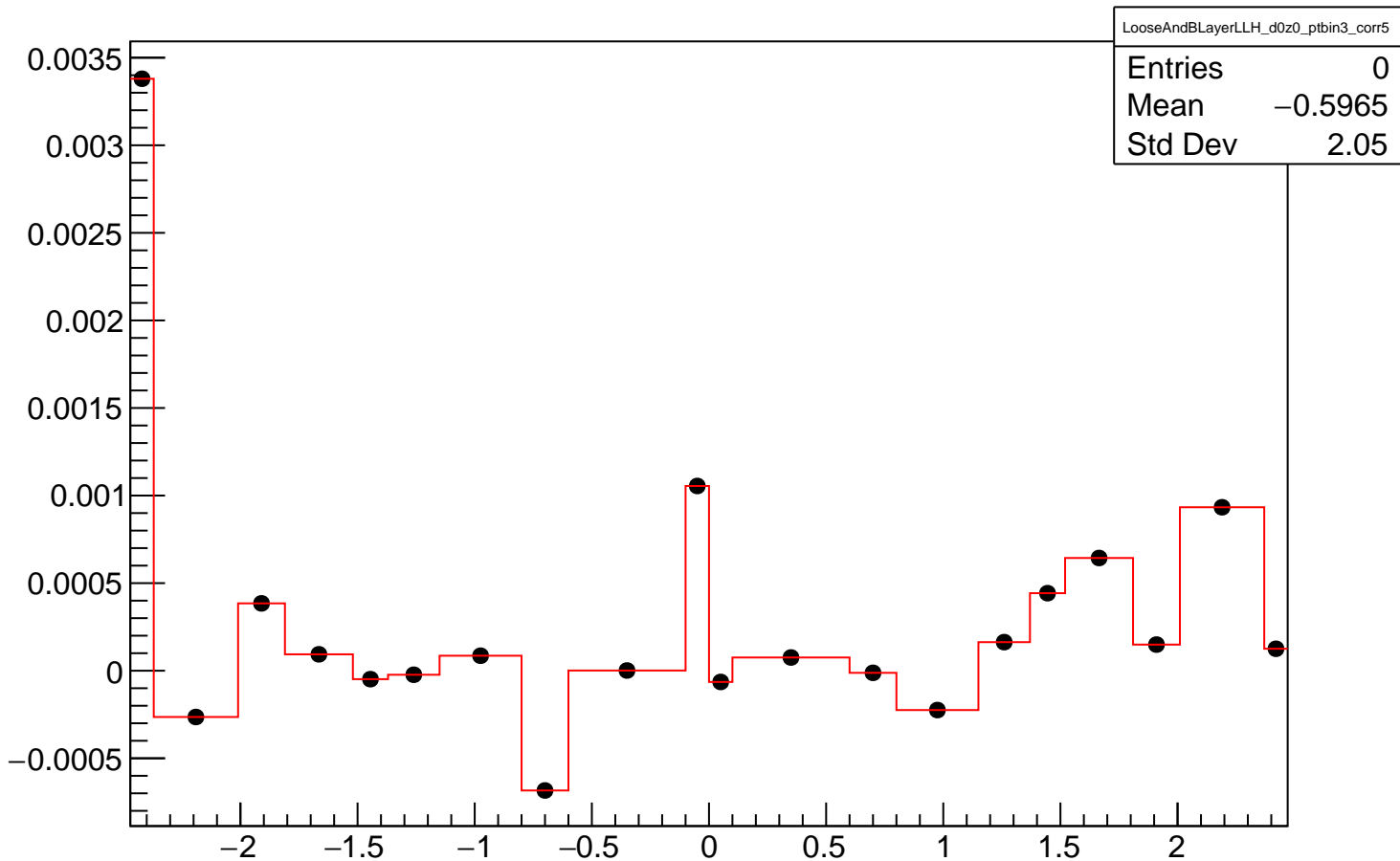




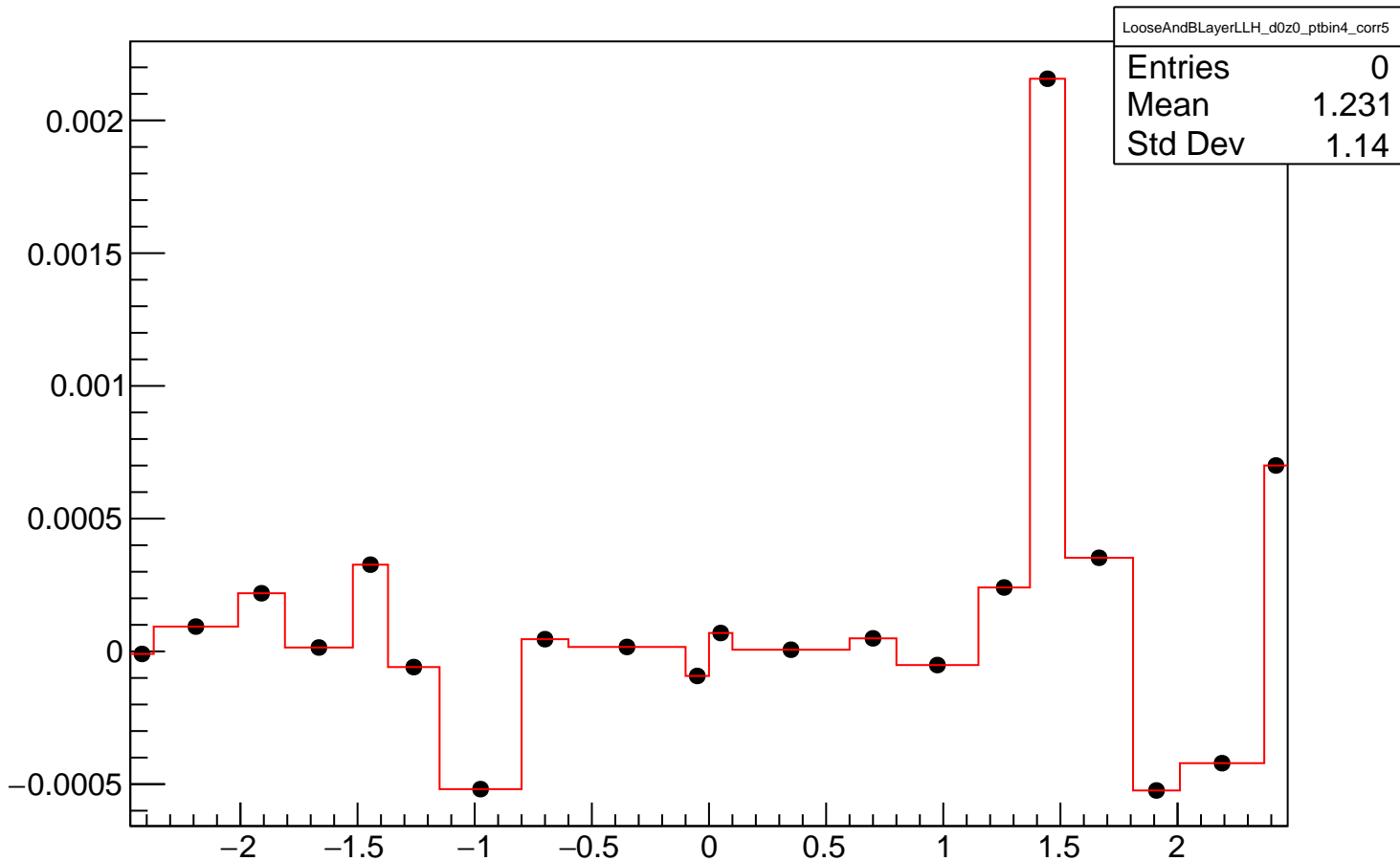
# FullSim\_LooseLLH\_d0z0\_corr5



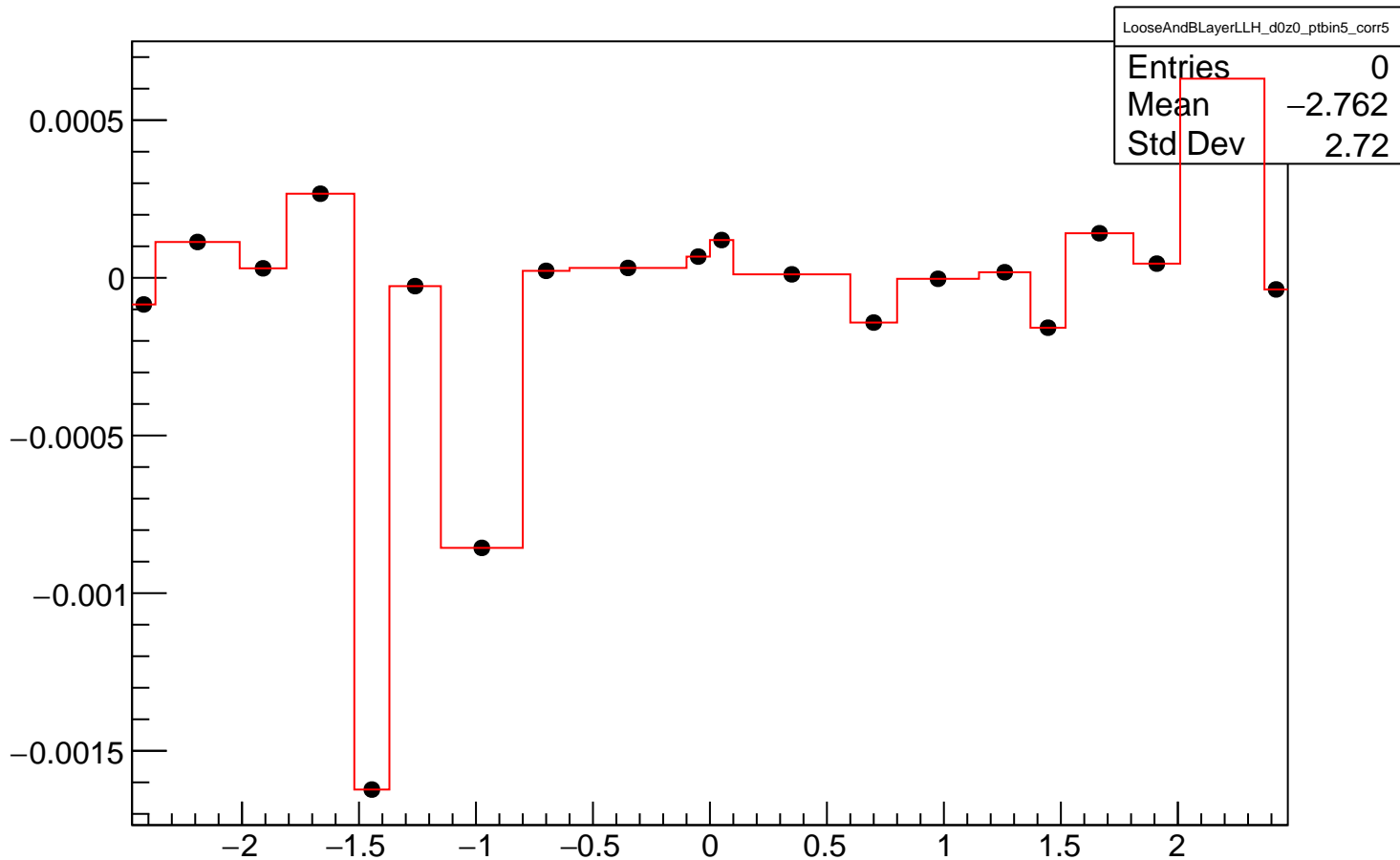
# FullSim\_LooseLLH\_d0z0\_corr5



# FullSim\_LooseLLH\_d0z0\_corr5



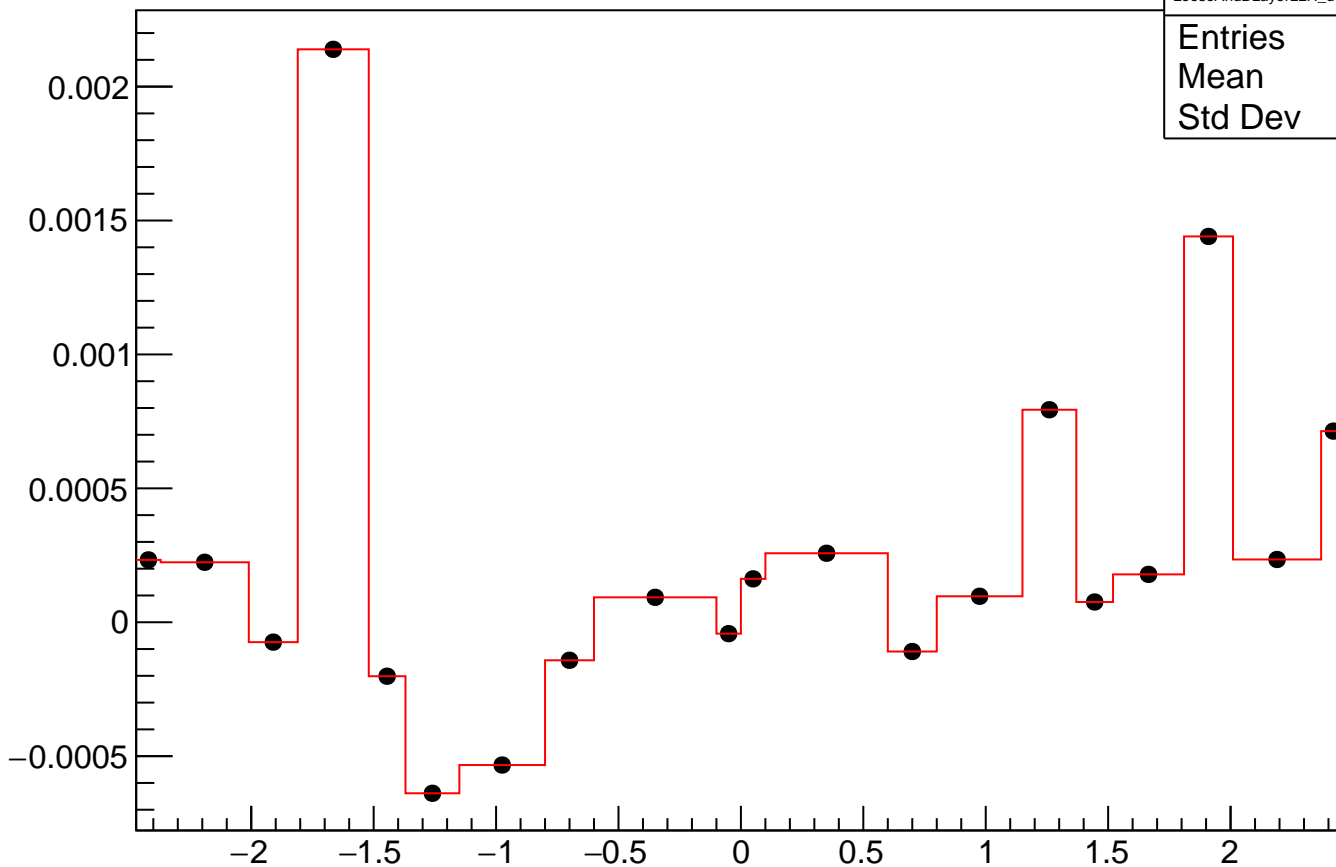
# FullSim\_LooseLLH\_d0z0\_corr5



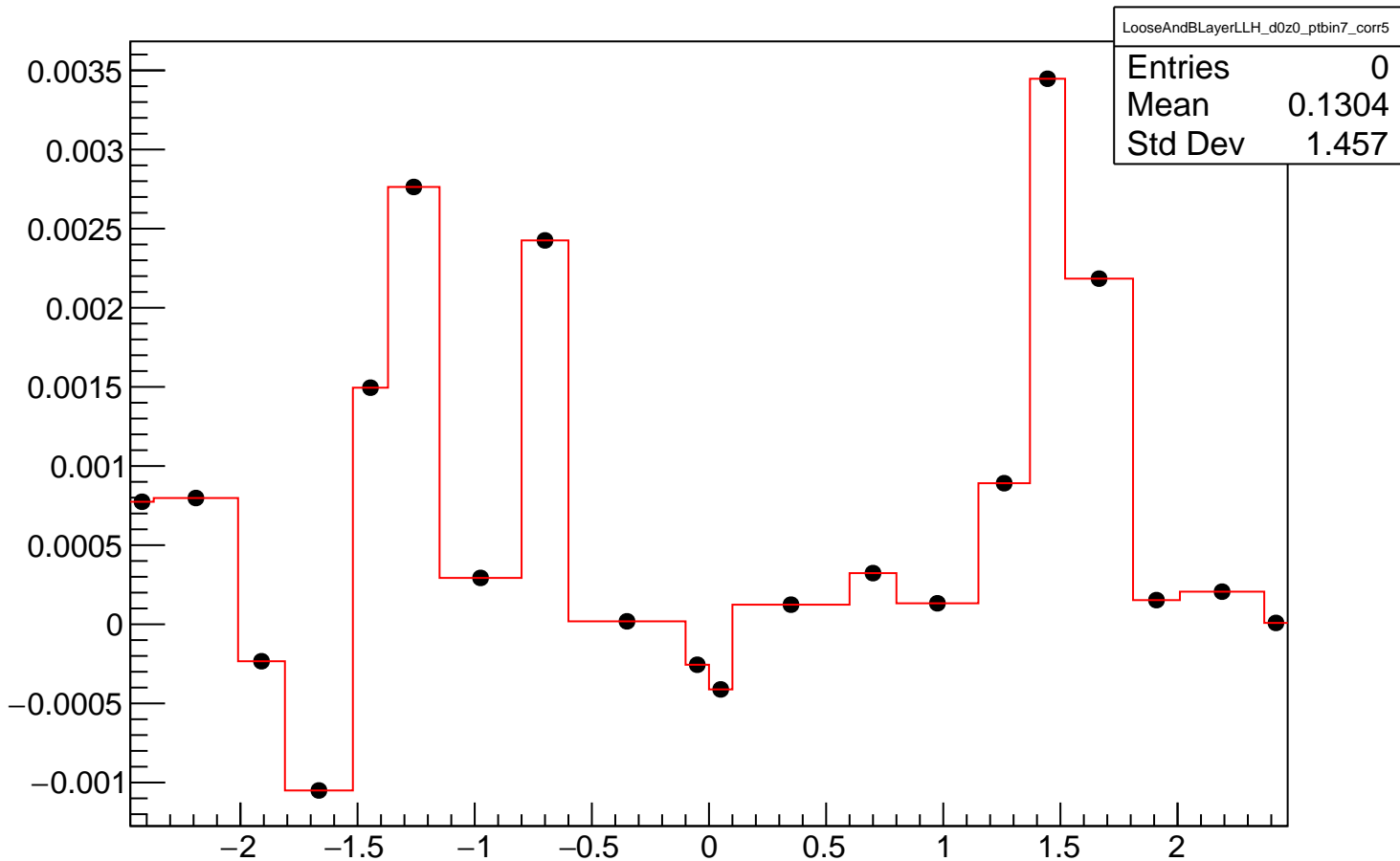
# FullSim\_LooseLLH\_d0z0\_corr5

LooseAndBLayerLLH\_d0z0\_ptbin6\_corr5

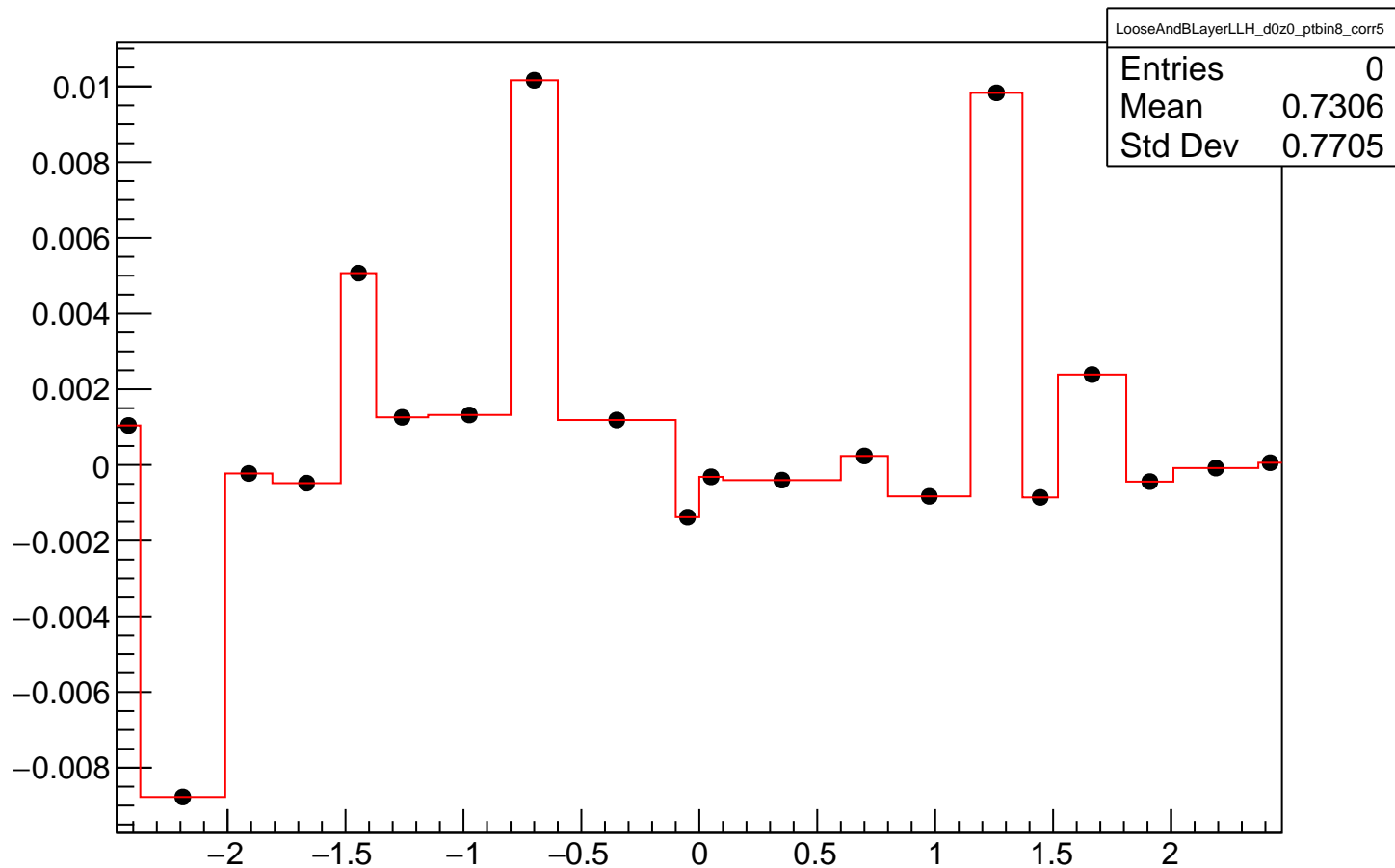
Entries	0
Mean	0.7602
Std Dev	1.796



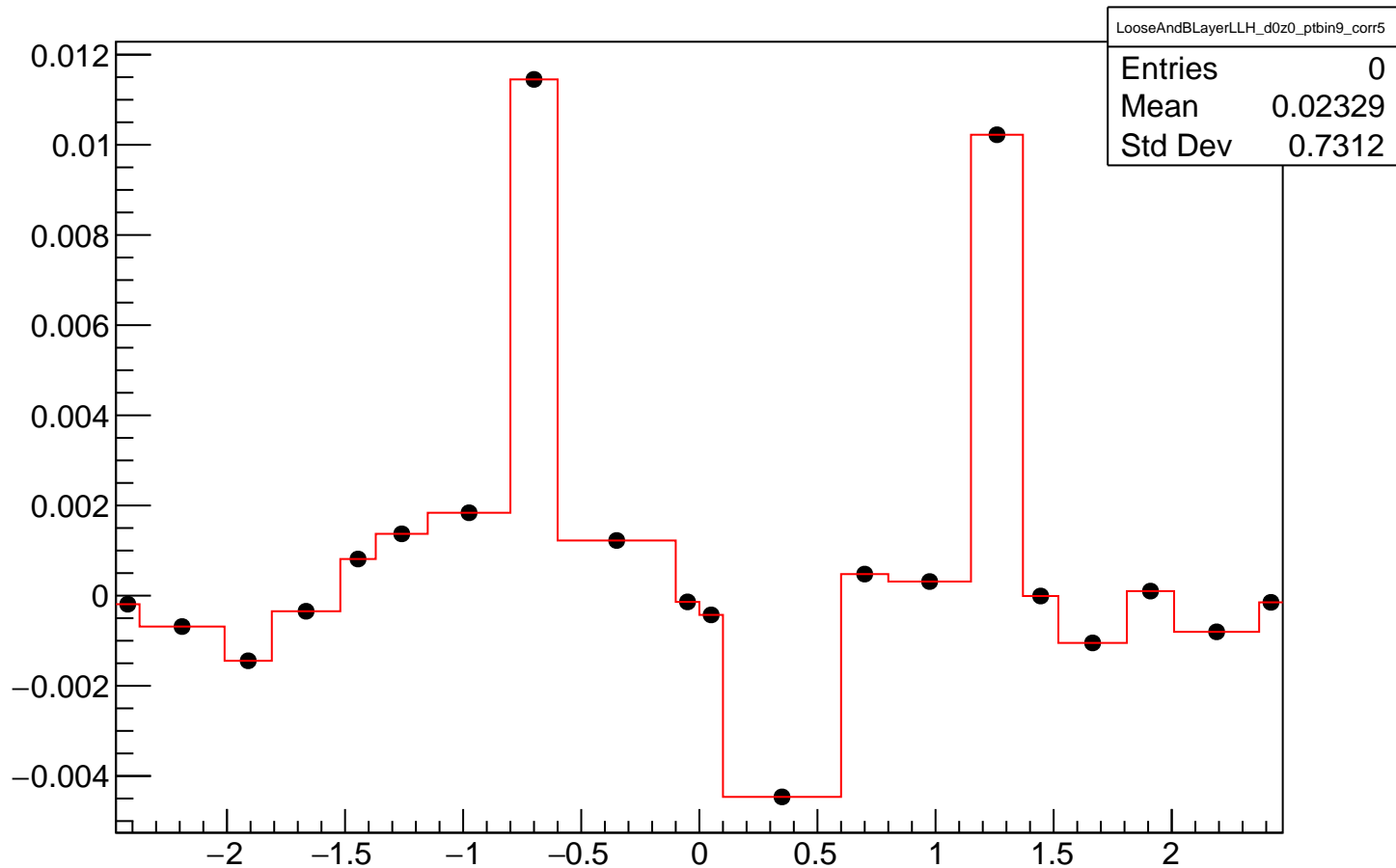
# FullSim\_LooseLLH\_d0z0\_corr5



# FullSim\_LooseLLH\_d0z0\_corr5

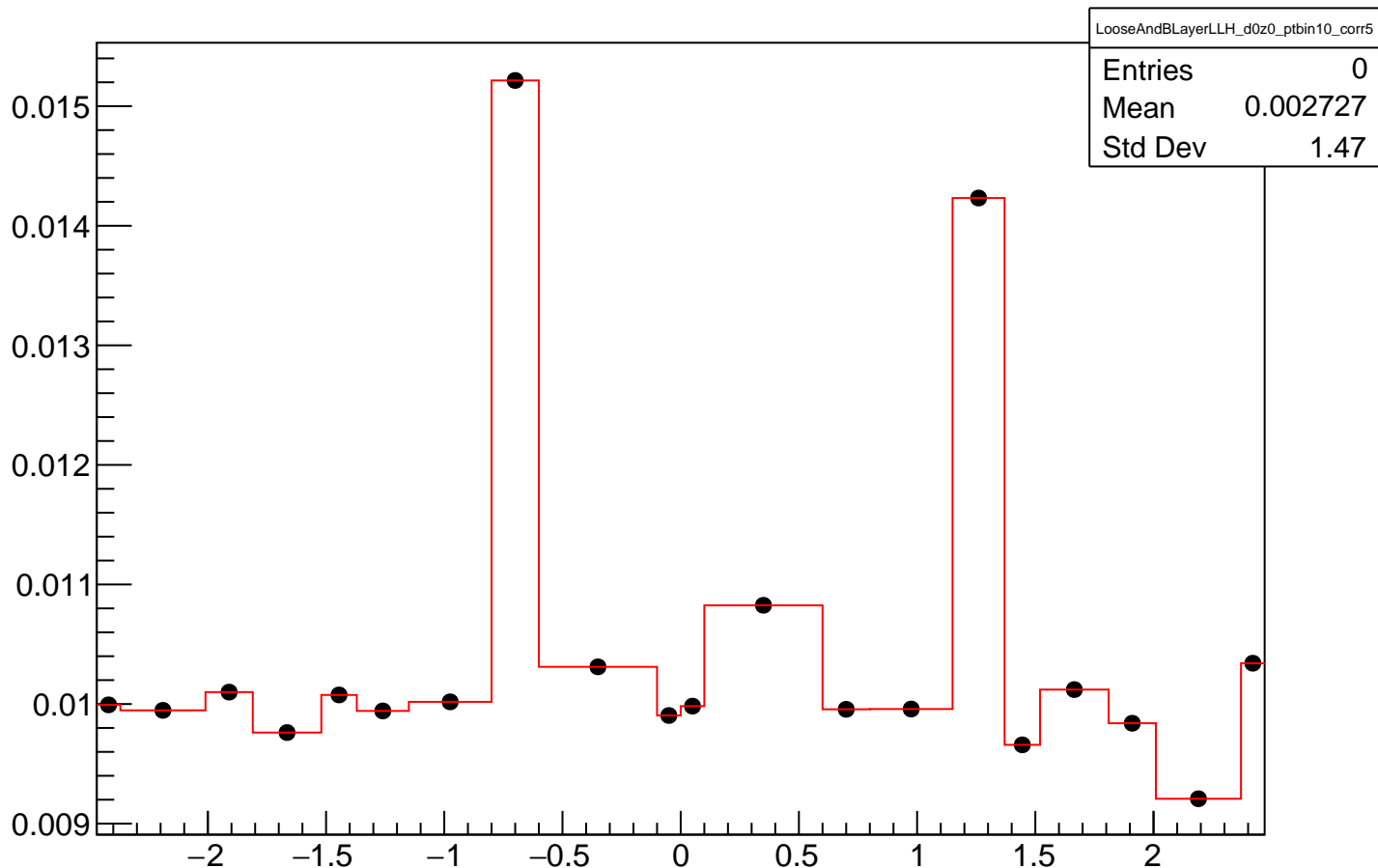


# FullSim\_LooseLLH\_d0z0\_corr5

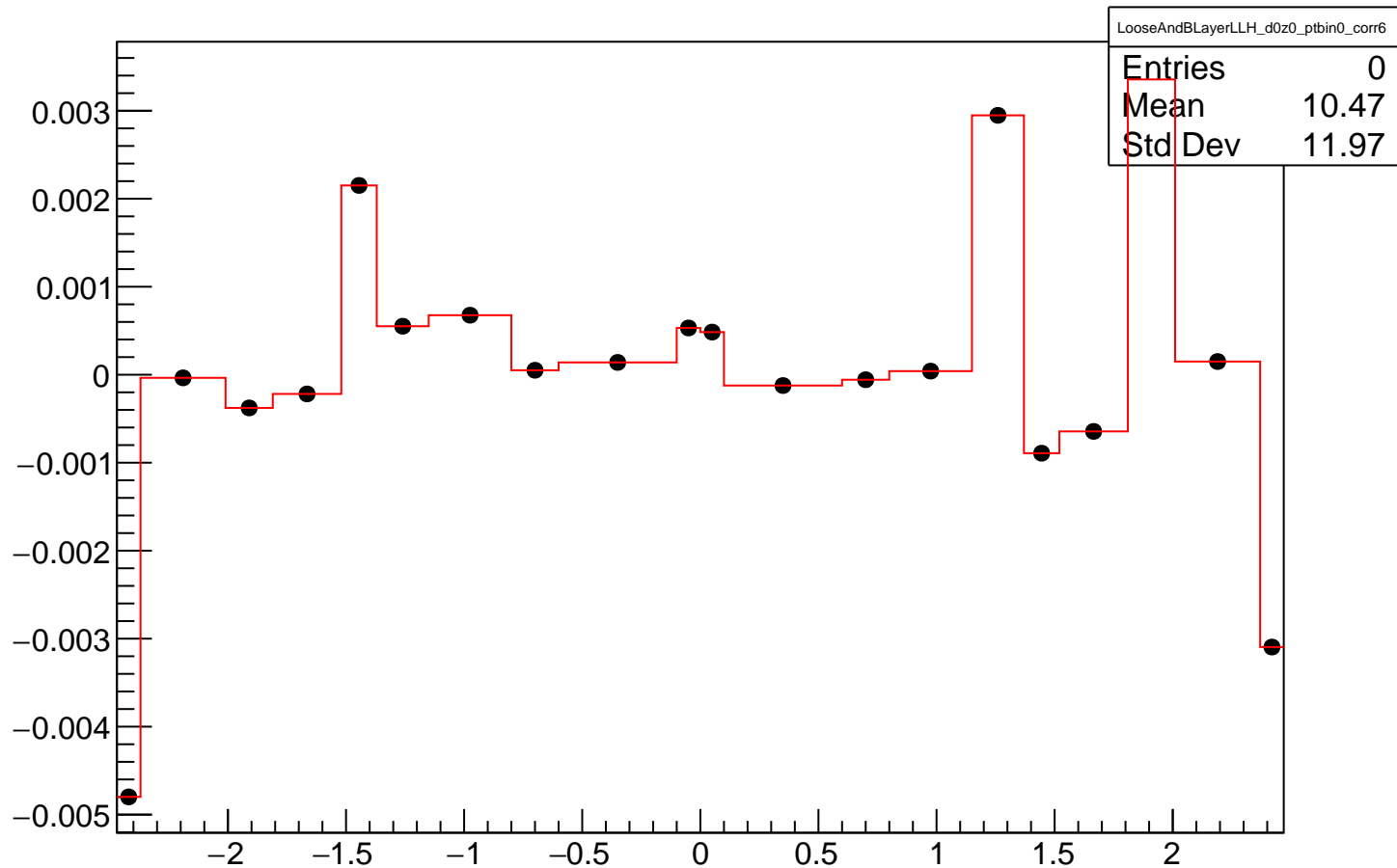




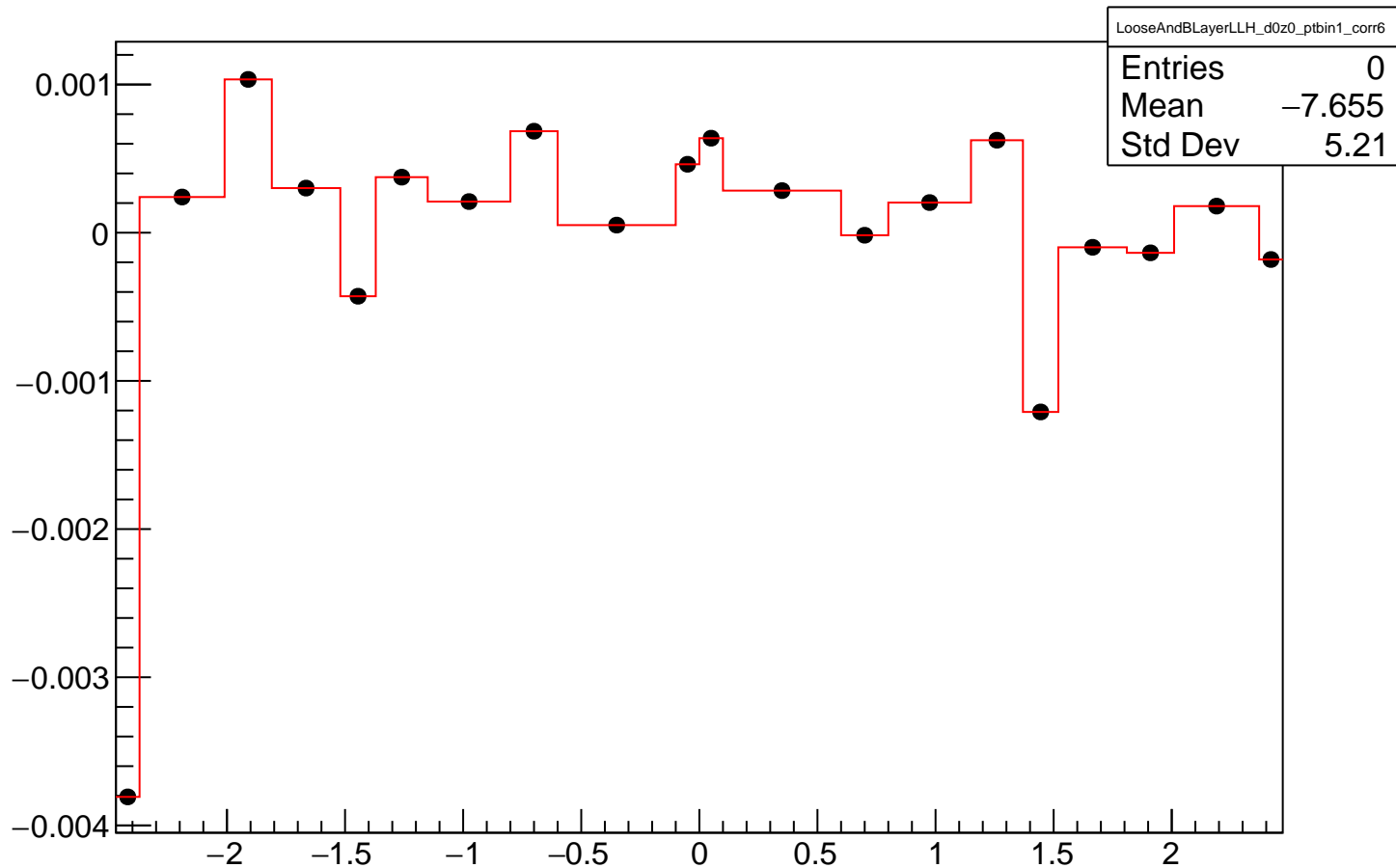
# FullSim\_LooseLLH\_d0z0\_corr5



# FullSim\_LooseLLH\_d0z0\_corr6

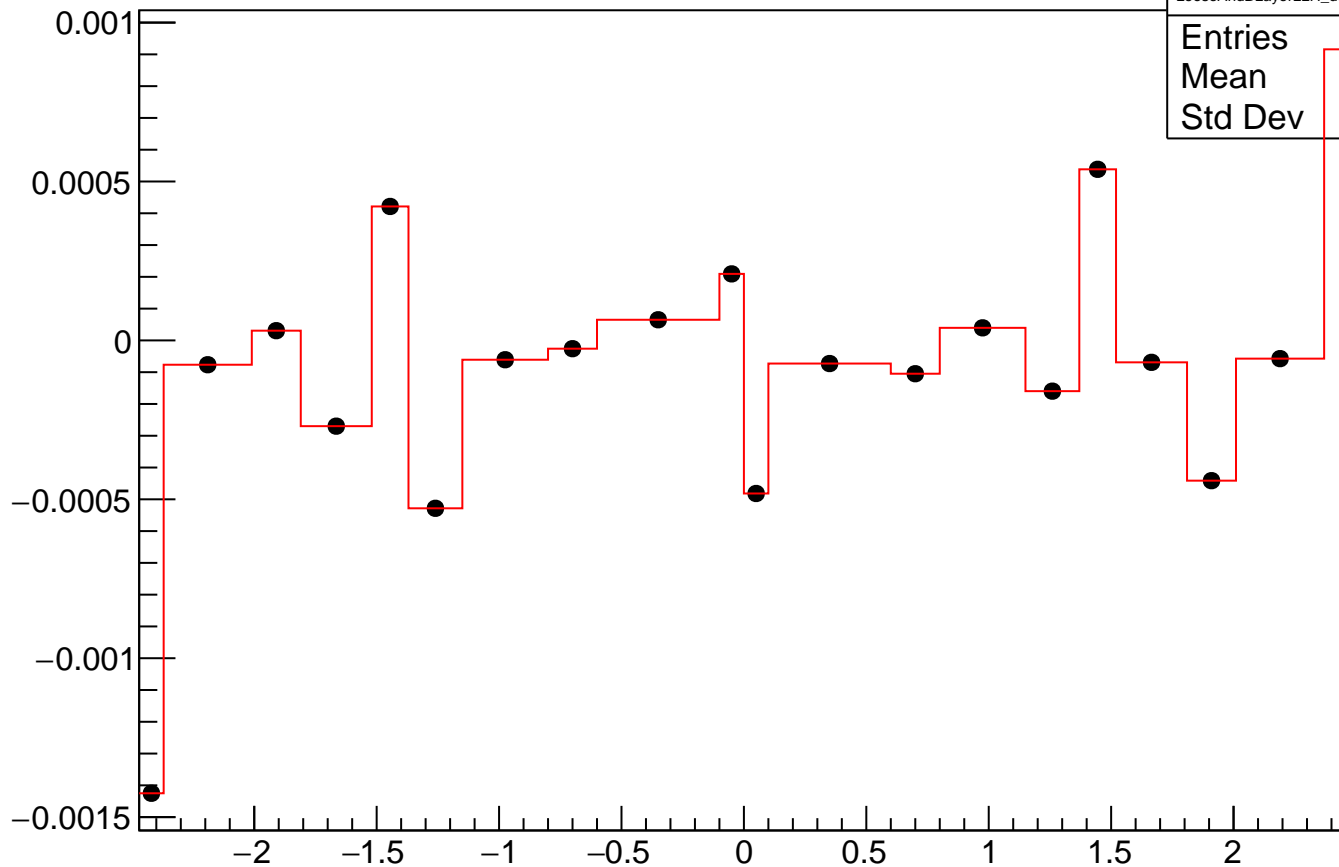


# FullSim\_LooseLLH\_d0z0\_corr6



# FullSim\_LooseLLH\_d0z0\_corr6

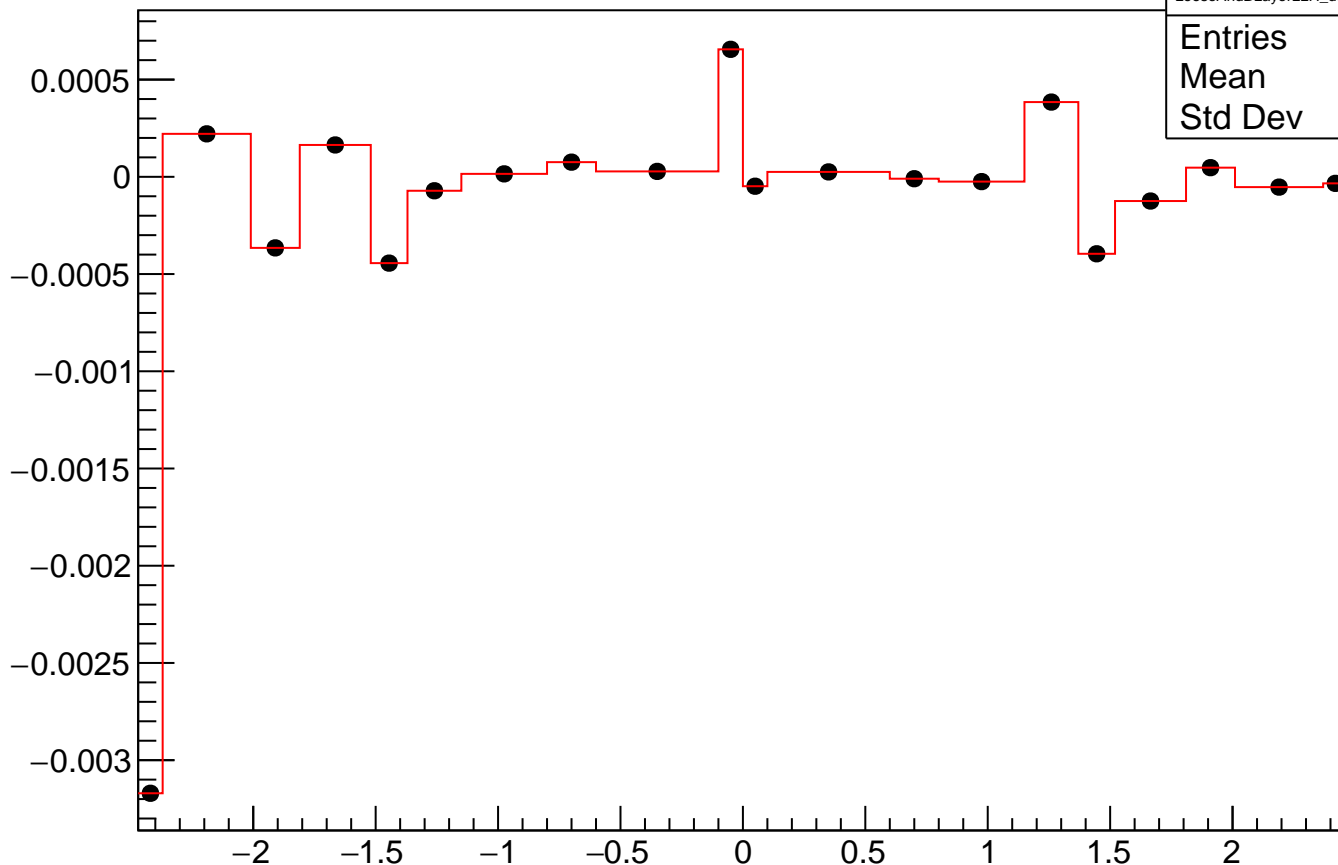
LooseAndBLayerLLH_d0z0_ptbin2_corr6	
Entries	0
Mean	-3.691
Std Dev	3.202



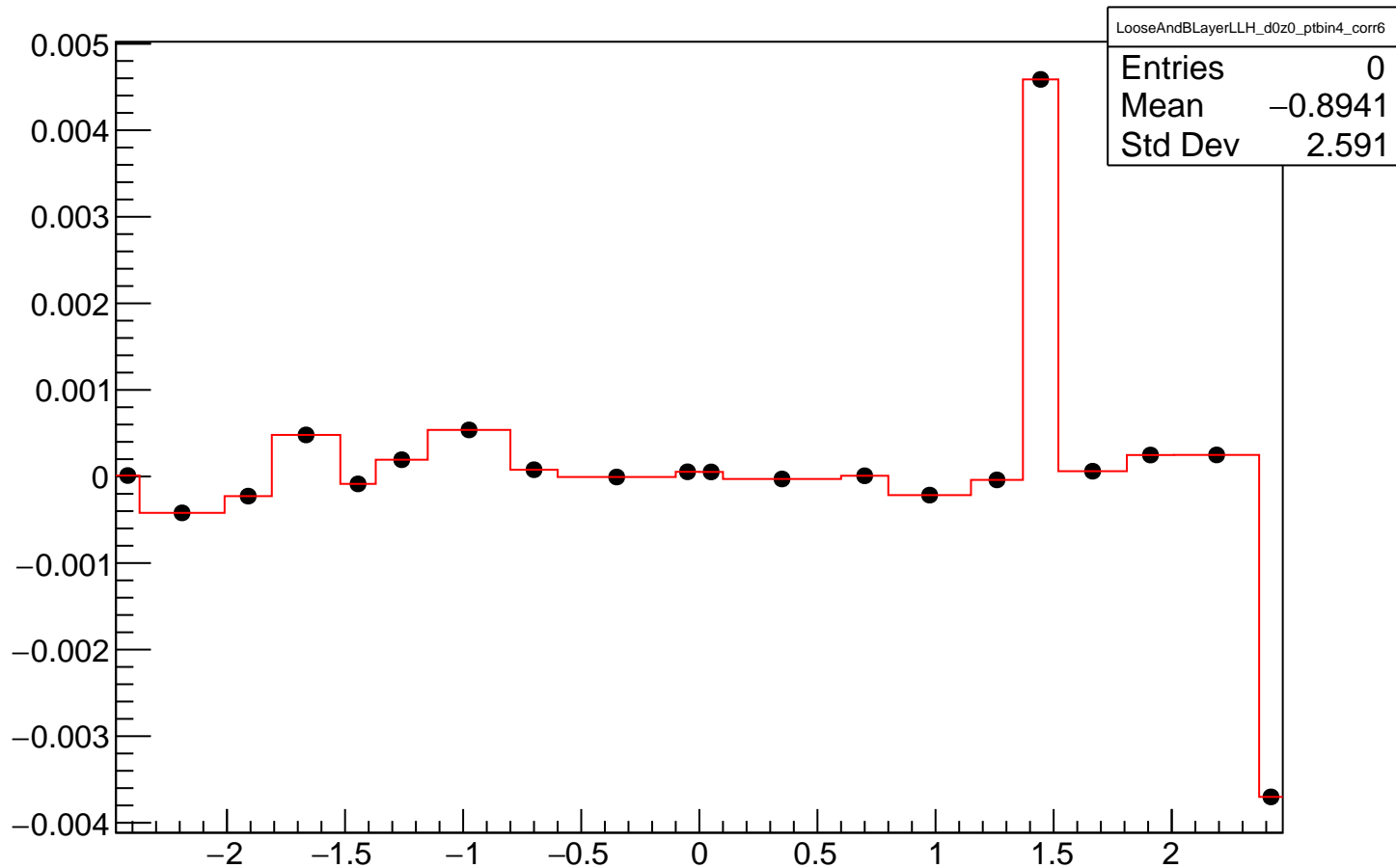
# FullSim\_LooseLLH\_d0z0\_corr6

LooseAndBLayerLLH\_d0z0\_ptbin3\_corr6

Entries	0
Mean	-2.498
Std Dev	0.4823



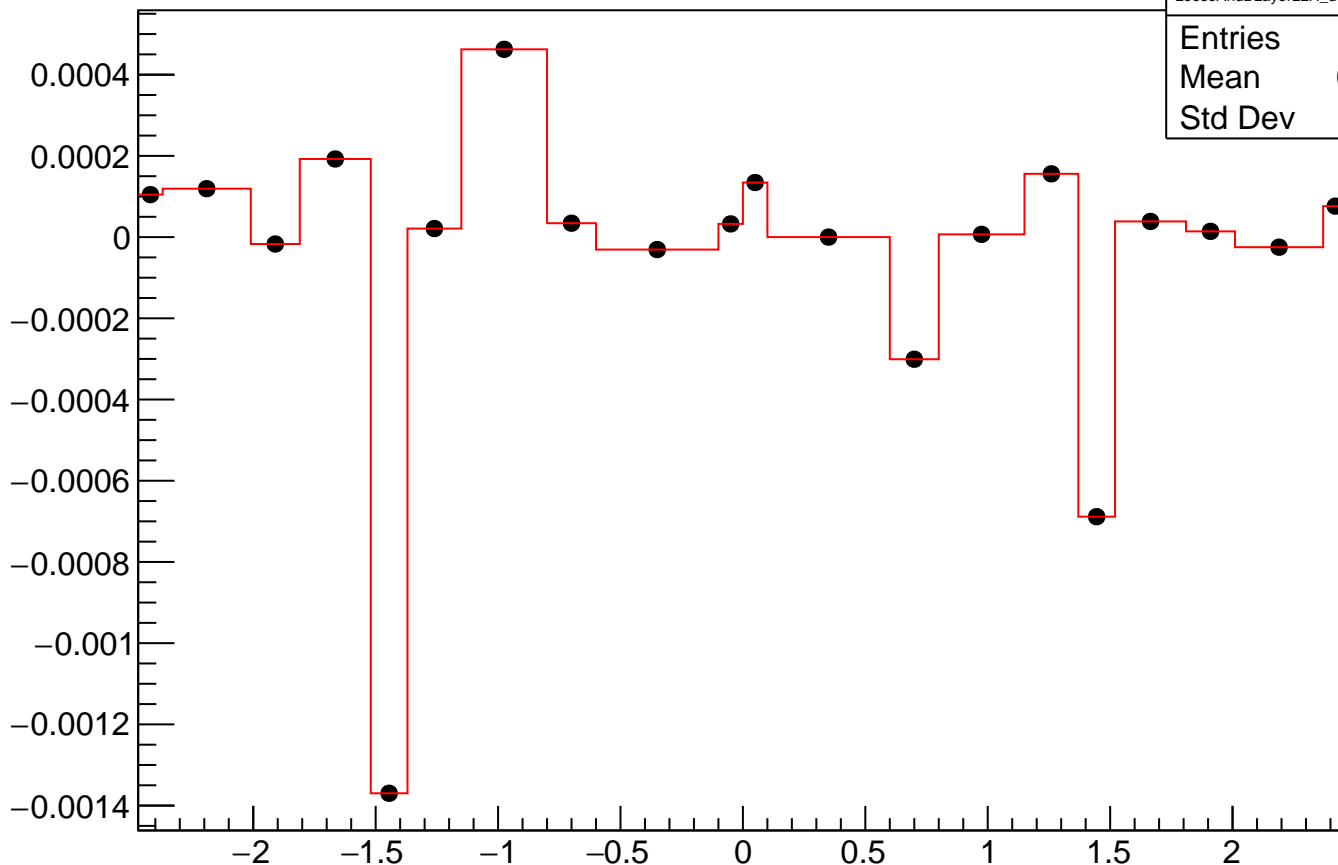
# FullSim\_LooseLLH\_d0z0\_corr6



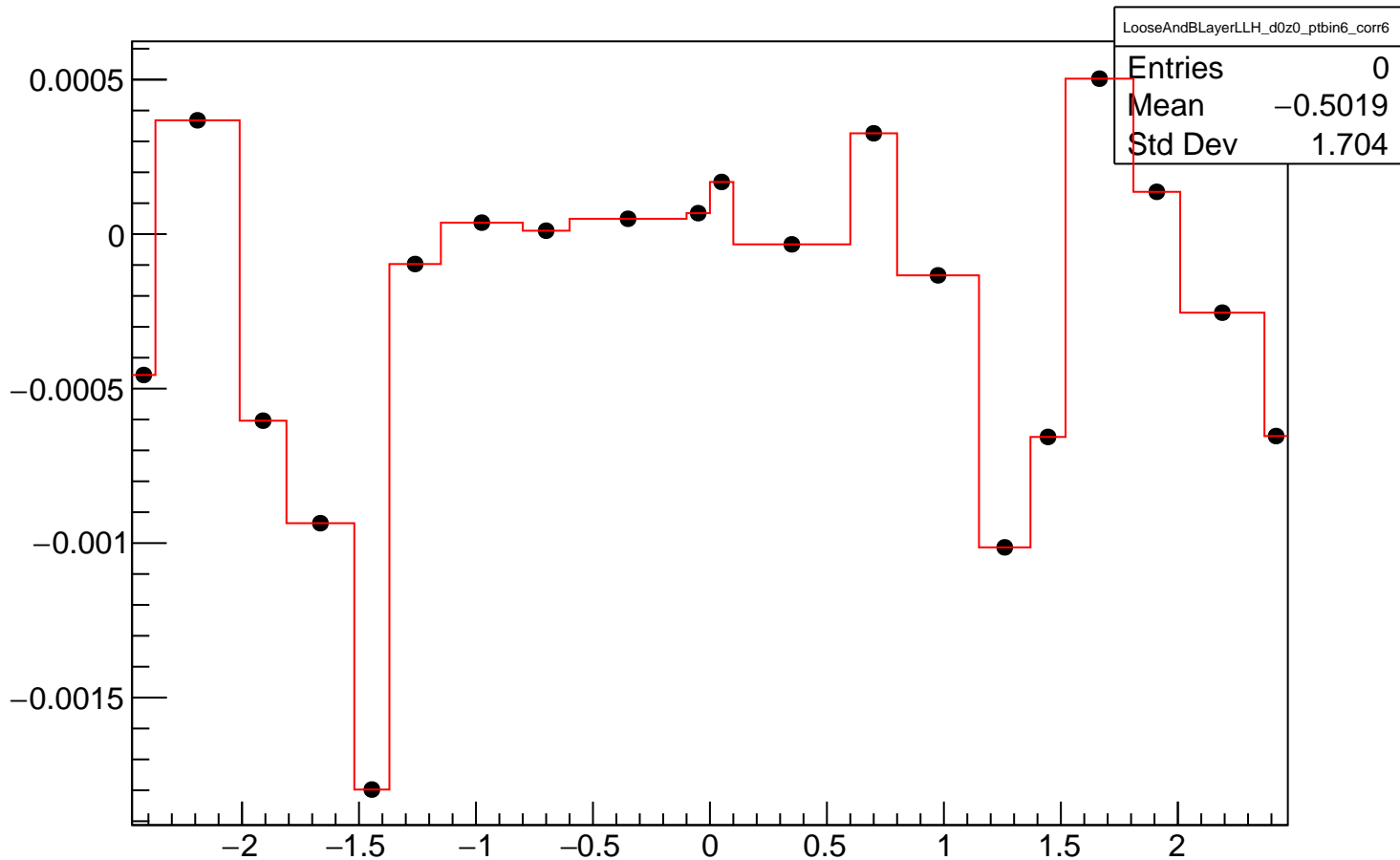
# FullSim\_LooseLLH\_d0z0\_corr6

LooseAndBLayerLLH\_d0z0\_ptbin5\_corr6

Entries	0
Mean	0.08623
Std Dev	1.223

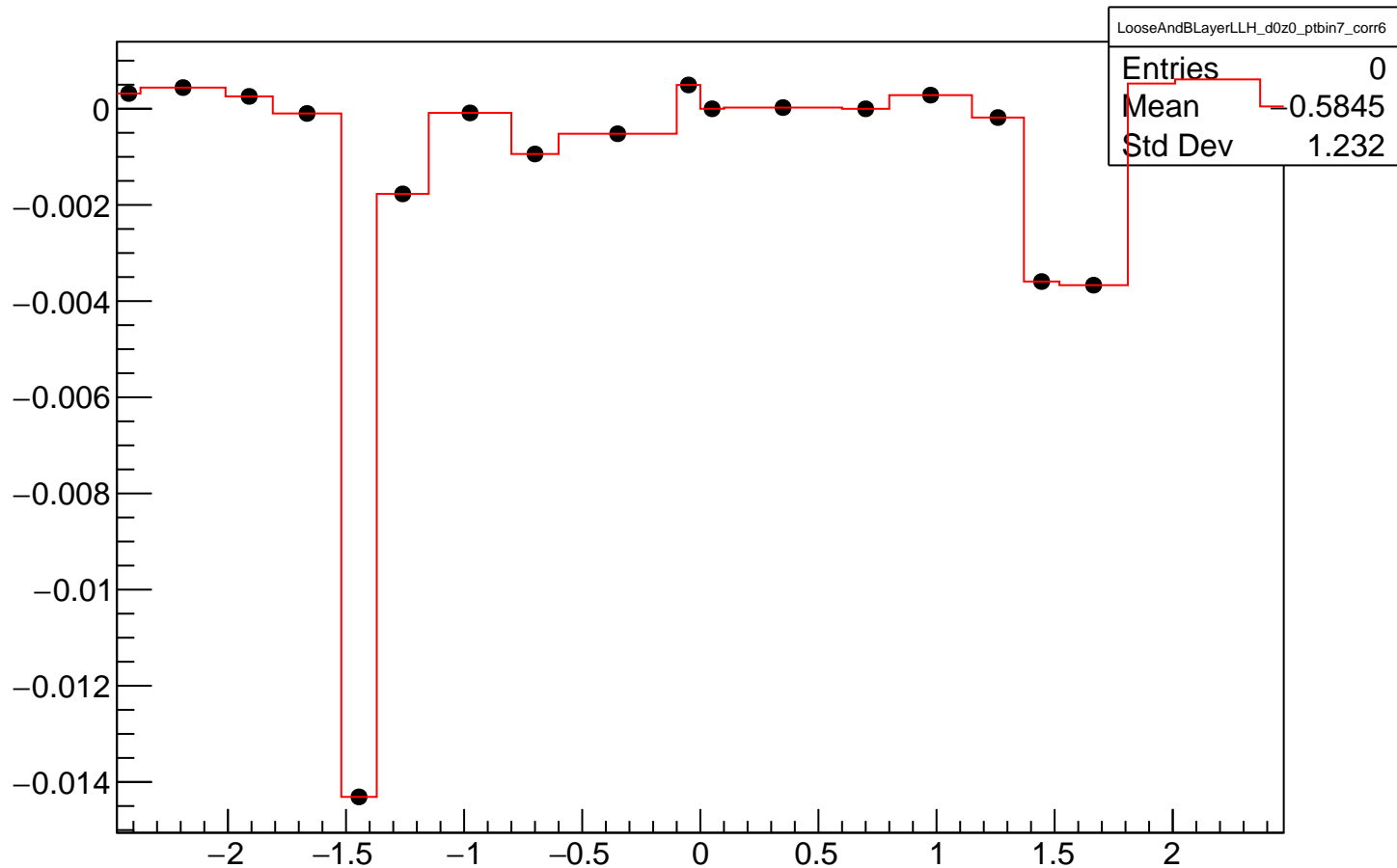


# FullSim\_LooseLLH\_d0z0\_corr6

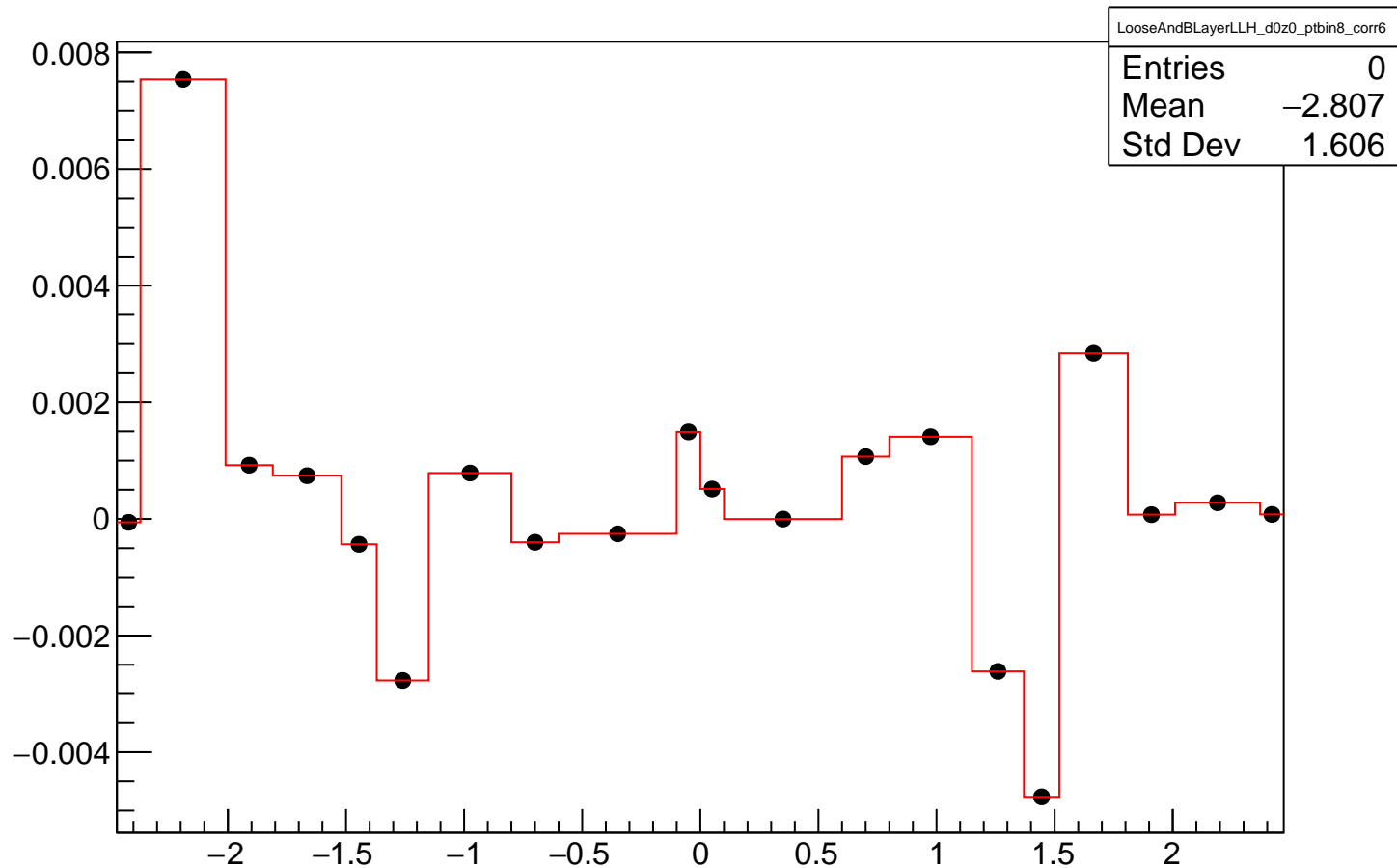




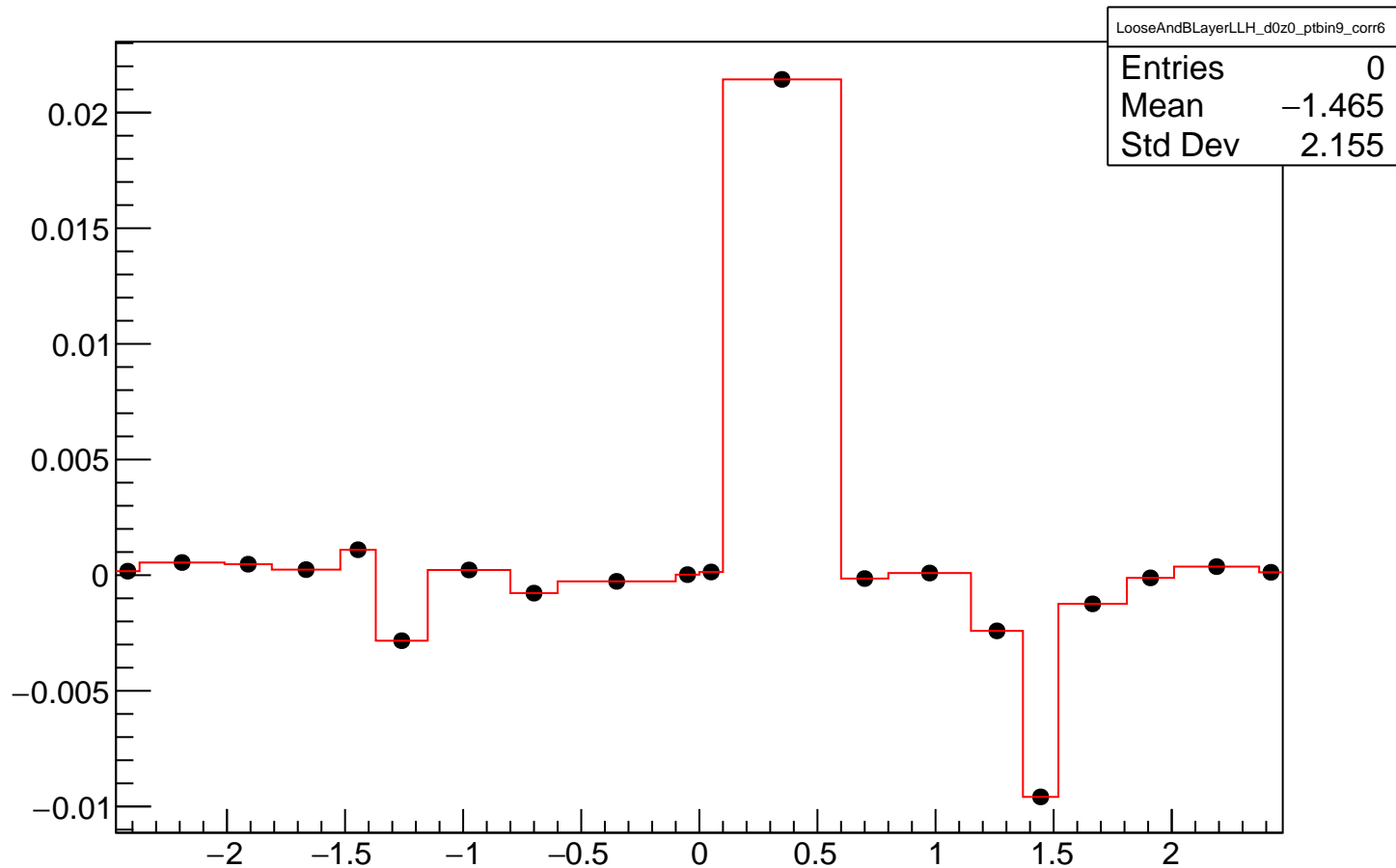
# FullSim\_LooseLLH\_d0z0\_corr6



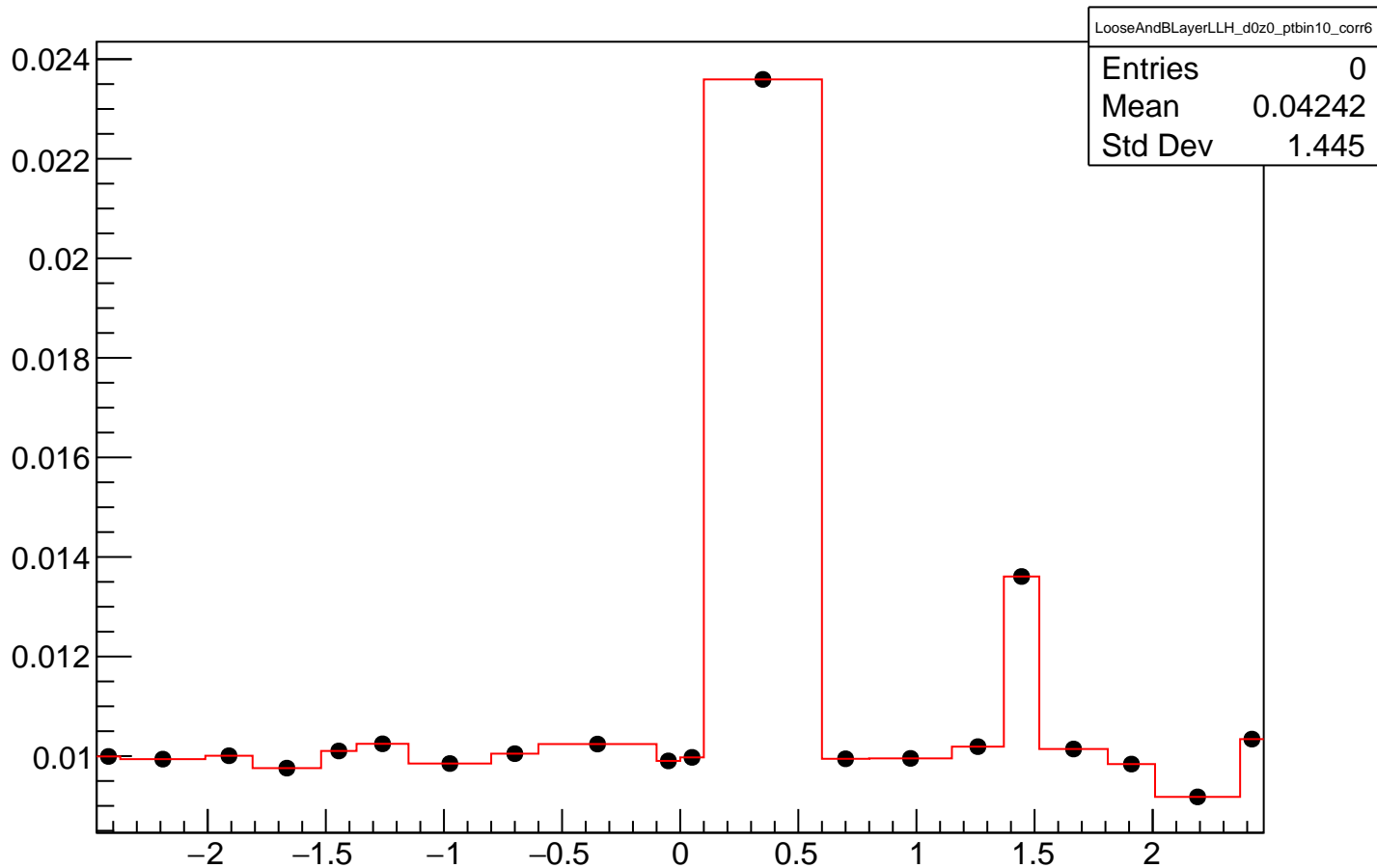
# FullSim\_LooseLLH\_d0z0\_corr6



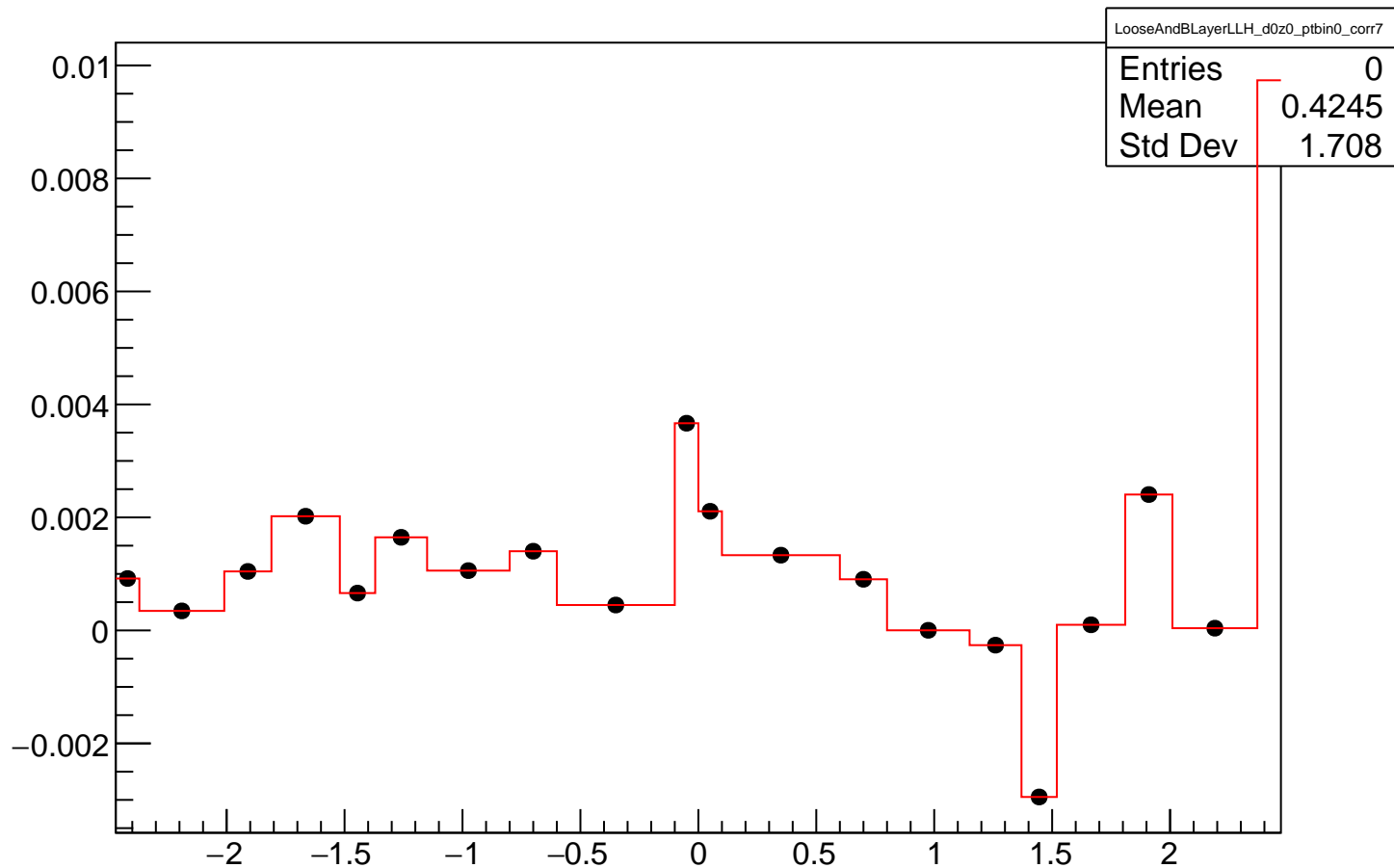
# FullSim\_LooseLLH\_d0z0\_corr6



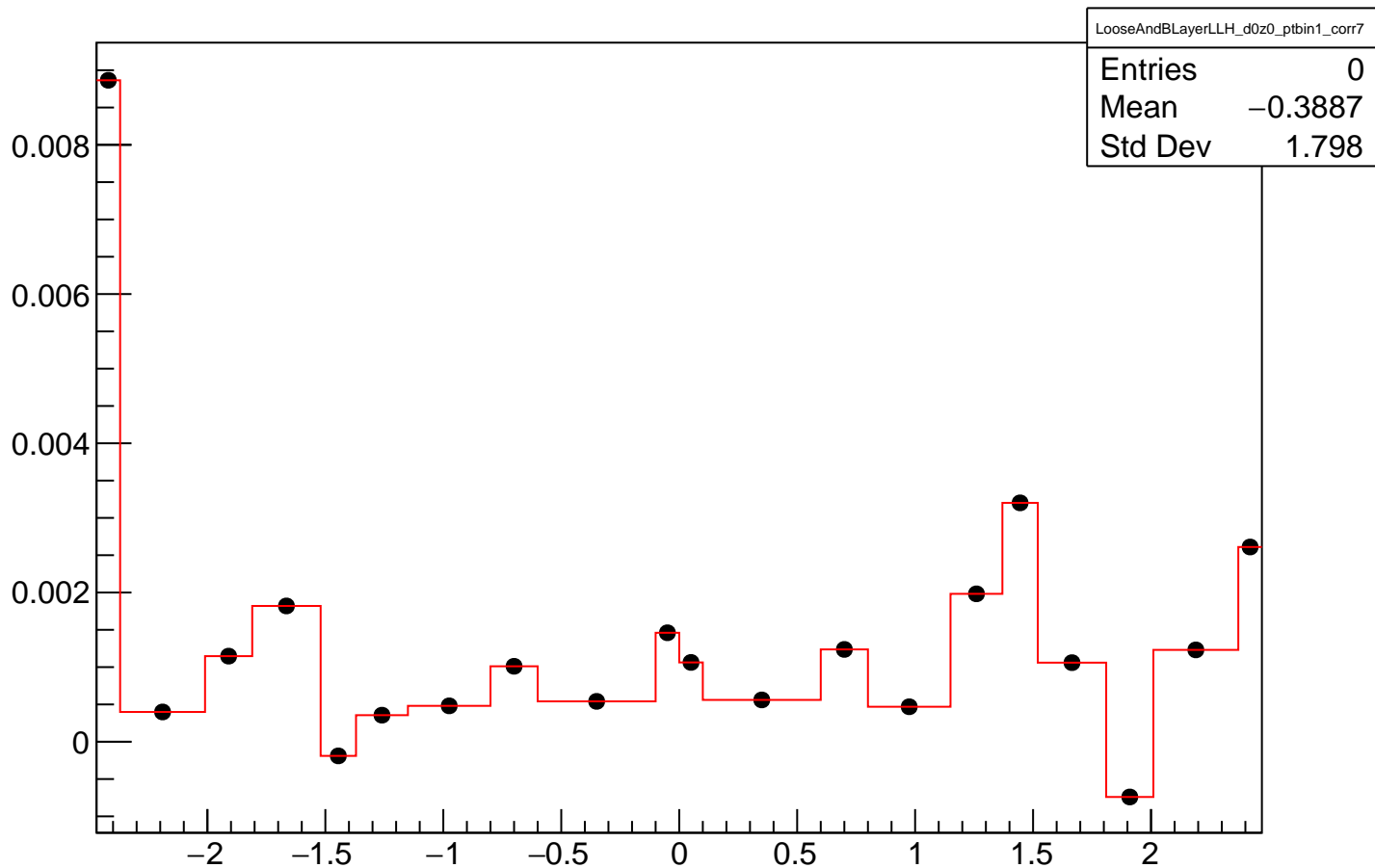
# FullSim\_LooseLLH\_d0z0\_corr6



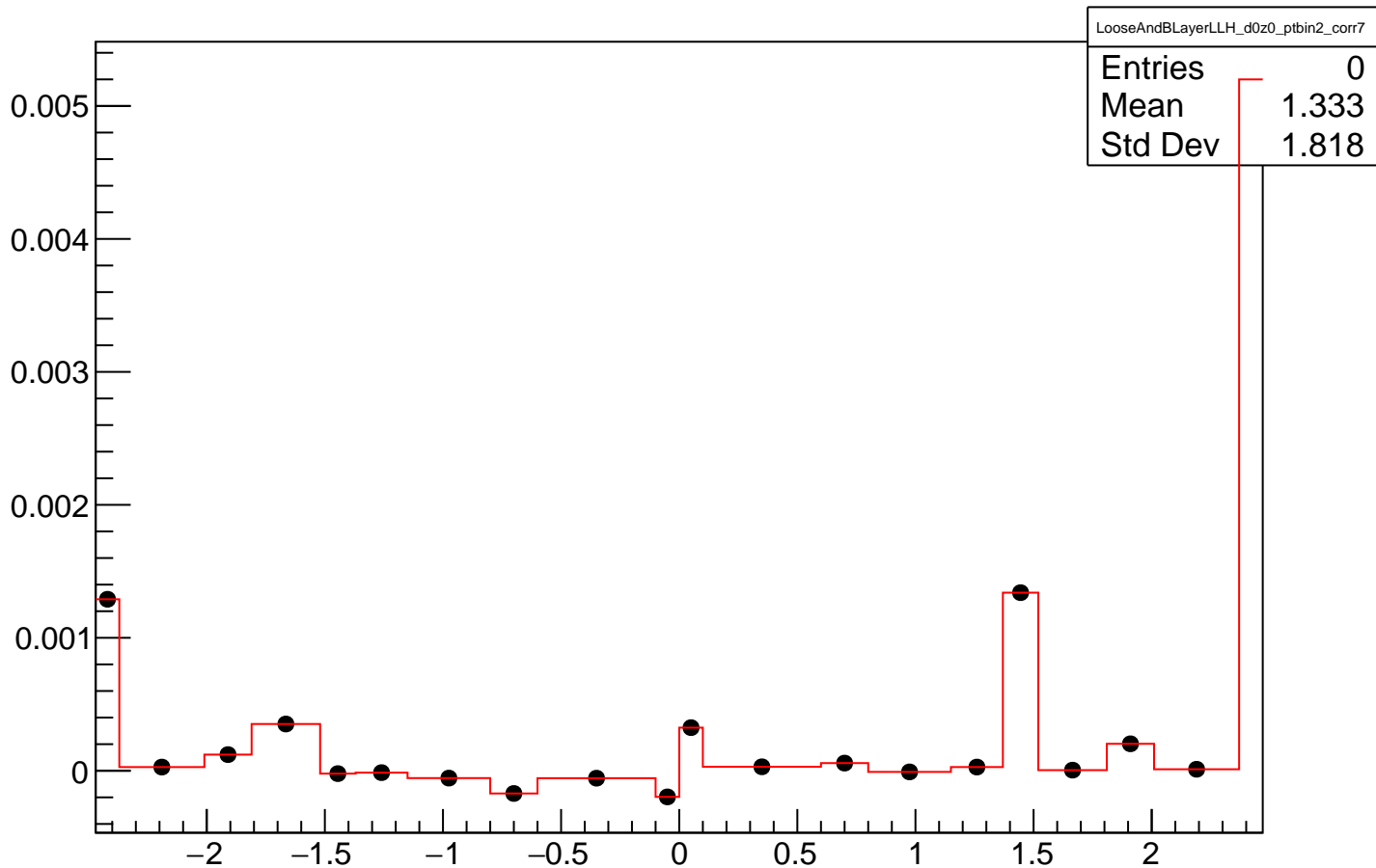
# FullSim\_LooseLLH\_d0z0\_corr7



# FullSim\_LooseLLH\_d0z0\_corr7



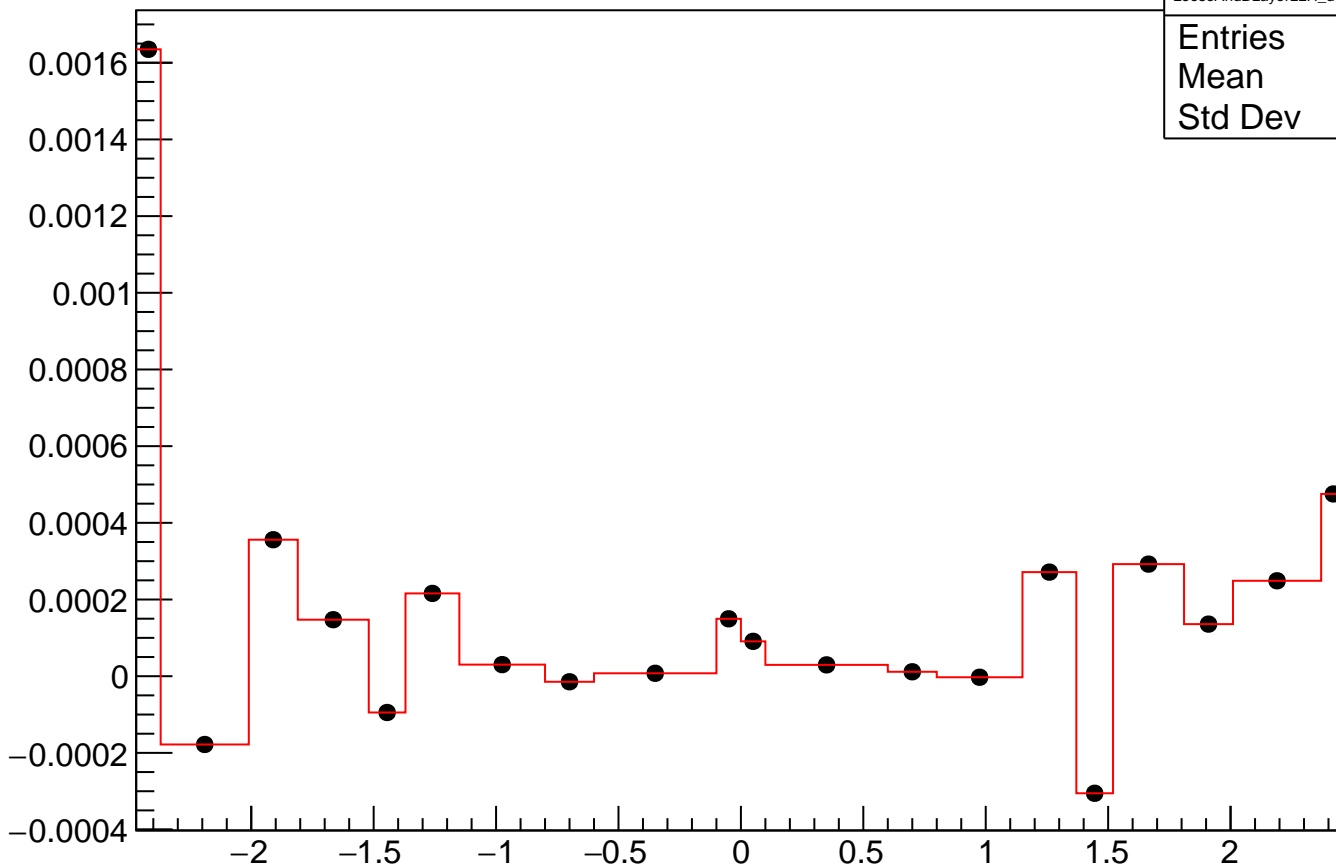
# FullSim\_LooseLLH\_d0z0\_corr7



# FullSim\_LooseLLH\_d0z0\_corr7

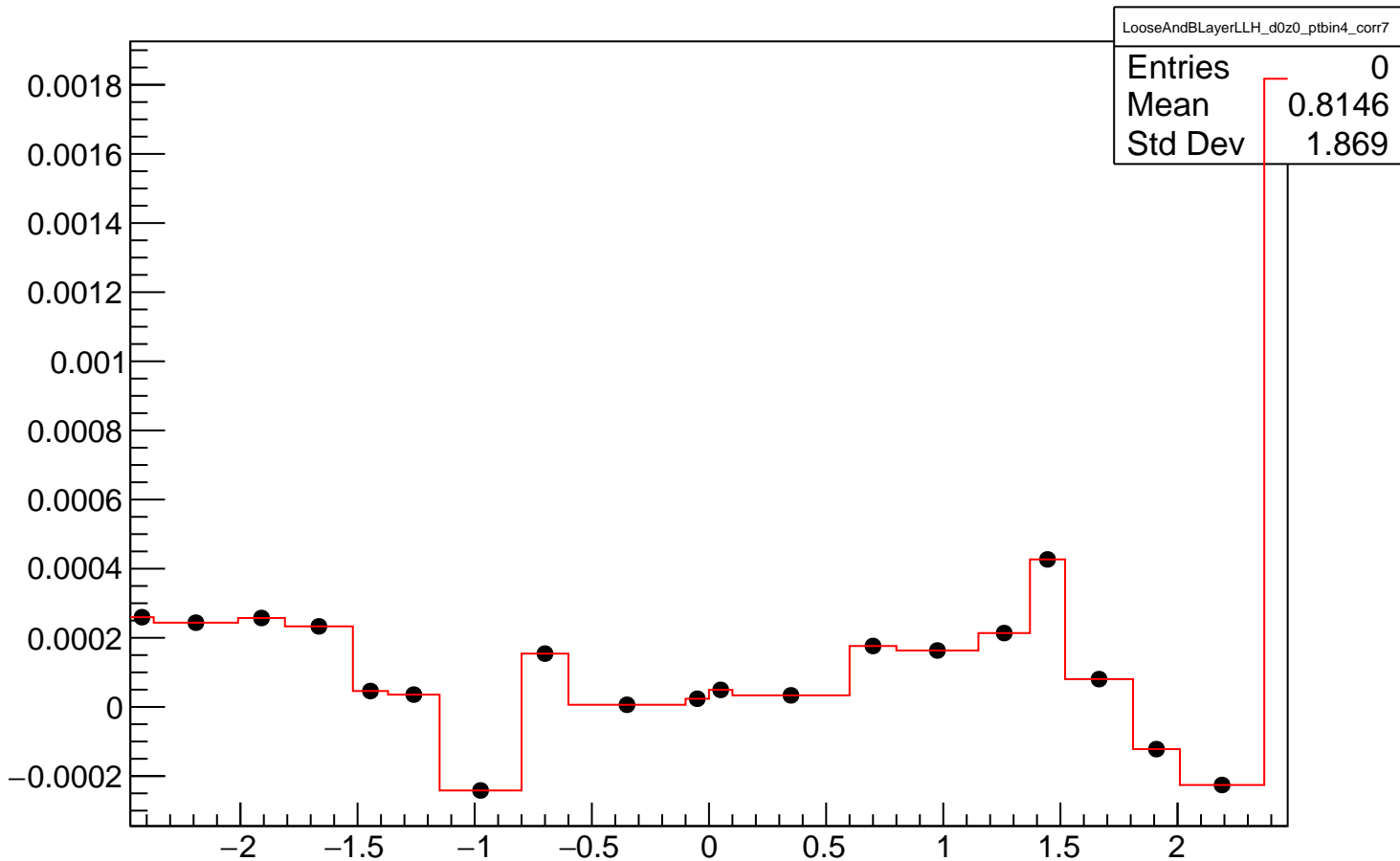
LooseAndBLayerLLH\_d0z0\_ptbin3\_corr7

Entries	0
Mean	-0.655
Std Dev	2.012

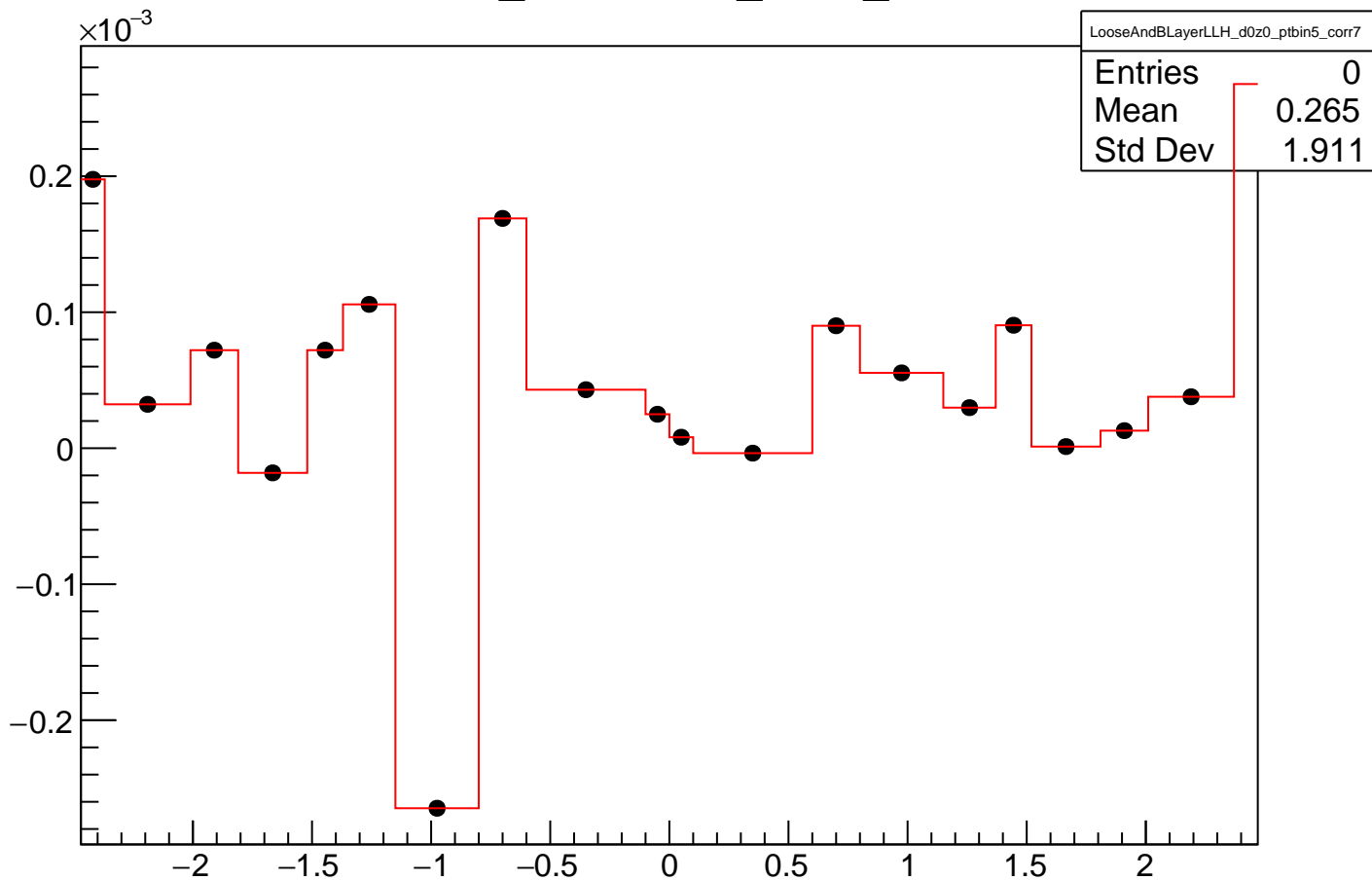




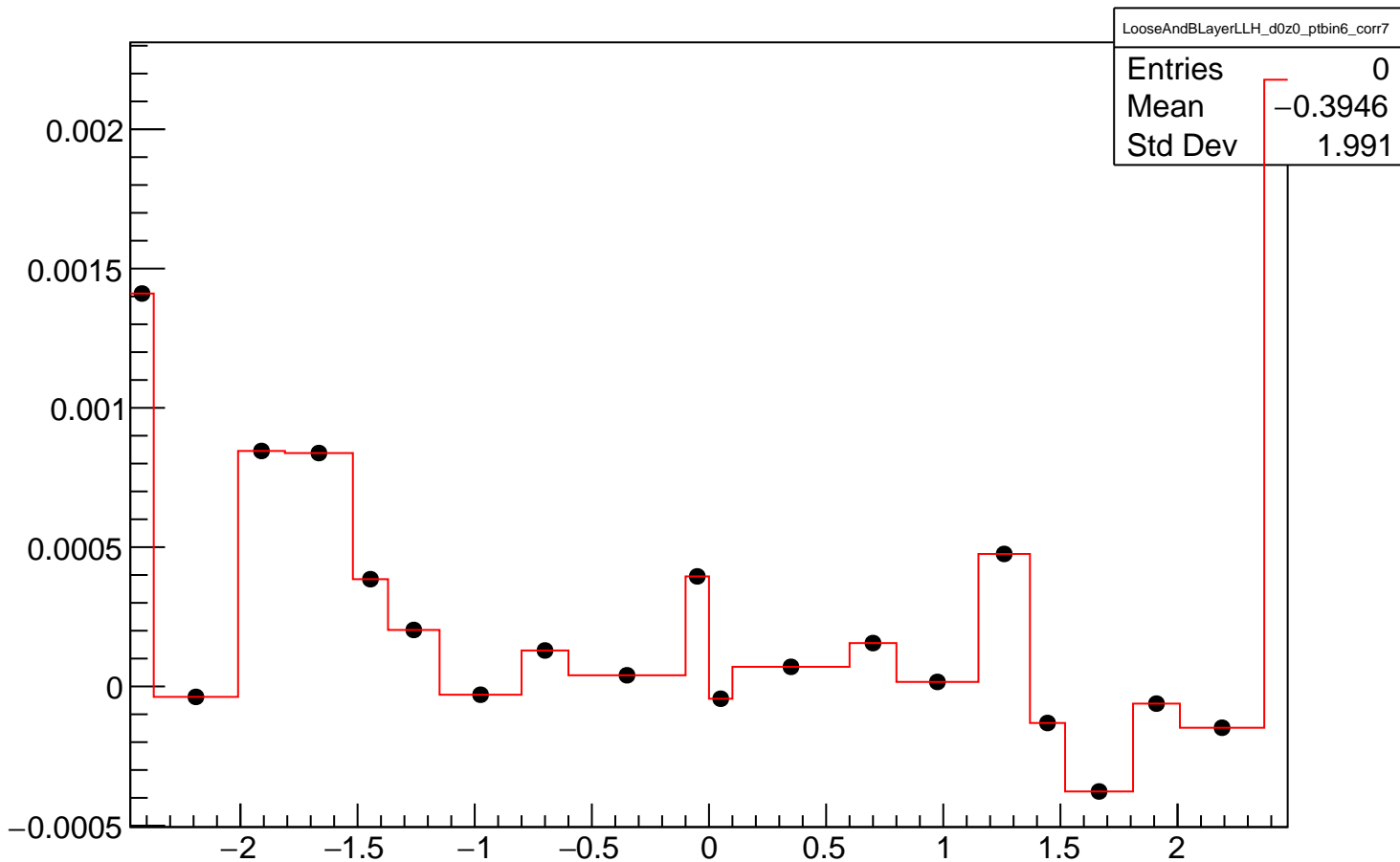
# FullSim\_LooseLLH\_d0z0\_corr7



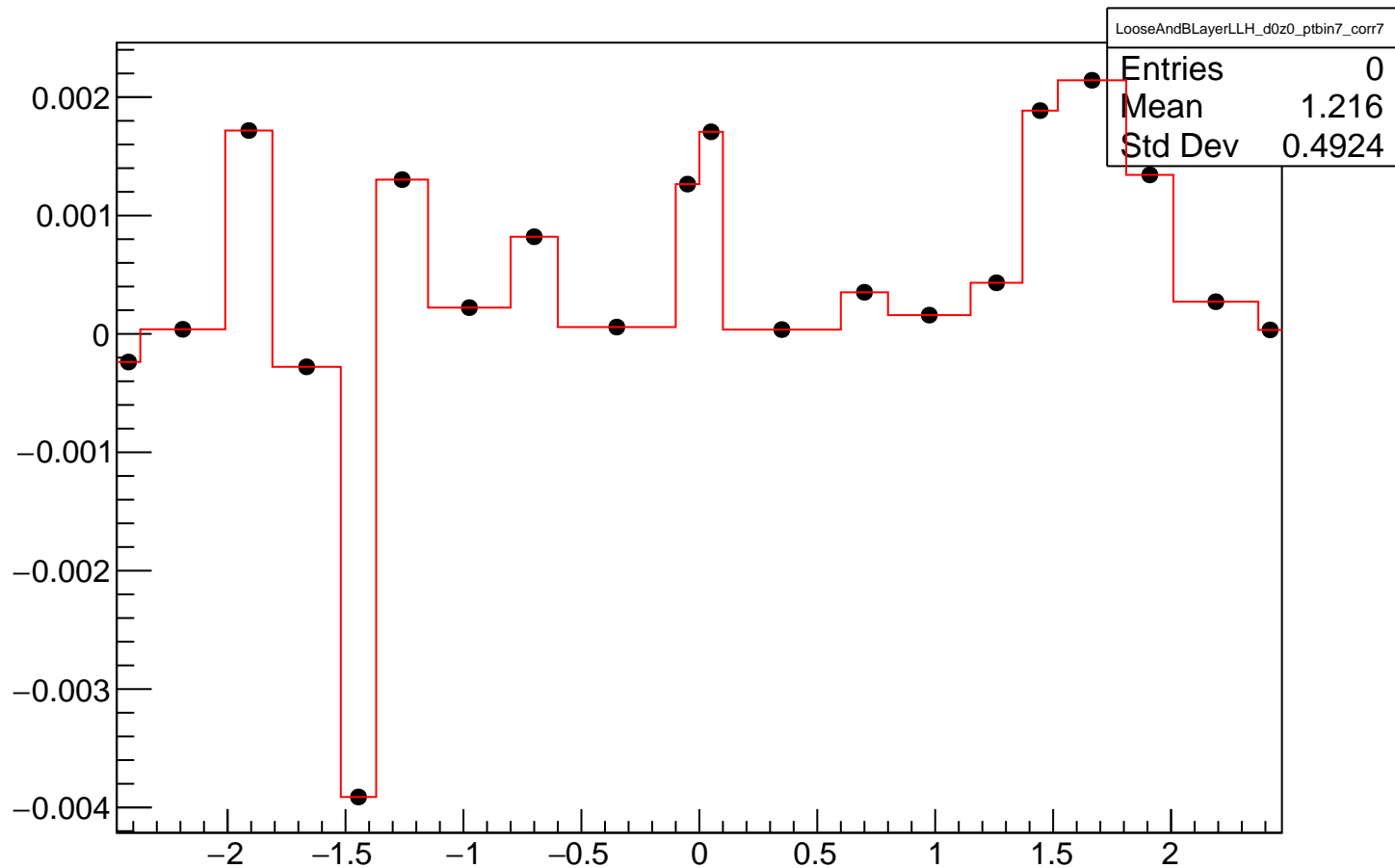
# FullSim\_LooseLLH\_d0z0\_corr7



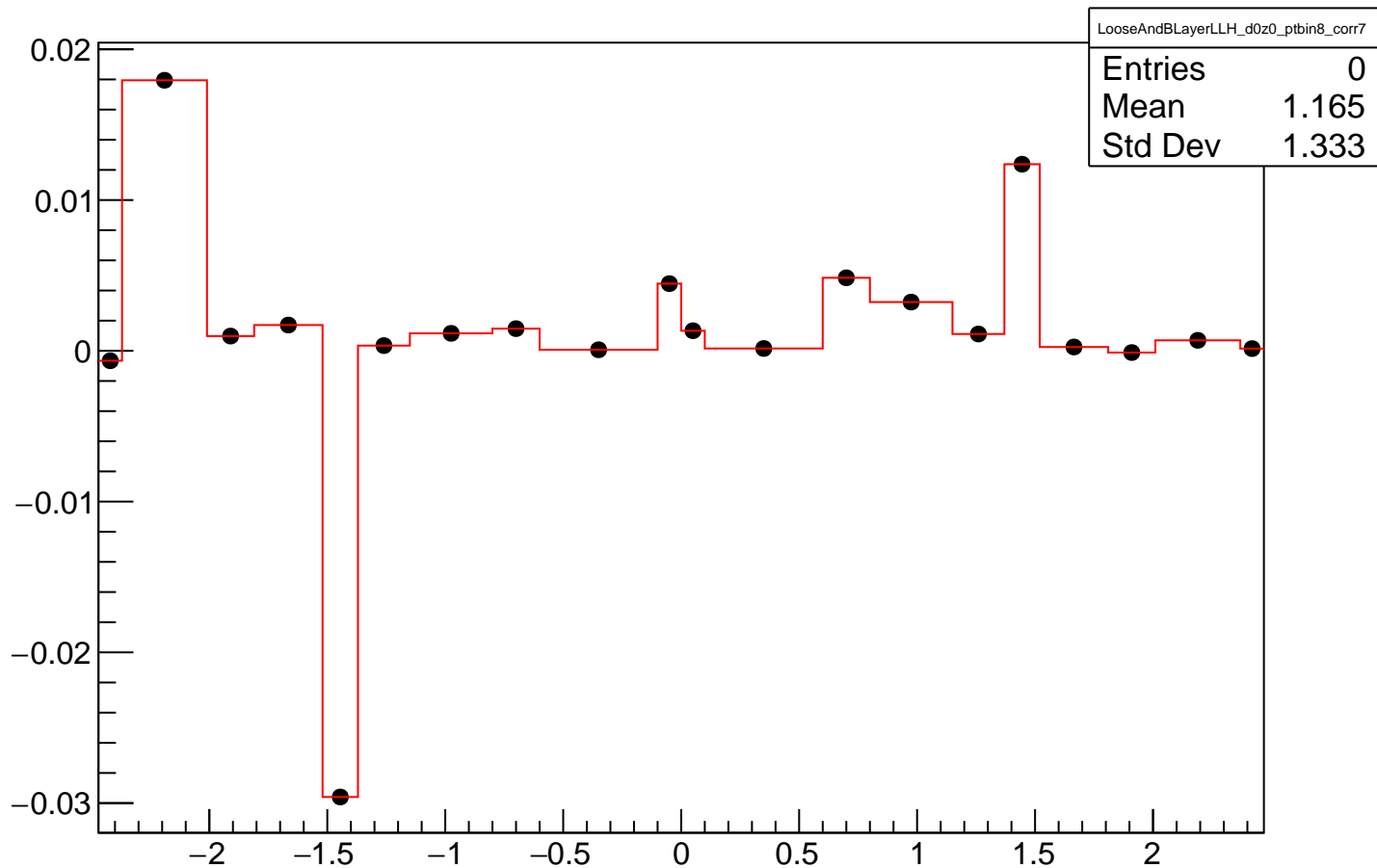
# FullSim\_LooseLLH\_d0z0\_corr7



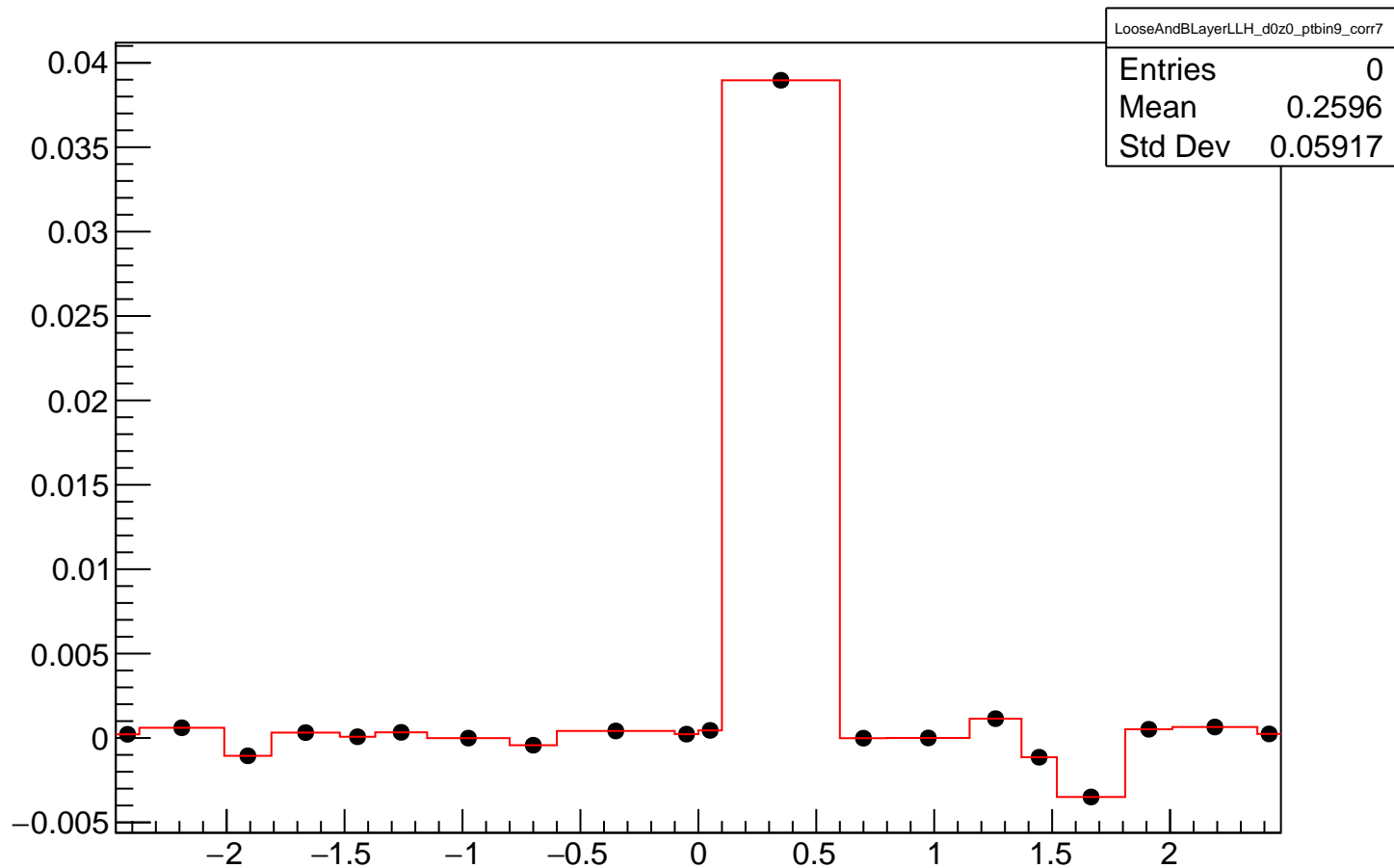
# FullSim\_LooseLLH\_d0z0\_corr7



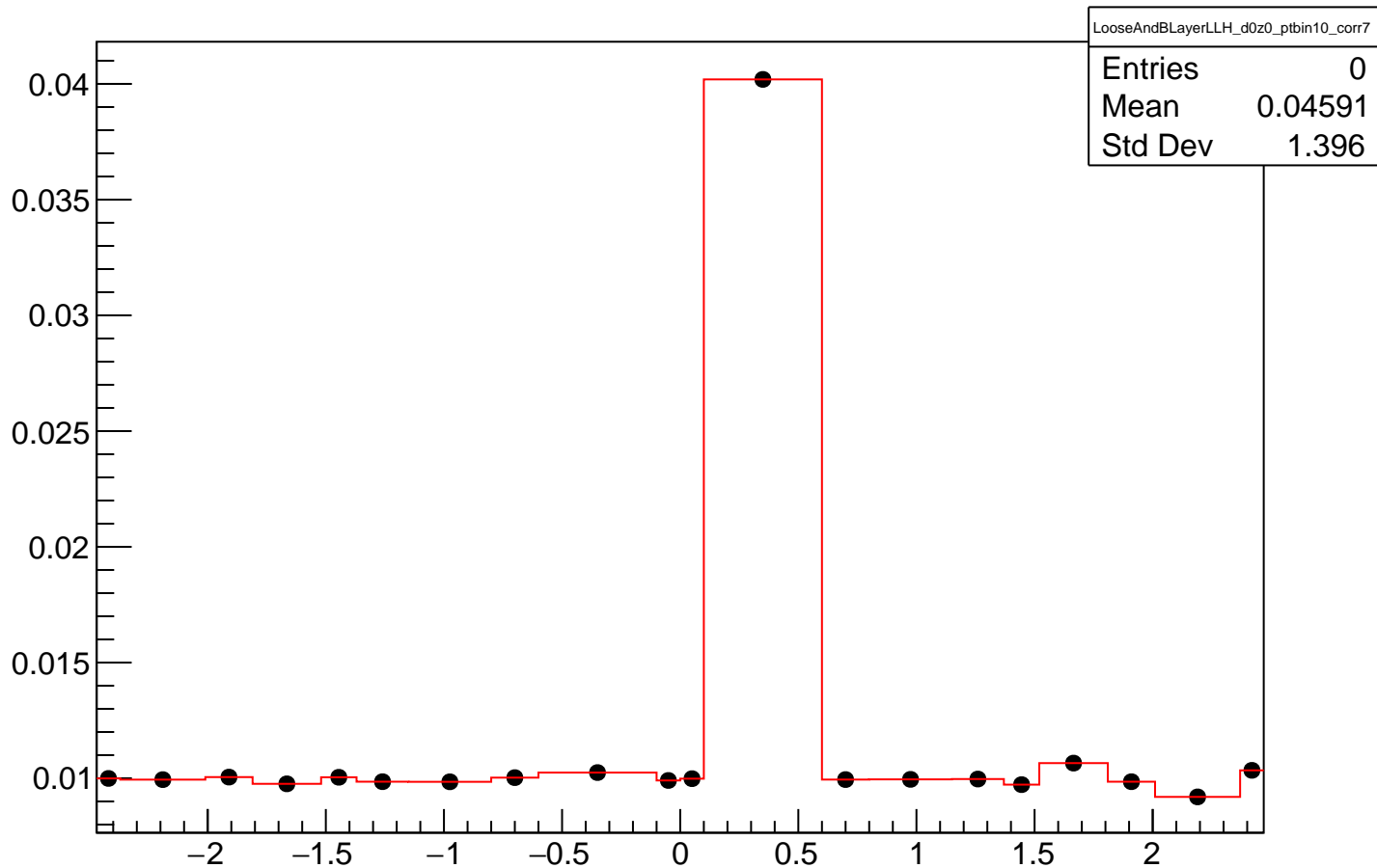
# FullSim\_LooseLLH\_d0z0\_corr7



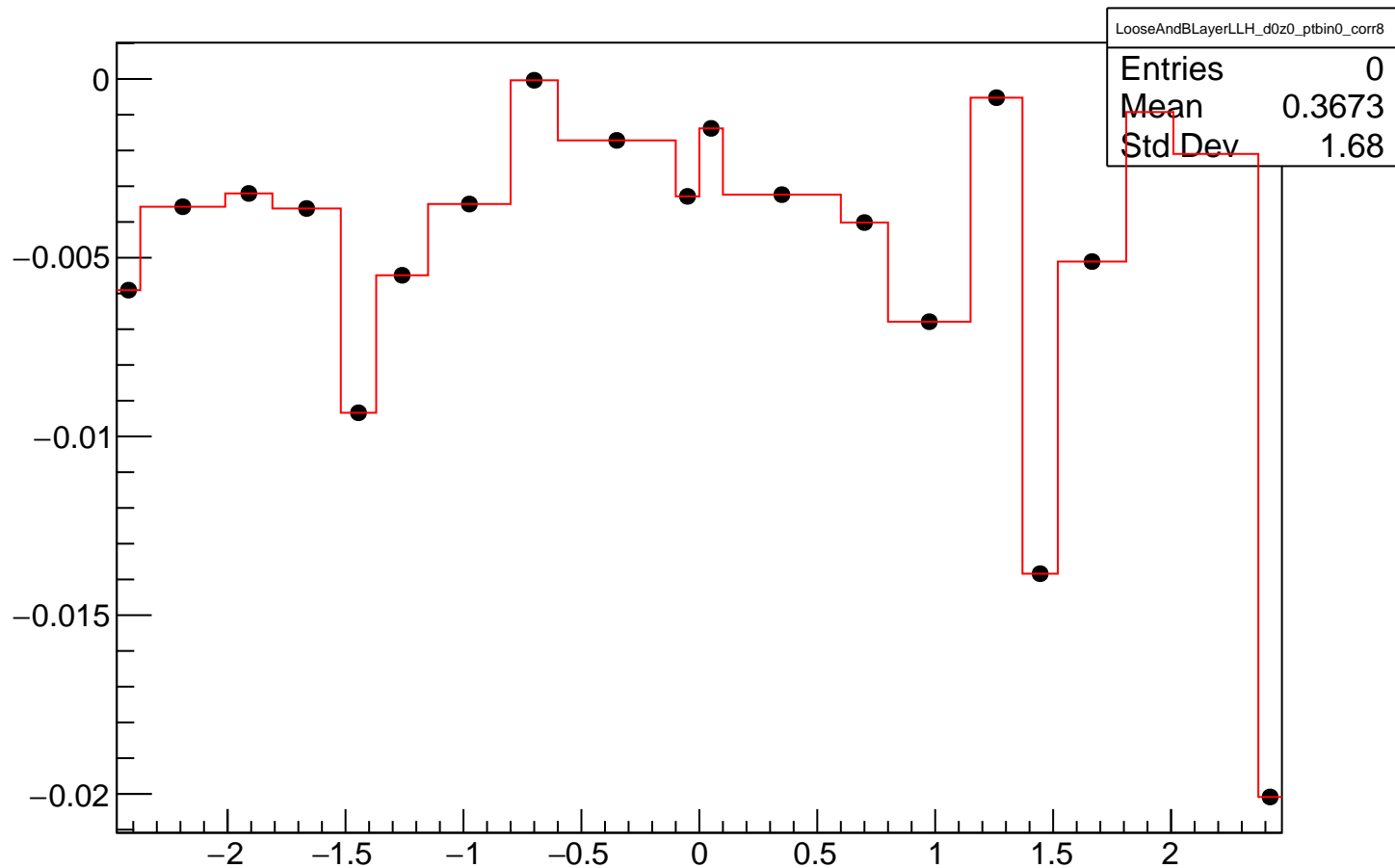
# FullSim\_LooseLLH\_d0z0\_corr7



# FullSim\_LooseLLH\_d0z0\_corr7

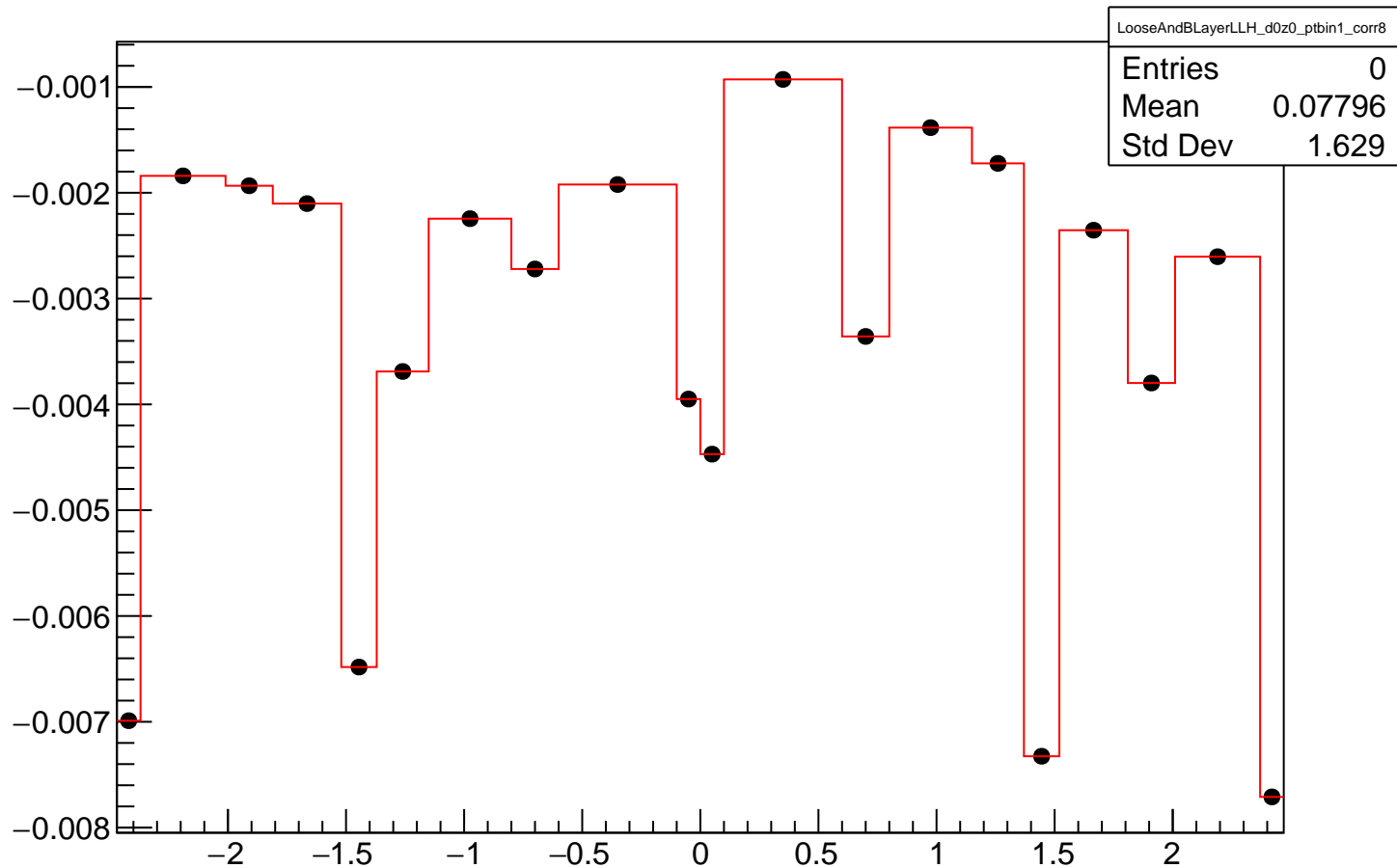


# FullSim\_LooseLLH\_d0z0\_corr8

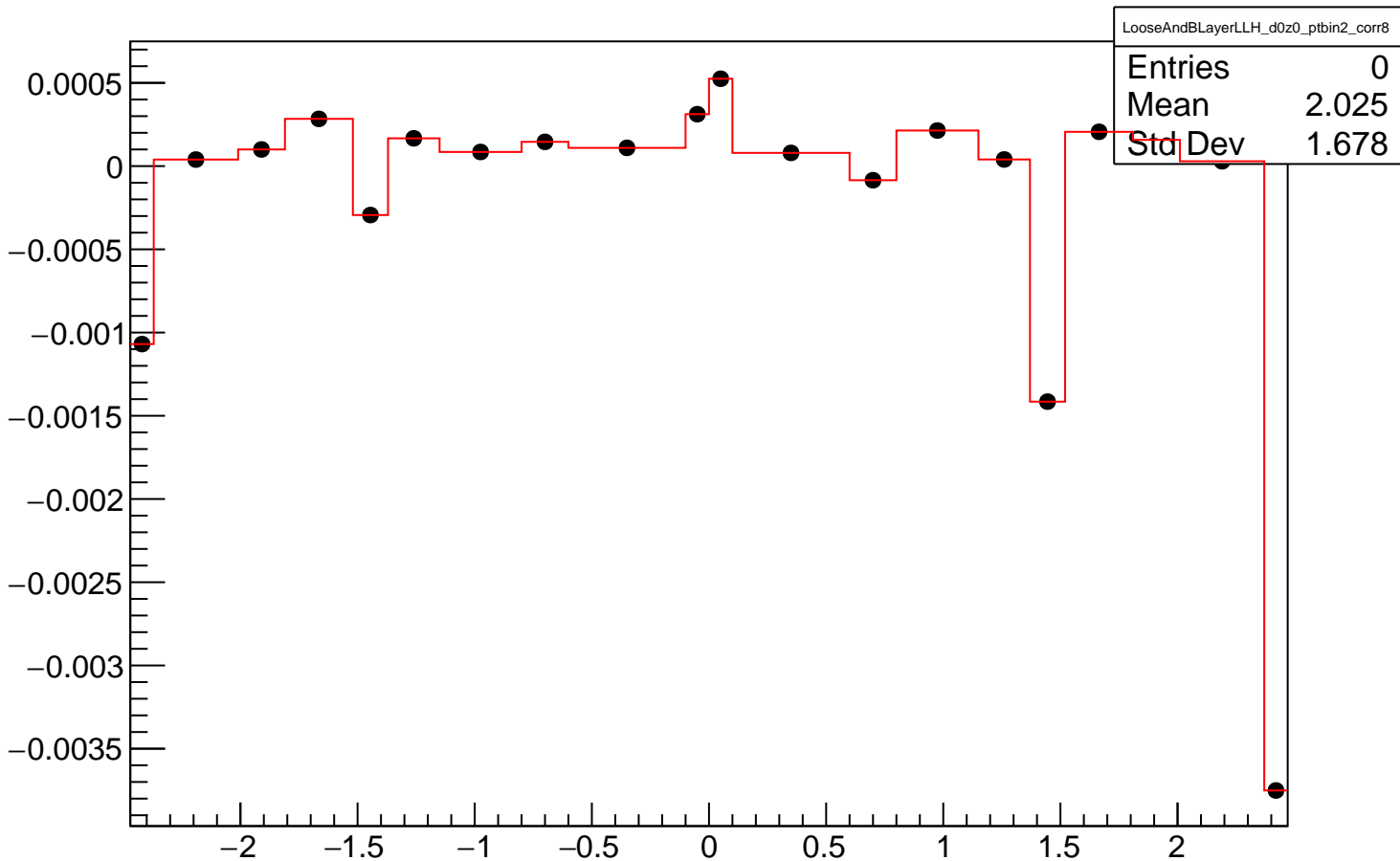




# FullSim\_LooseLLH\_d0z0\_corr8



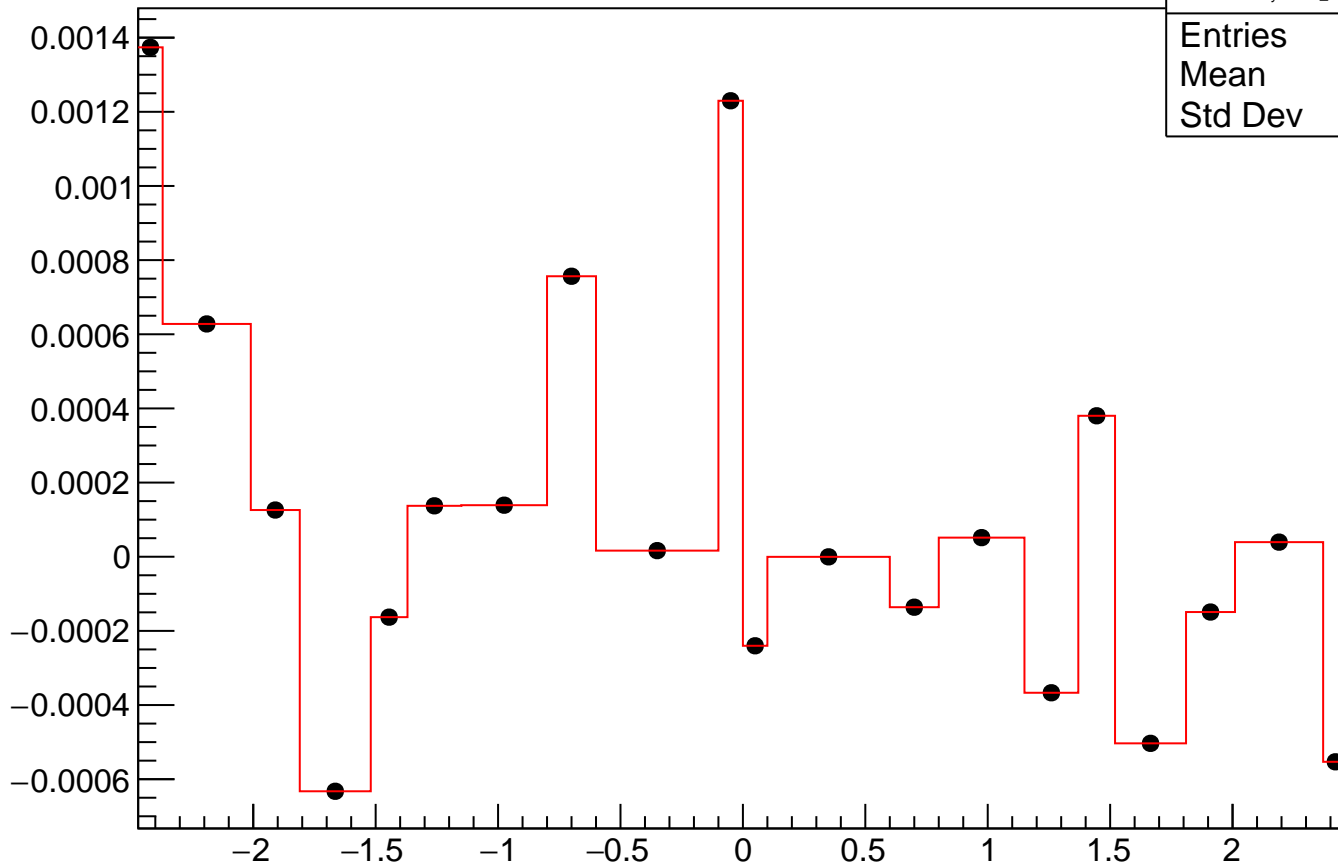
# FullSim\_LooseLLH\_d0z0\_corr8



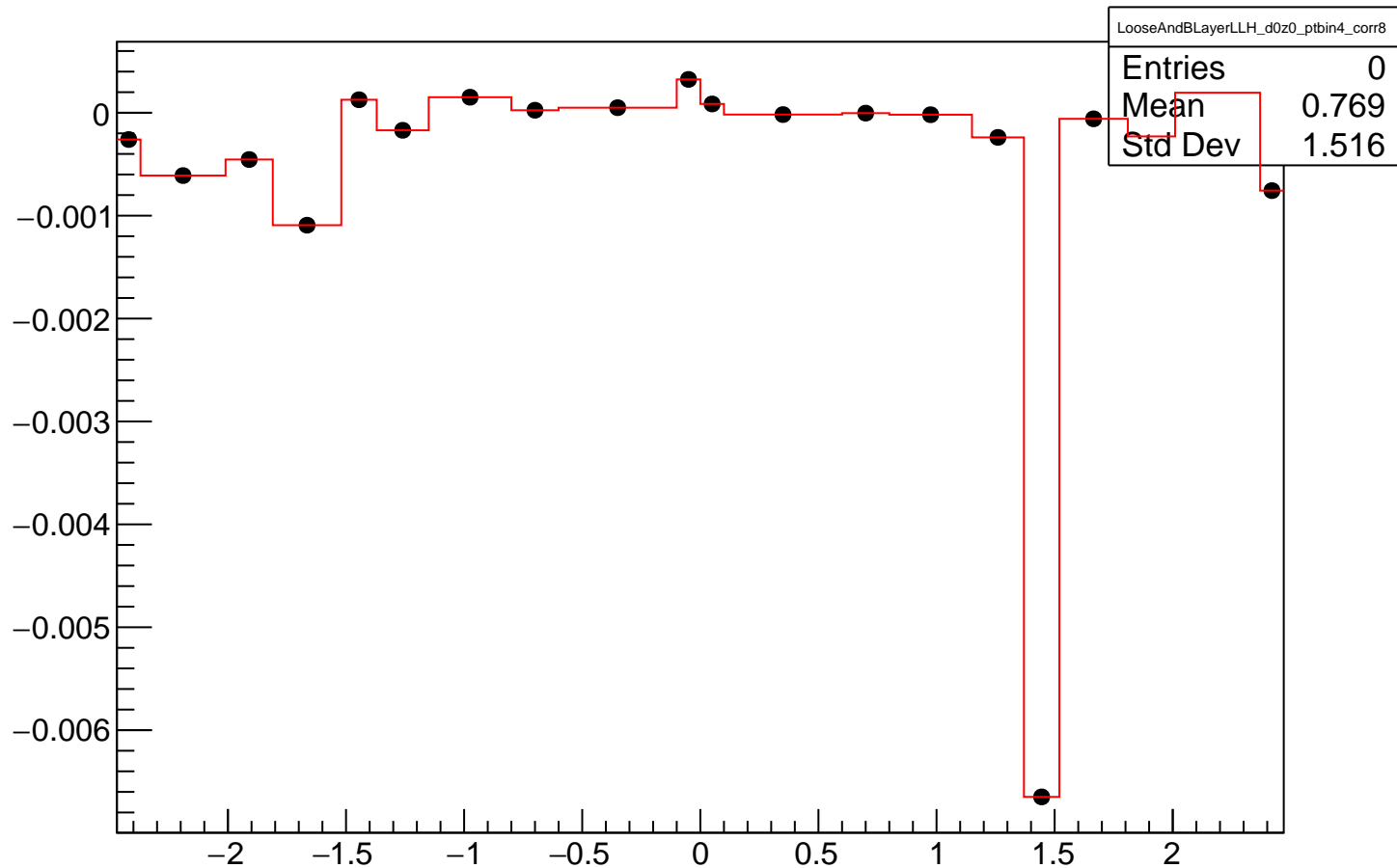
# FullSim\_LooseLLH\_d0z0\_corr8

LooseAndBLayerLLH\_d0z0\_ptbin3\_corr8

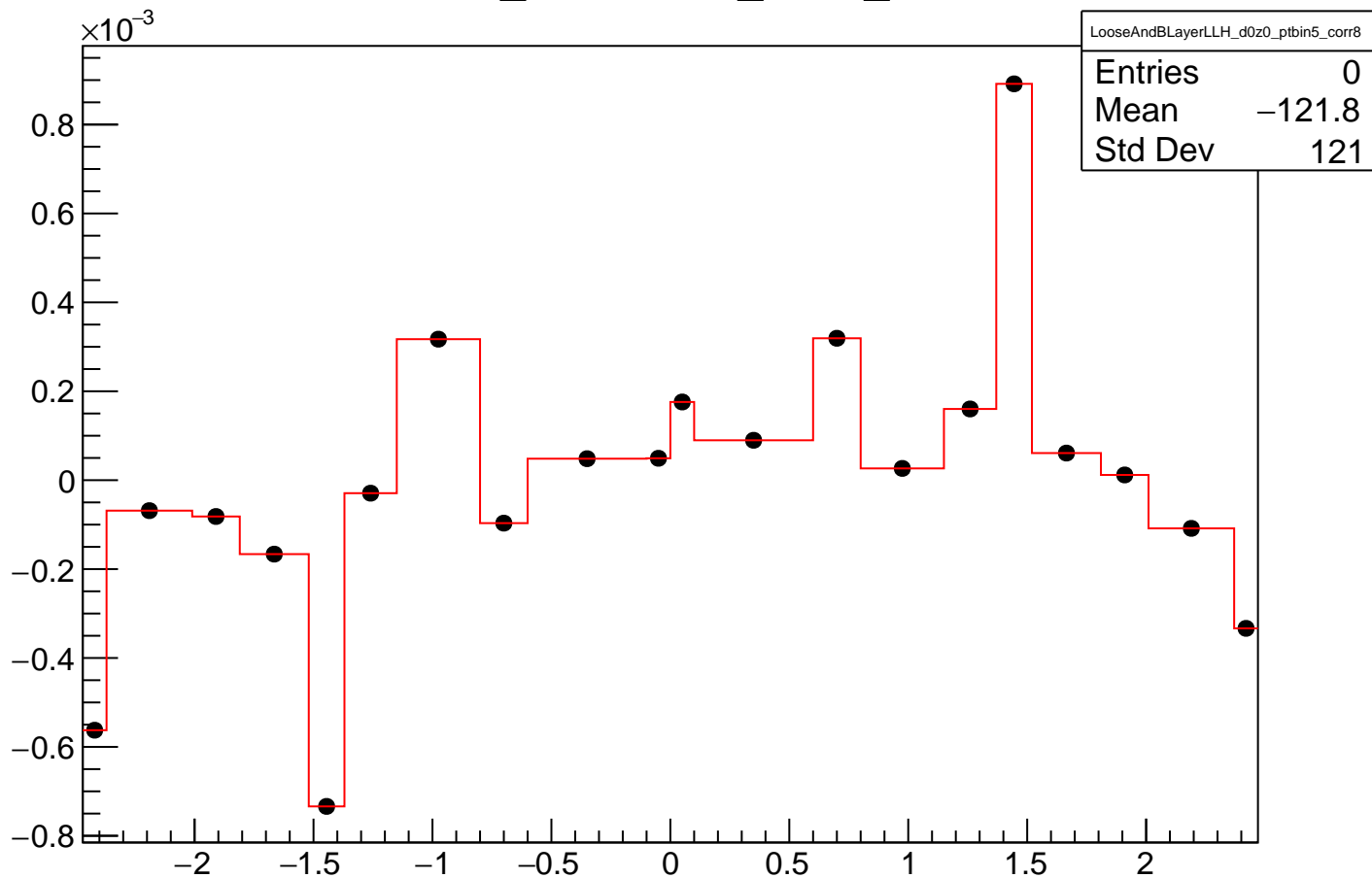
Entries	0
Mean	-3.236
Std Dev	2.821



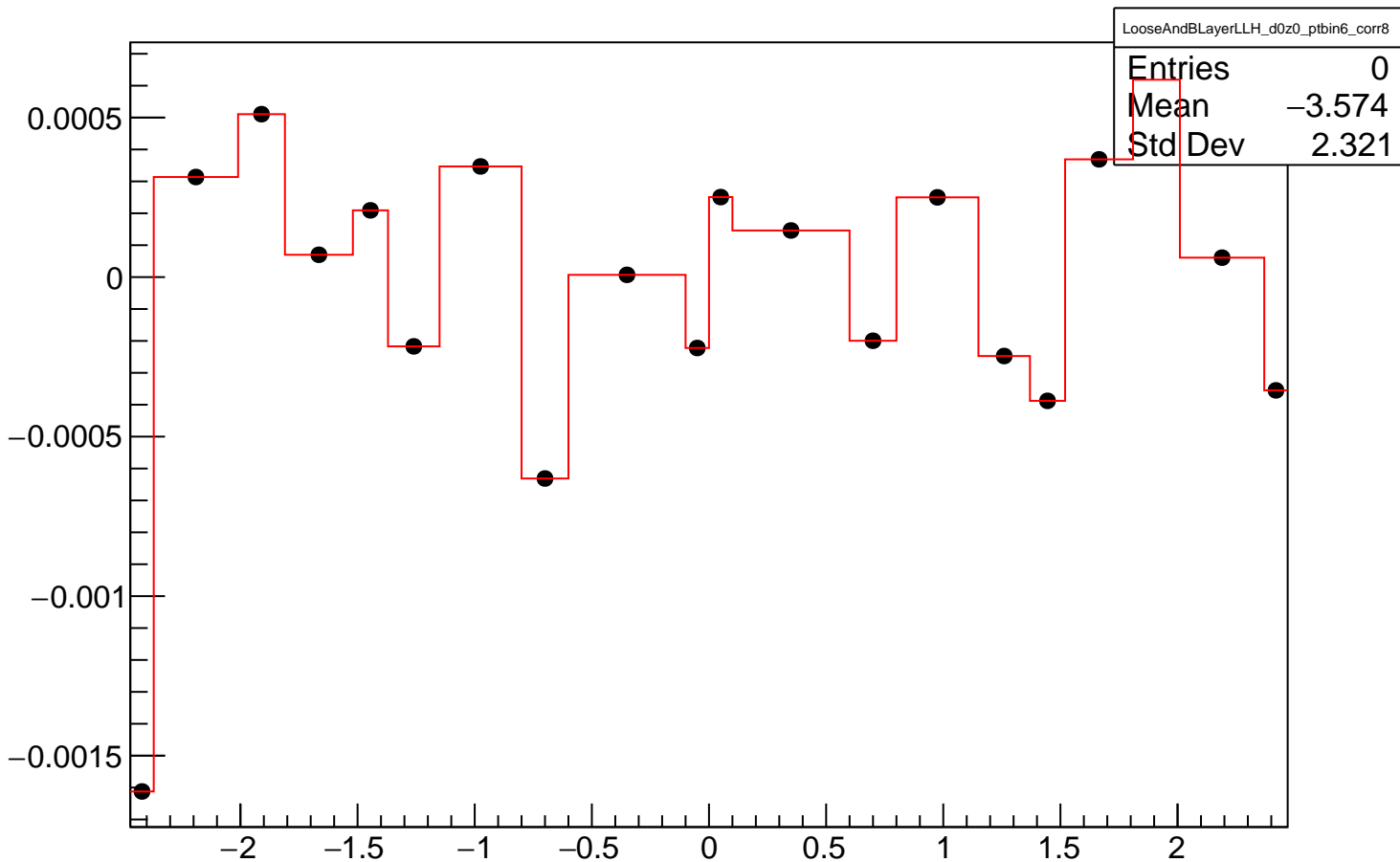
# FullSim\_LooseLLH\_d0z0\_corr8



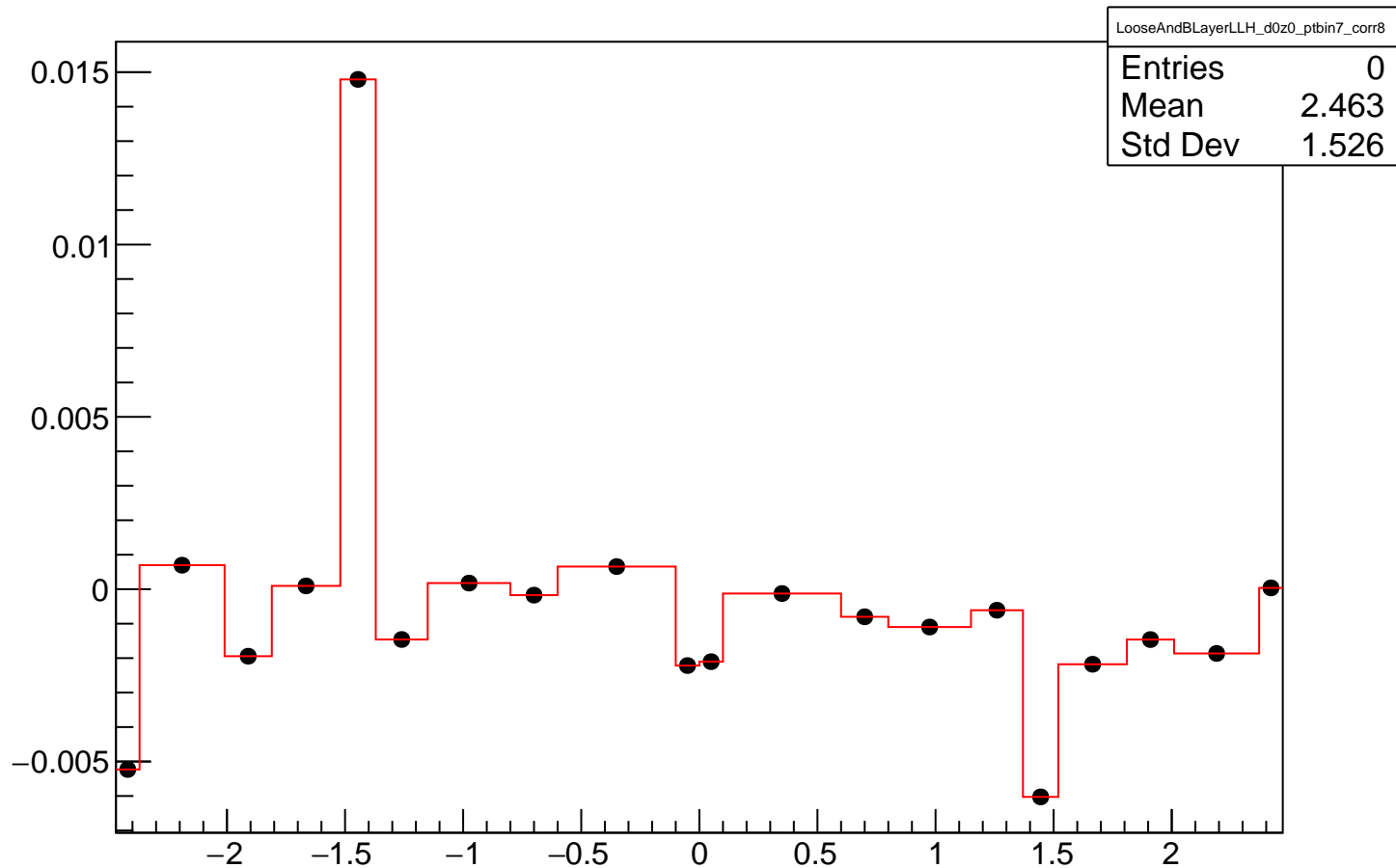
# FullSim\_LooseLLH\_d0z0\_corr8



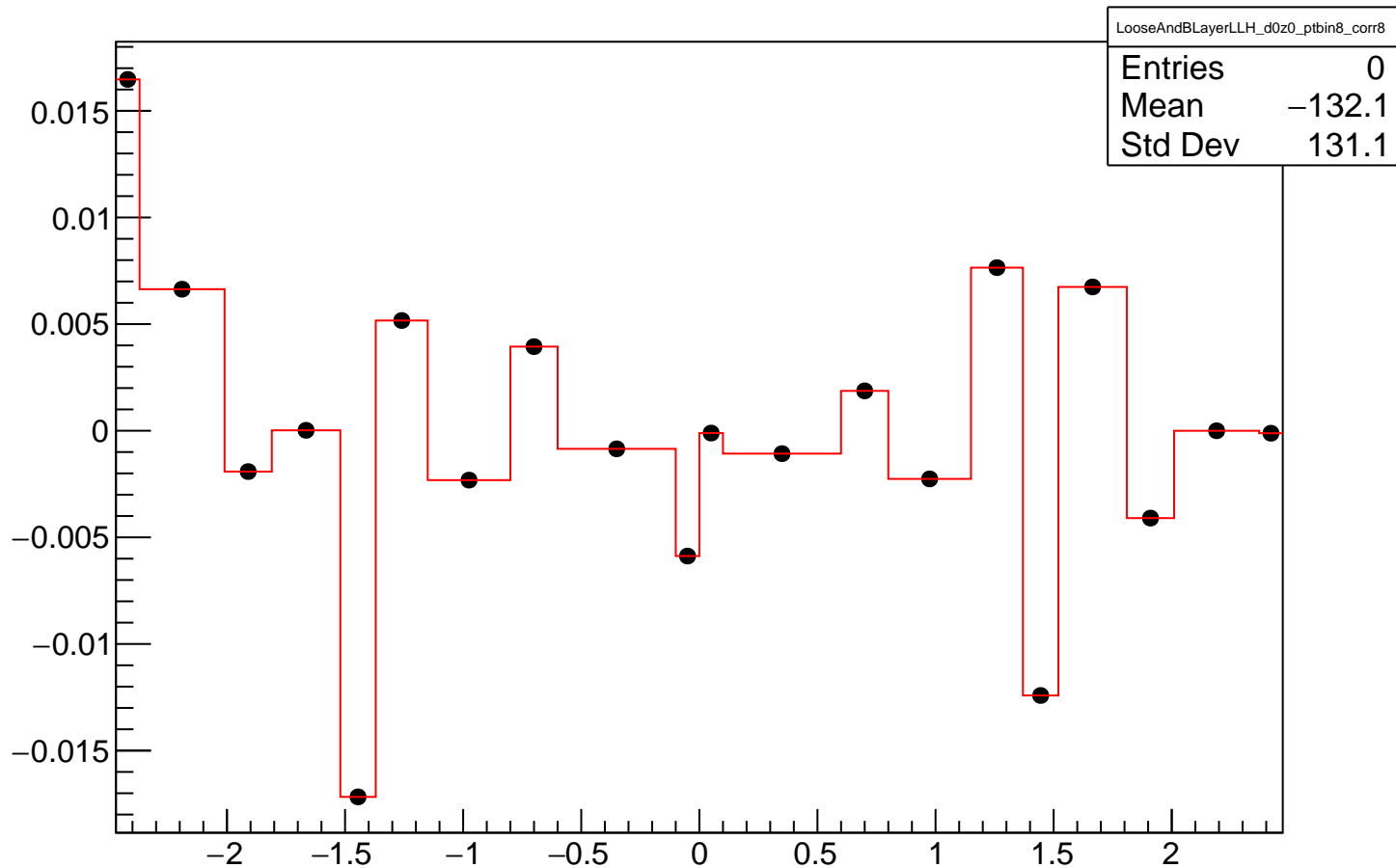
# FullSim\_LooseLLH\_d0z0\_corr8



# FullSim\_LooseLLH\_d0z0\_corr8

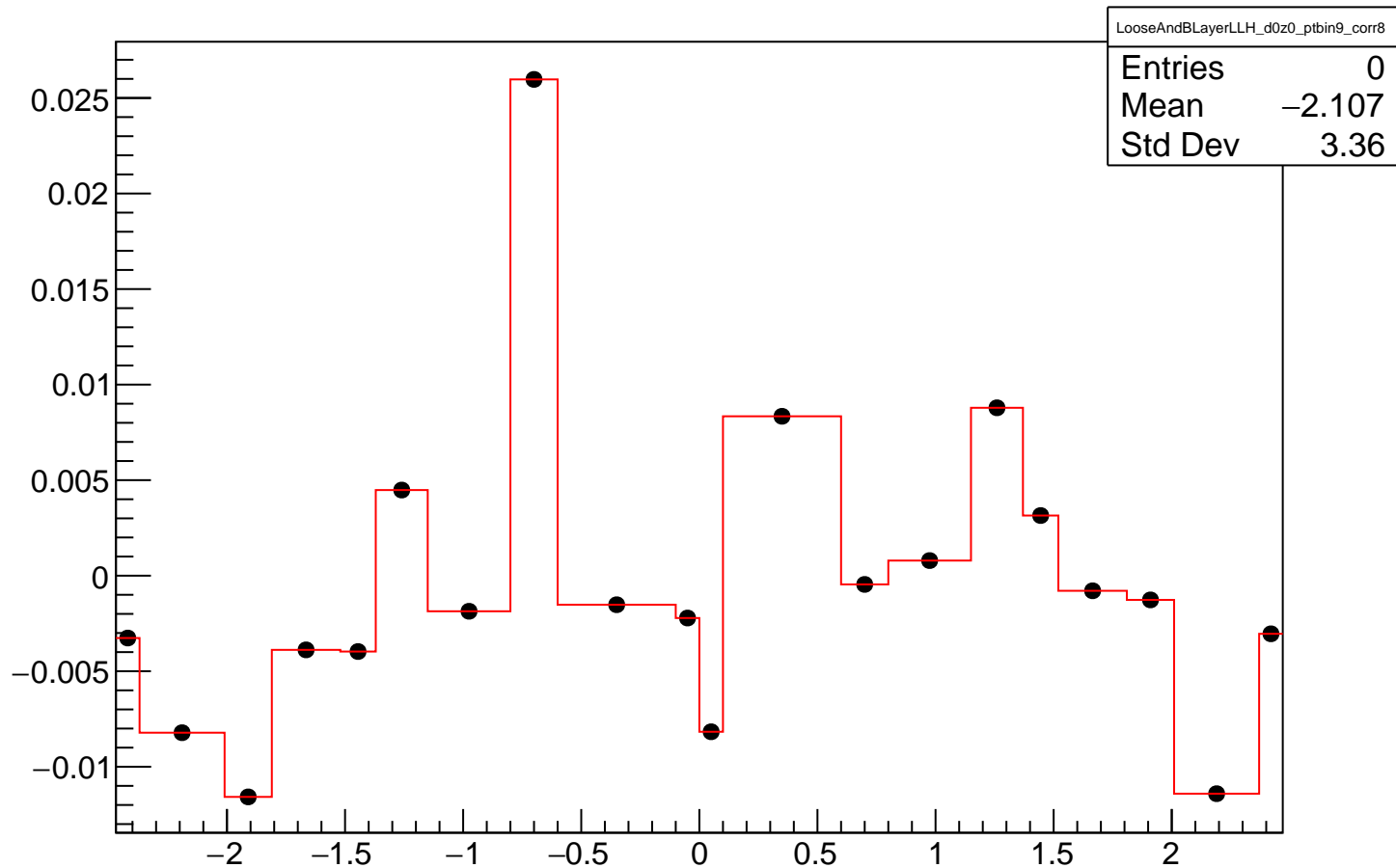


# FullSim\_LooseLLH\_d0z0\_corr8

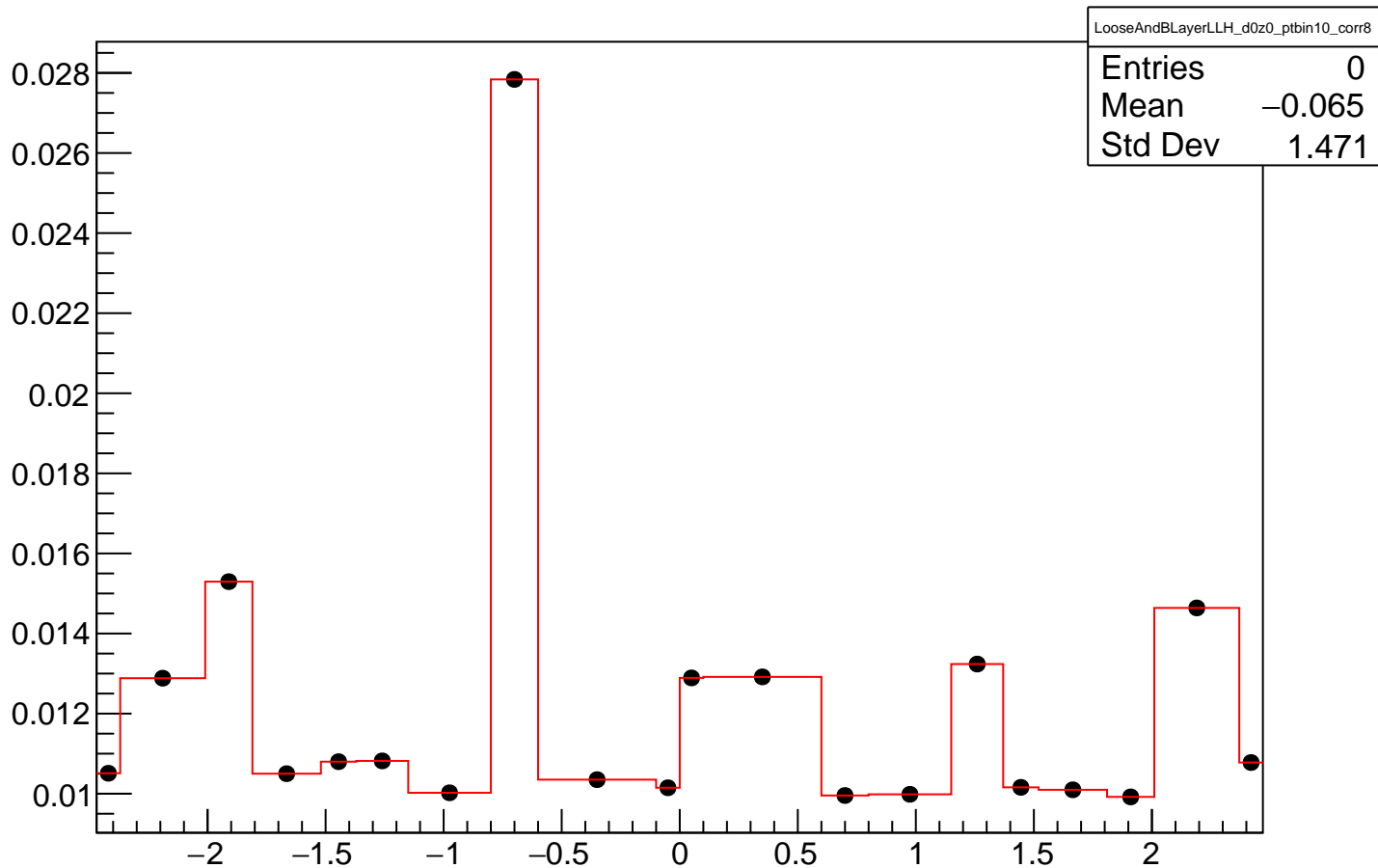




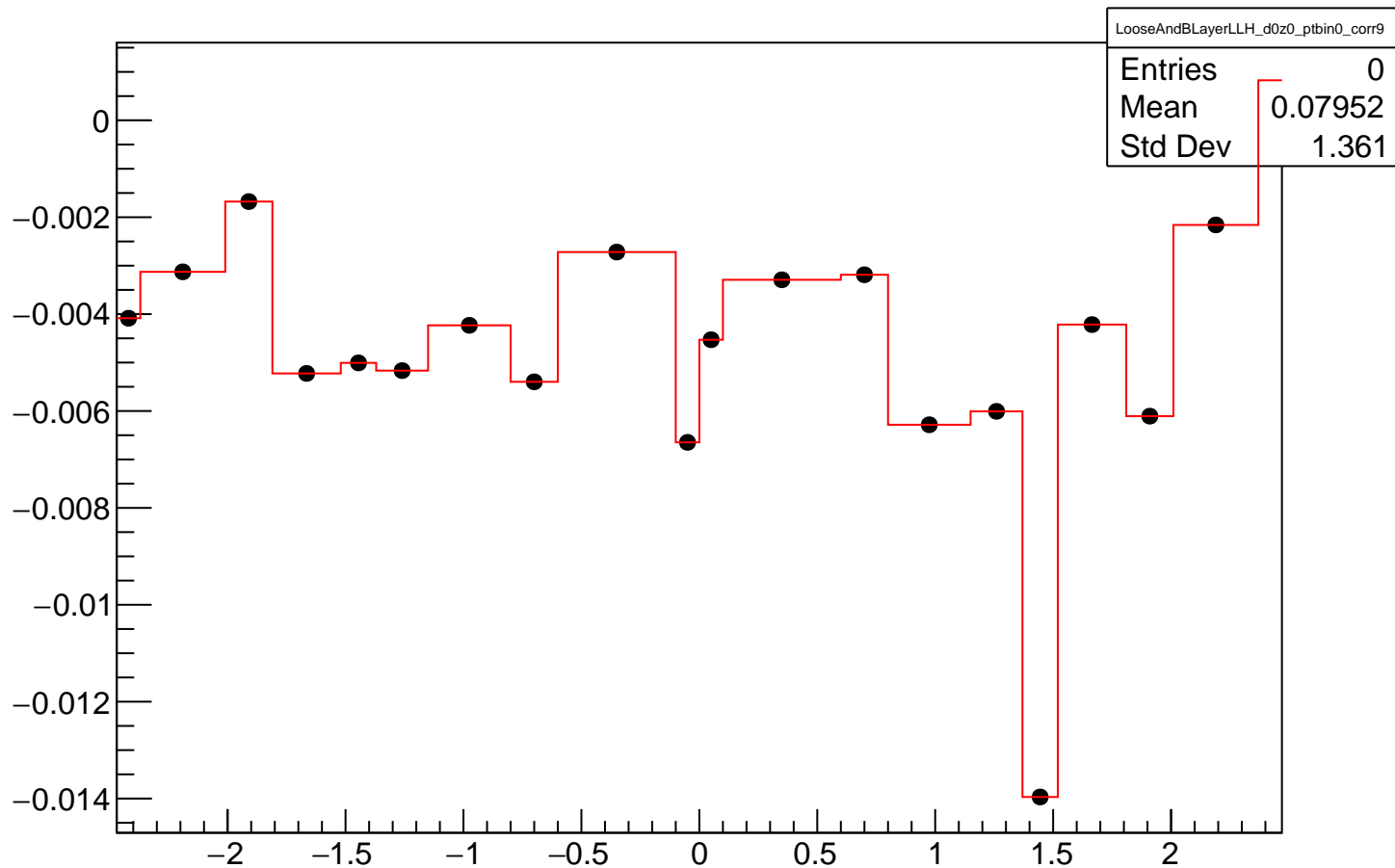
# FullSim\_LooseLLH\_d0z0\_corr8



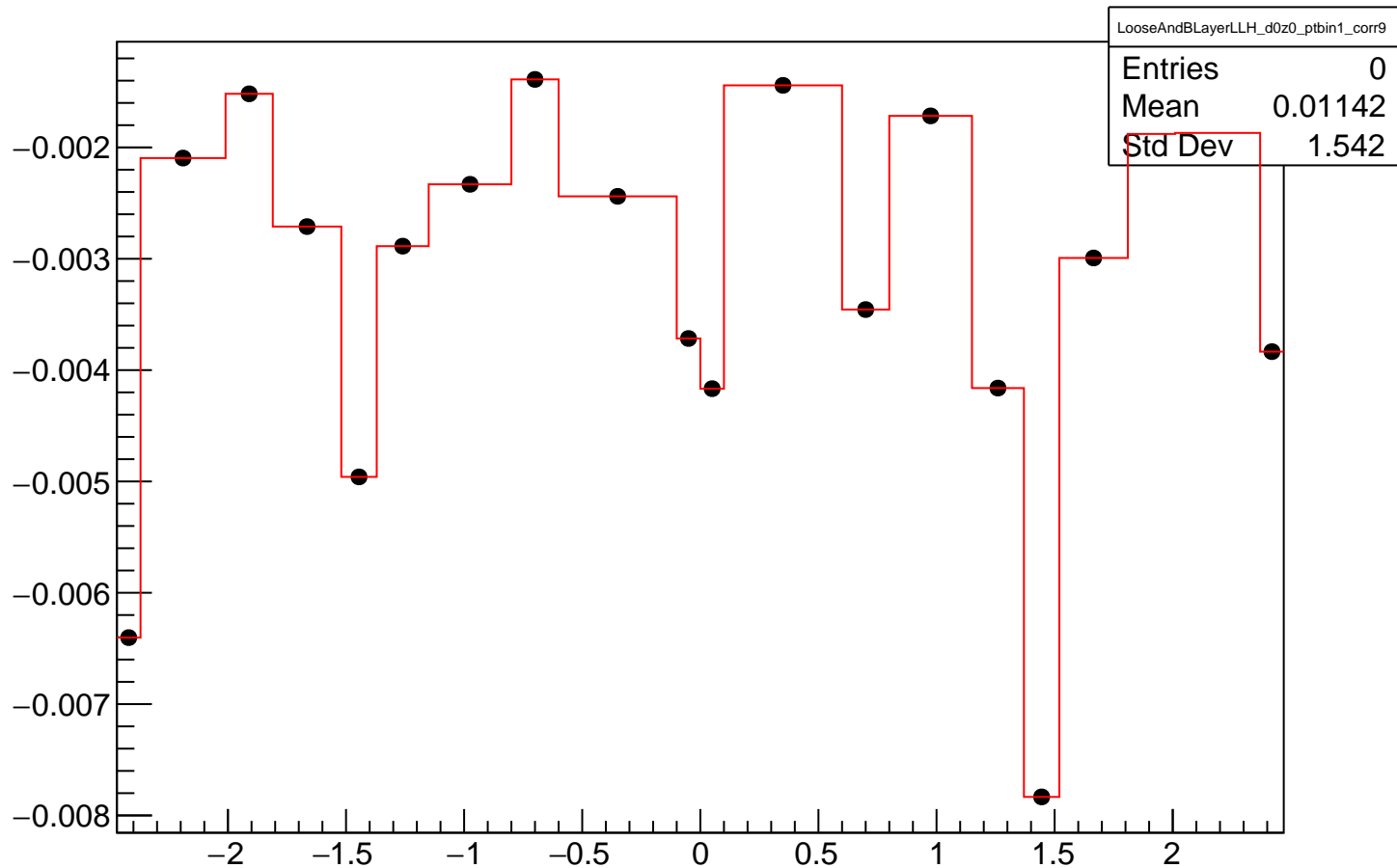
# FullSim\_LooseLLH\_d0z0\_corr8



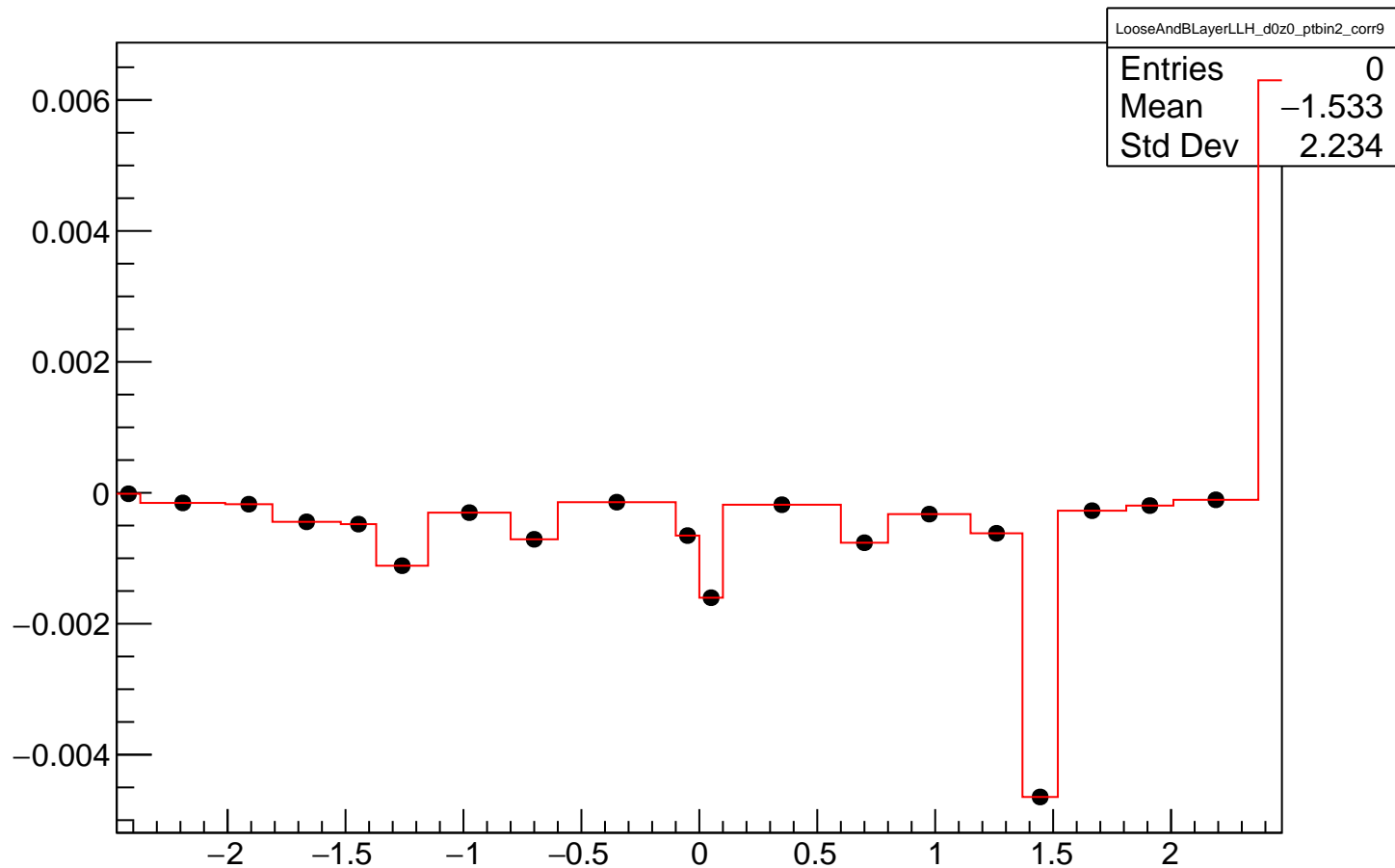
# FullSim\_LooseLLH\_d0z0\_corr9



# FullSim\_LooseLLH\_d0z0\_corr9



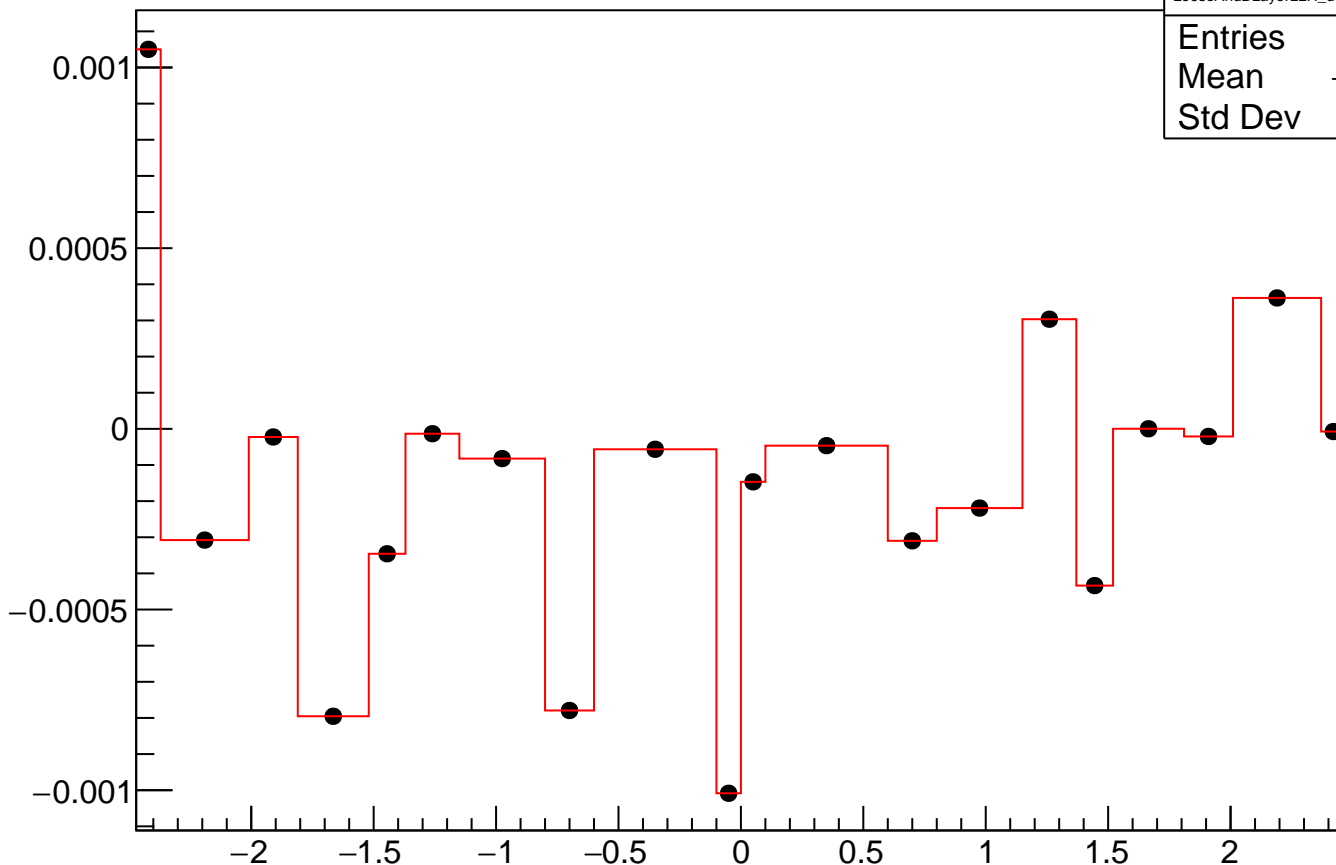
# FullSim\_LooseLLH\_d0z0\_corr9



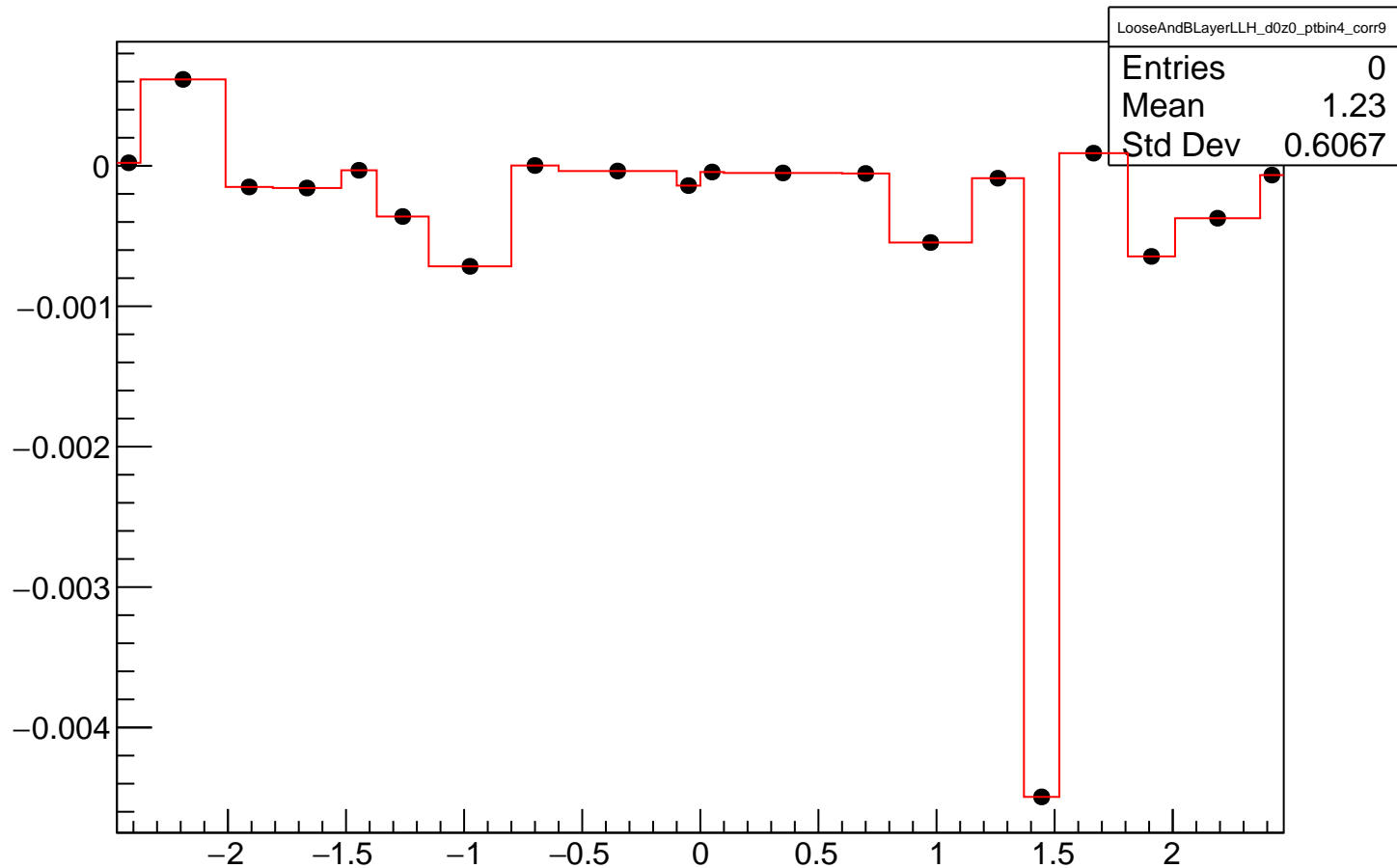
# FullSim\_LooseLLH\_d0z0\_corr9

LooseAndBLayerLLH\_d0z0\_ptbin3\_corr9

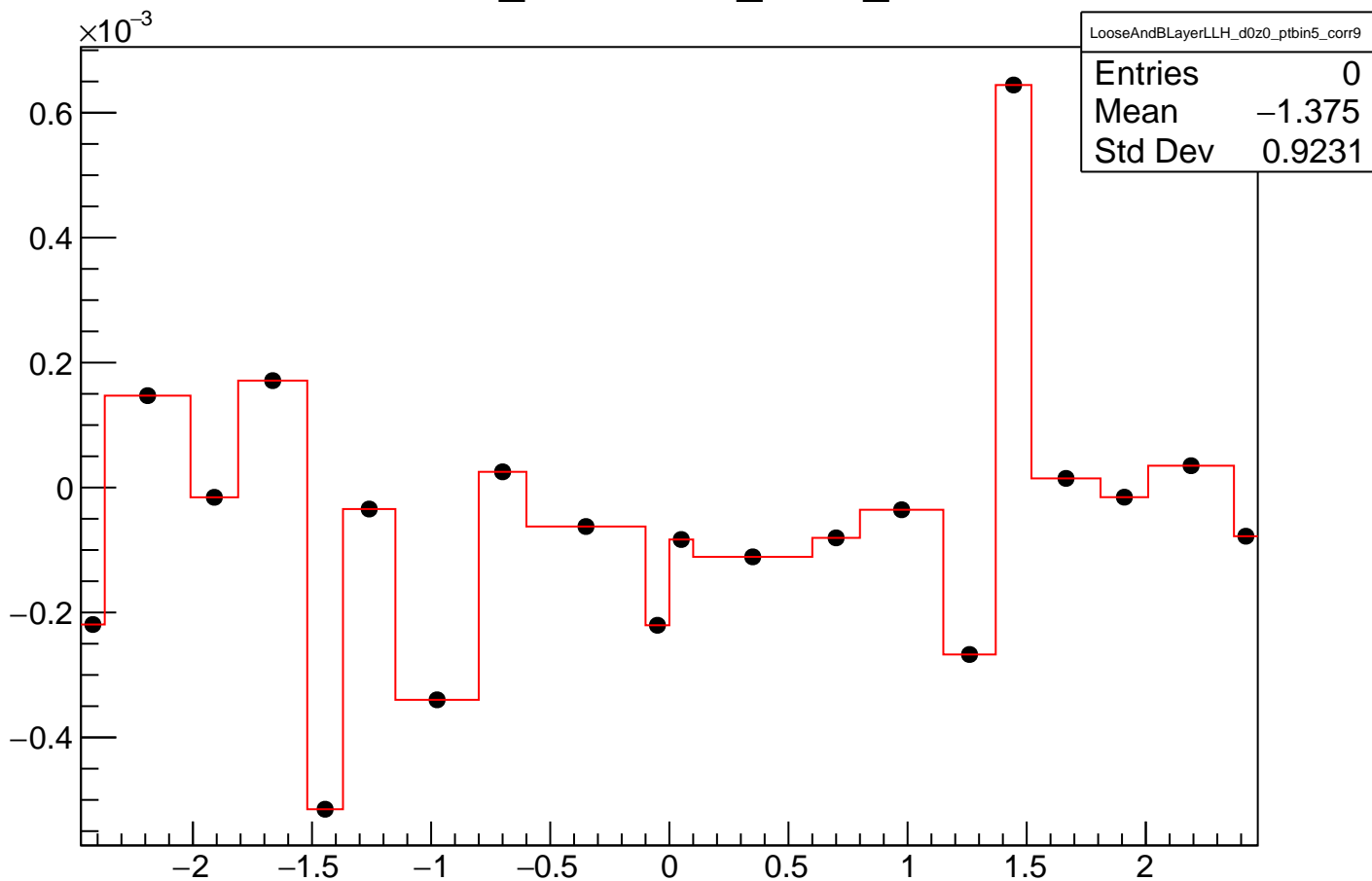
Entries	0
Mean	-0.2601
Std Dev	0.8736



# FullSim\_LooseLLH\_d0z0\_corr9

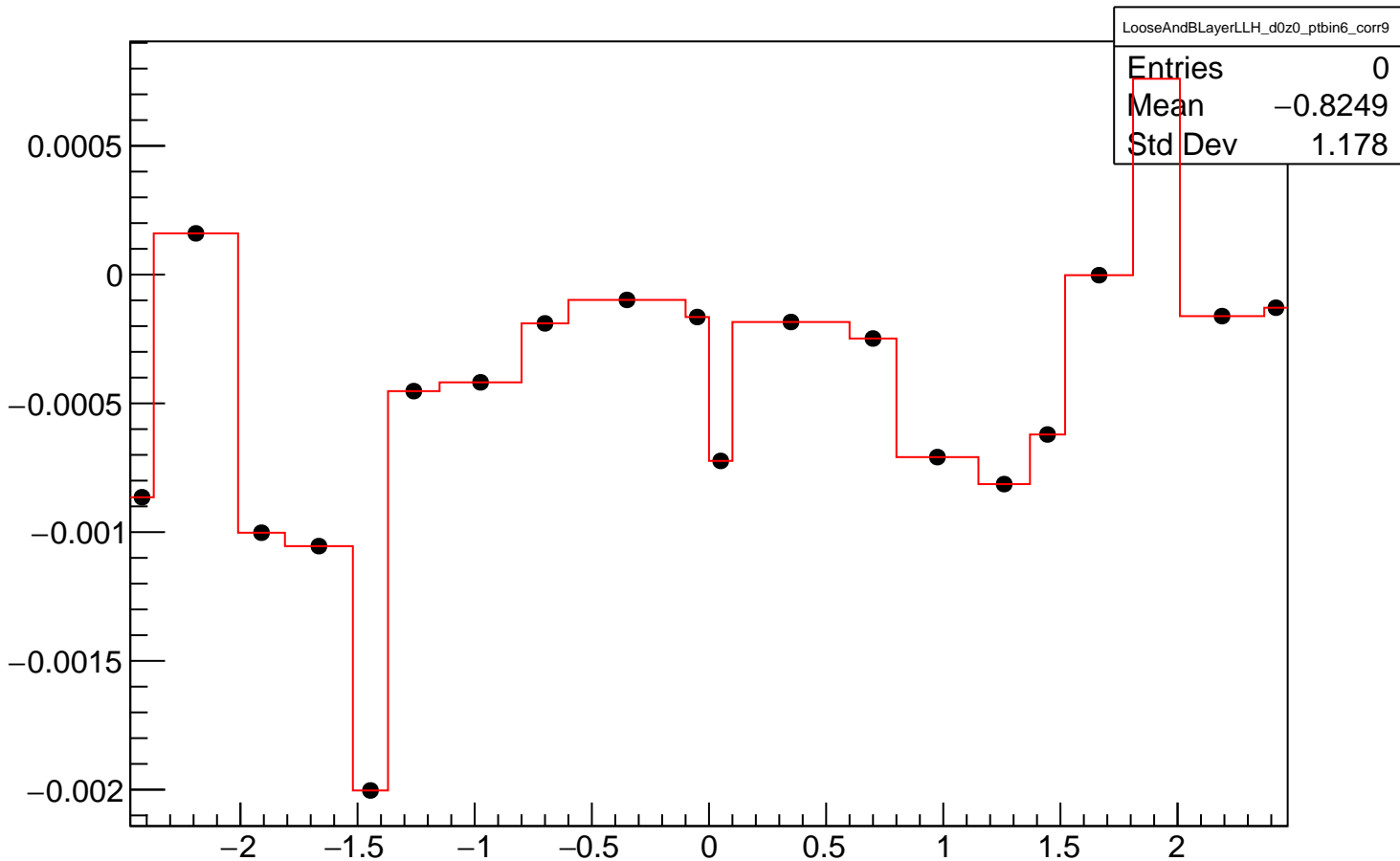


# FullSim\_LooseLLH\_d0z0\_corr9

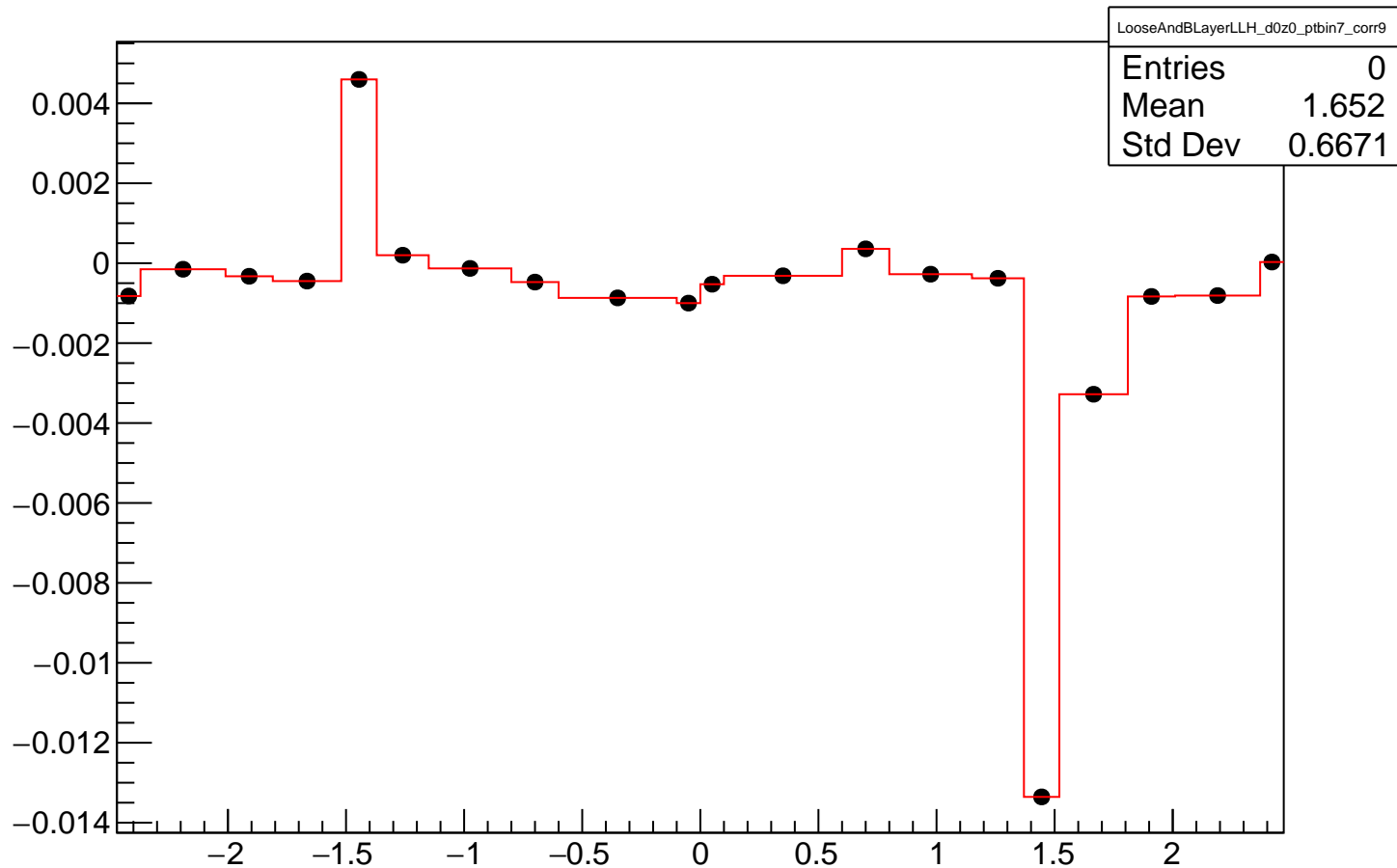




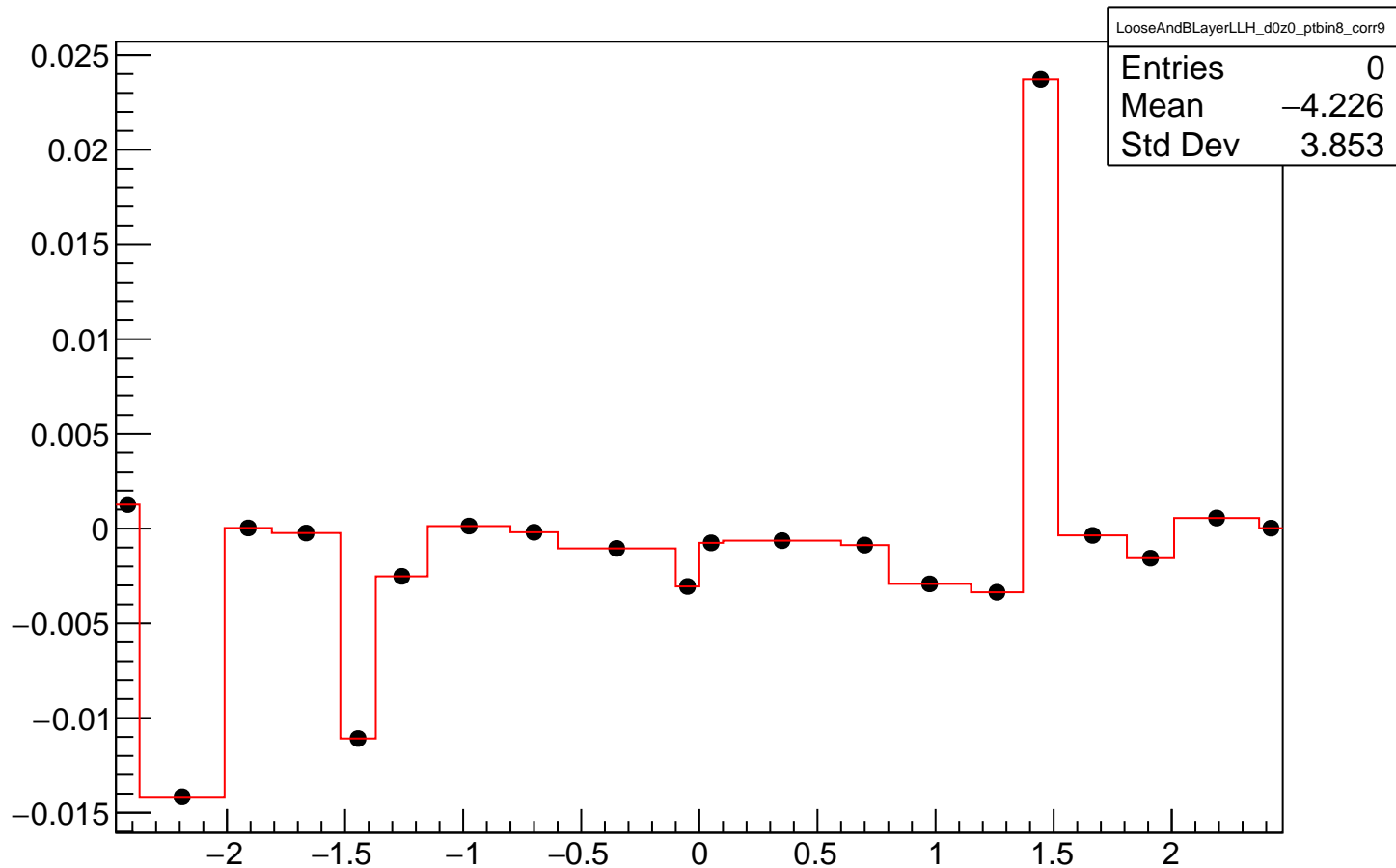
# FullSim\_LooseLLH\_d0z0\_corr9



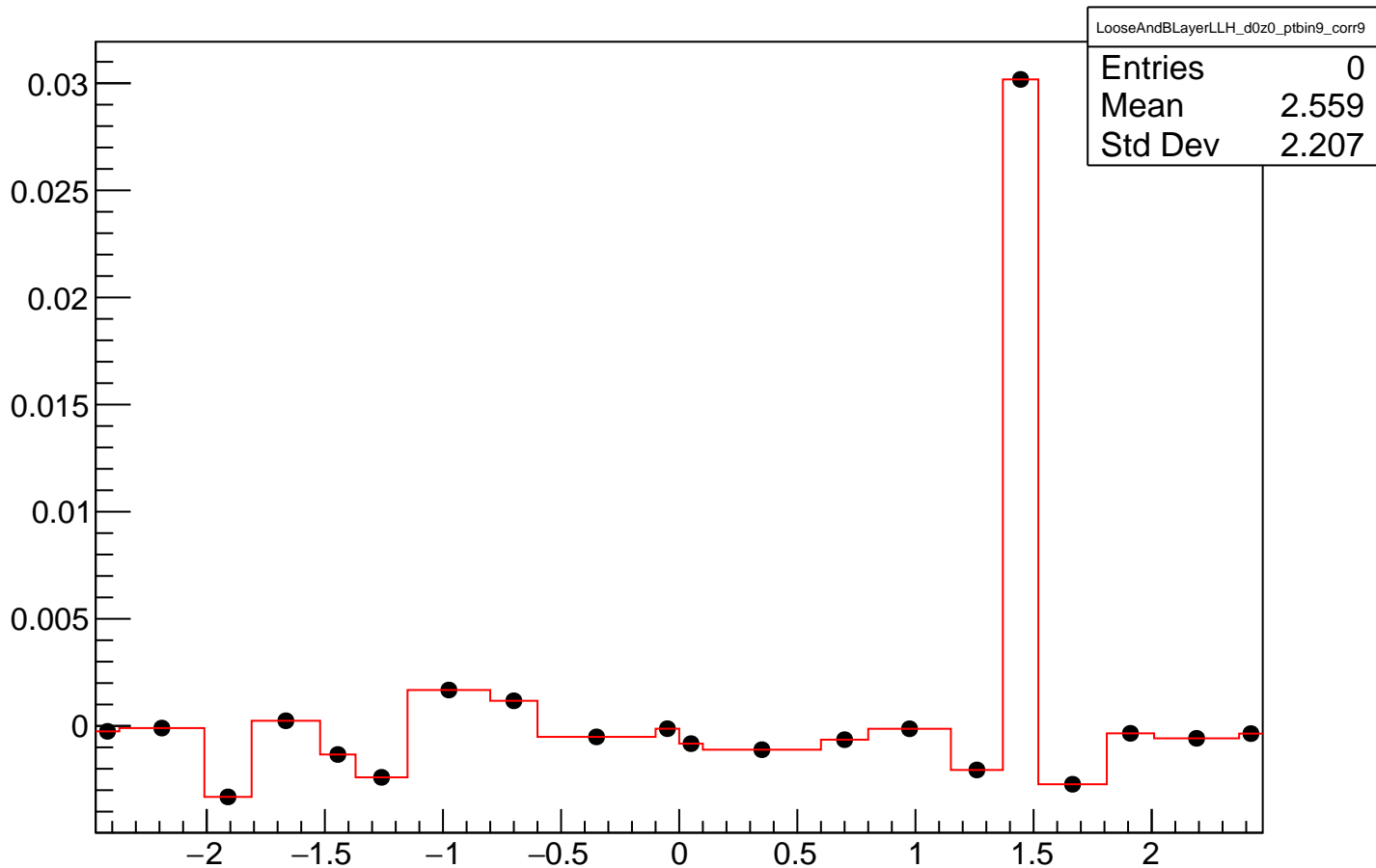
# FullSim\_LooseLLH\_d0z0\_corr9



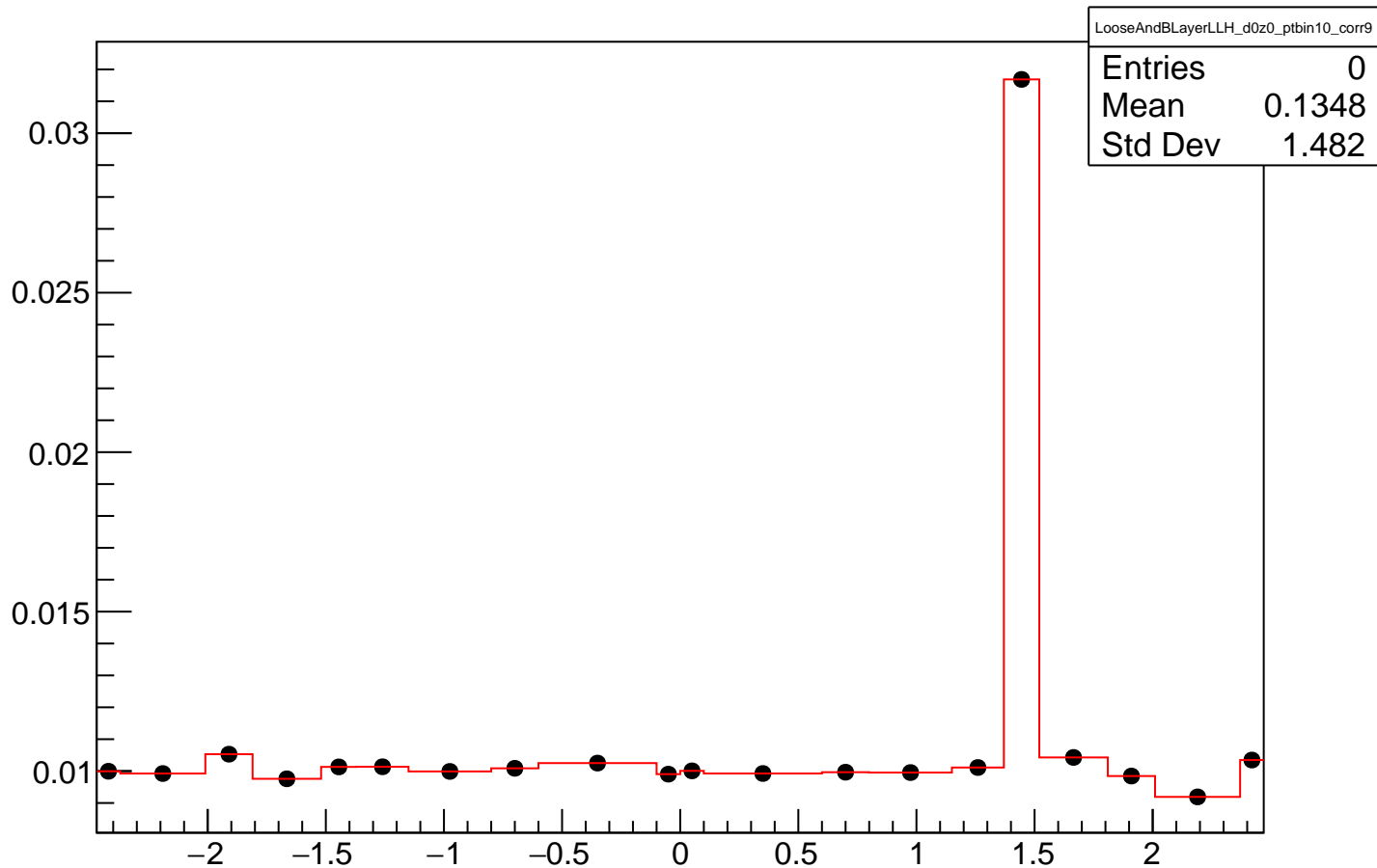
# FullSim\_LooseLLH\_d0z0\_corr9



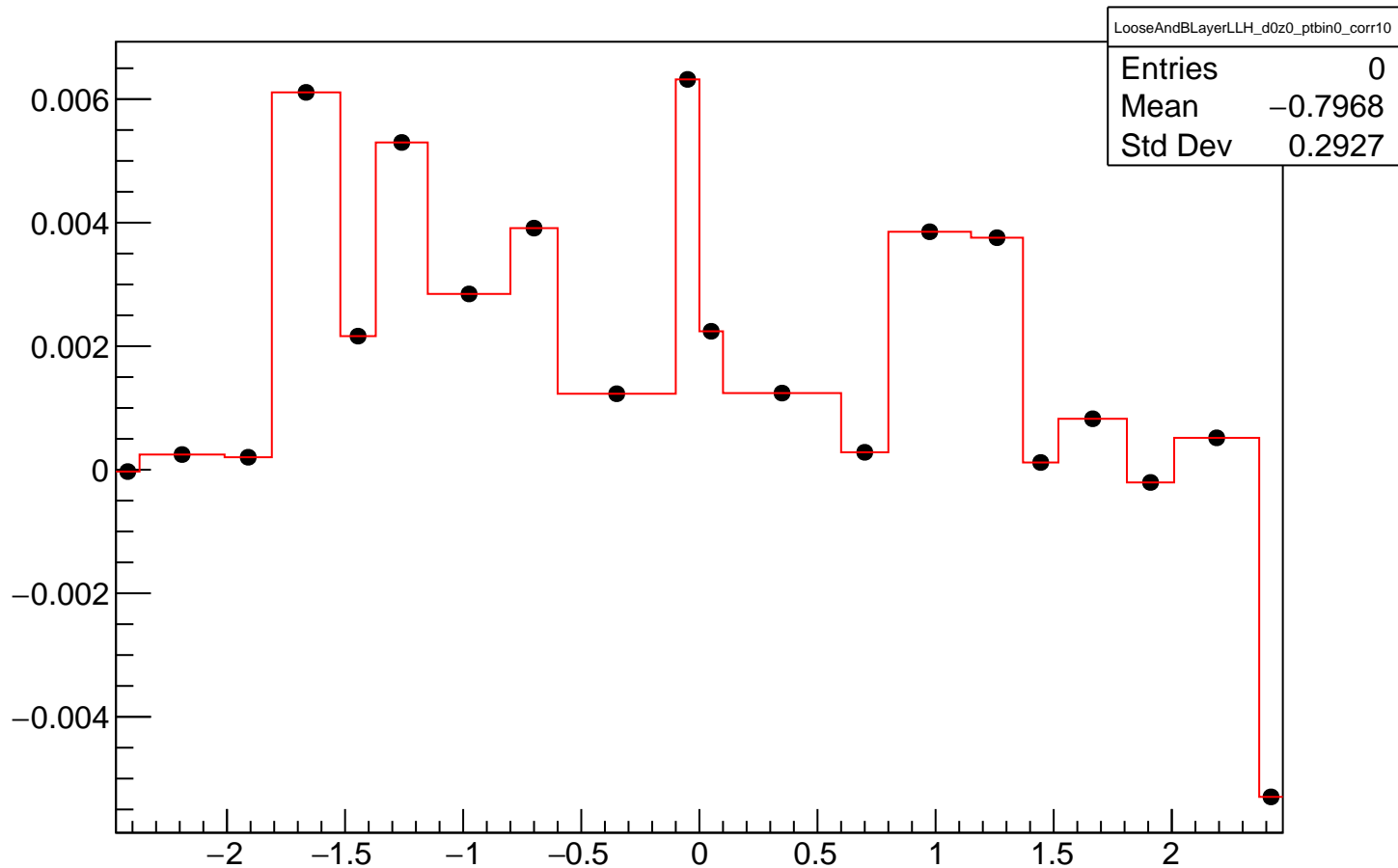
# FullSim\_LooseLLH\_d0z0\_corr9



# FullSim\_LooseLLH\_d0z0\_corr9



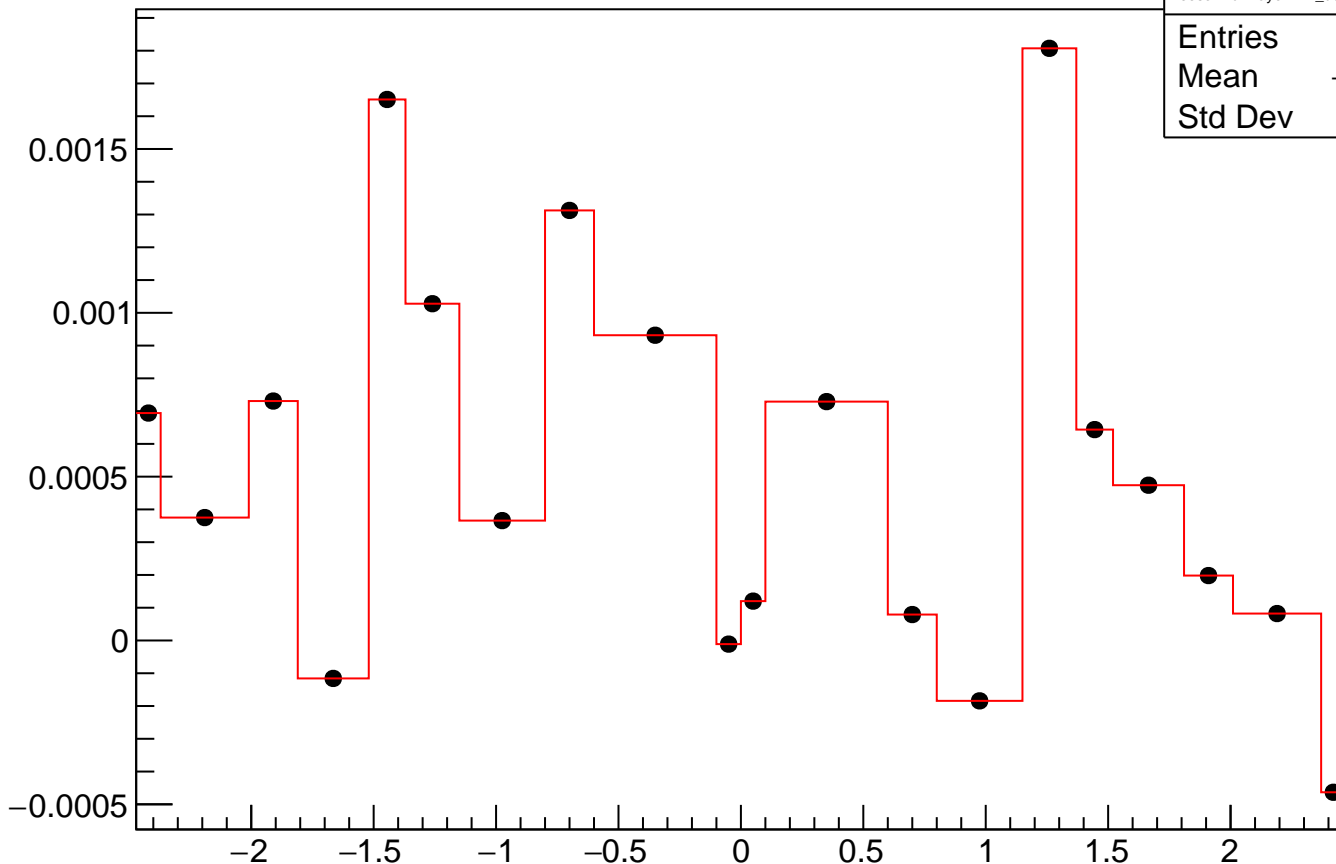
# FullSim\_LooseLLH\_d0z0\_corr10



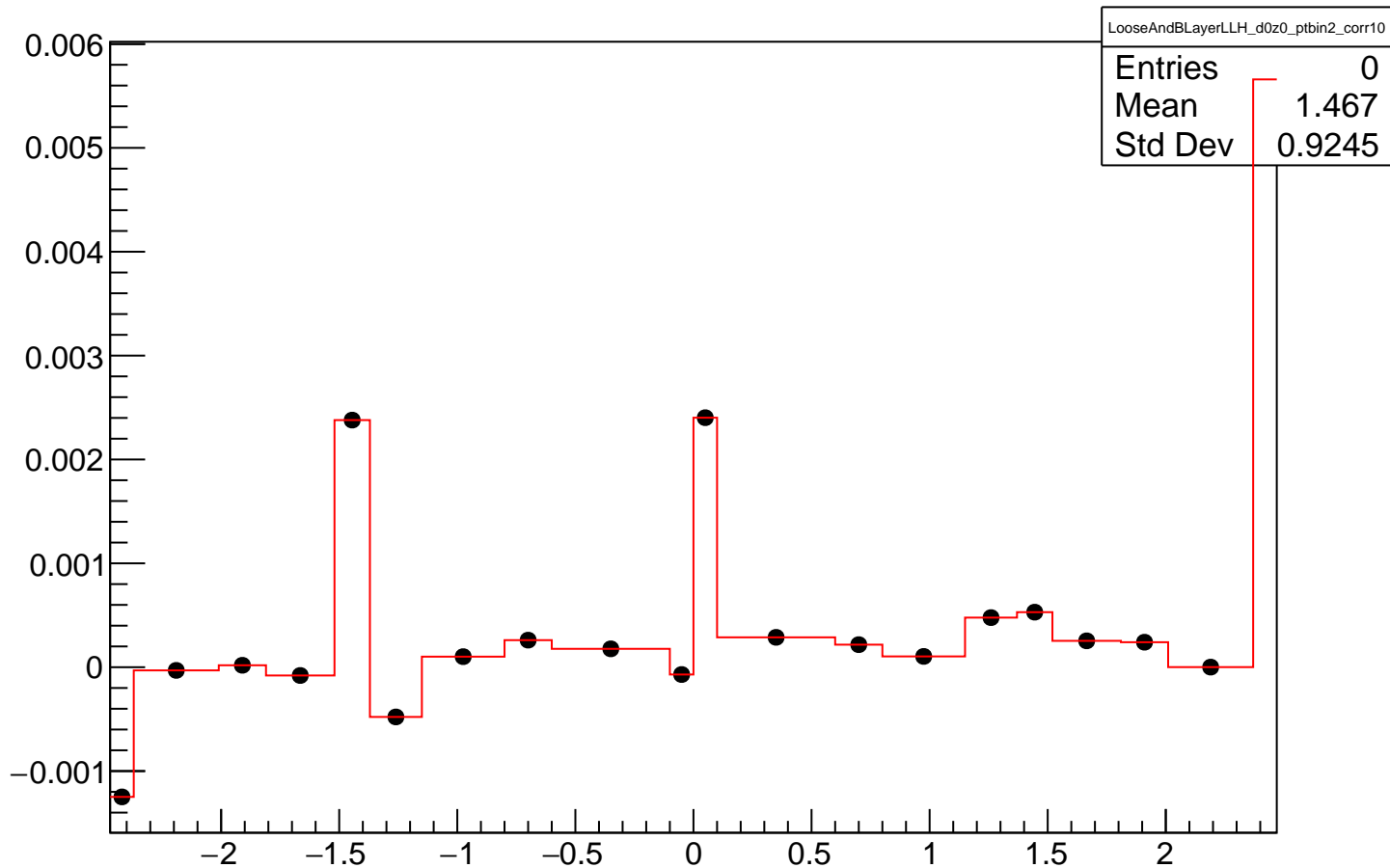
# FullSim\_LooseLLH\_d0z0\_corr10

LooseAndBLayerLLH\_d0z0\_ptbin1\_corr10

Entries	0
Mean	-0.5184
Std Dev	1.217

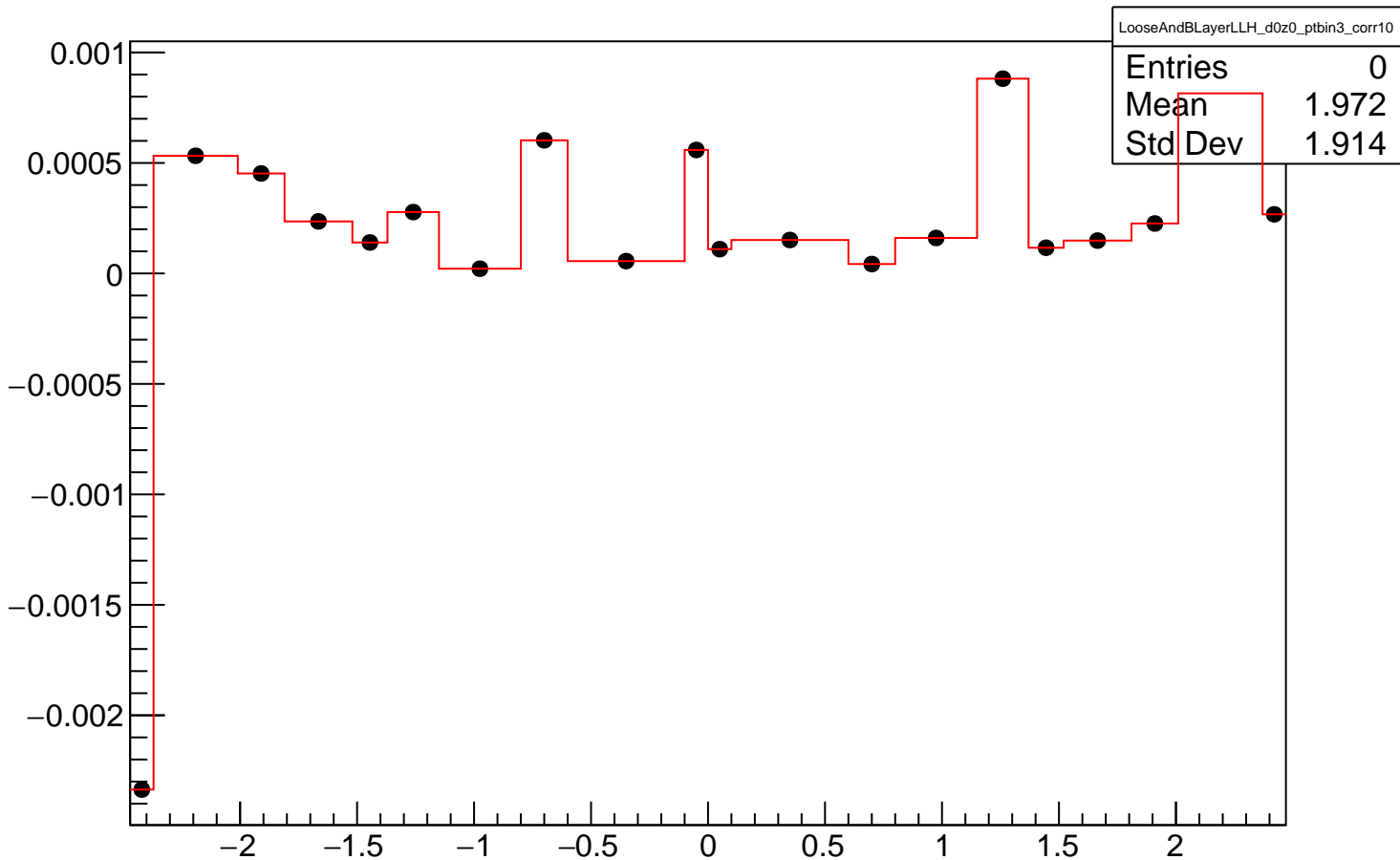


# FullSim\_LooseLLH\_d0z0\_corr10

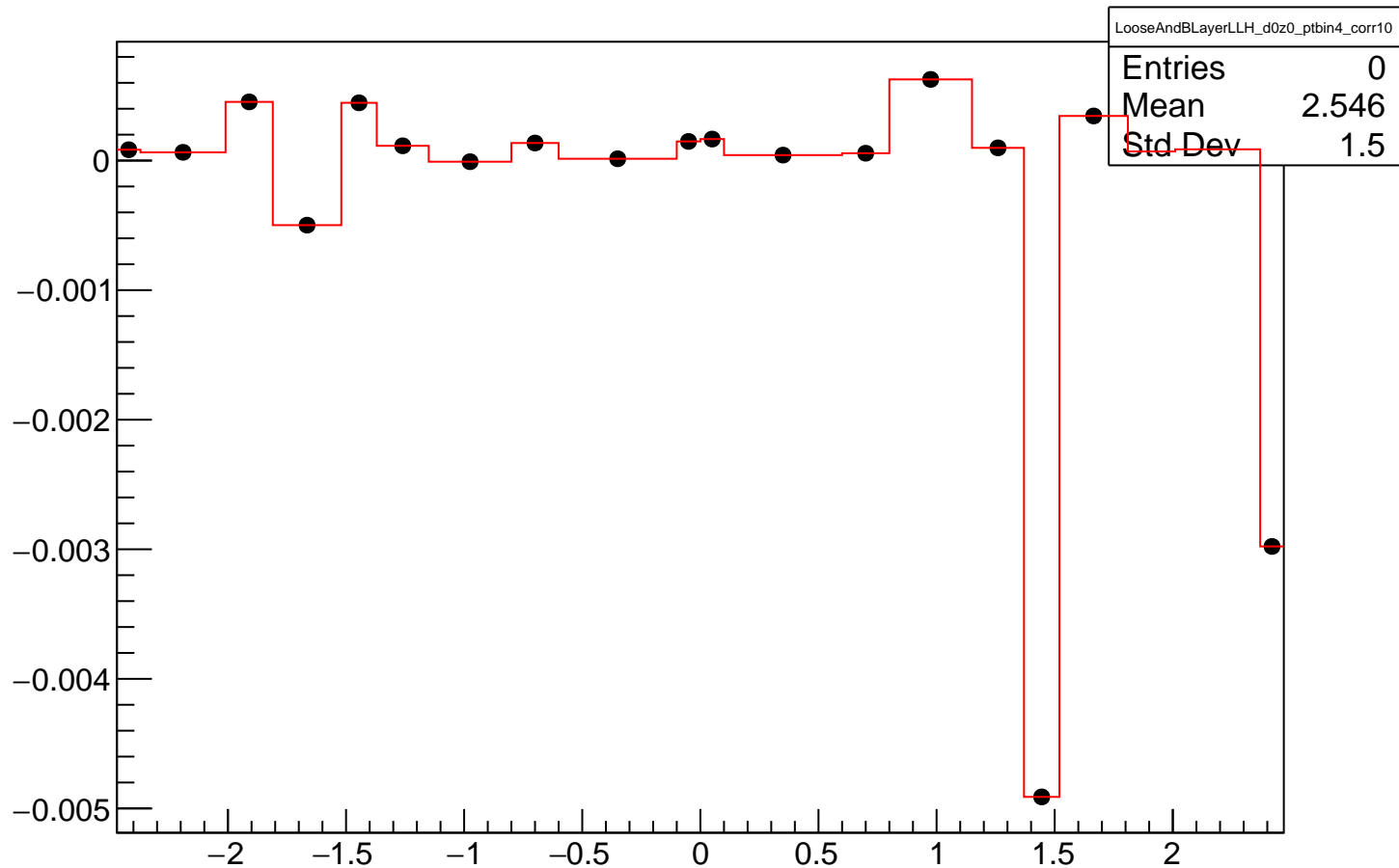




# FullSim\_LooseLLH\_d0z0\_corr10



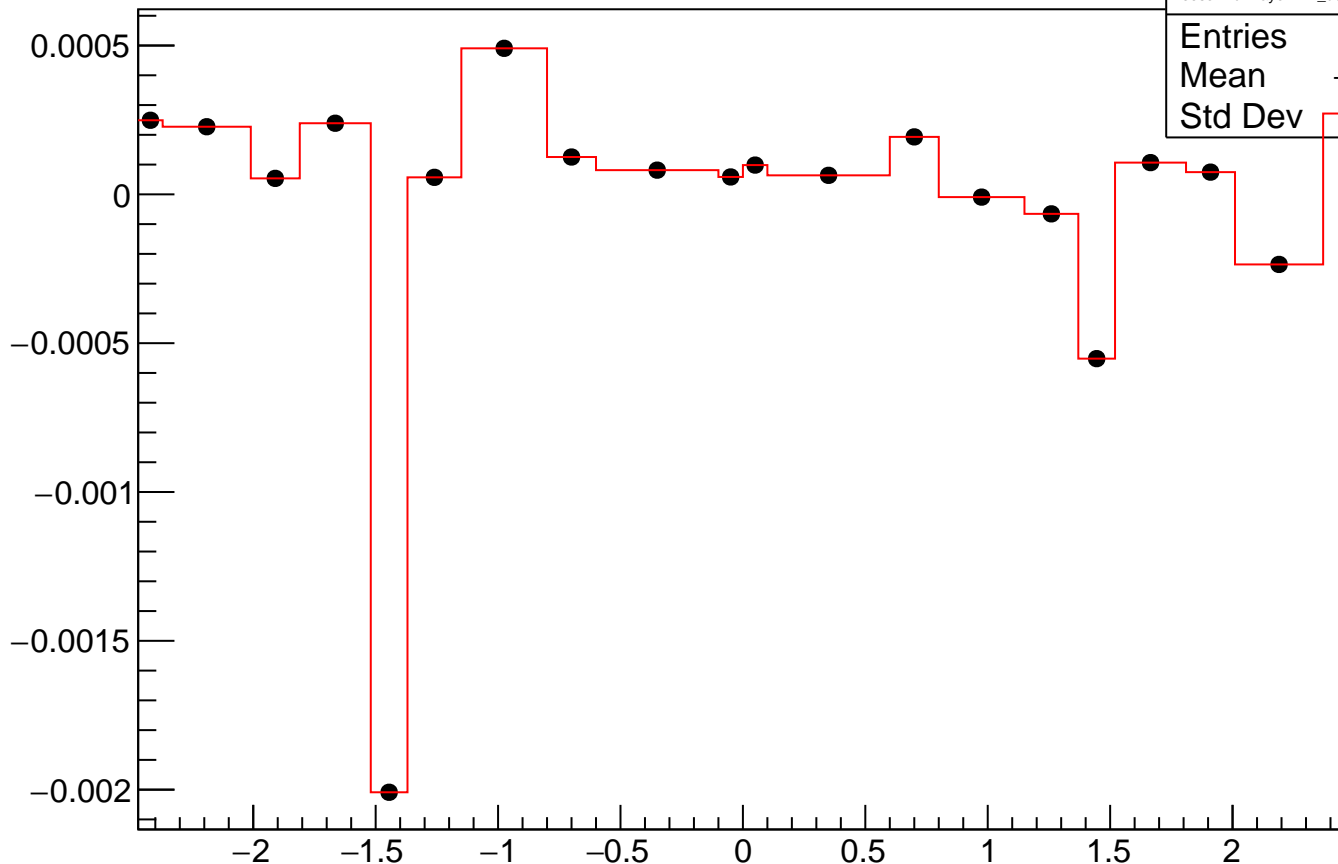
# FullSim\_LooseLLH\_d0z0\_corr10



# FullSim\_LooseLLH\_d0z0\_corr10

LooseAndBLayerLLH\_d0z0\_ptbin5\_corr10

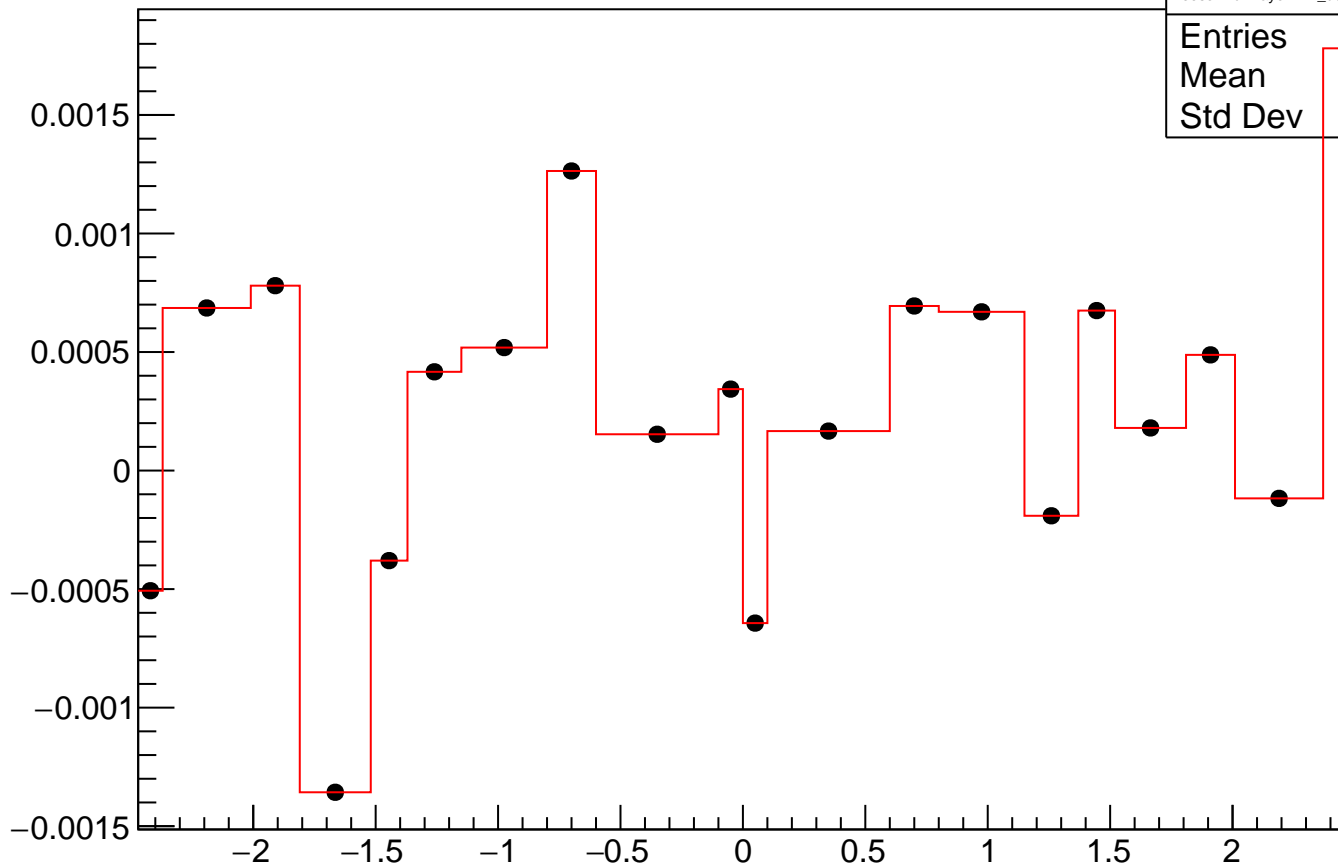
Entries	0
Mean	-0.7641
Std Dev	0.1663



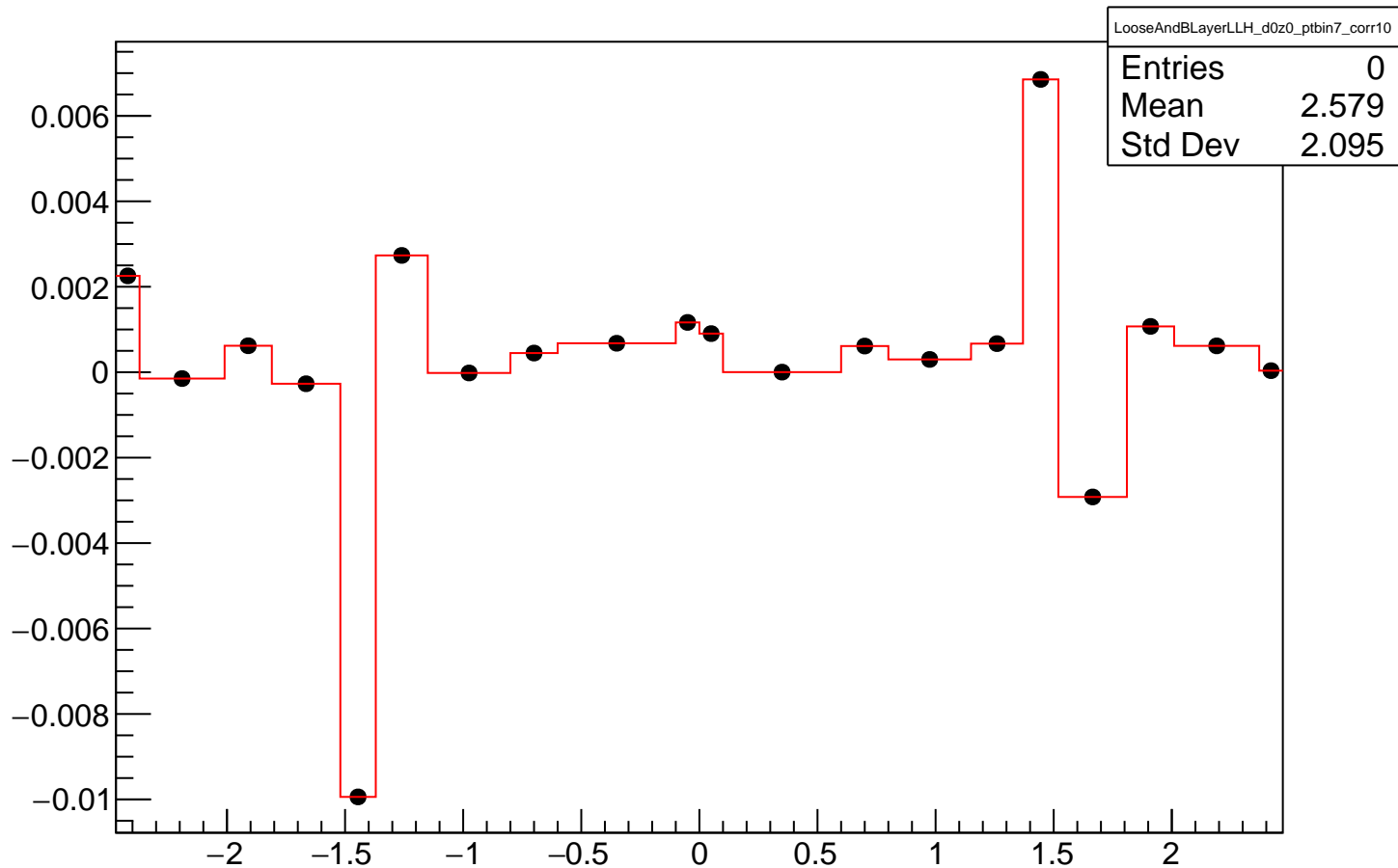
# FullSim\_LooseLLH\_d0z0\_corr10

LooseAndBLayerLLH\_d0z0\_ptbin6\_corr10

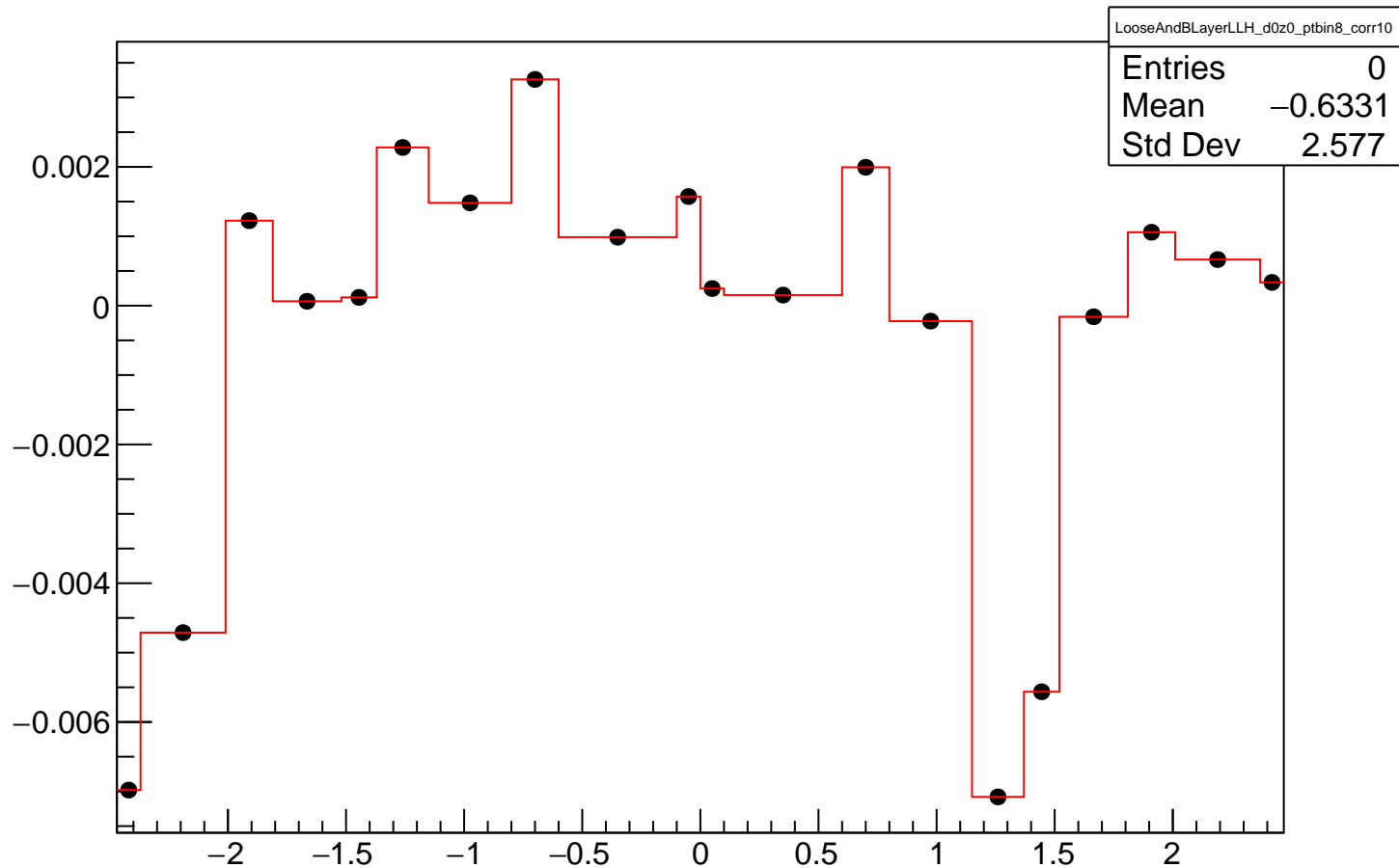
Entries	0
Mean	1.111
Std Dev	1.172



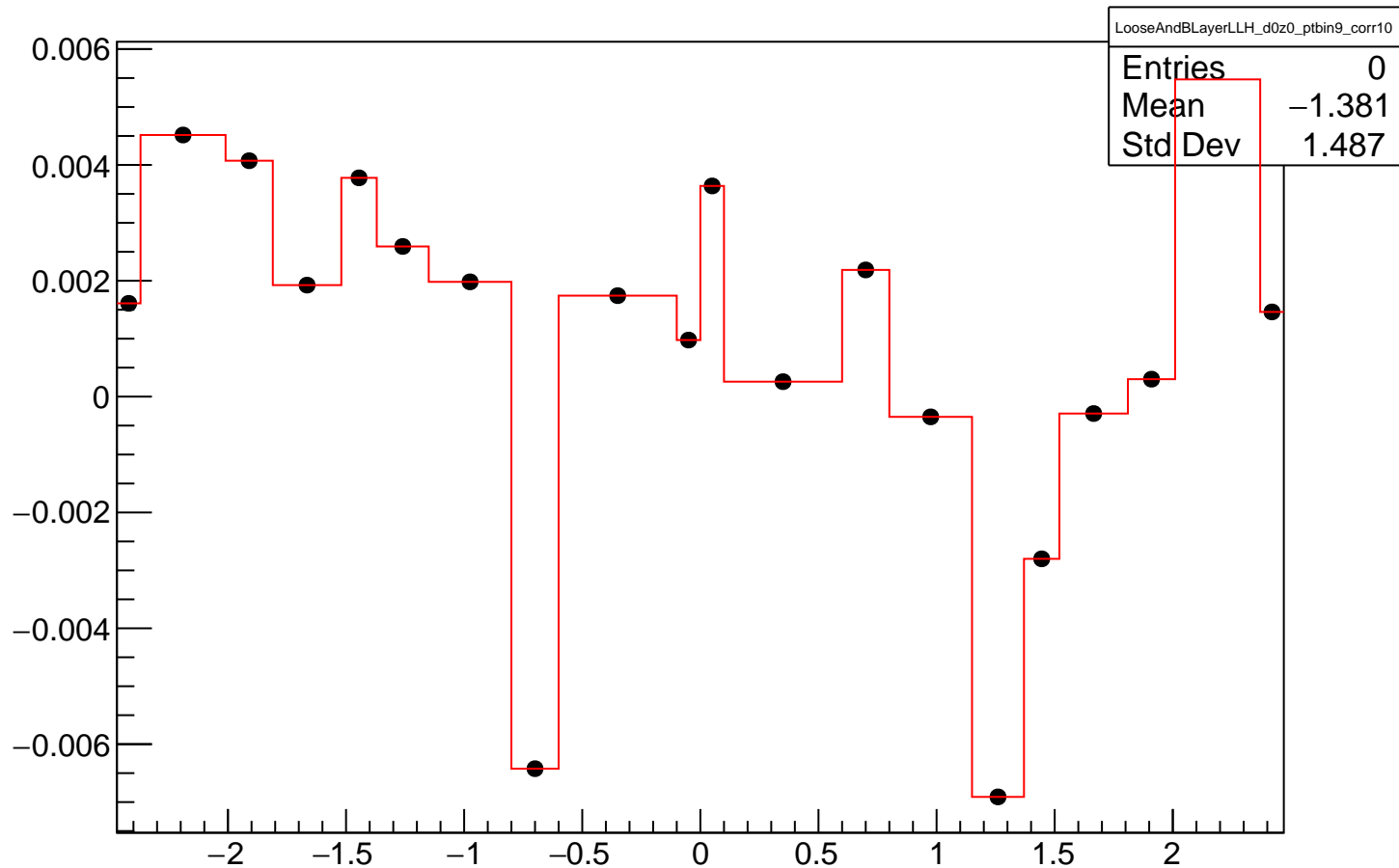
# FullSim\_LooseLLH\_d0z0\_corr10



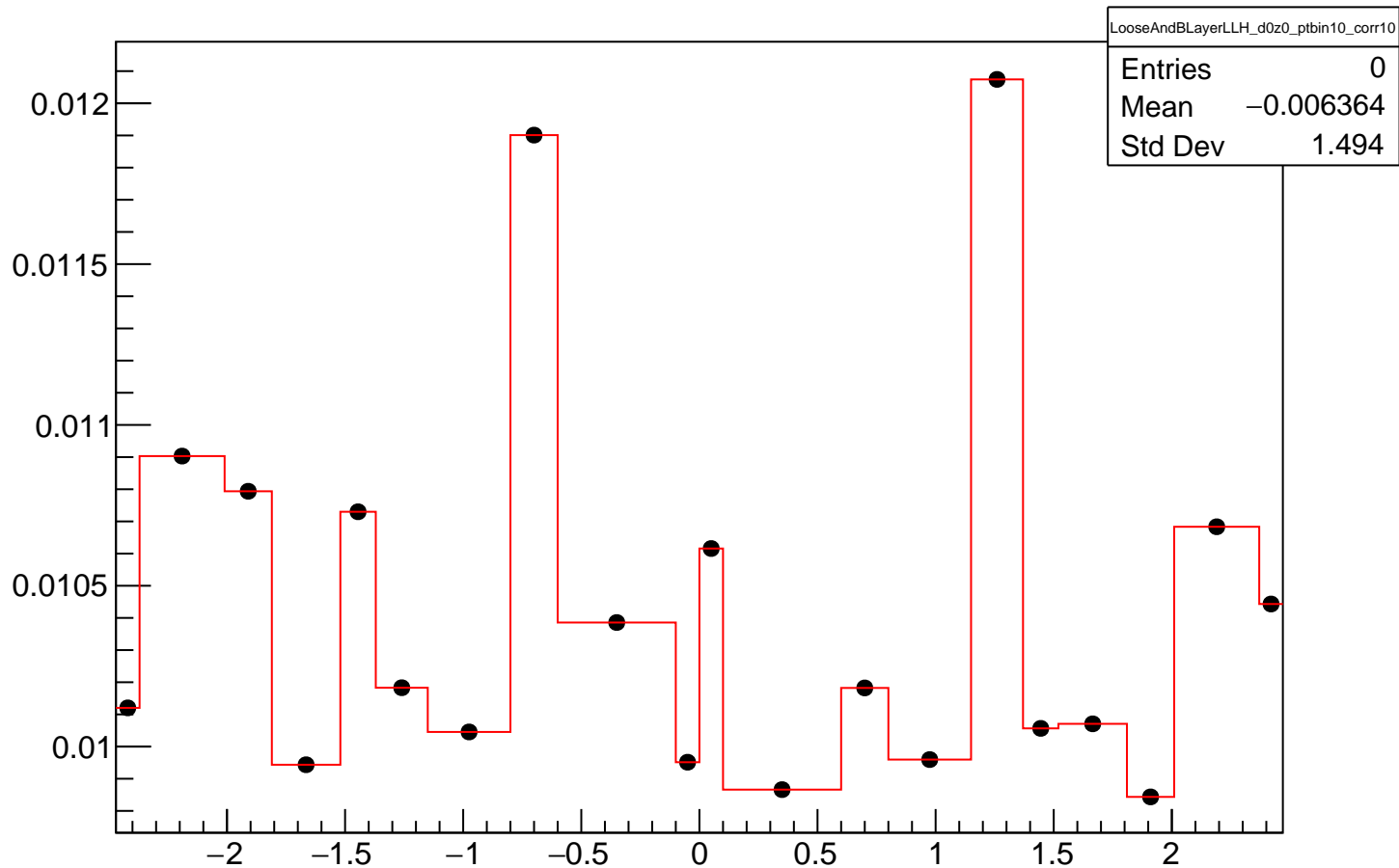
# FullSim\_LooseLLH\_d0z0\_corr10



# FullSim\_LooseLLH\_d0z0\_corr10

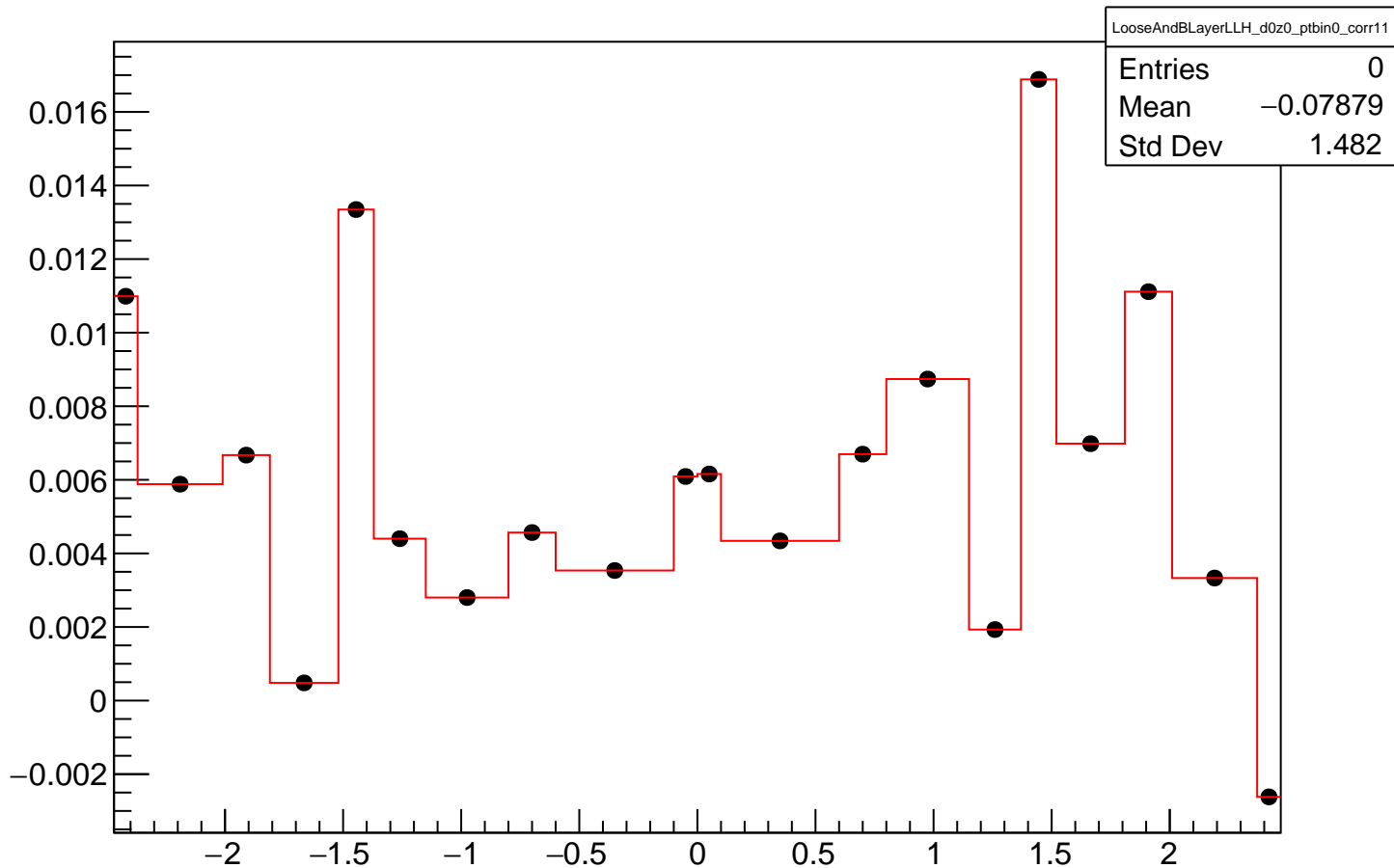


# FullSim\_LooseLLH\_d0z0\_corr10

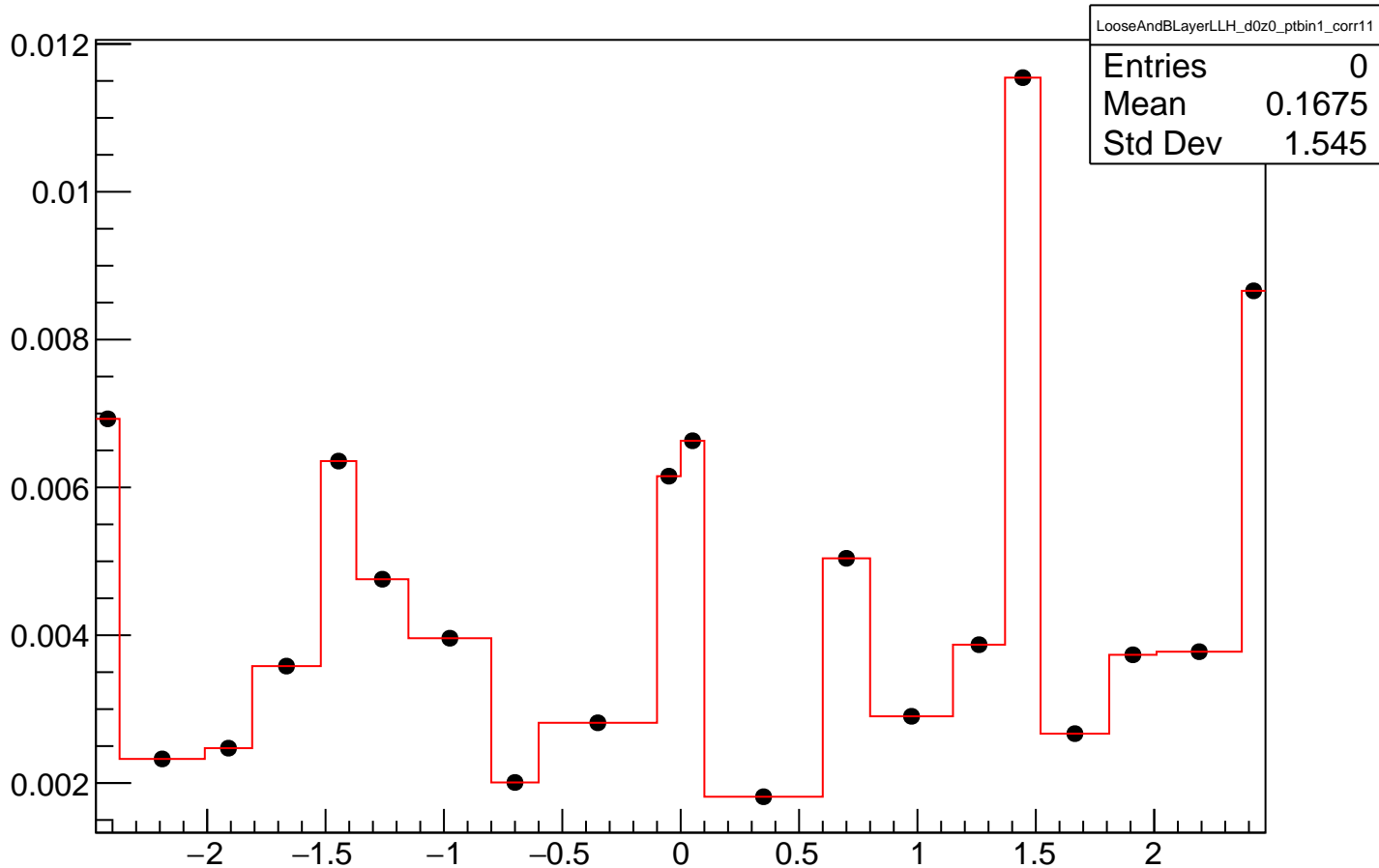




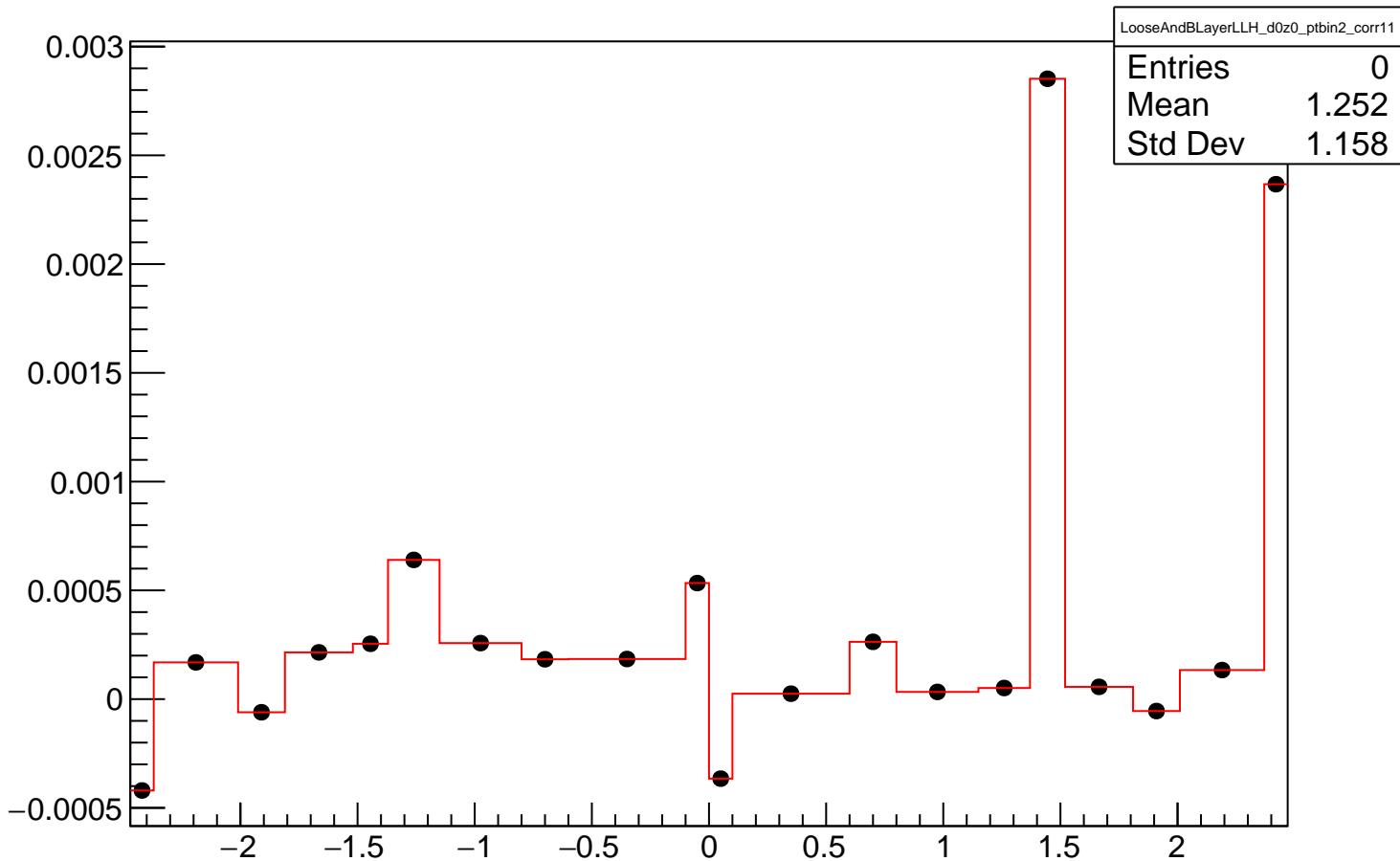
# FullSim\_LooseLLH\_d0z0\_corr11



# FullSim\_LooseLLH\_d0z0\_corr11



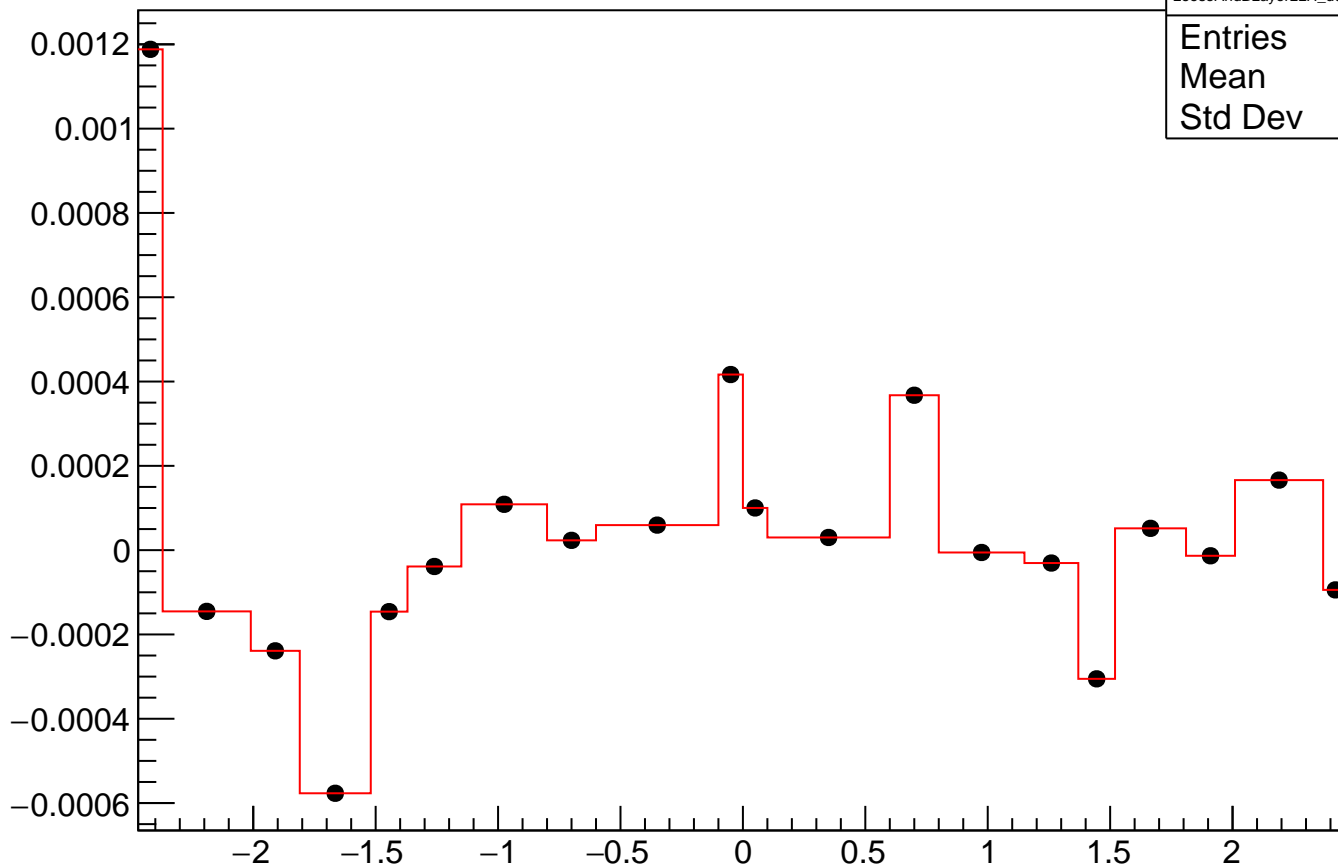
# FullSim\_LooseLLH\_d0z0\_corr11



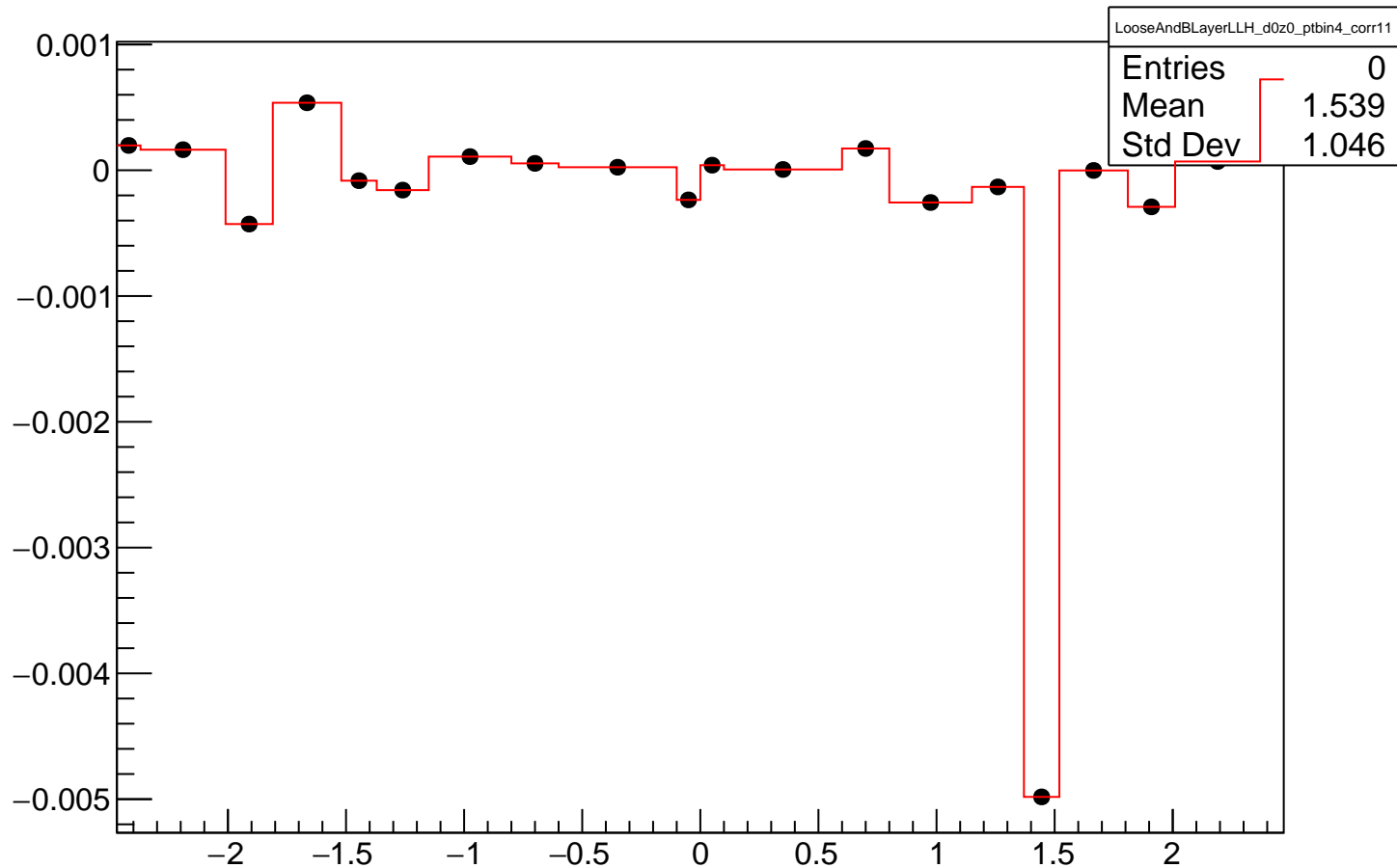
# FullSim\_LooseLLH\_d0z0\_corr11

LooseAndBLayerLLH\_d0z0\_ptbin3\_corr11

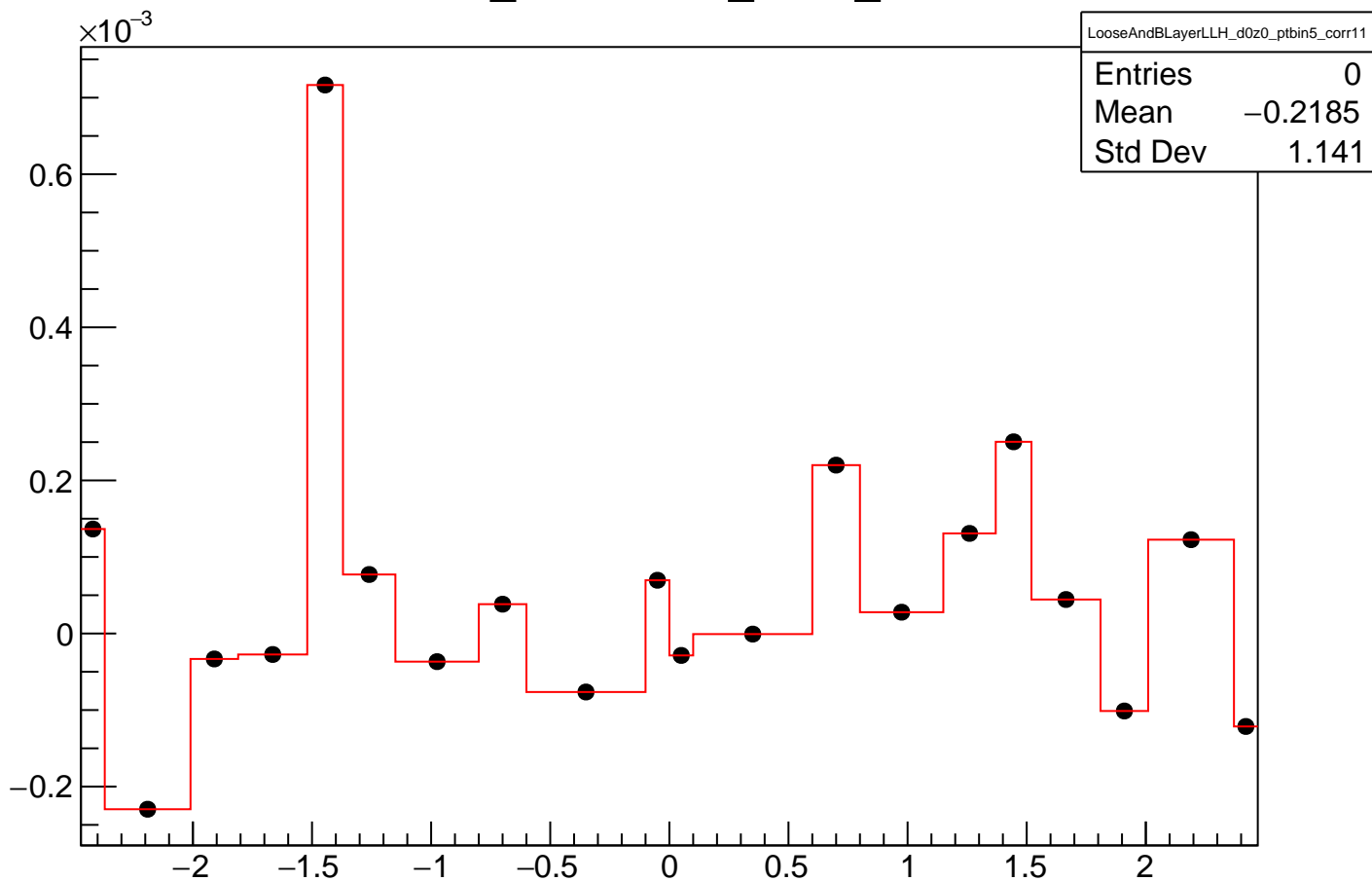
Entries	0
Mean	-1.157
Std Dev	1.532



# FullSim\_LooseLLH\_d0z0\_corr11

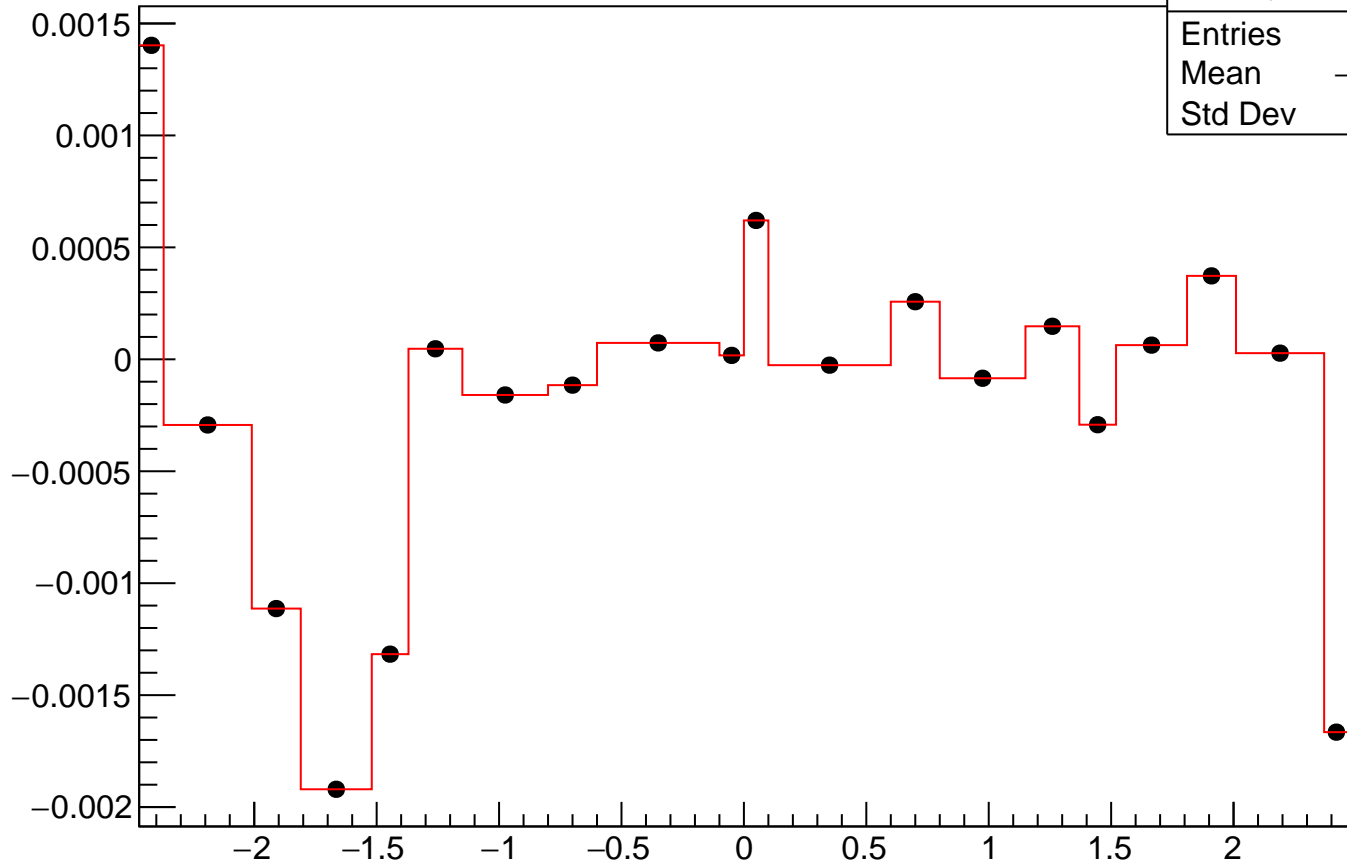


# FullSim\_LooseLLH\_d0z0\_corr11

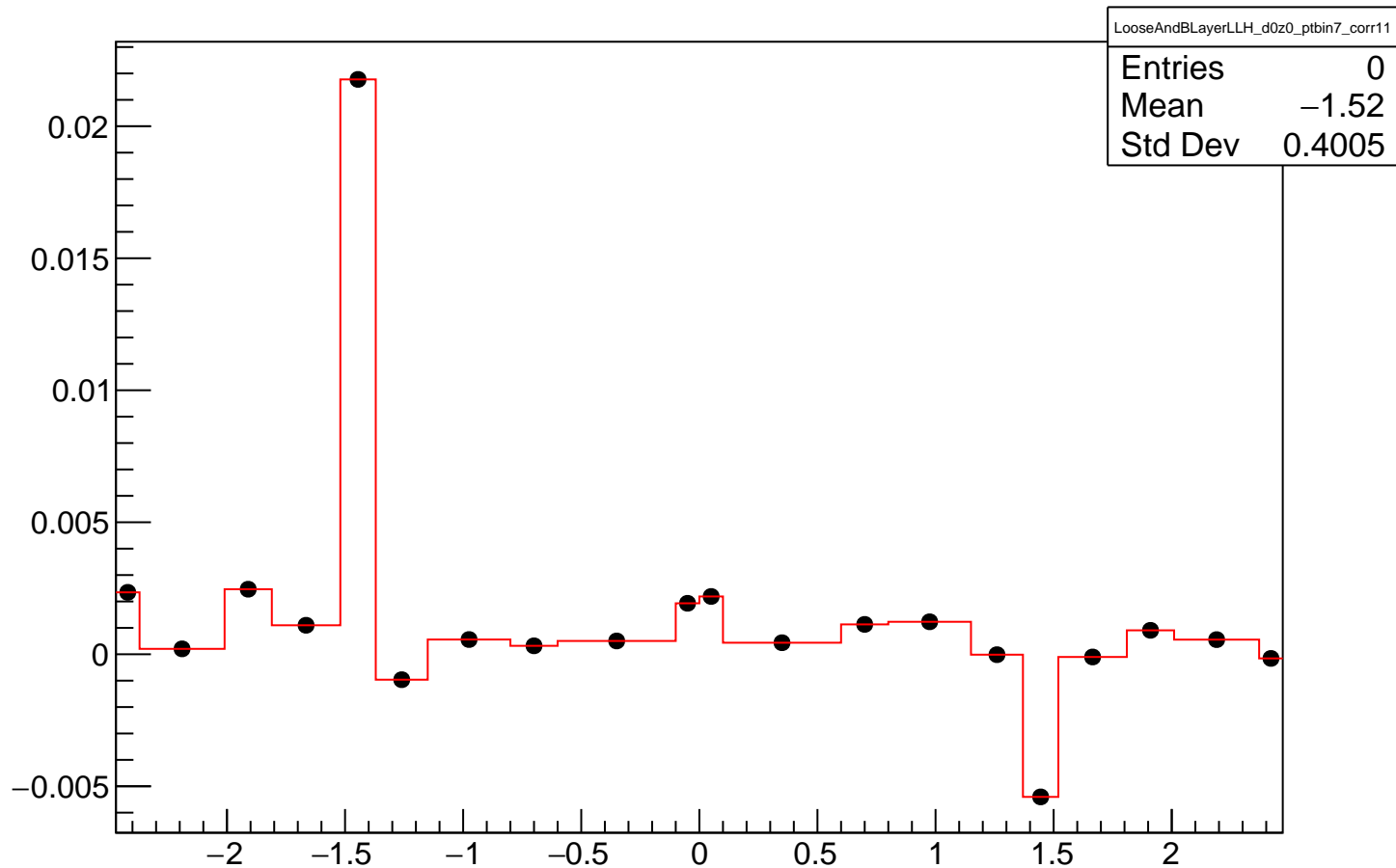


# FullSim\_LooseLLH\_d0z0\_corr11

LooseAndBLayerLLH_d0z0_ptbin6_corr11	
Entries	0
Mean	-0.3428
Std Dev	1.84

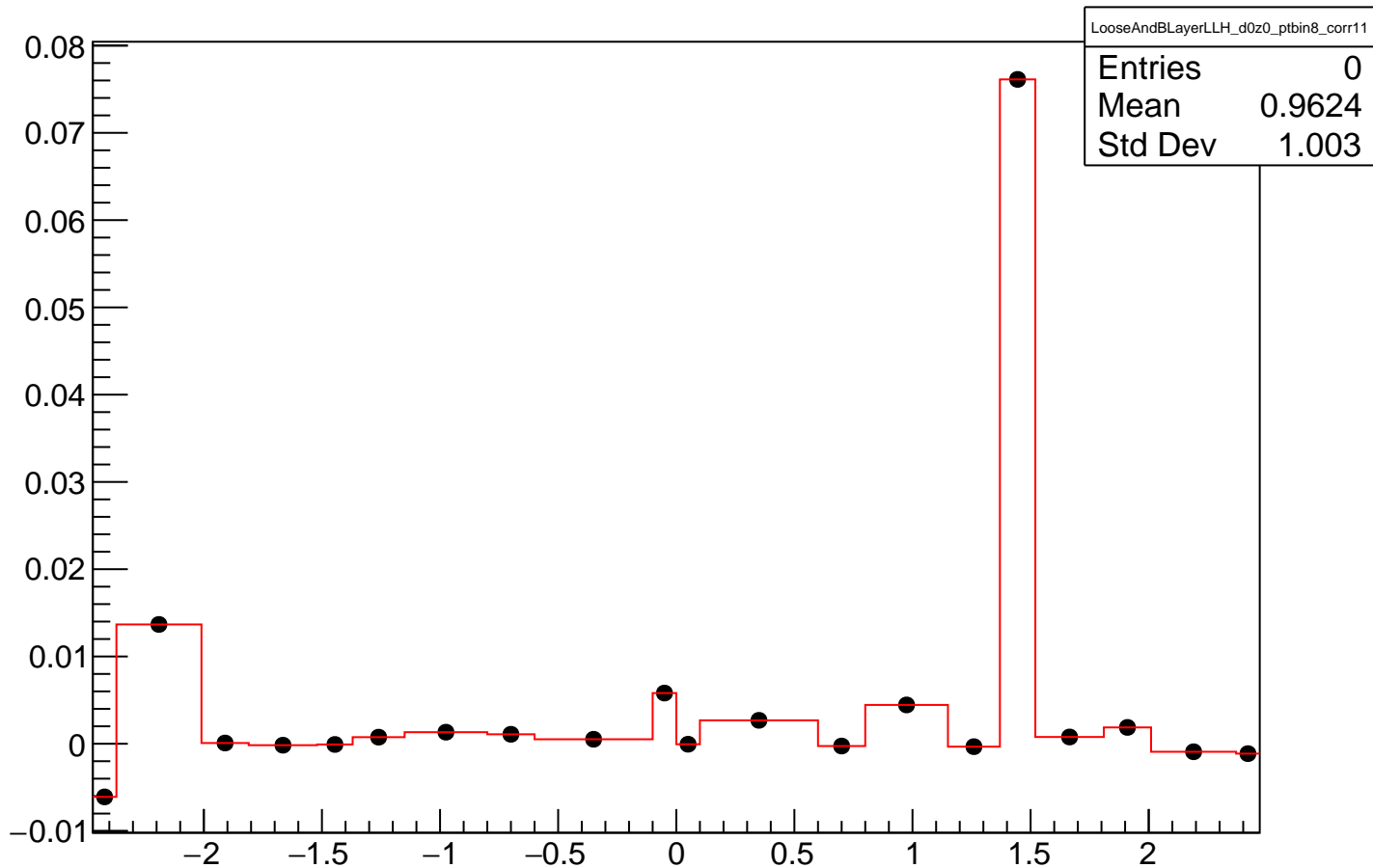


# FullSim\_LooseLLH\_d0z0\_corr11

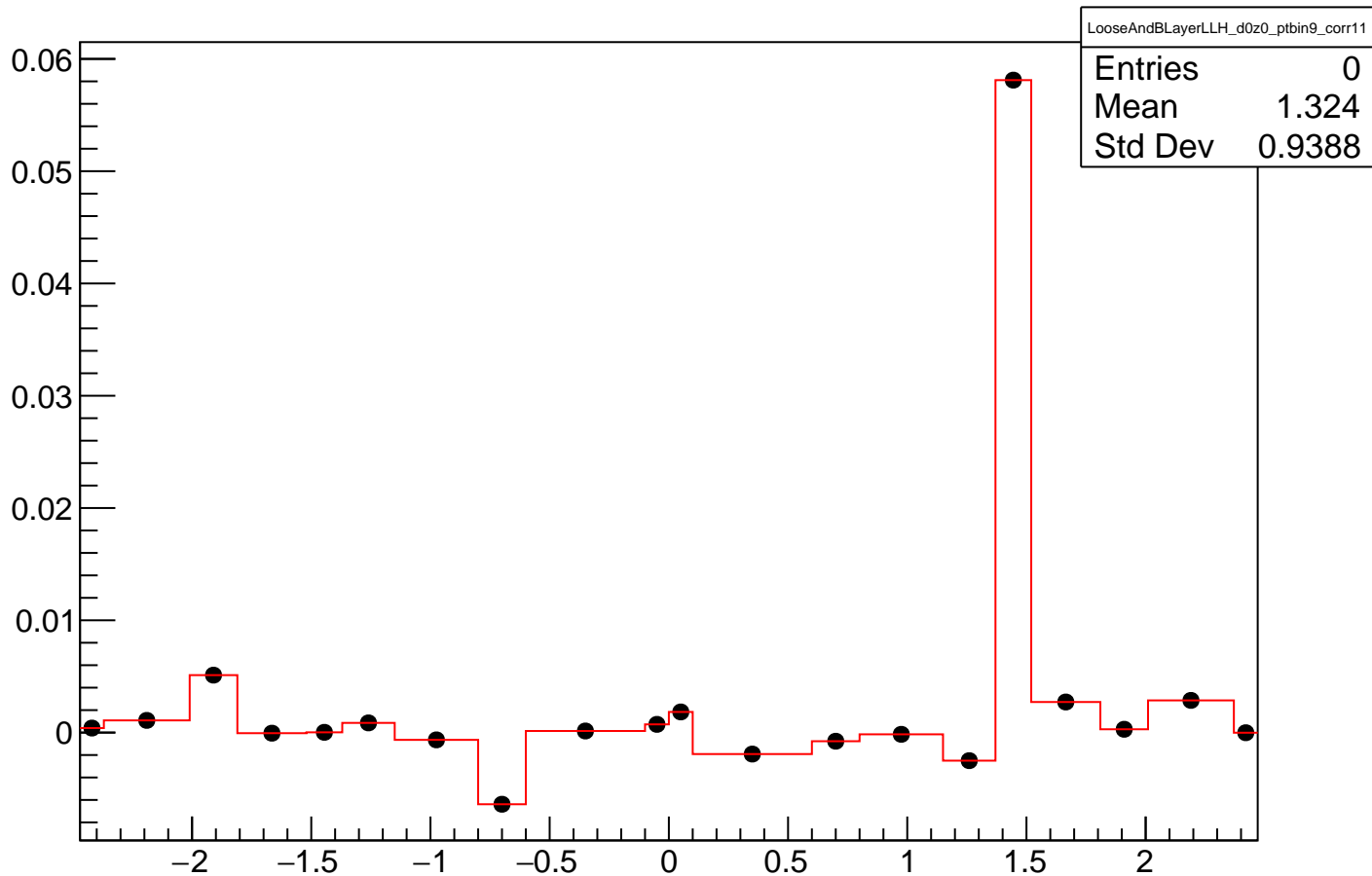




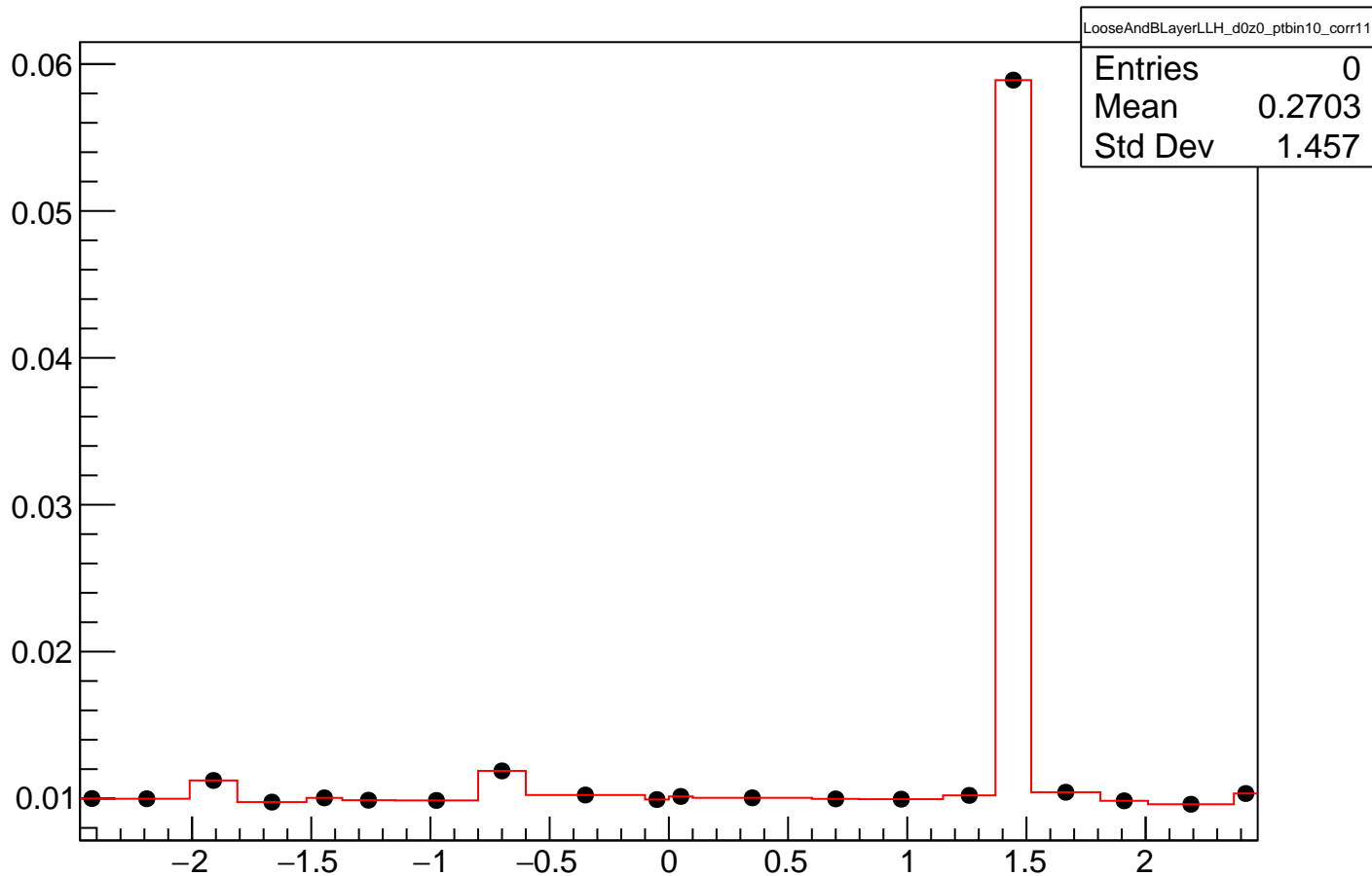
# FullSim\_LooseLLH\_d0z0\_corr11



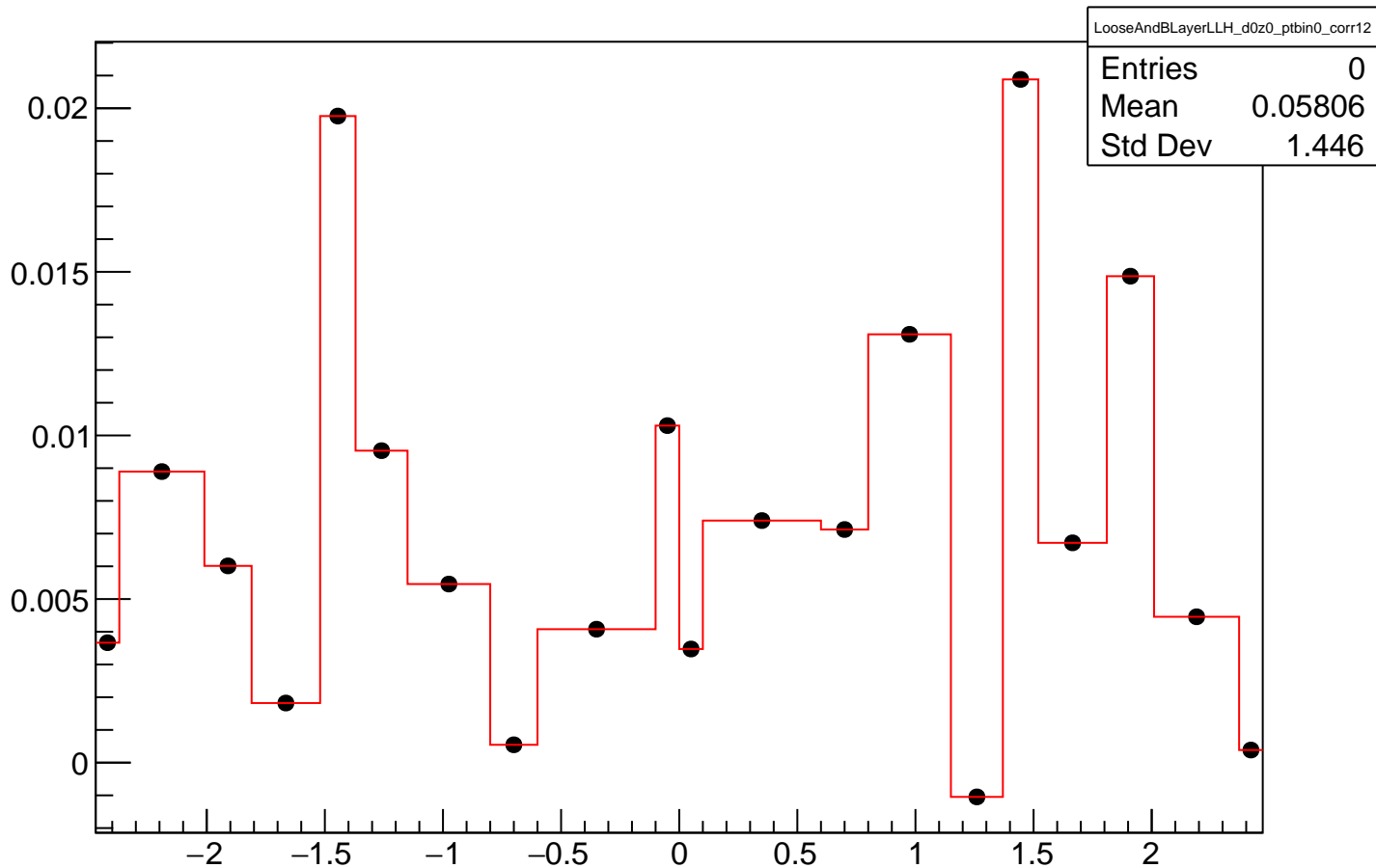
# FullSim\_LooseLLH\_d0z0\_corr11



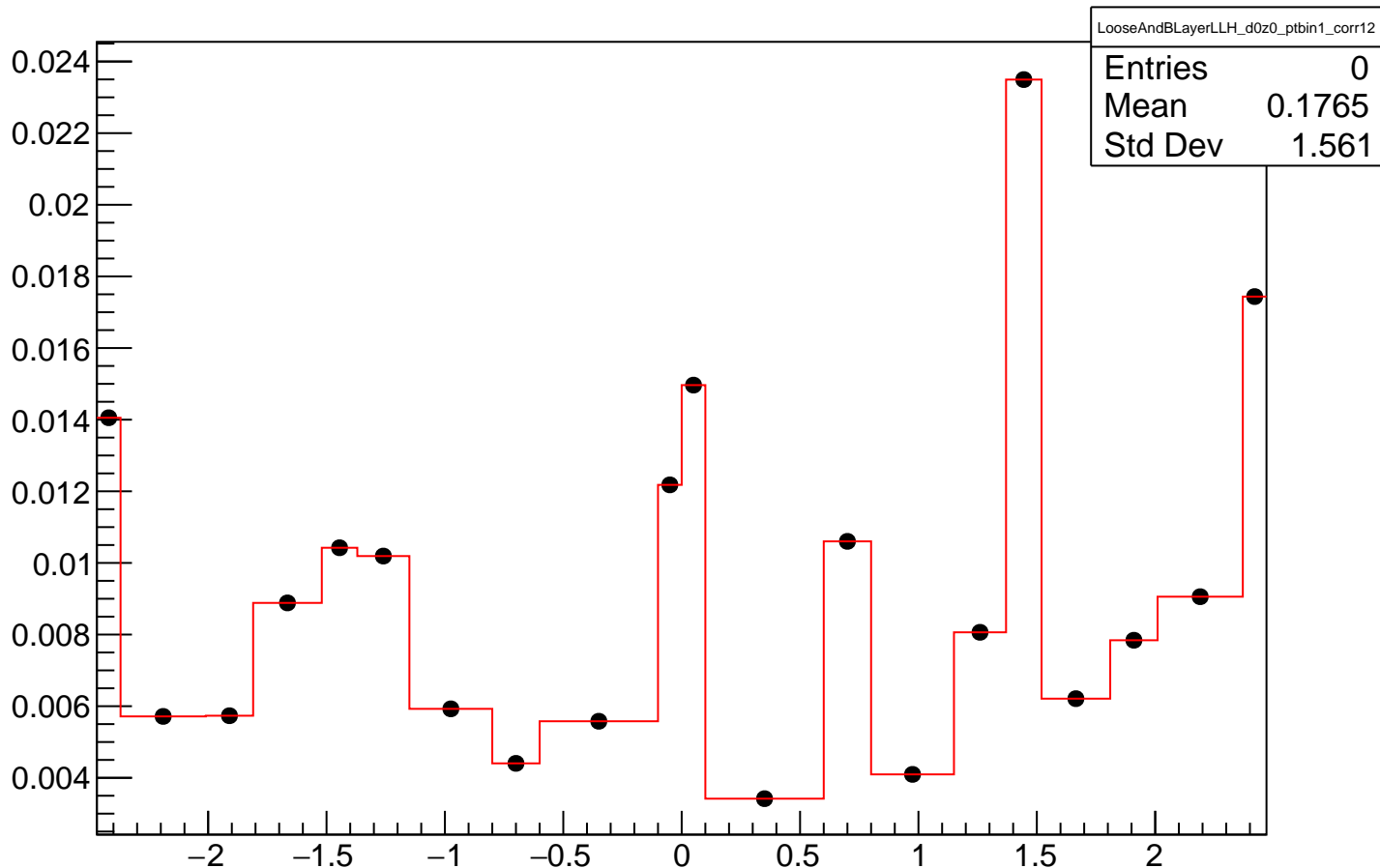
# FullSim\_LooseLLH\_d0z0\_corr11



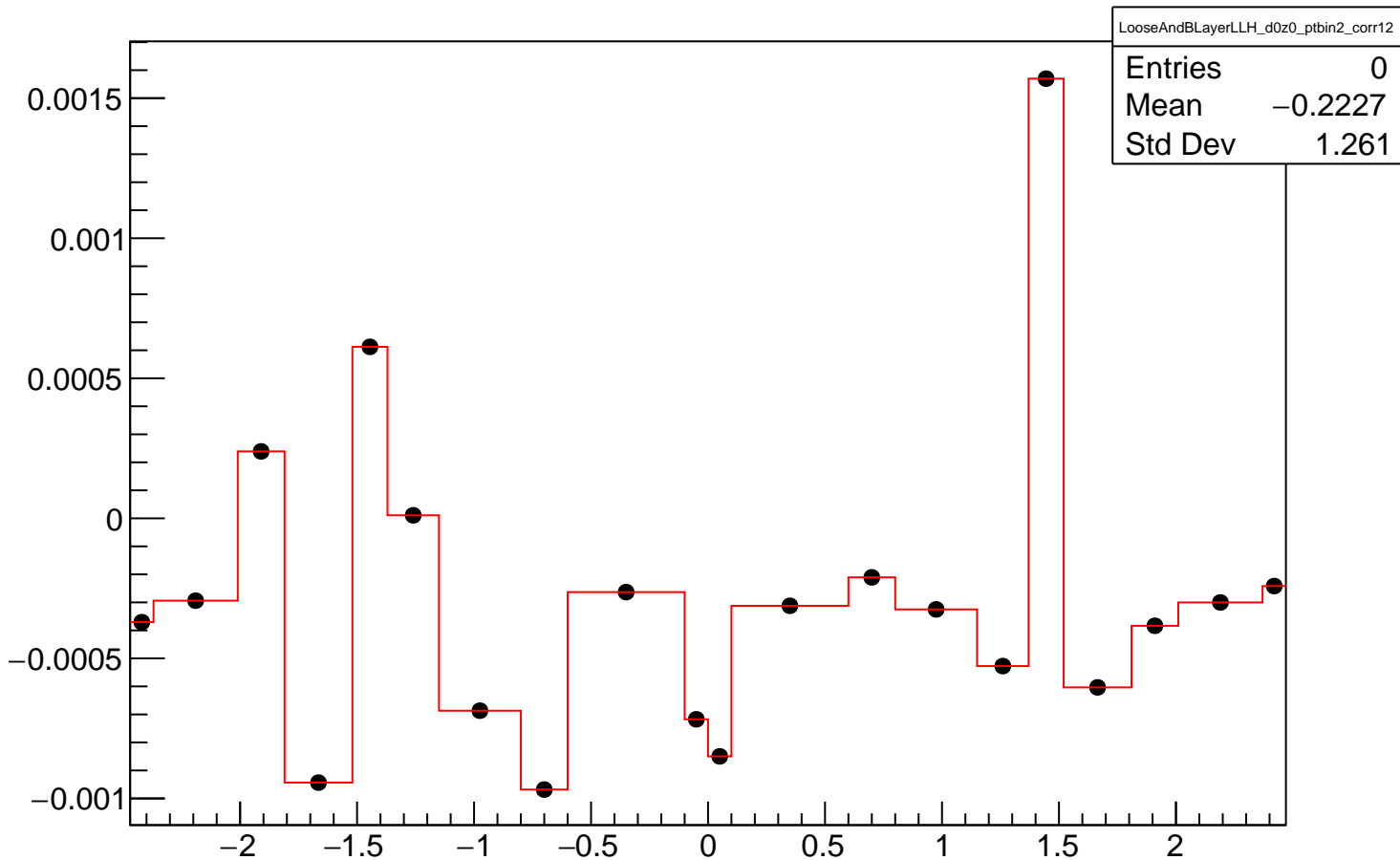
# FullSim\_LooseLLH\_d0z0\_corr12



# FullSim\_LooseLLH\_d0z0\_corr12

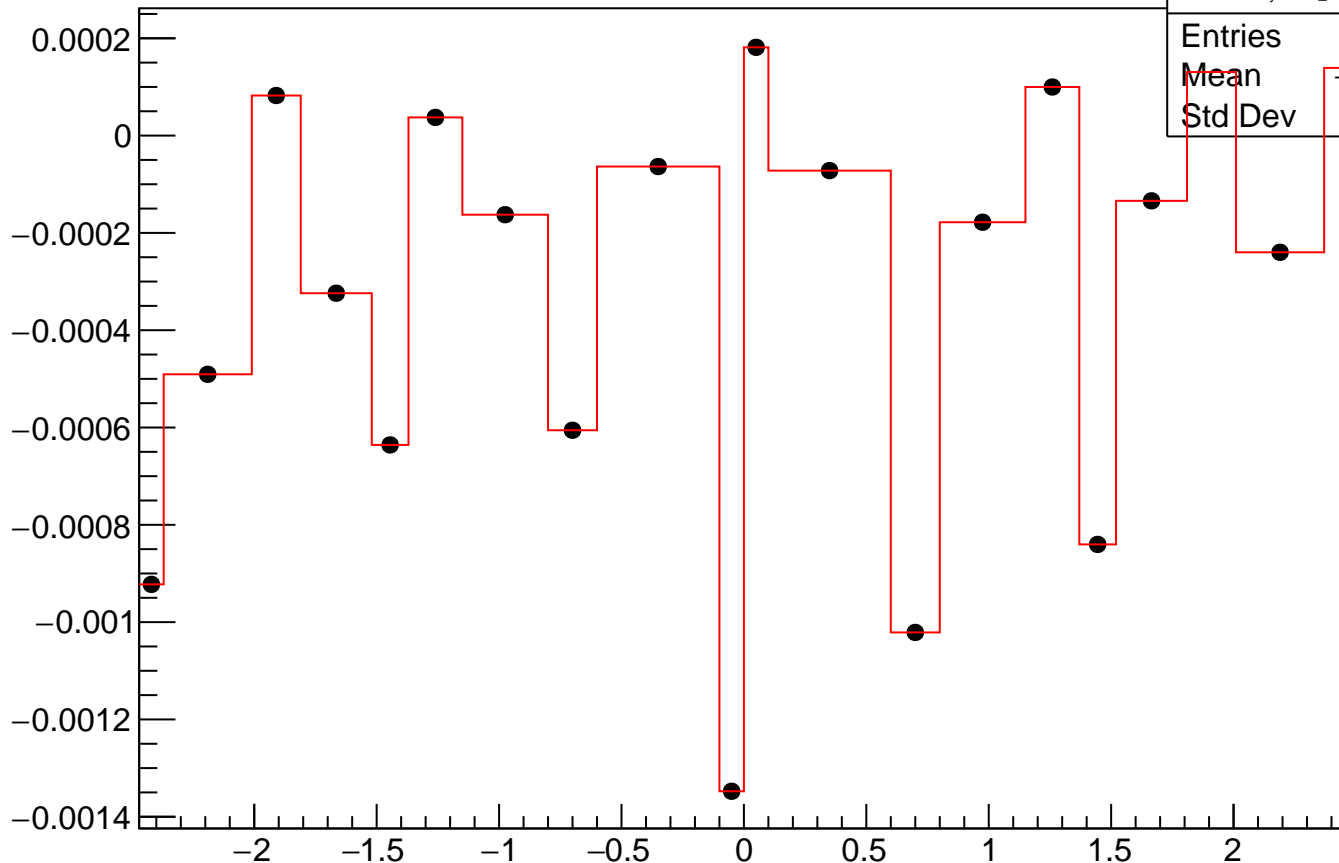


# FullSim\_LooseLLH\_d0z0\_corr12



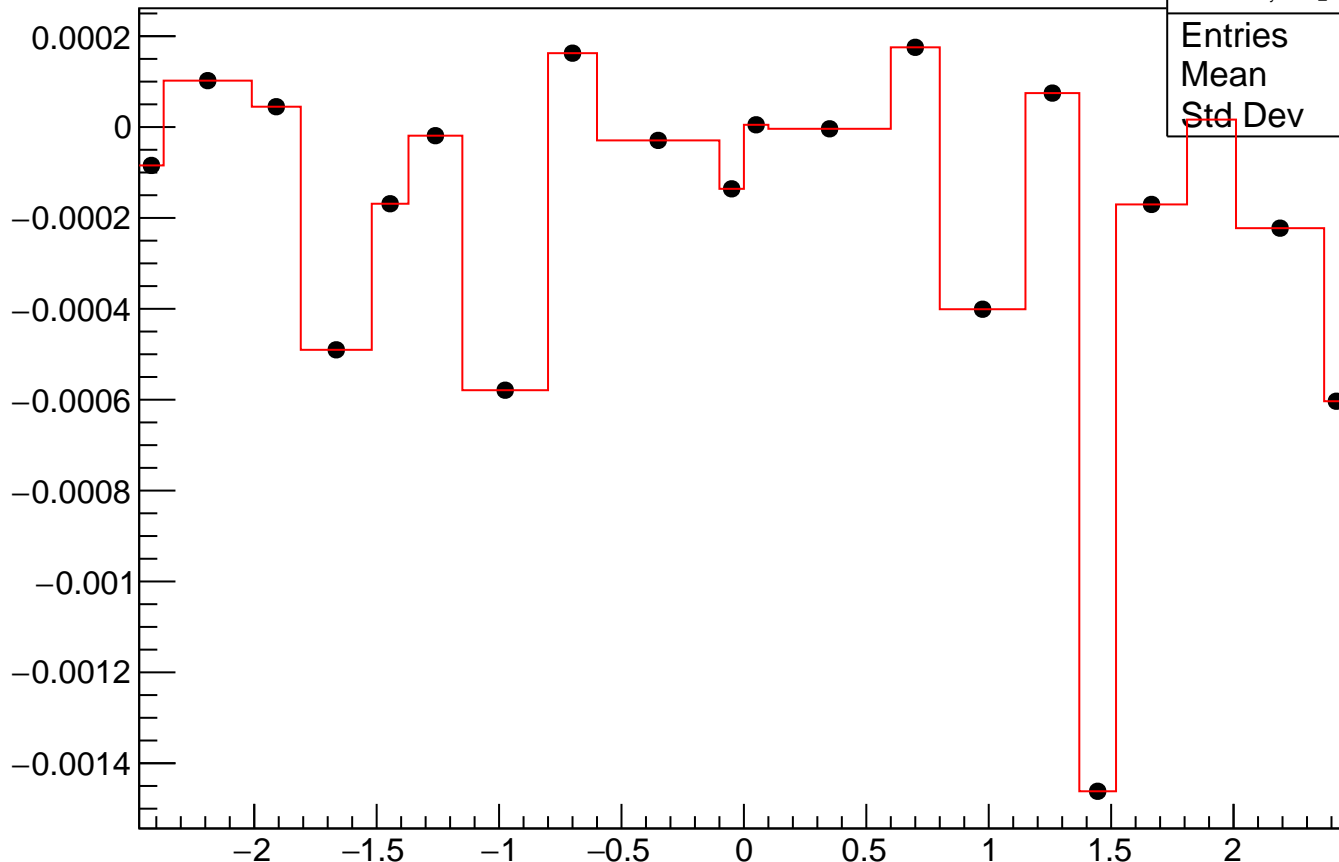
# FullSim\_LooseLLH\_d0z0\_corr12

LooseAndBLayerLLH_d0z0_ptbin3_corr12	
Entries	0
Mean	-0.4833
Std Dev	1.32



# FullSim\_LooseLLH\_d0z0\_corr12

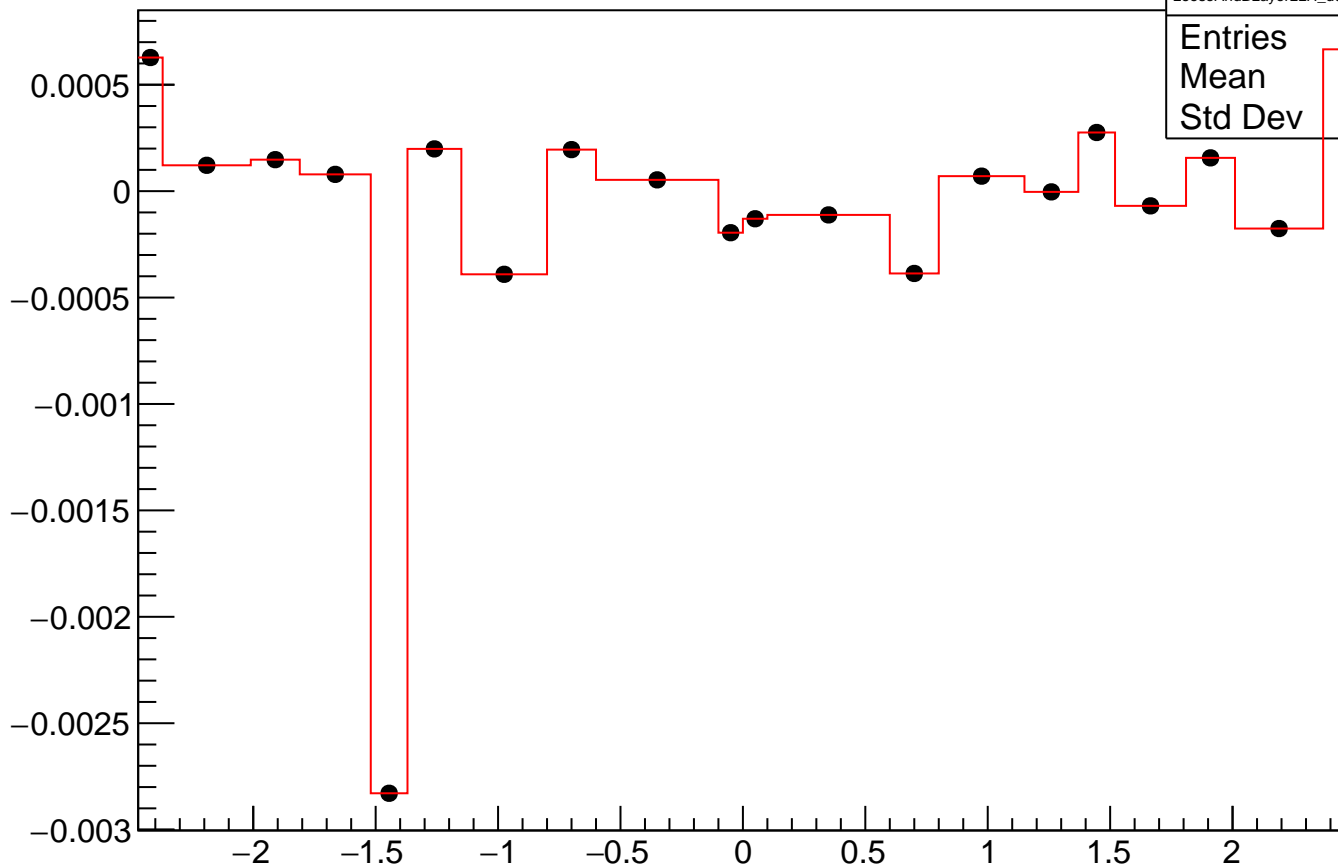
LooseAndBLayerLLH_d0z0_ptbin4_corr12	
Entries	0
Mean	0.8024
Std Dev	1.44



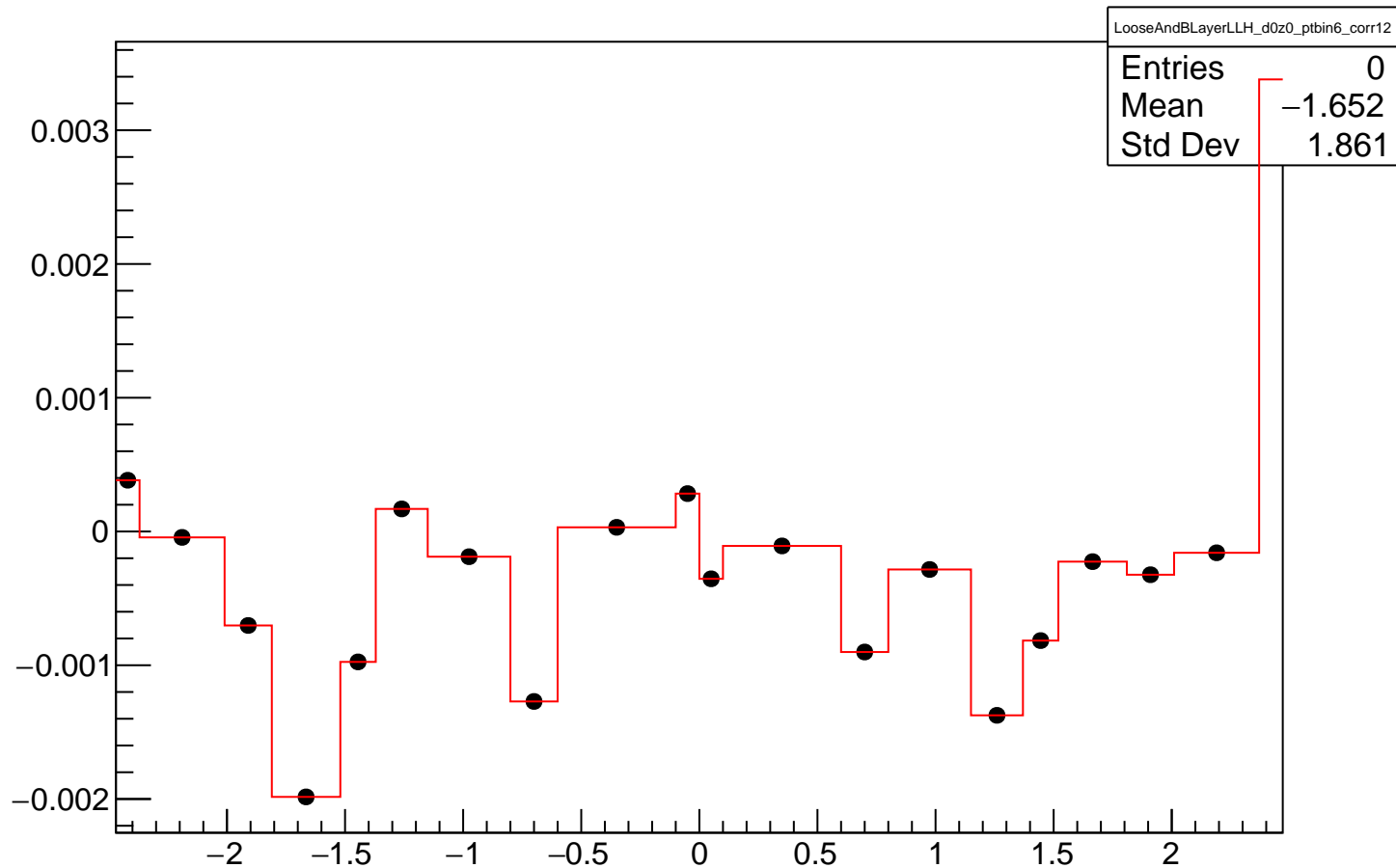


# FullSim\_LooseLLH\_d0z0\_corr12

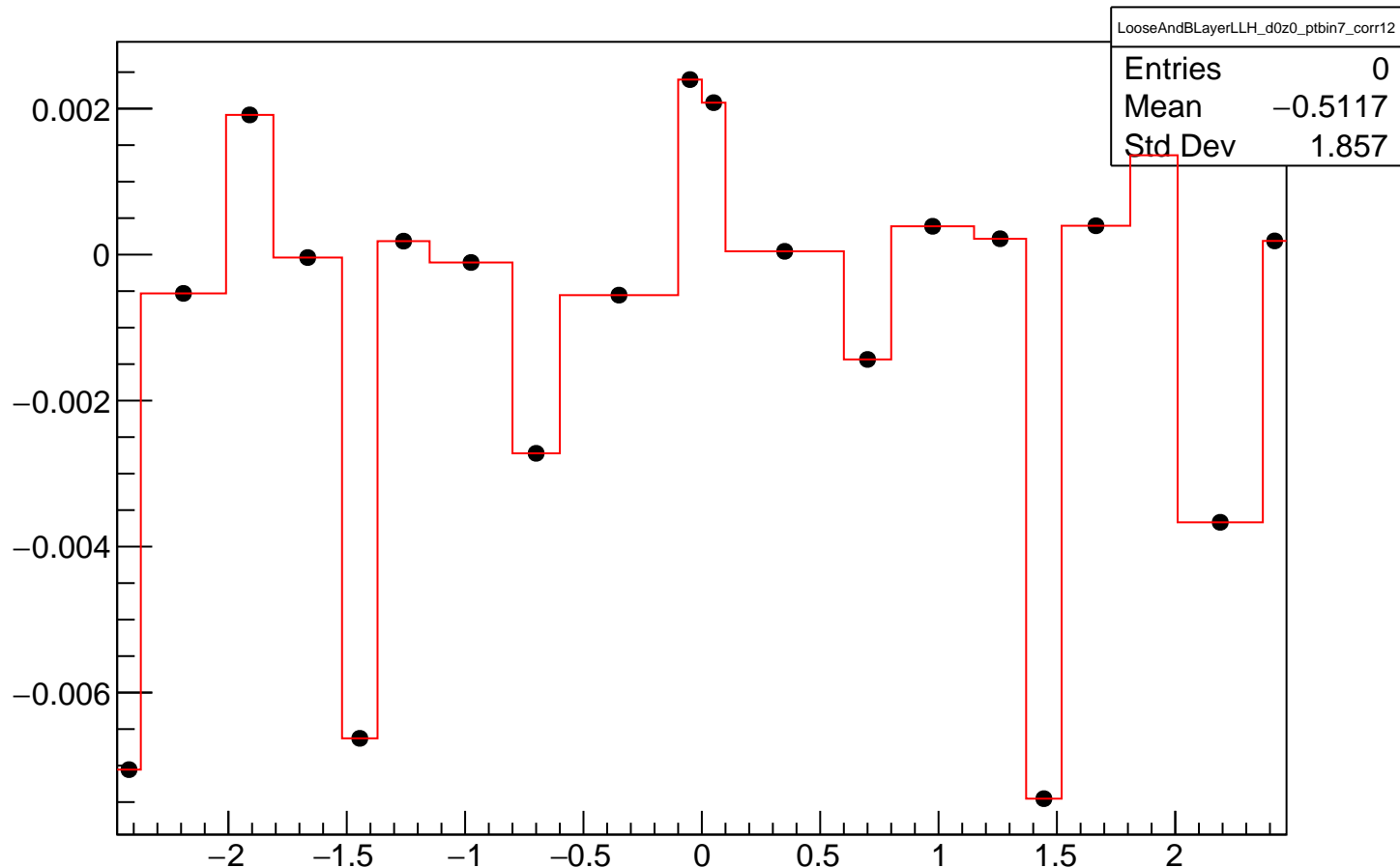
LooseAndBLayerLLH_d0z0_ptbin5_corr12	
Entries	0
Mean	-2.019
Std Dev	2.42



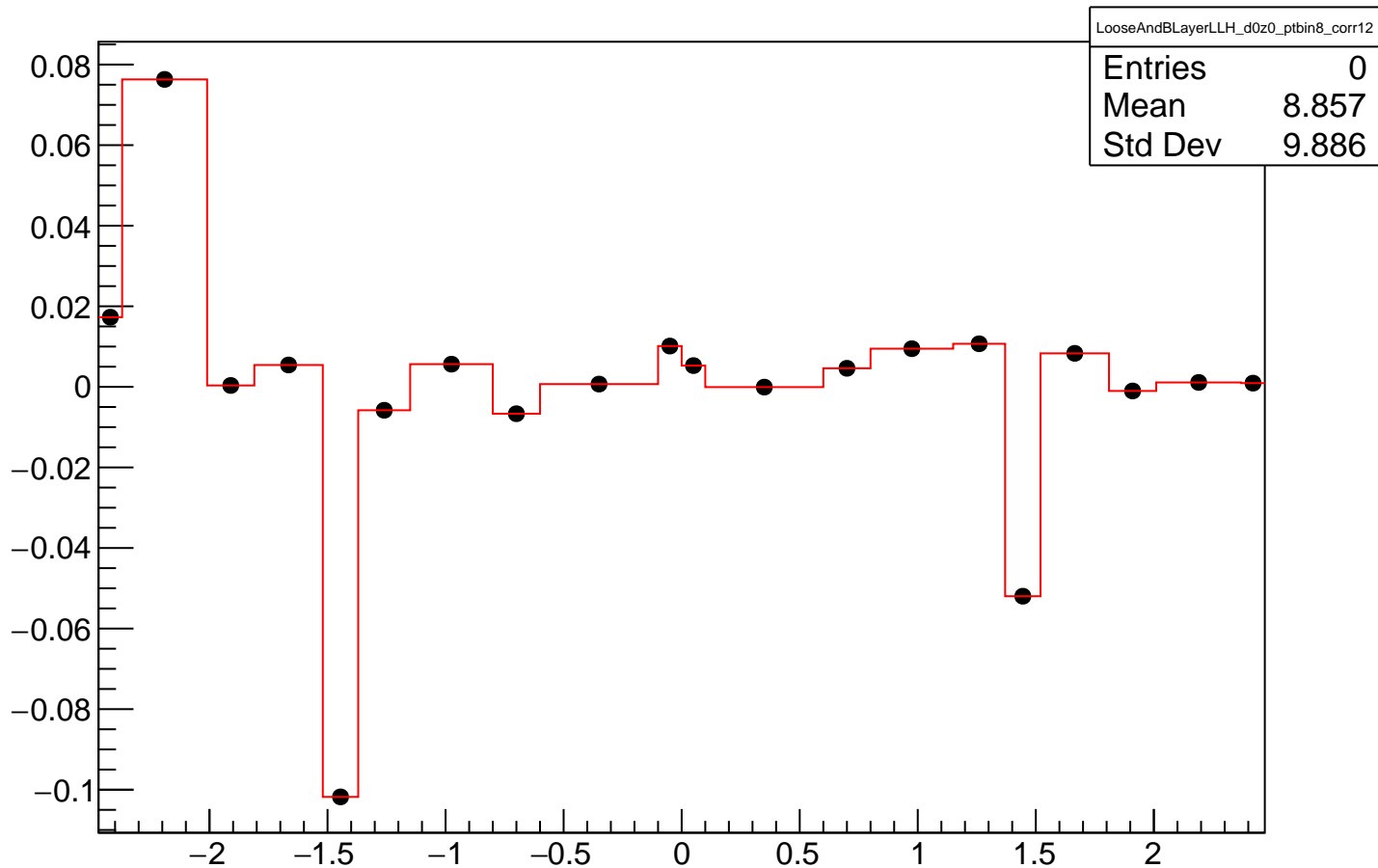
# FullSim\_LooseLLH\_d0z0\_corr12



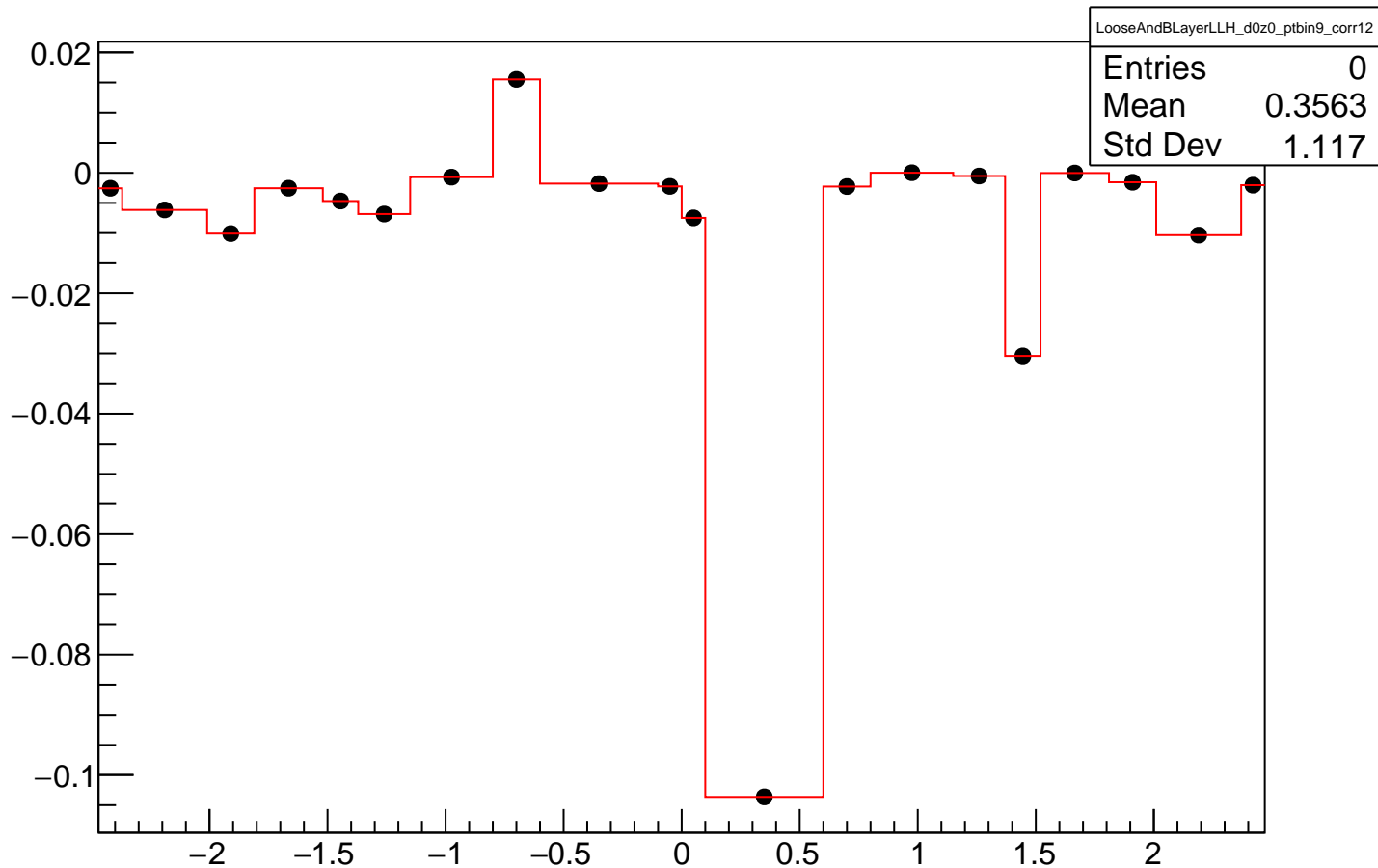
# FullSim\_LooseLLH\_d0z0\_corr12



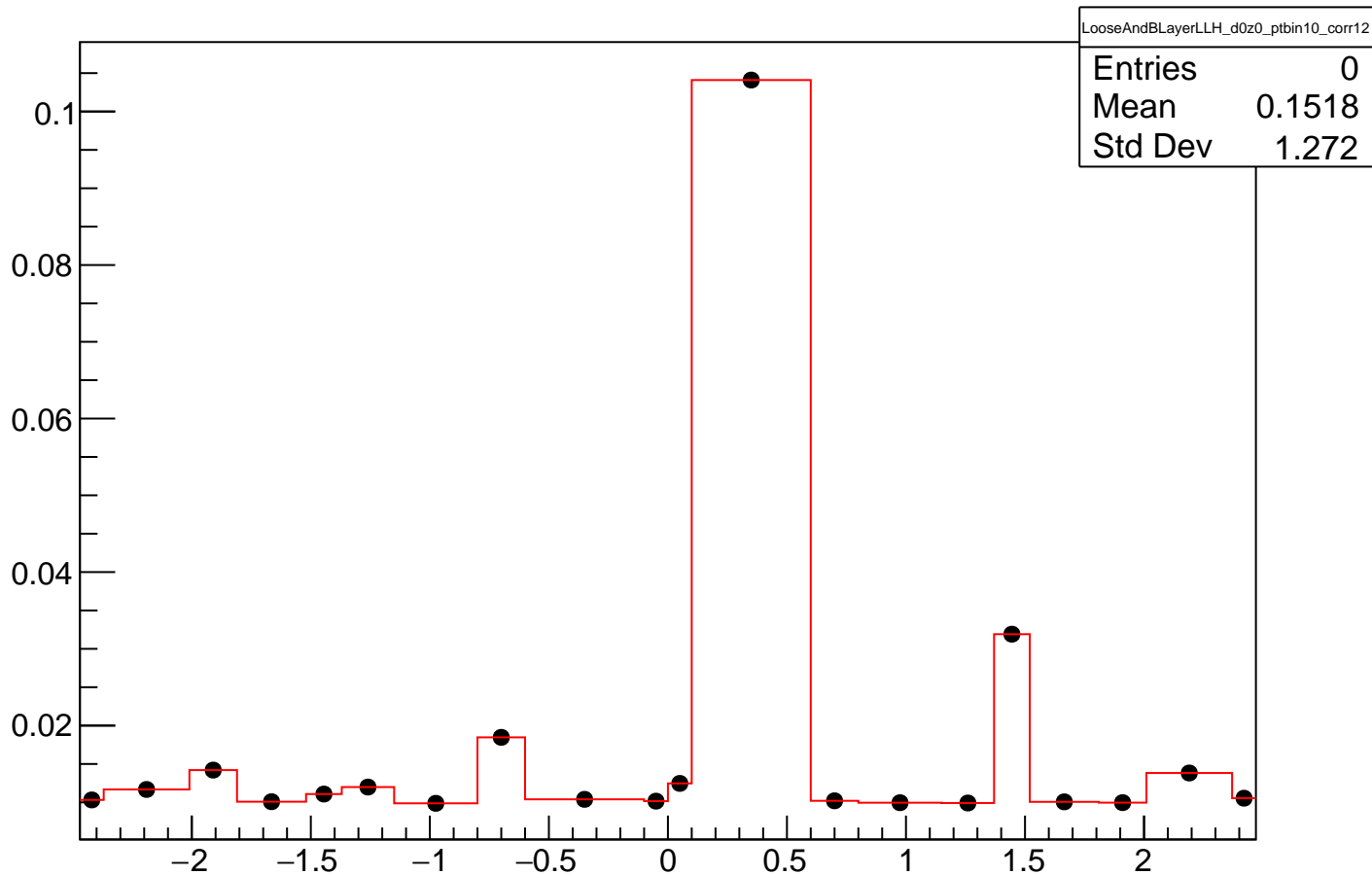
# FullSim\_LooseLLH\_d0z0\_corr12



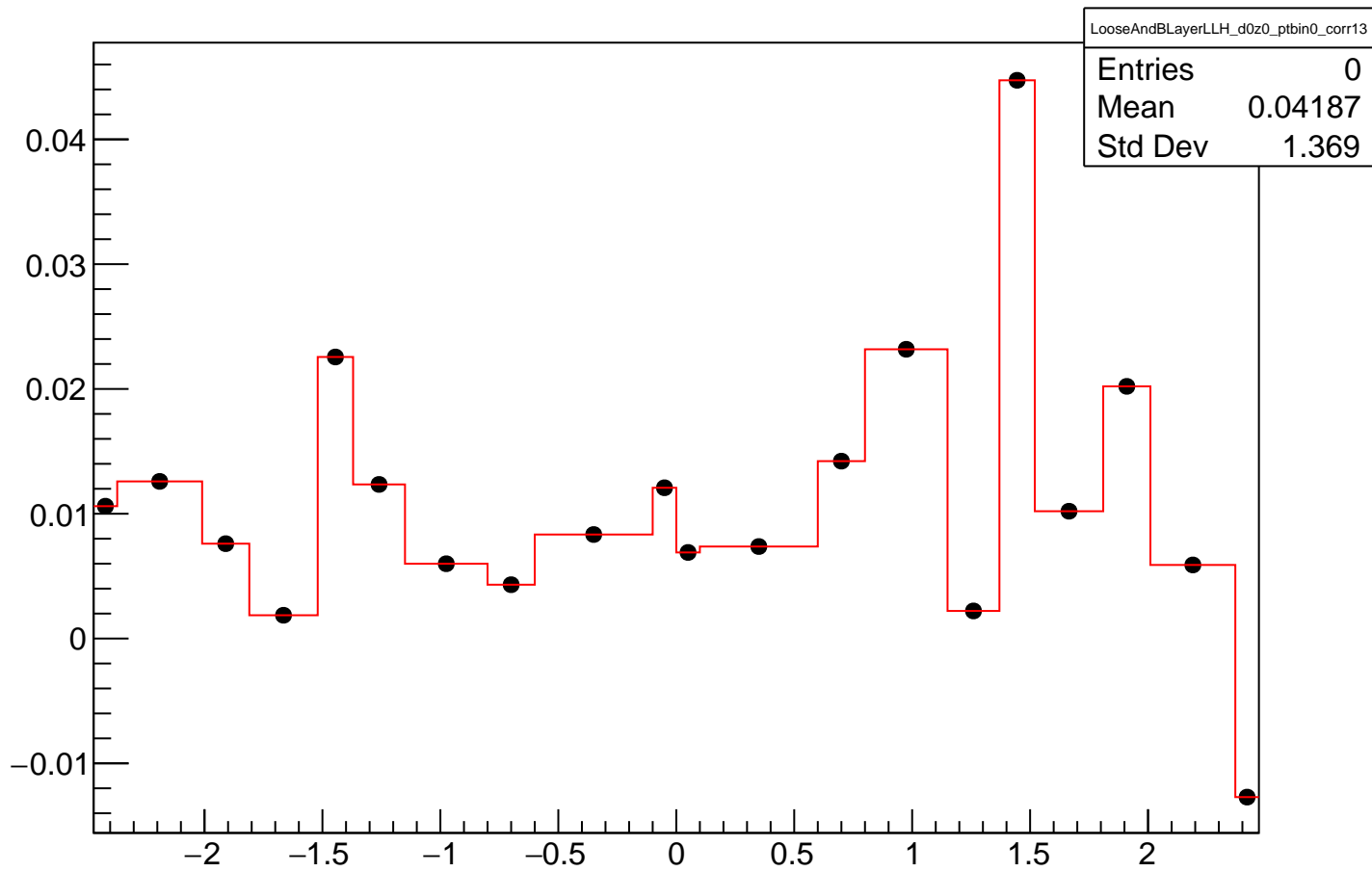
# FullSim\_LooseLLH\_d0z0\_corr12



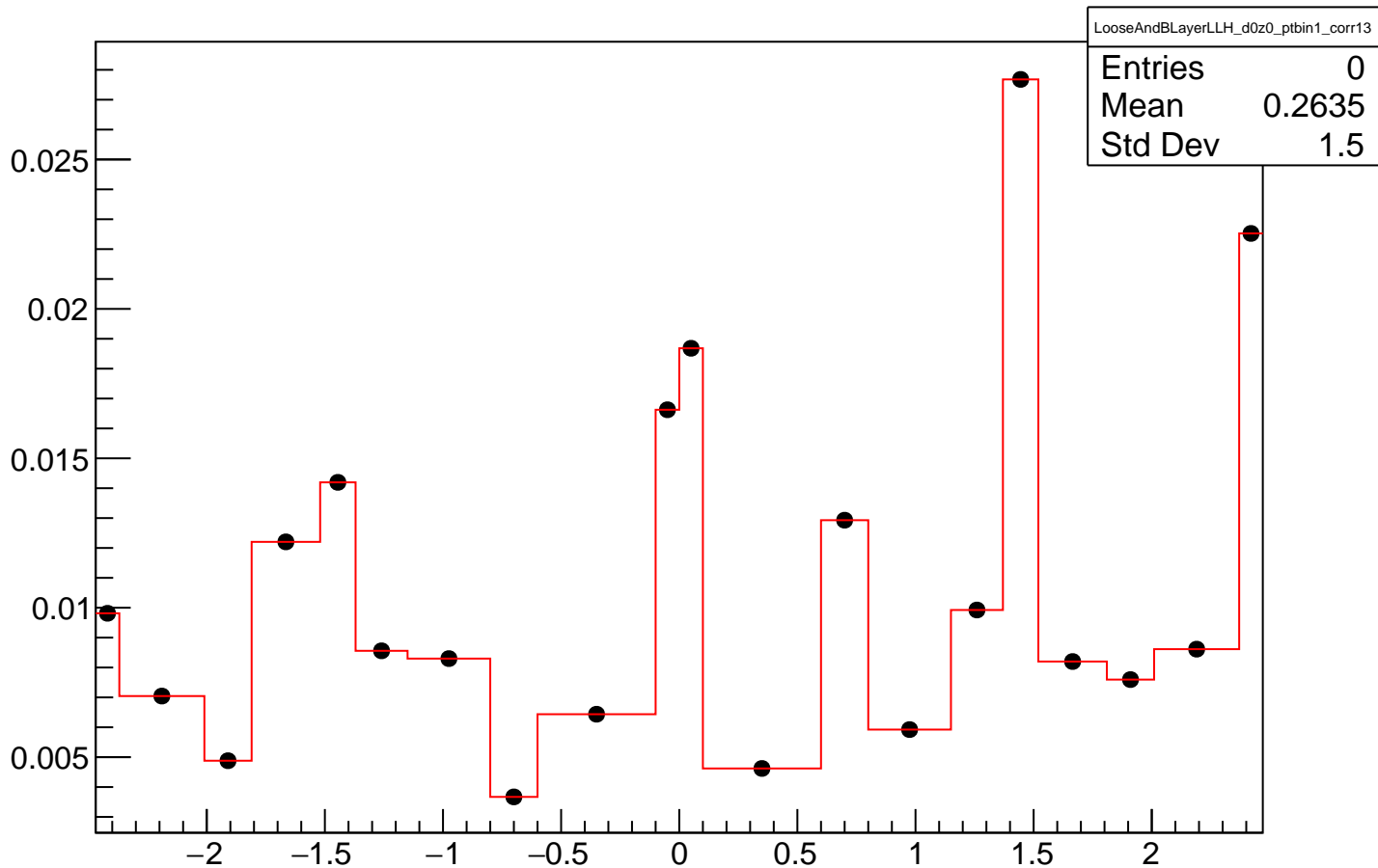
# FullSim\_LooseLLH\_d0z0\_corr12



# FullSim\_LooseLLH\_d0z0\_corr13

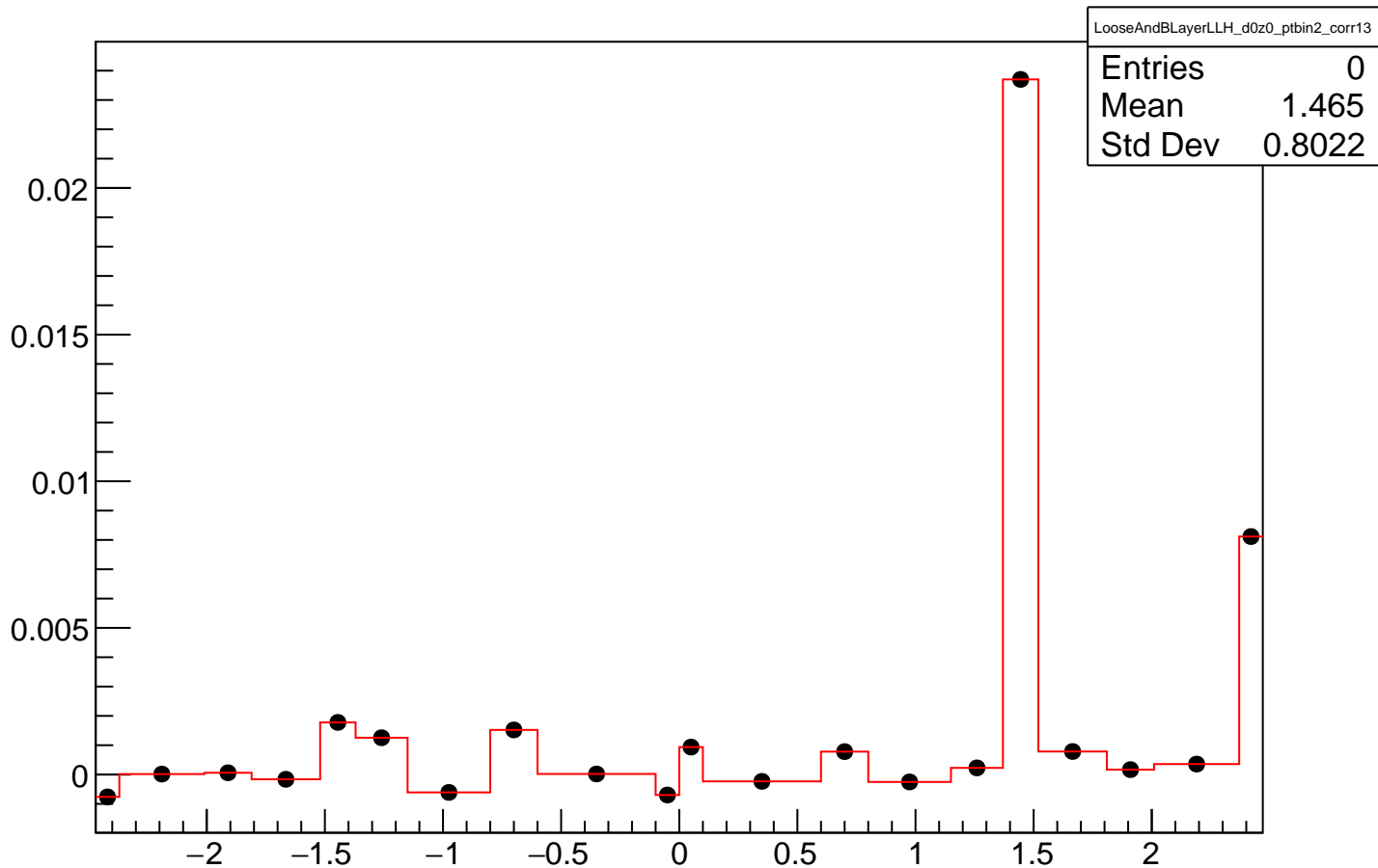


# FullSim\_LooseLLH\_d0z0\_corr13

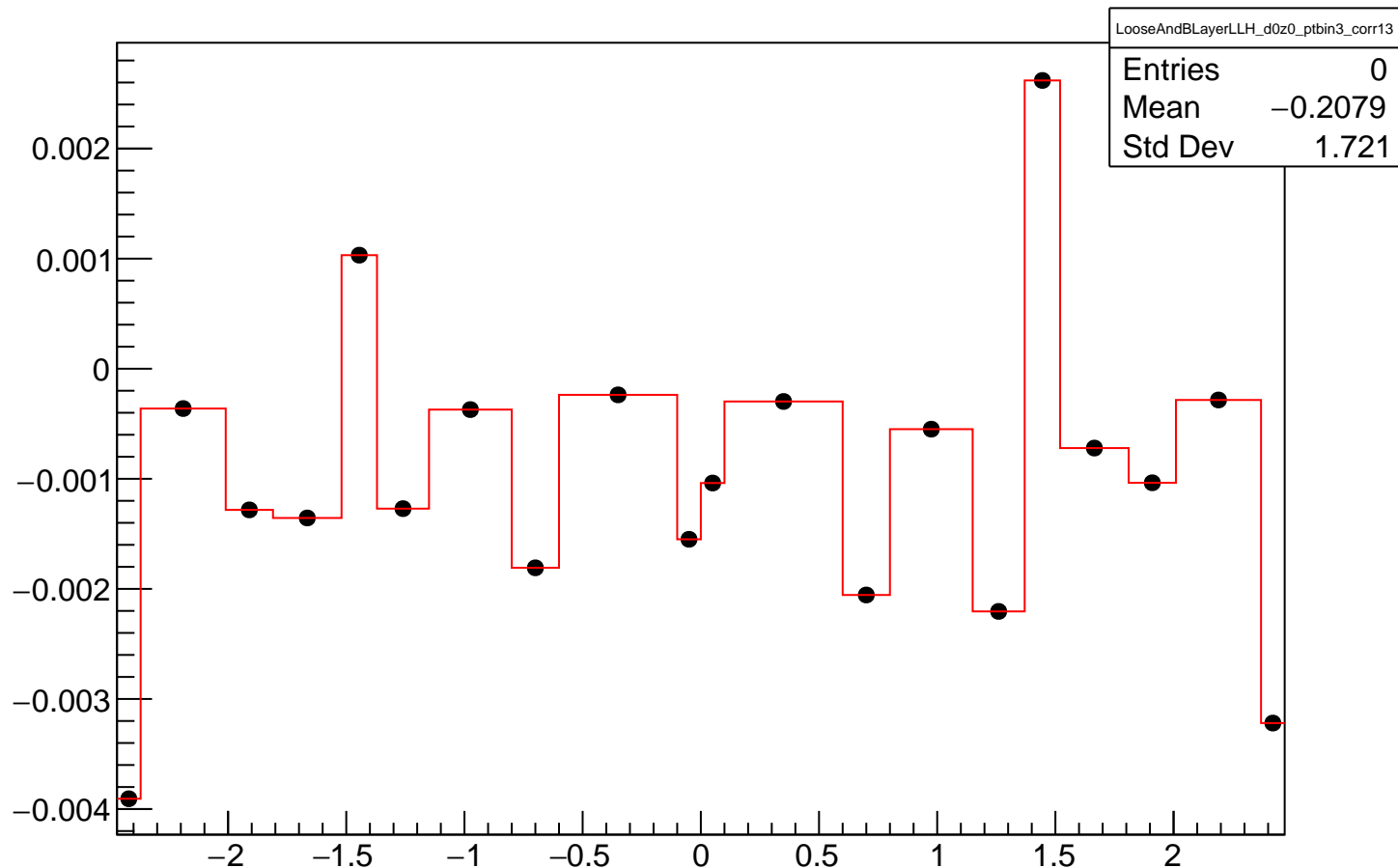




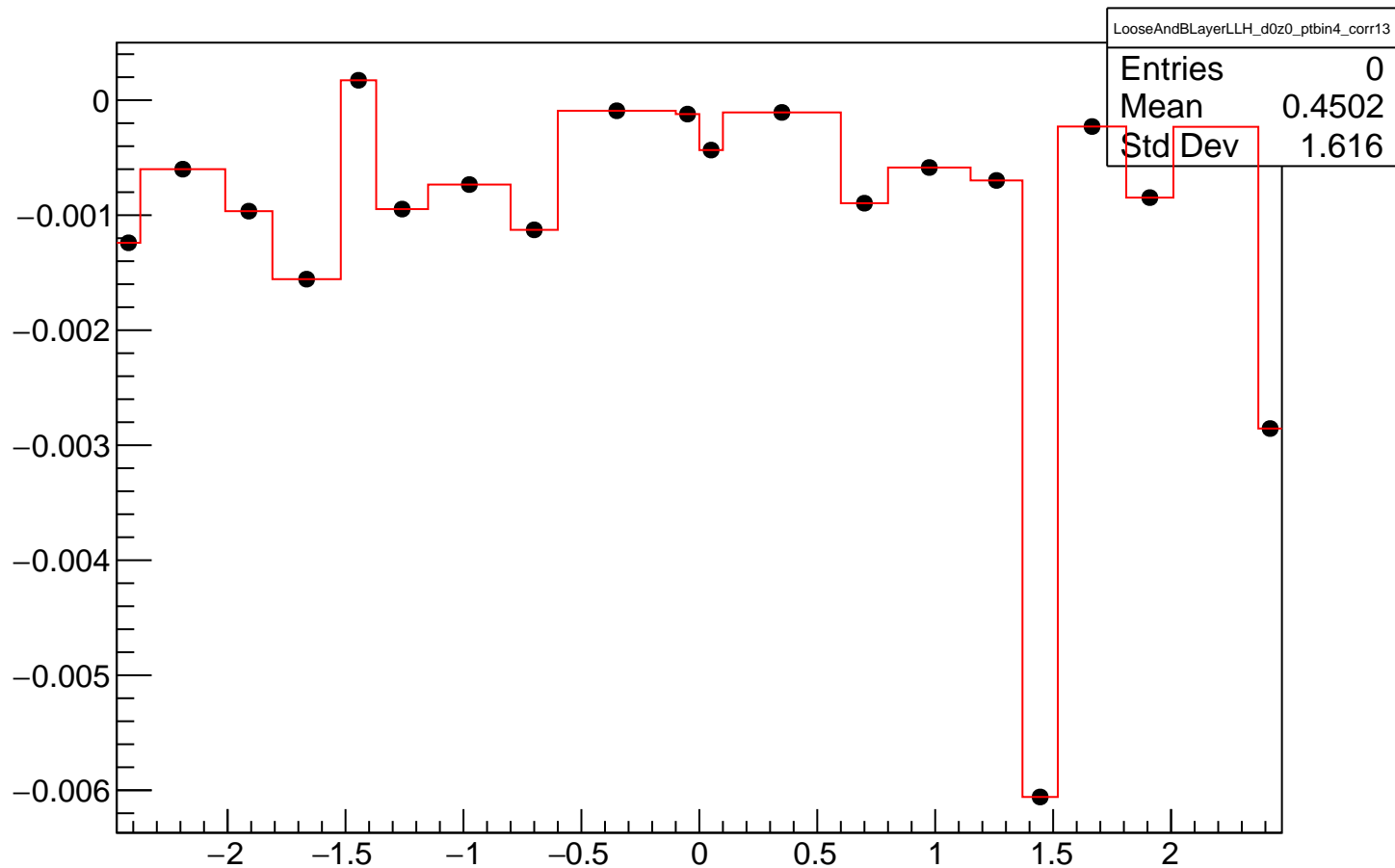
# FullSim\_LooseLLH\_d0z0\_corr13



# FullSim\_LooseLLH\_d0z0\_corr13

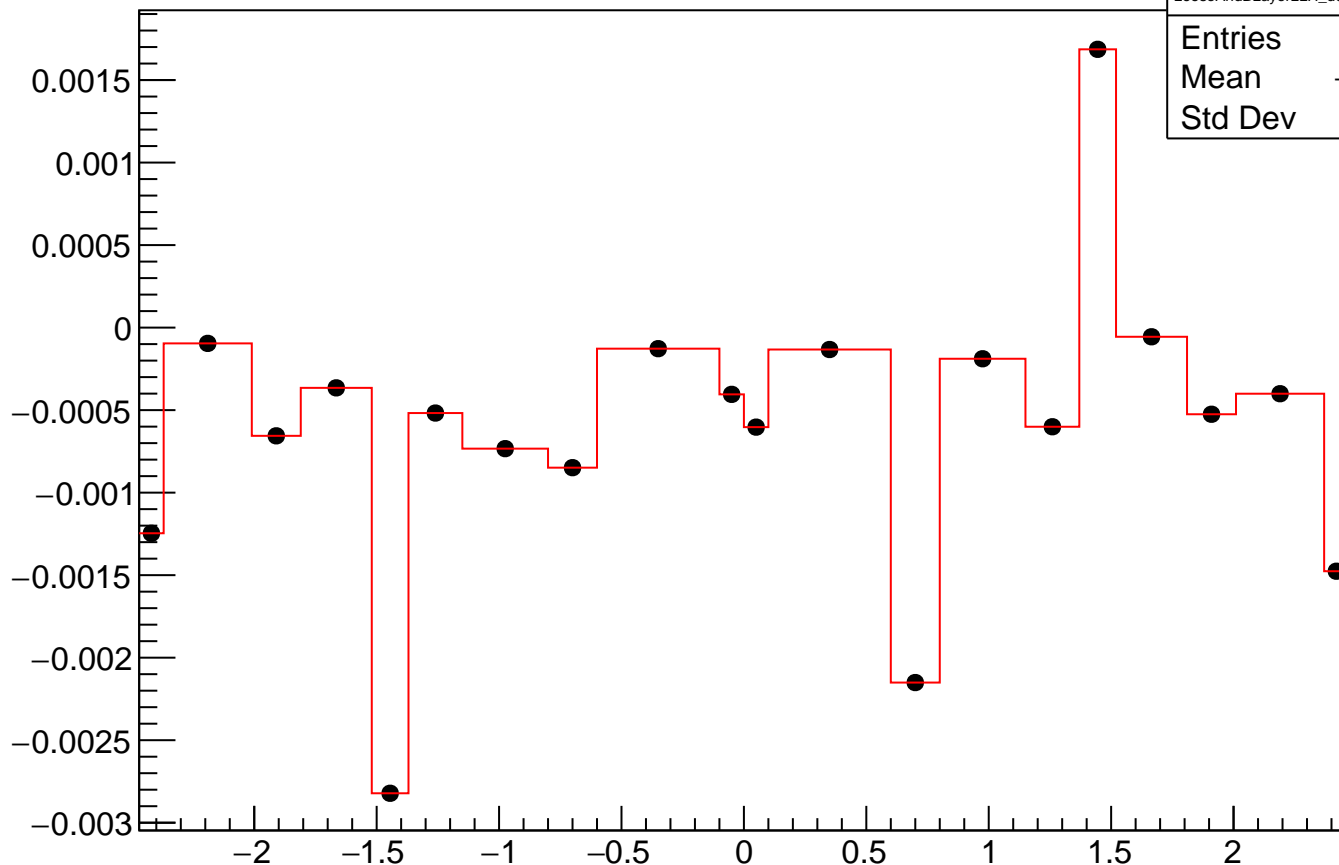


# FullSim\_LooseLLH\_d0z0\_corr13

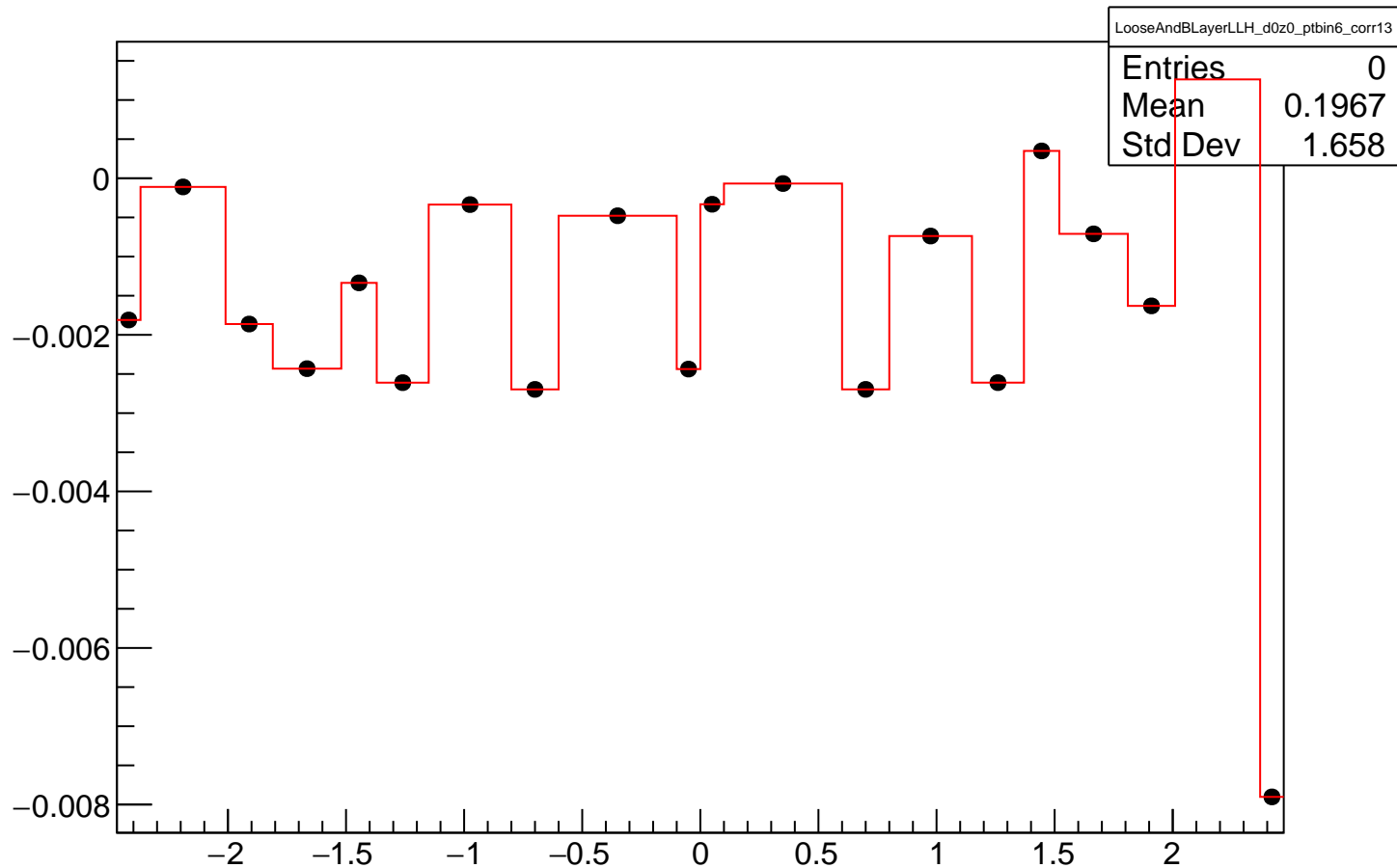


# FullSim\_LooseLLH\_d0z0\_corr13

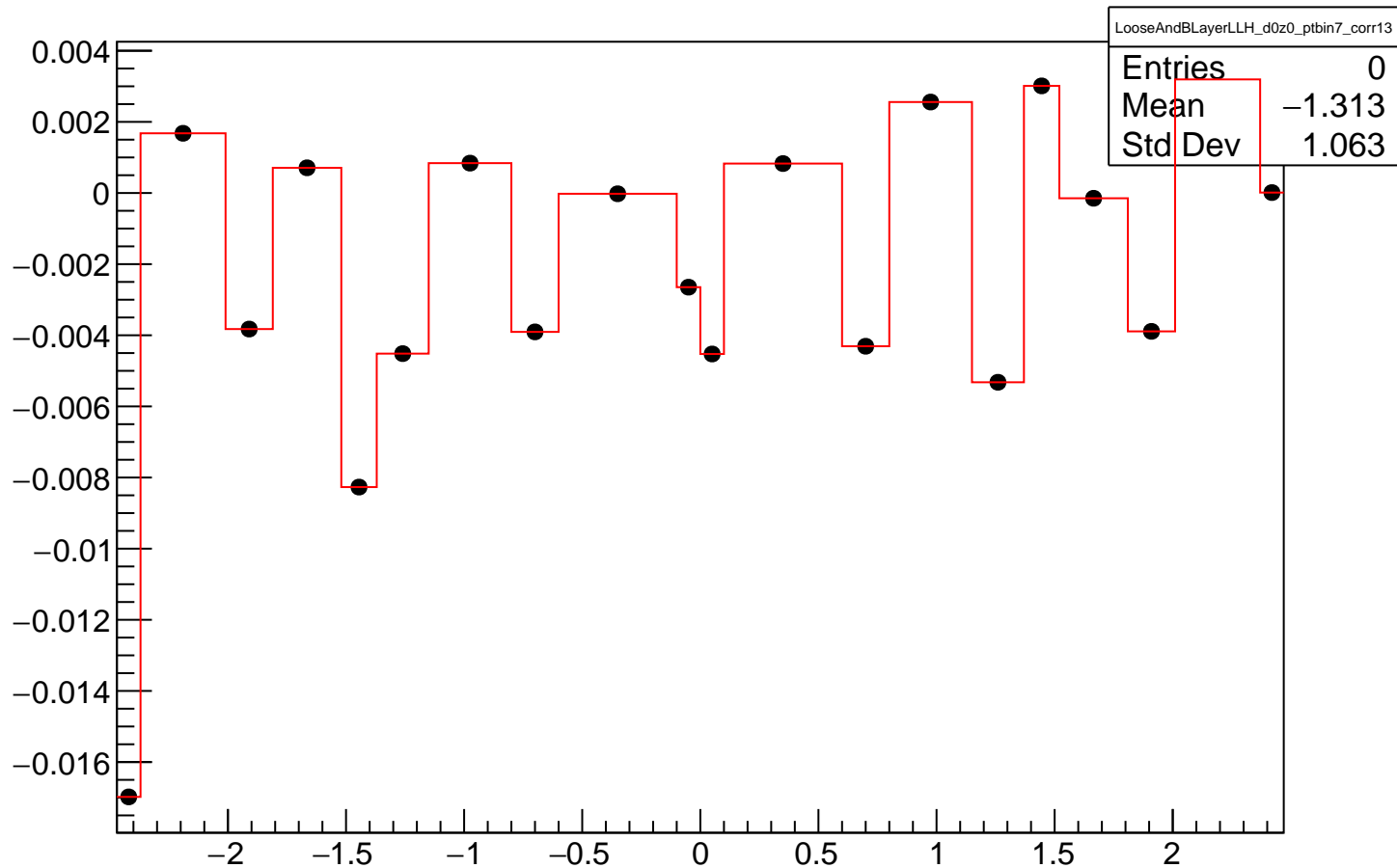
LooseAndBLayerLLH_d0z0_ptbin5_corr13	
Entries	0
Mean	-0.4532
Std Dev	1.506



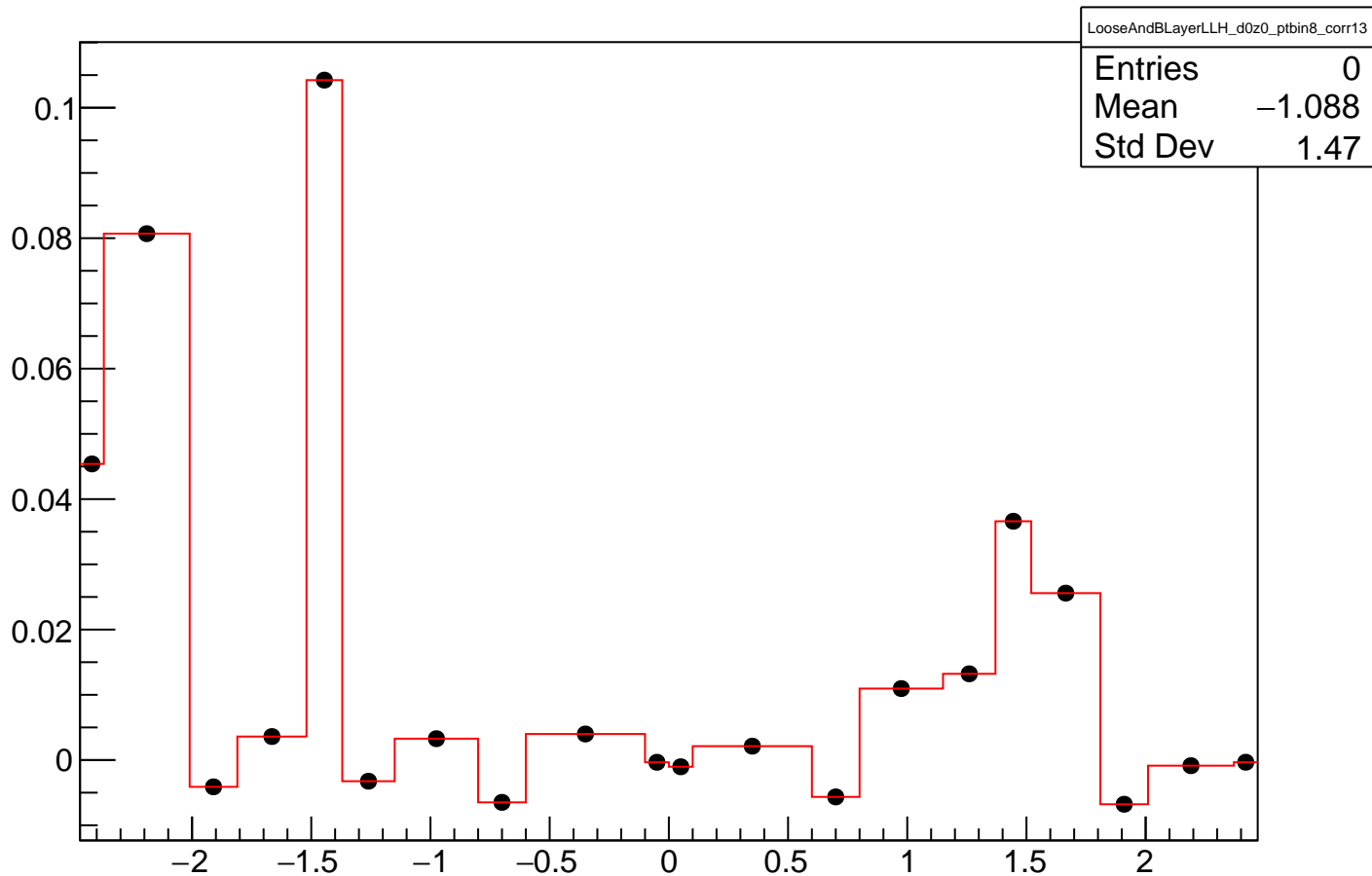
# FullSim\_LooseLLH\_d0z0\_corr13



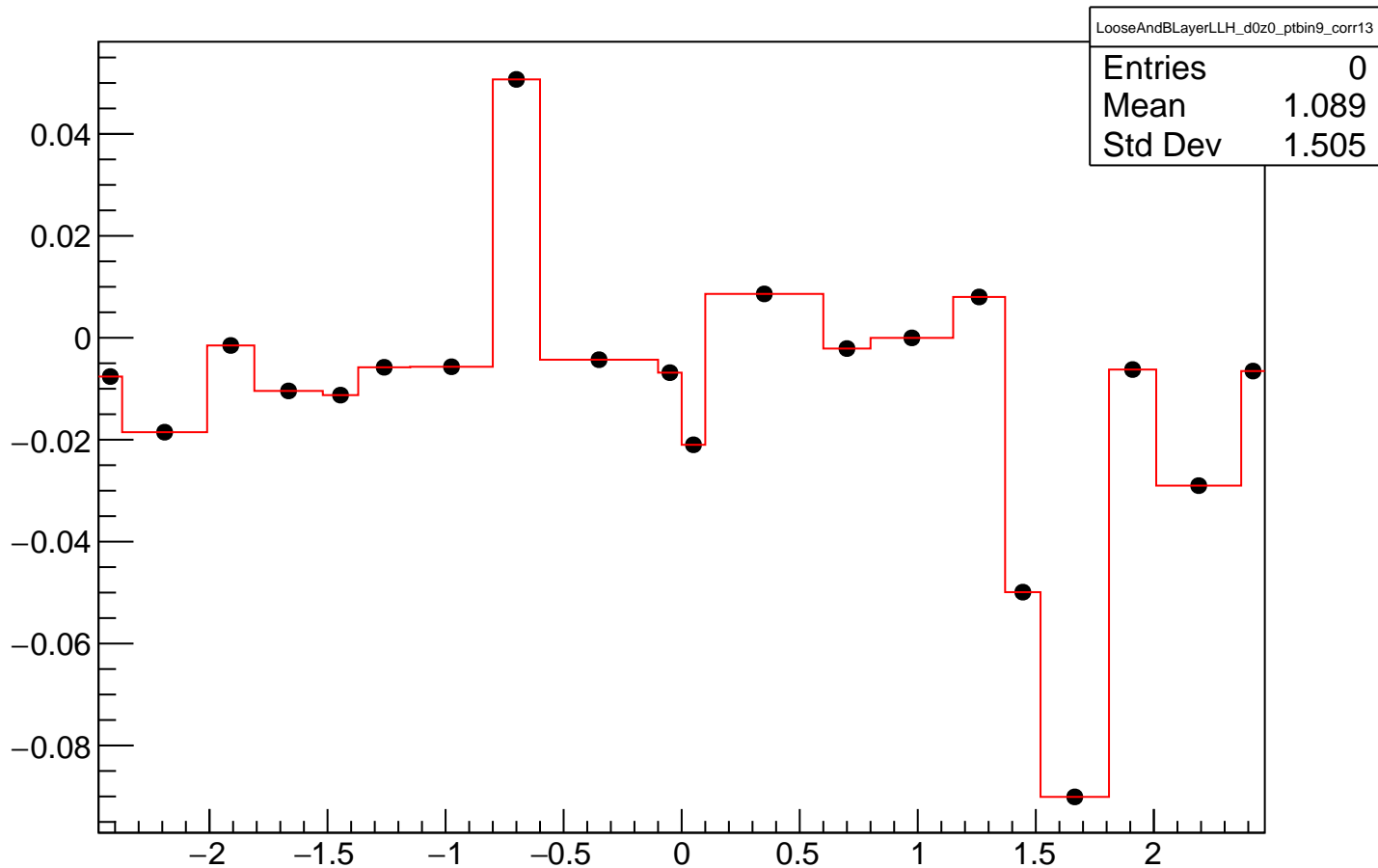
# FullSim\_LooseLLH\_d0z0\_corr13



# FullSim\_LooseLLH\_d0z0\_corr13

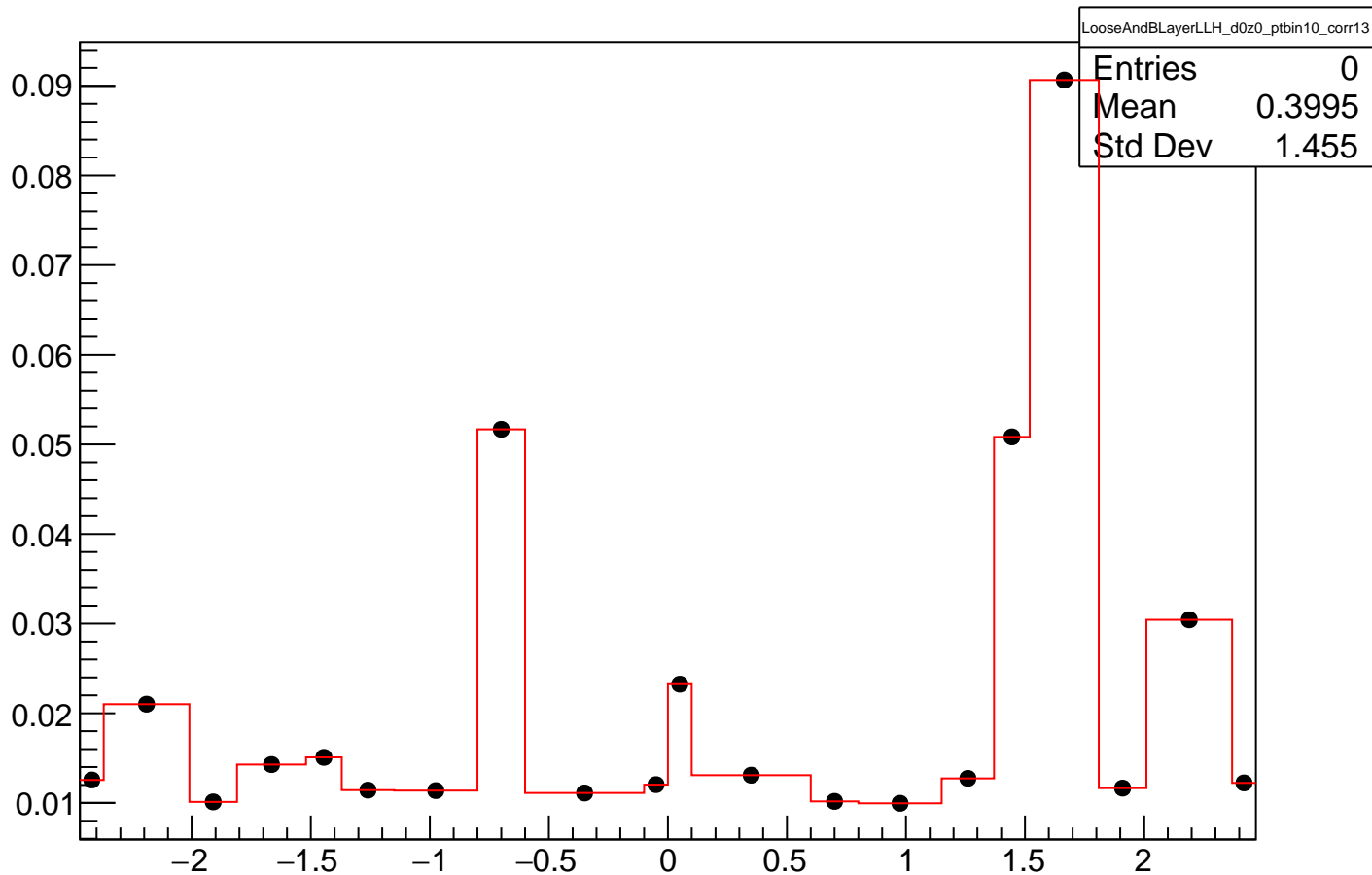


# FullSim\_LooseLLH\_d0z0\_corr13

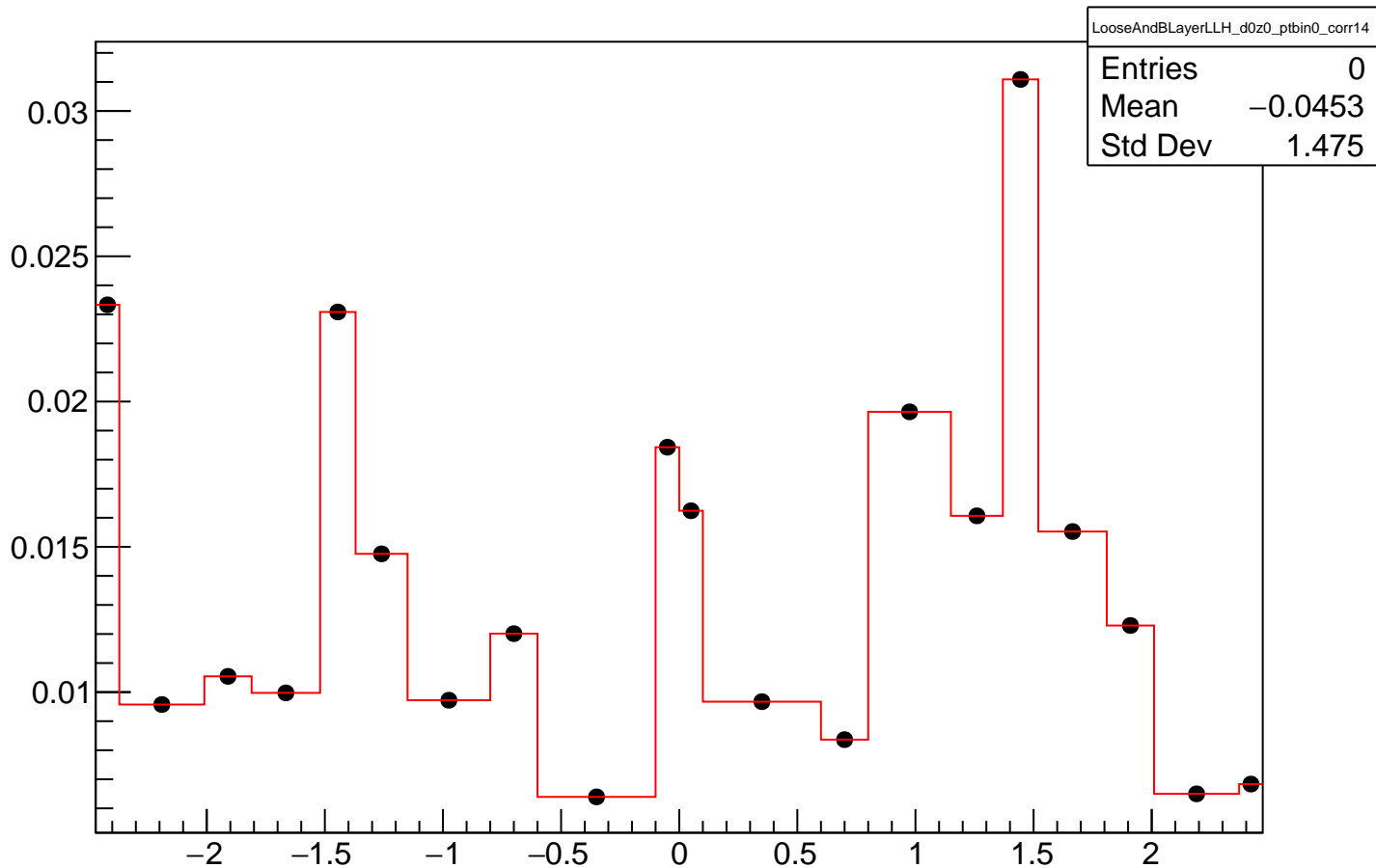




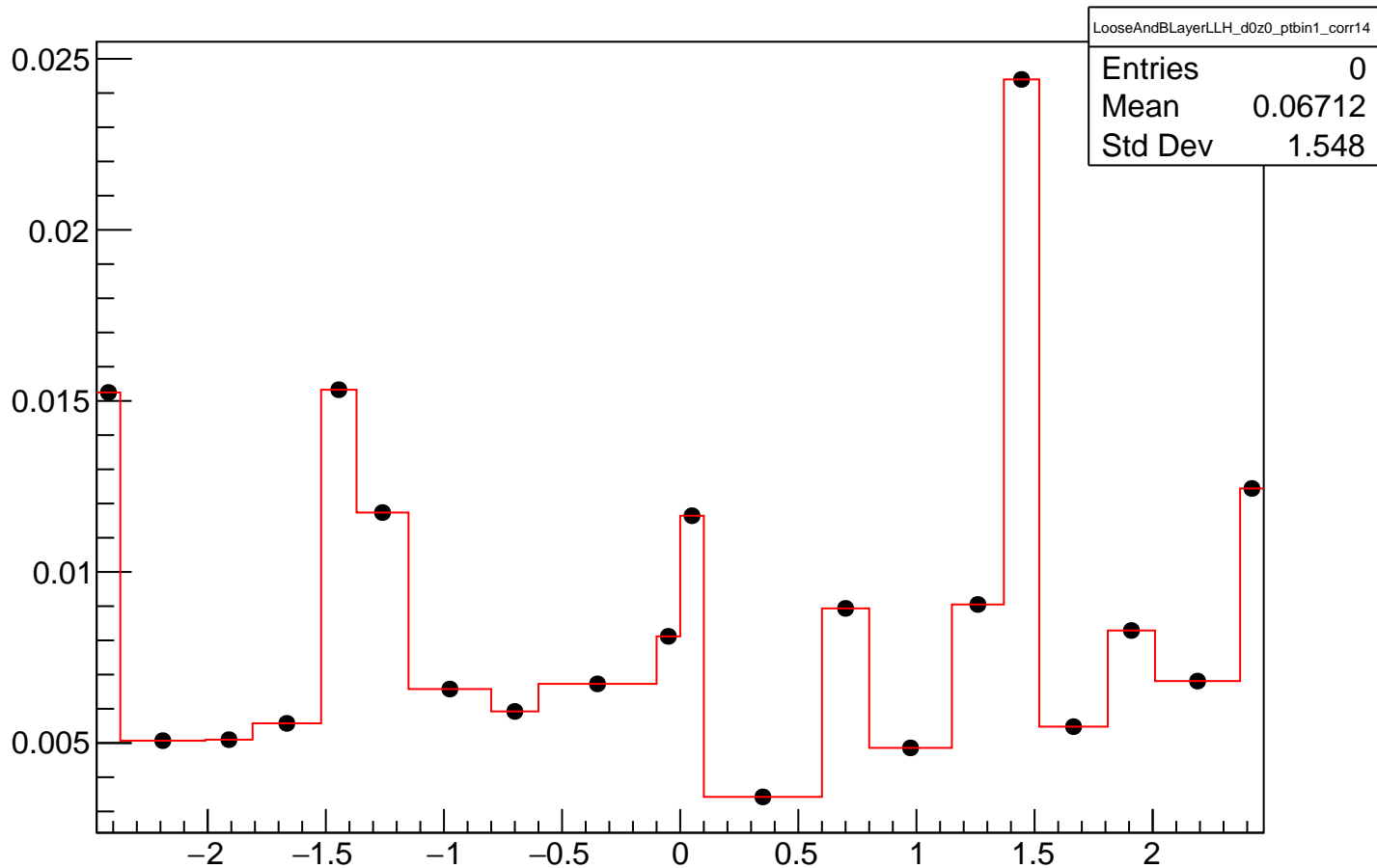
# FullSim\_LooseLLH\_d0z0\_corr13



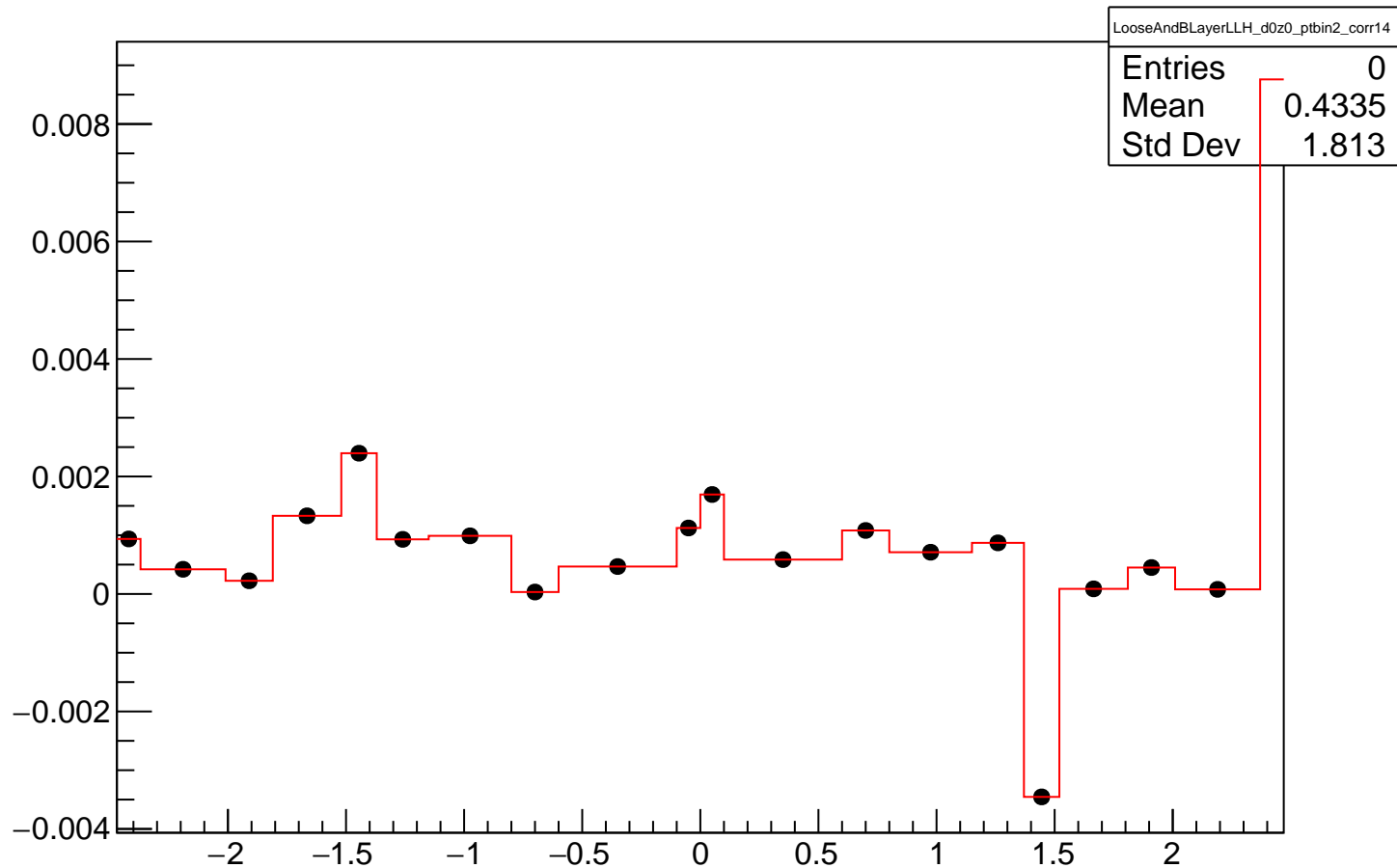
# FullSim\_LooseLLH\_d0z0\_corr14



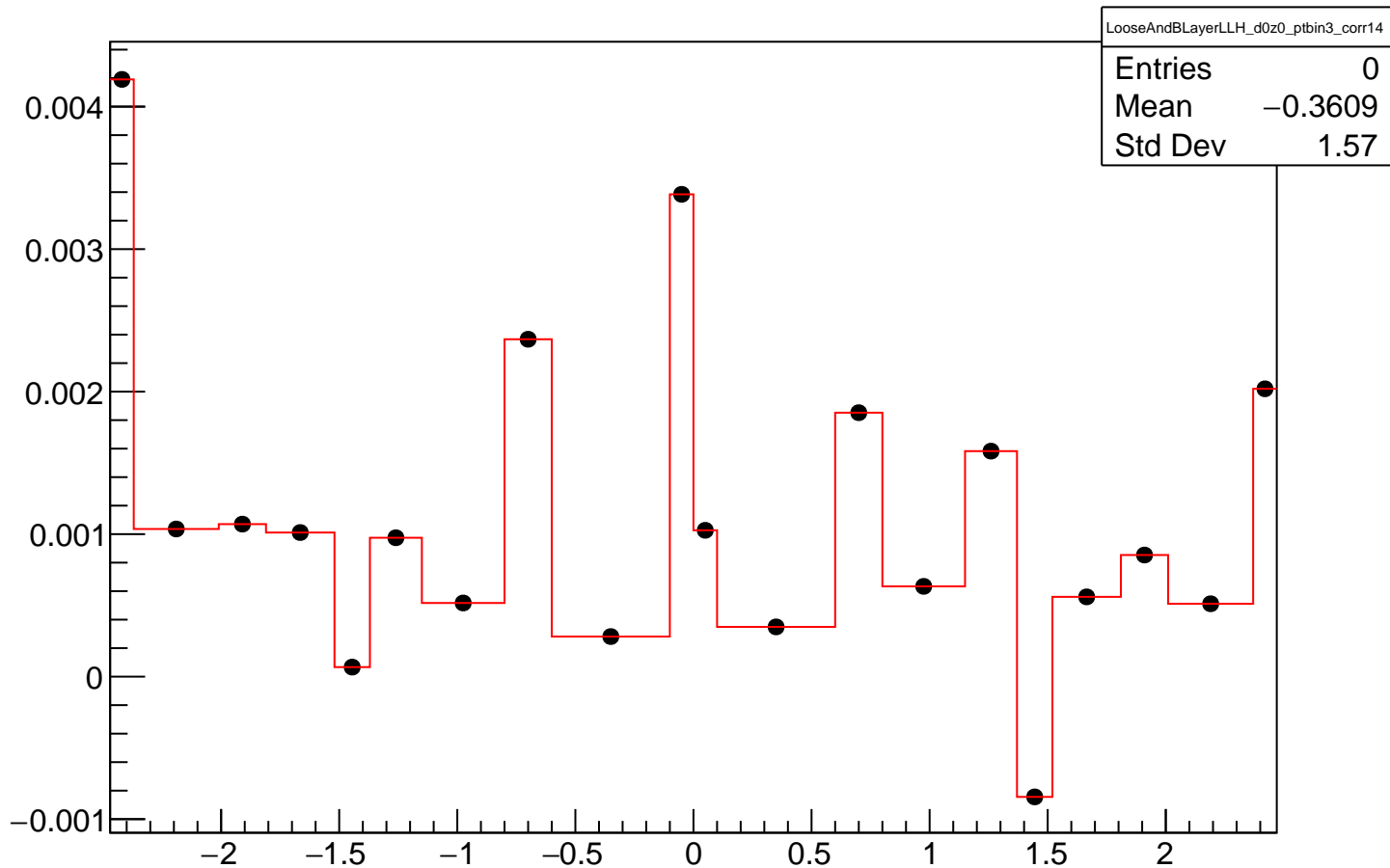
# FullSim\_LooseLLH\_d0z0\_corr14



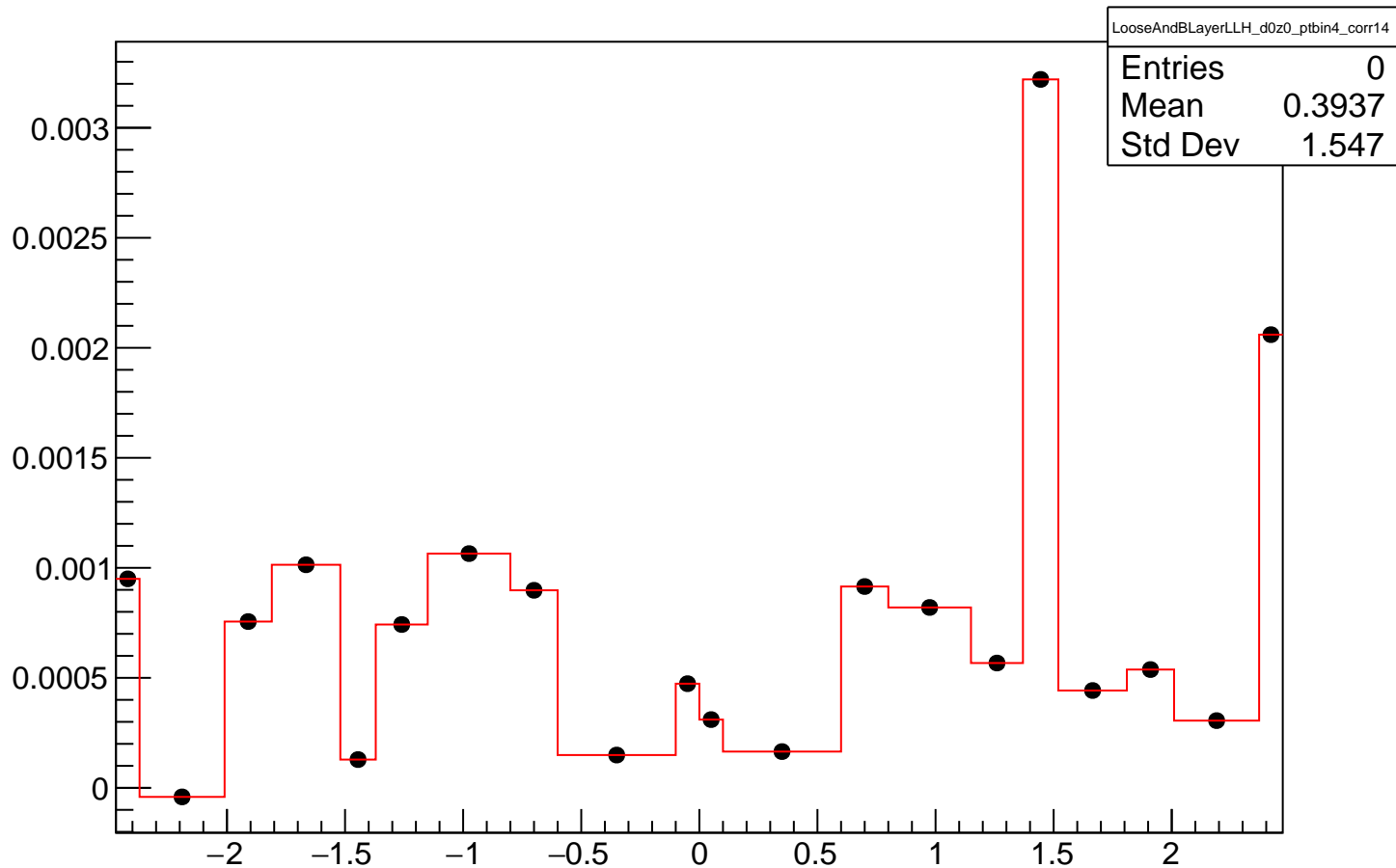
# FullSim\_LooseLLH\_d0z0\_corr14



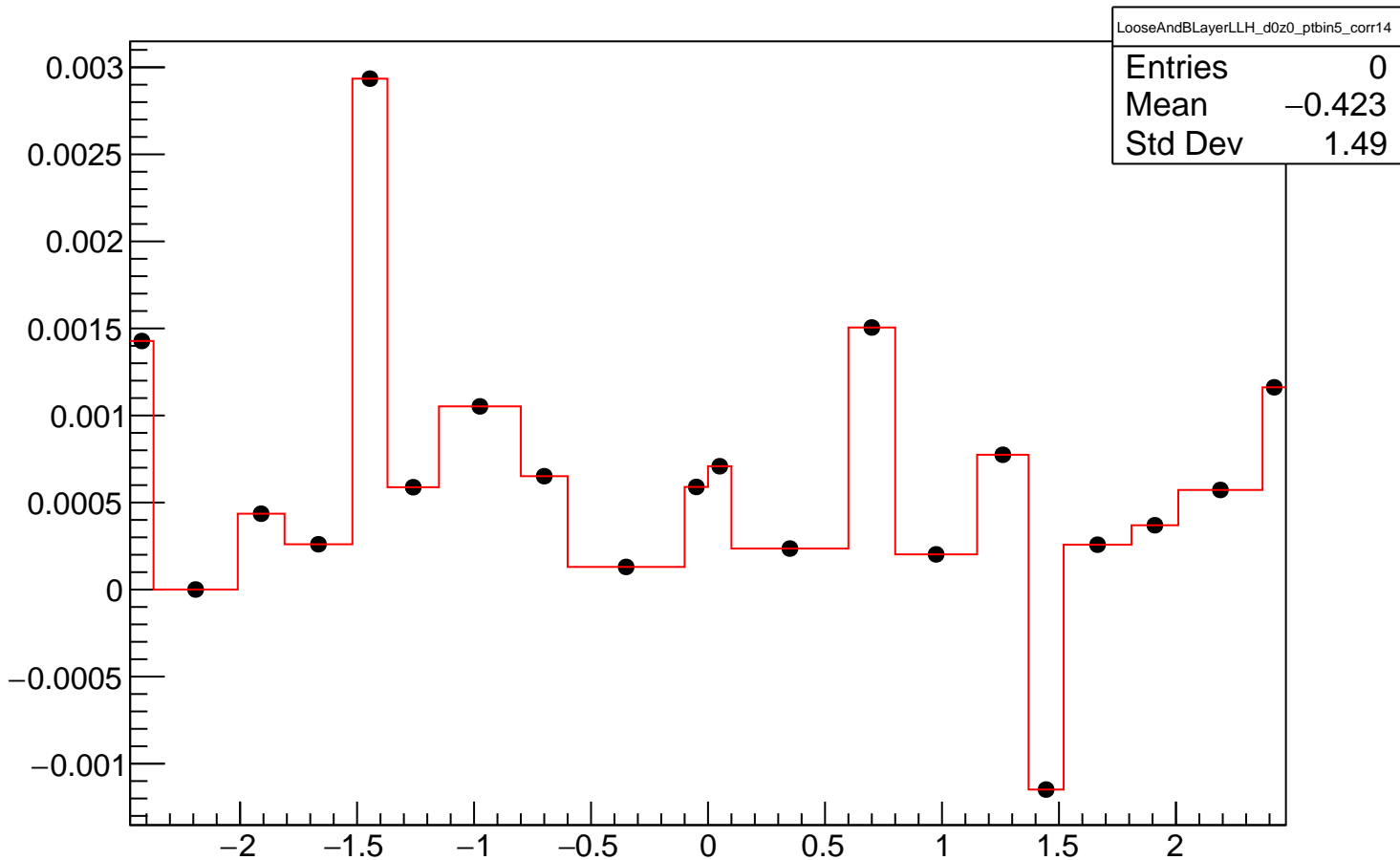
# FullSim\_LooseLLH\_d0z0\_corr14



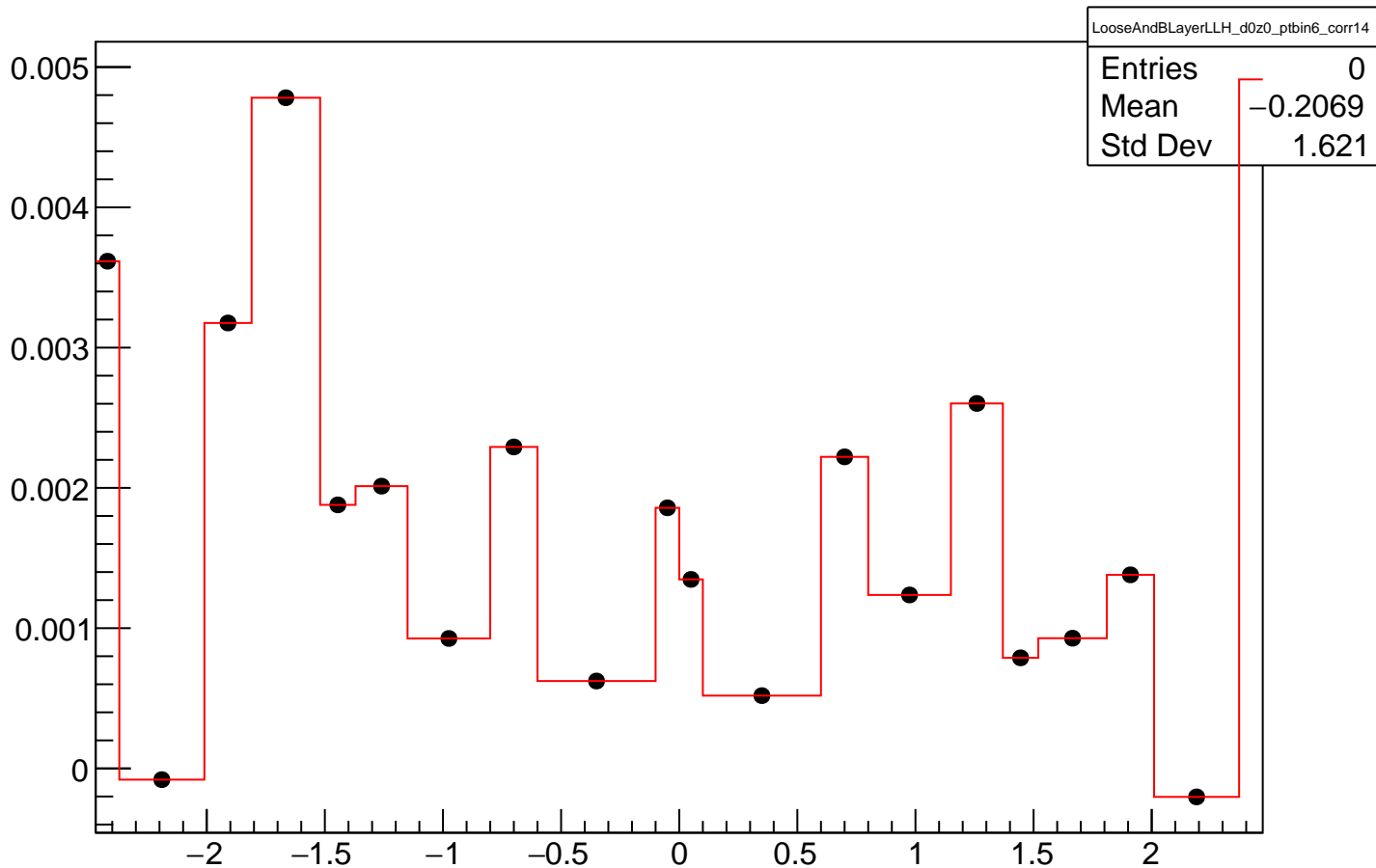
# FullSim\_LooseLLH\_d0z0\_corr14



# FullSim\_LooseLLH\_d0z0\_corr14

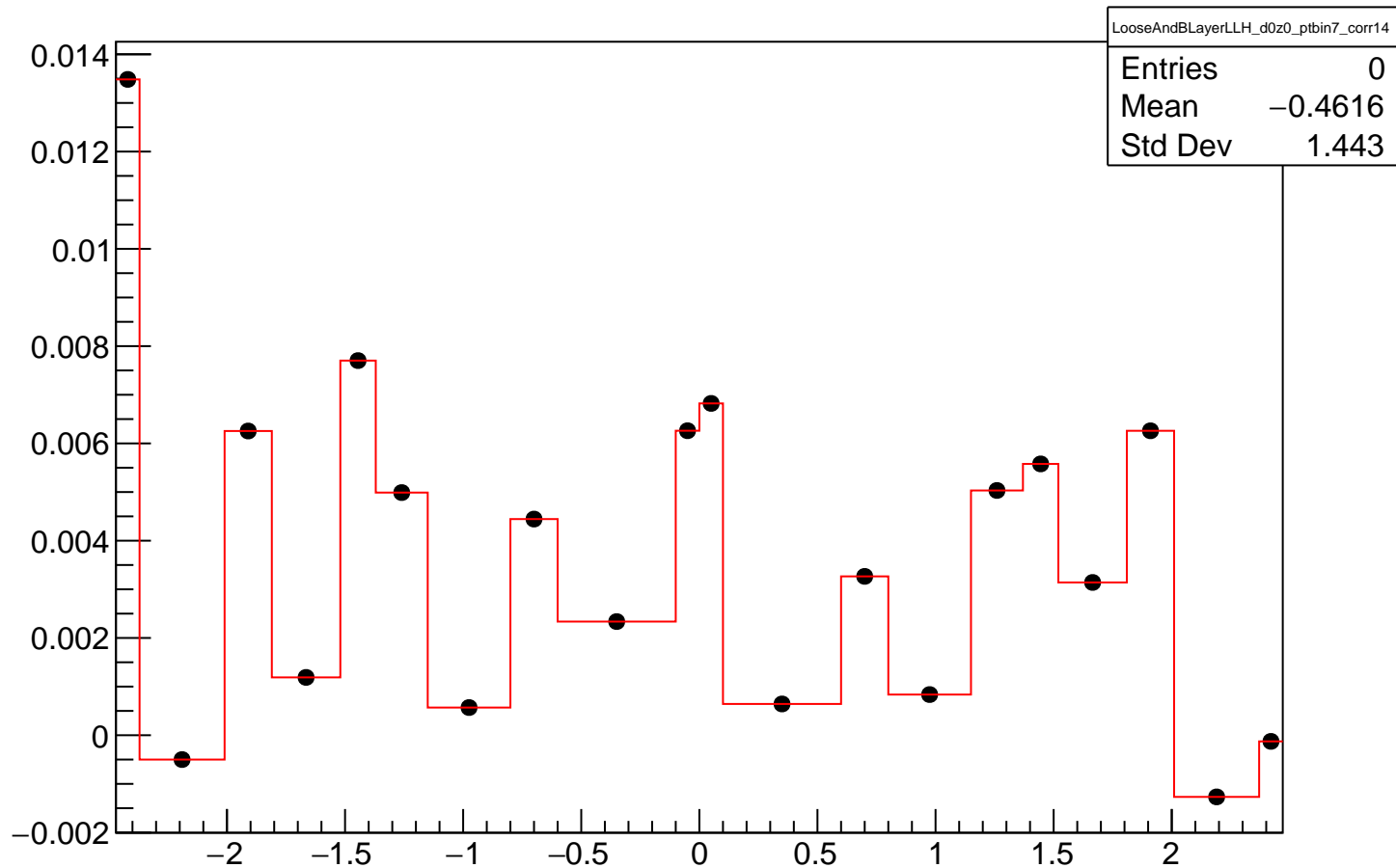


# FullSim\_LooseLLH\_d0z0\_corr14

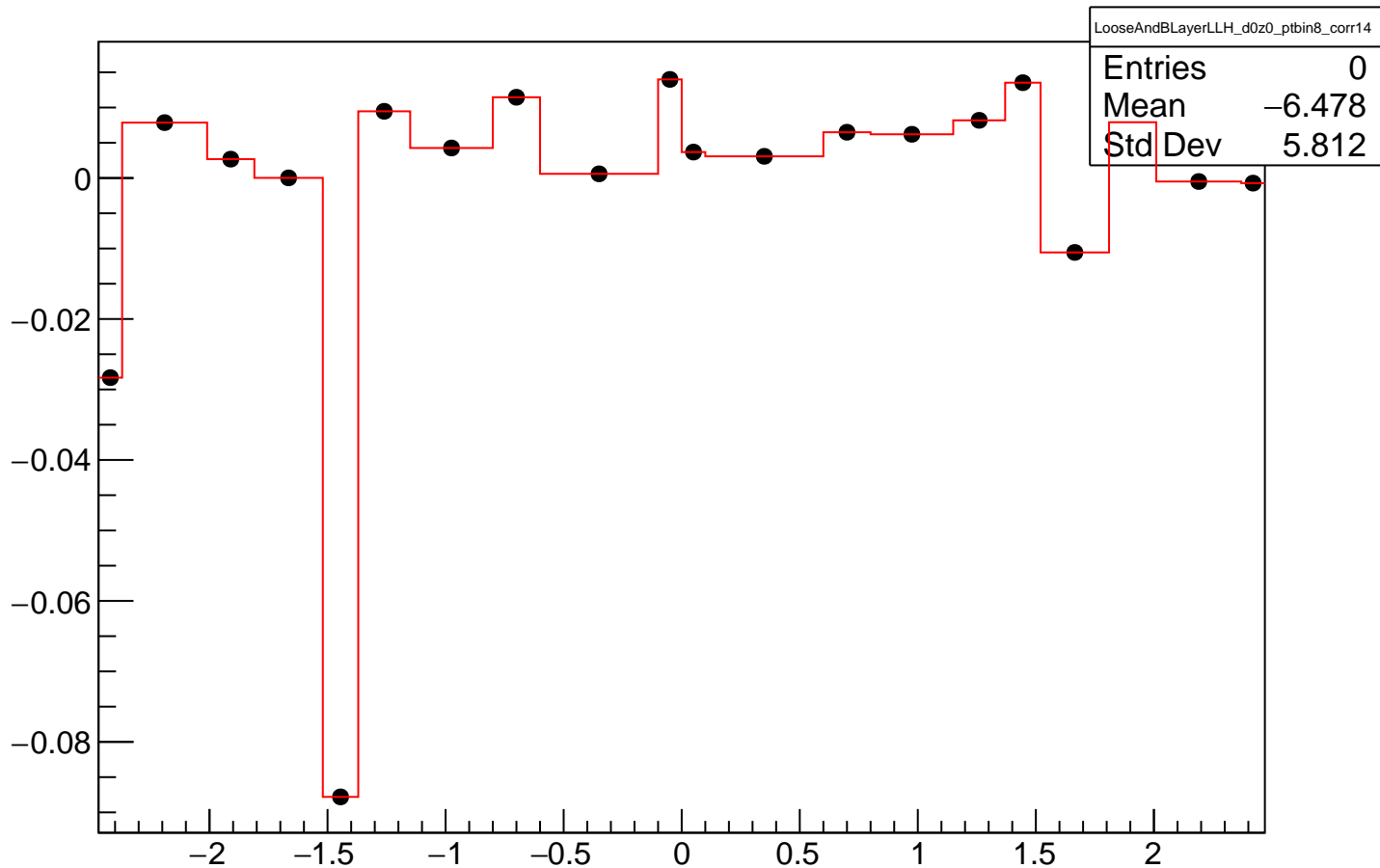




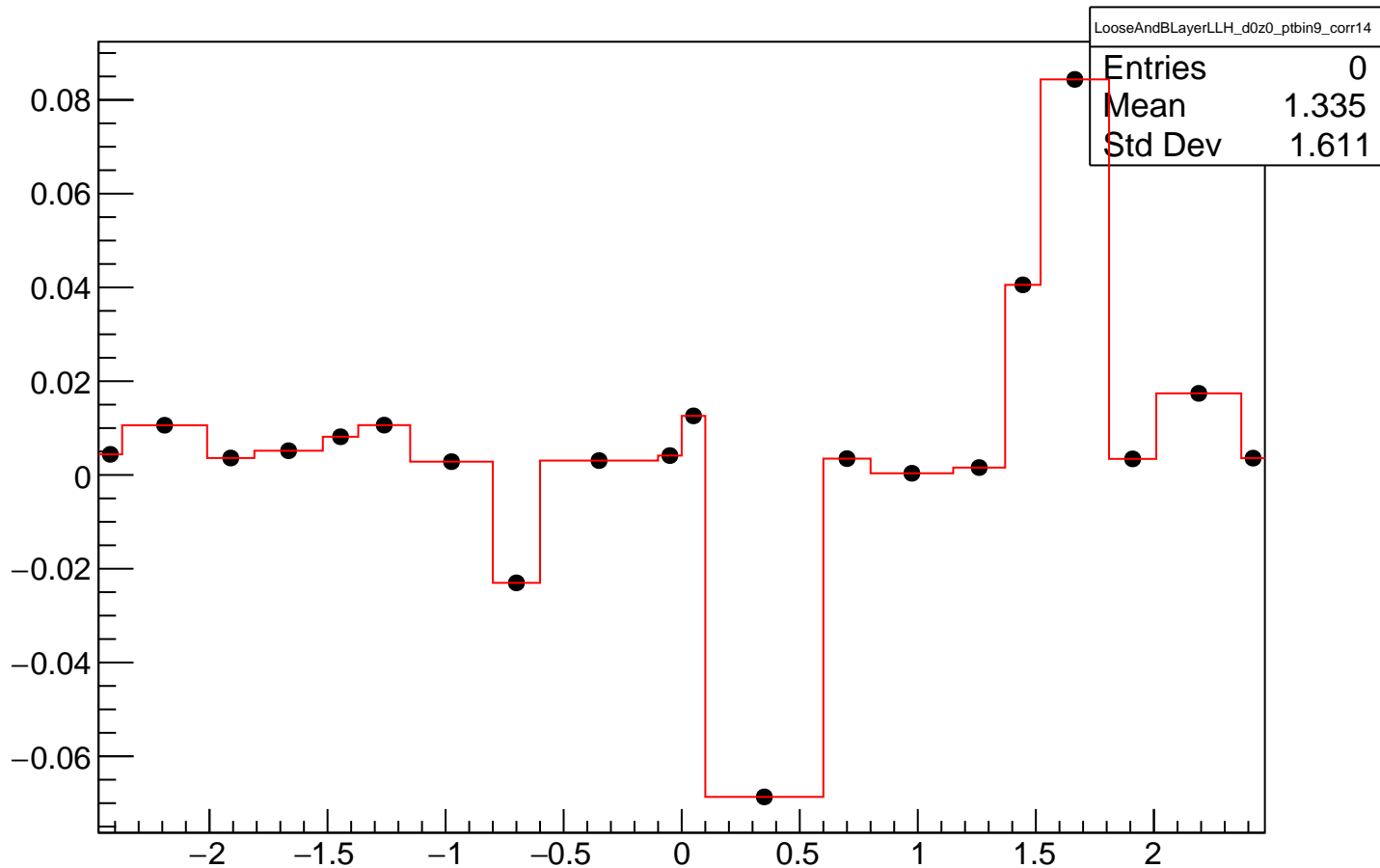
# FullSim\_LooseLLH\_d0z0\_corr14



# FullSim\_LooseLLH\_d0z0\_corr14



# FullSim\_LooseLLH\_d0z0\_corr14



# FullSim\_LooseLLH\_d0z0\_corr14

