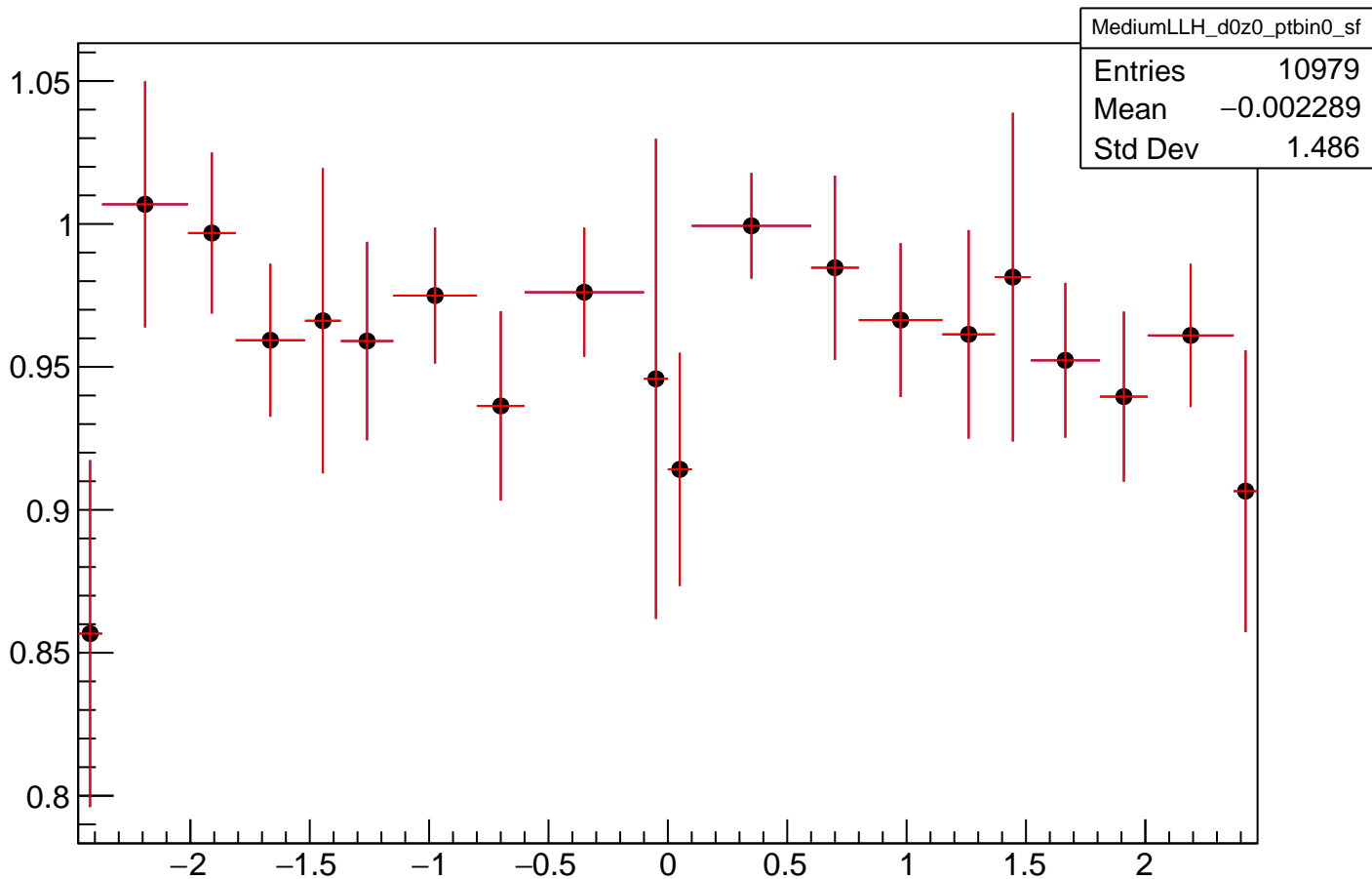
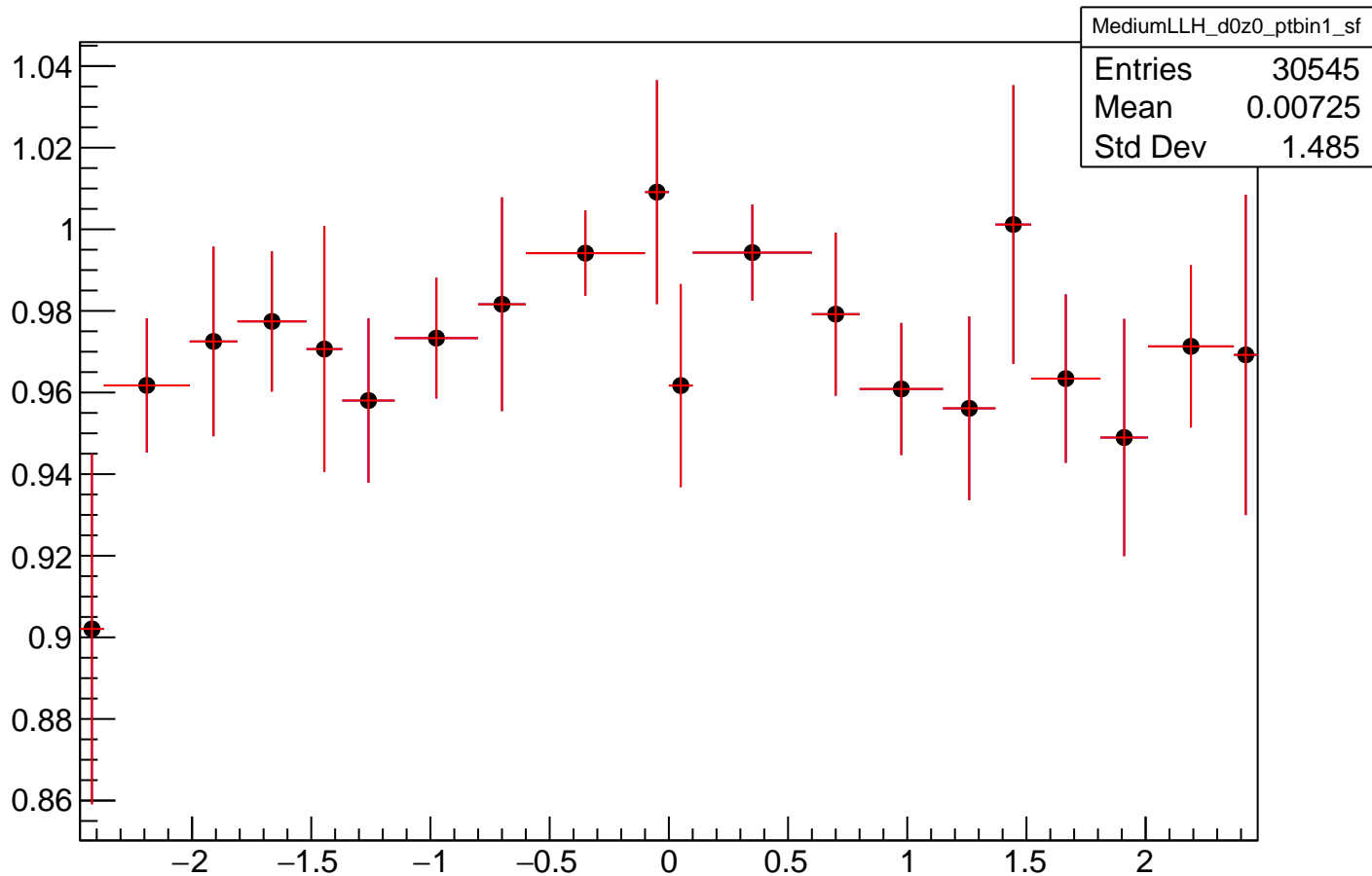


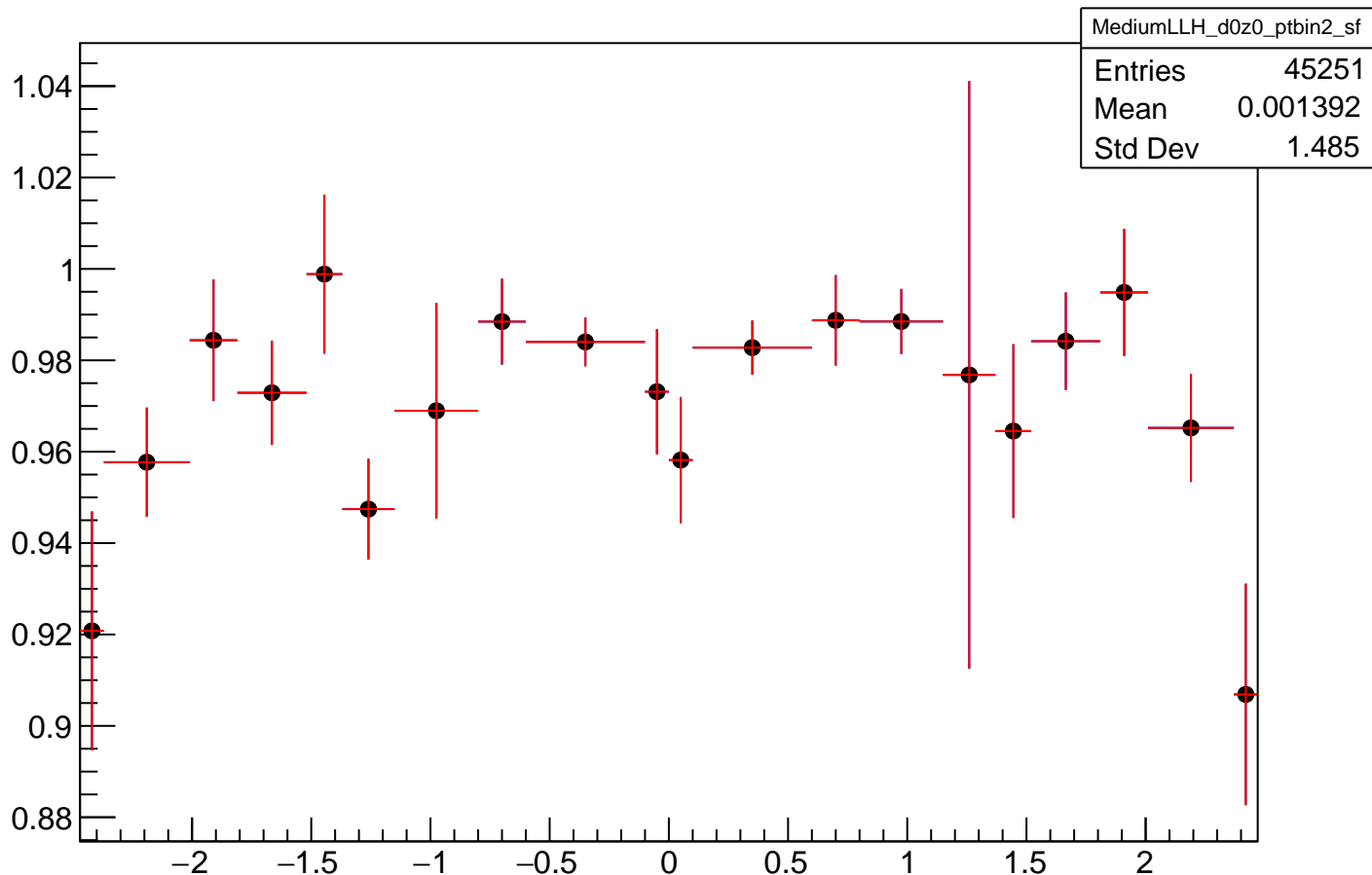
# FullSim\_MediumLLH\_d0z0\_sf



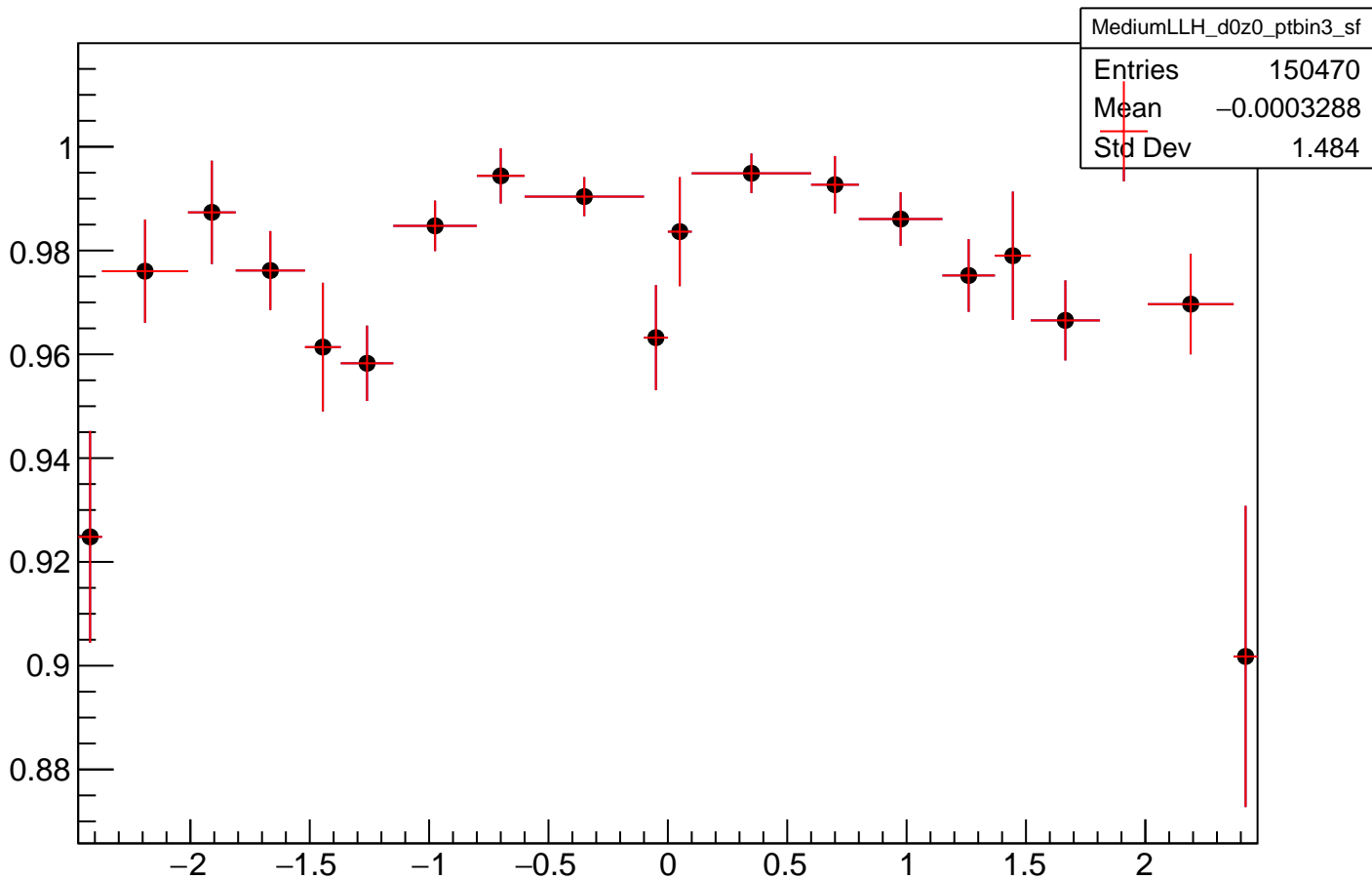
# FullSim\_MediumLLH\_d0z0\_sf



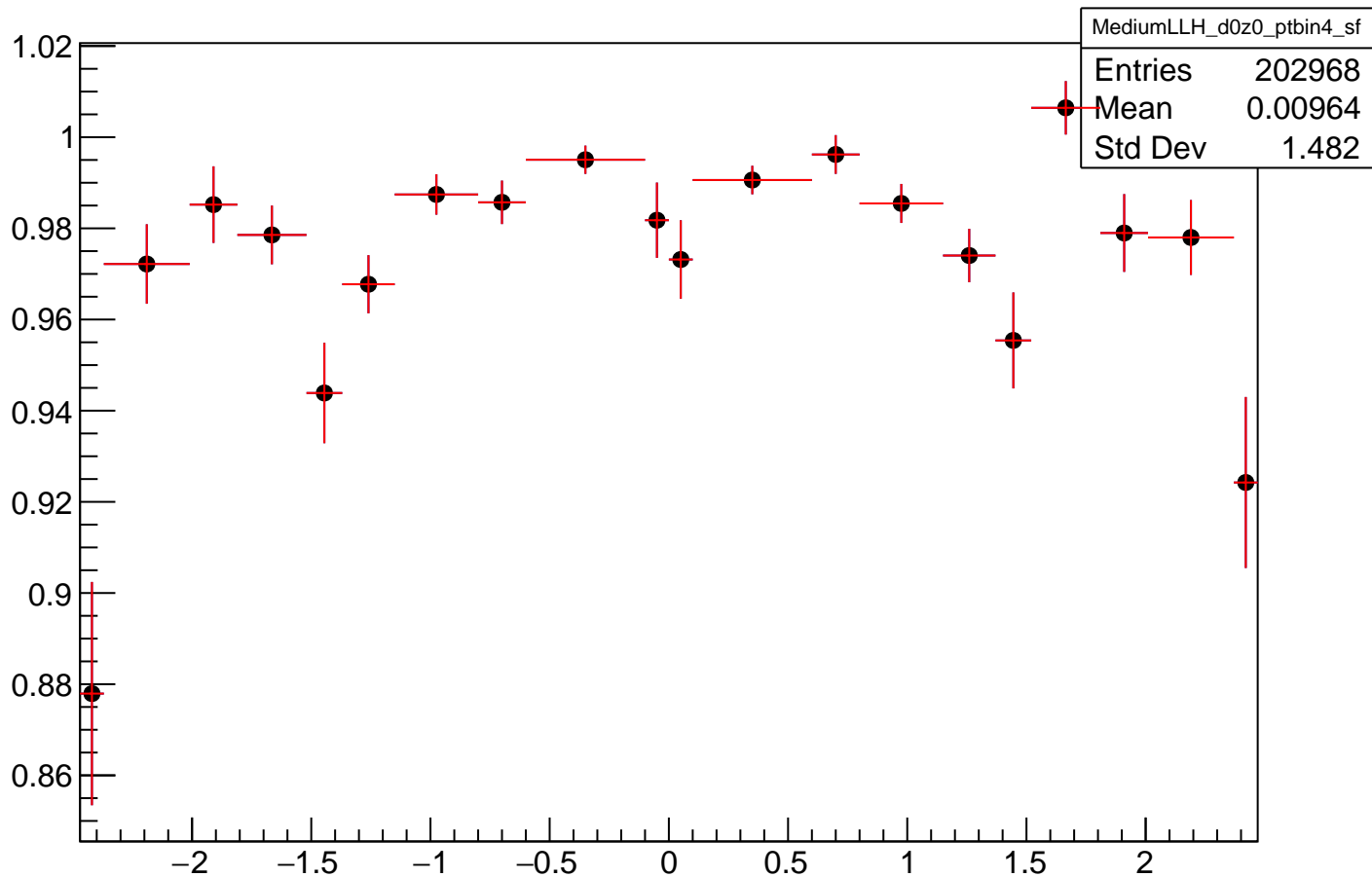
# FullSim\_MediumLLH\_d0z0\_sf



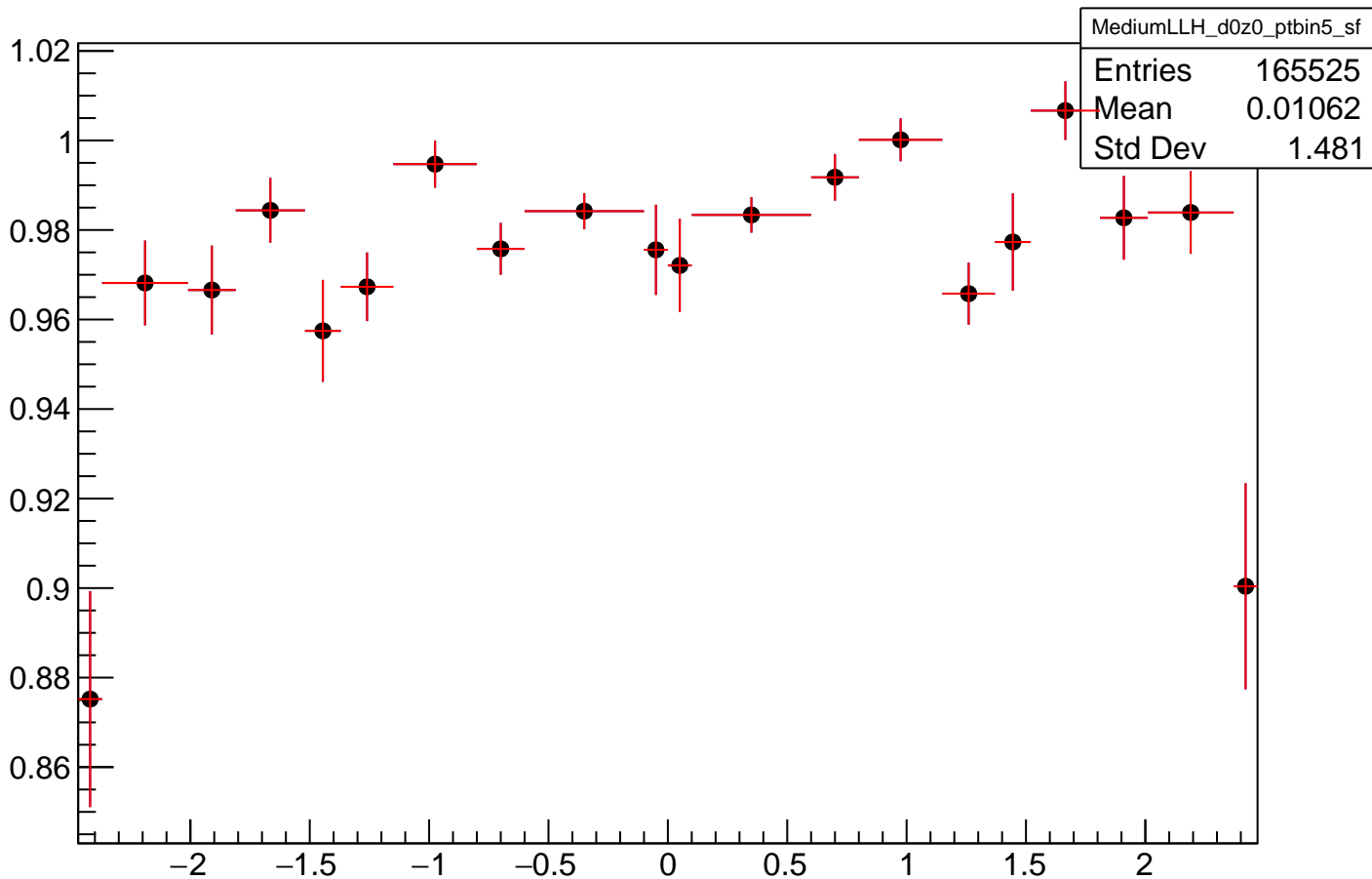
# FullSim\_MediumLLH\_d0z0\_sf



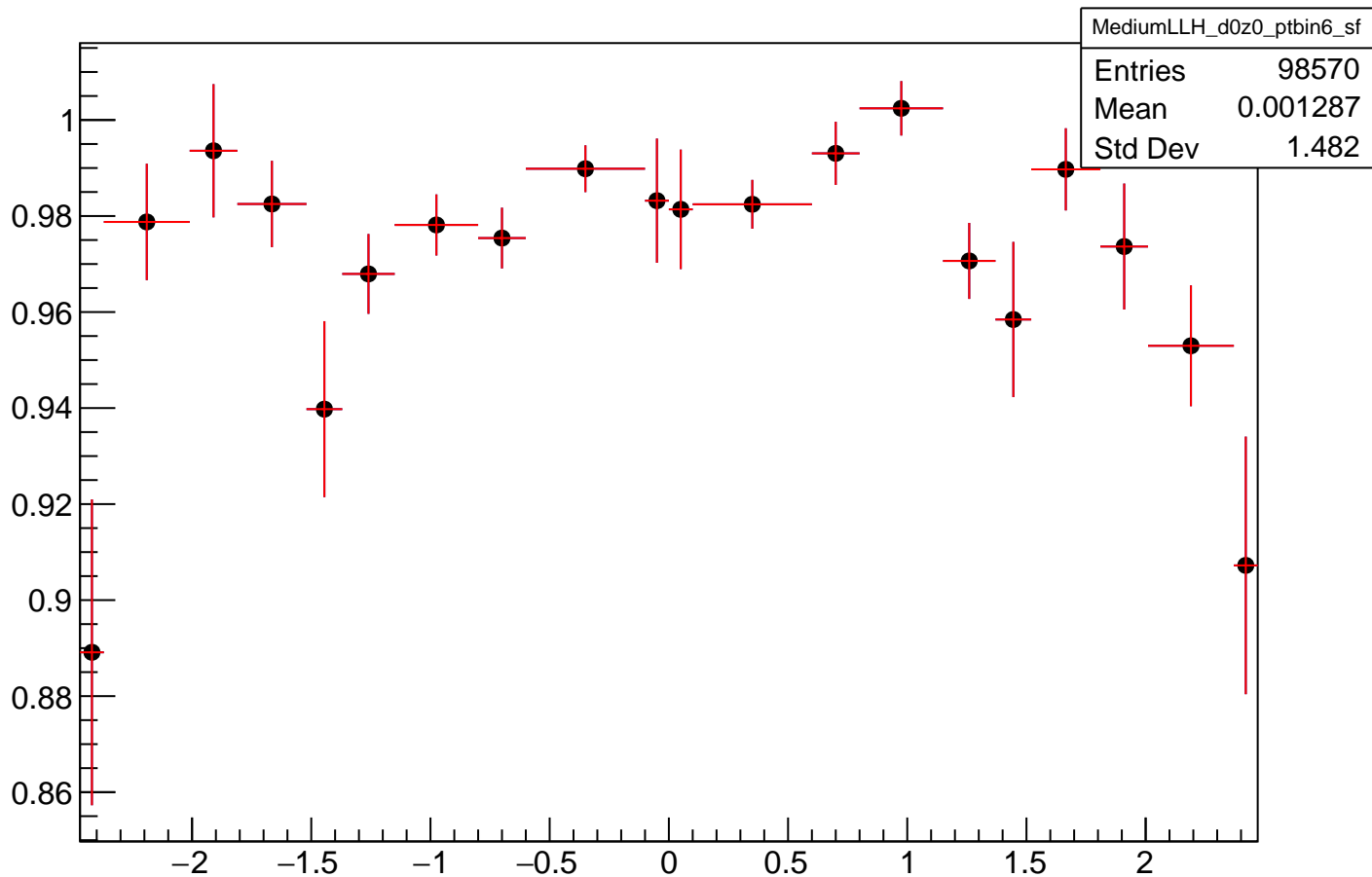
# FullSim\_MediumLLH\_d0z0\_sf



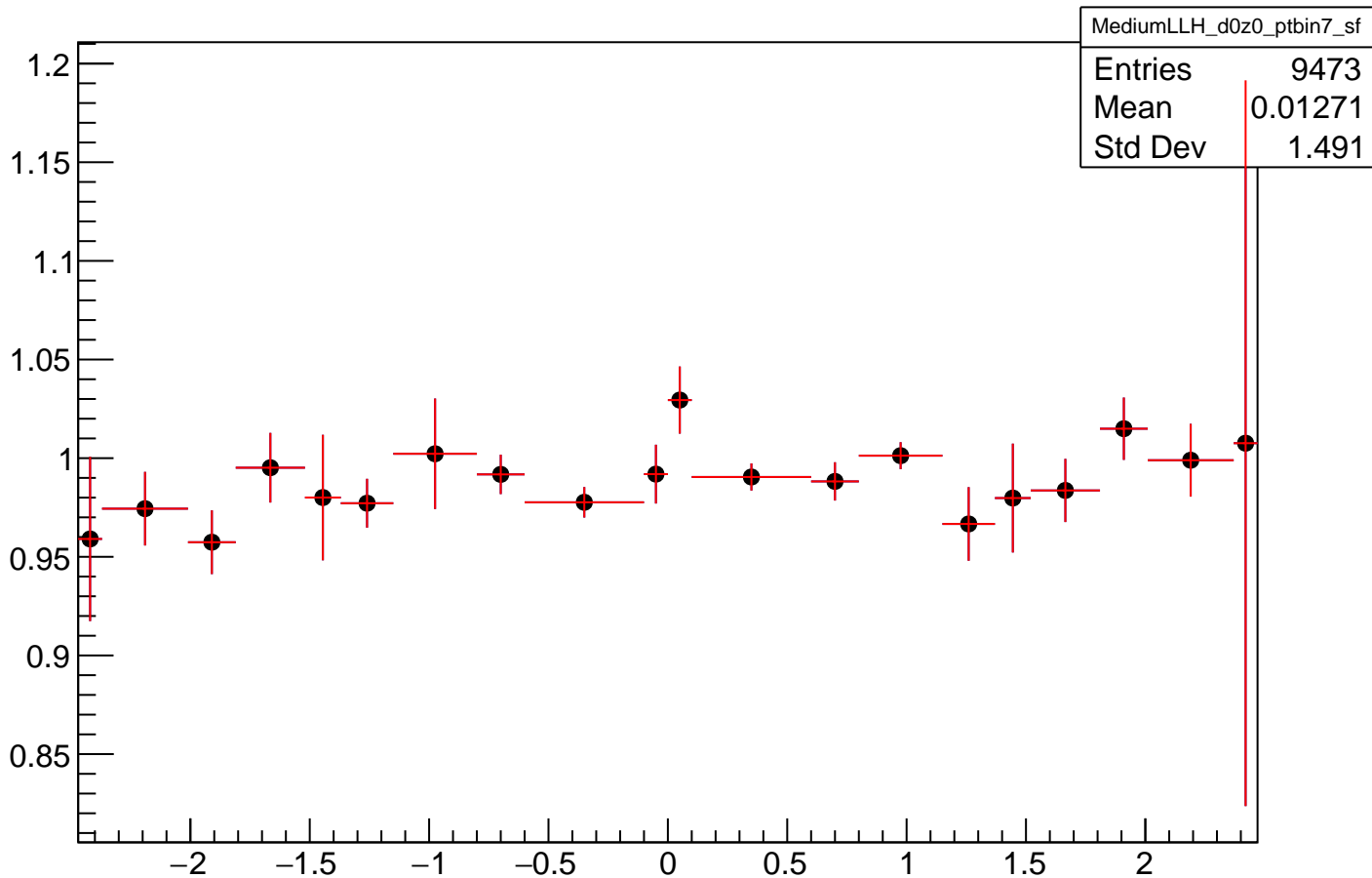
# FullSim\_MediumLLH\_d0z0\_sf



# FullSim\_MediumLLH\_d0z0\_sf

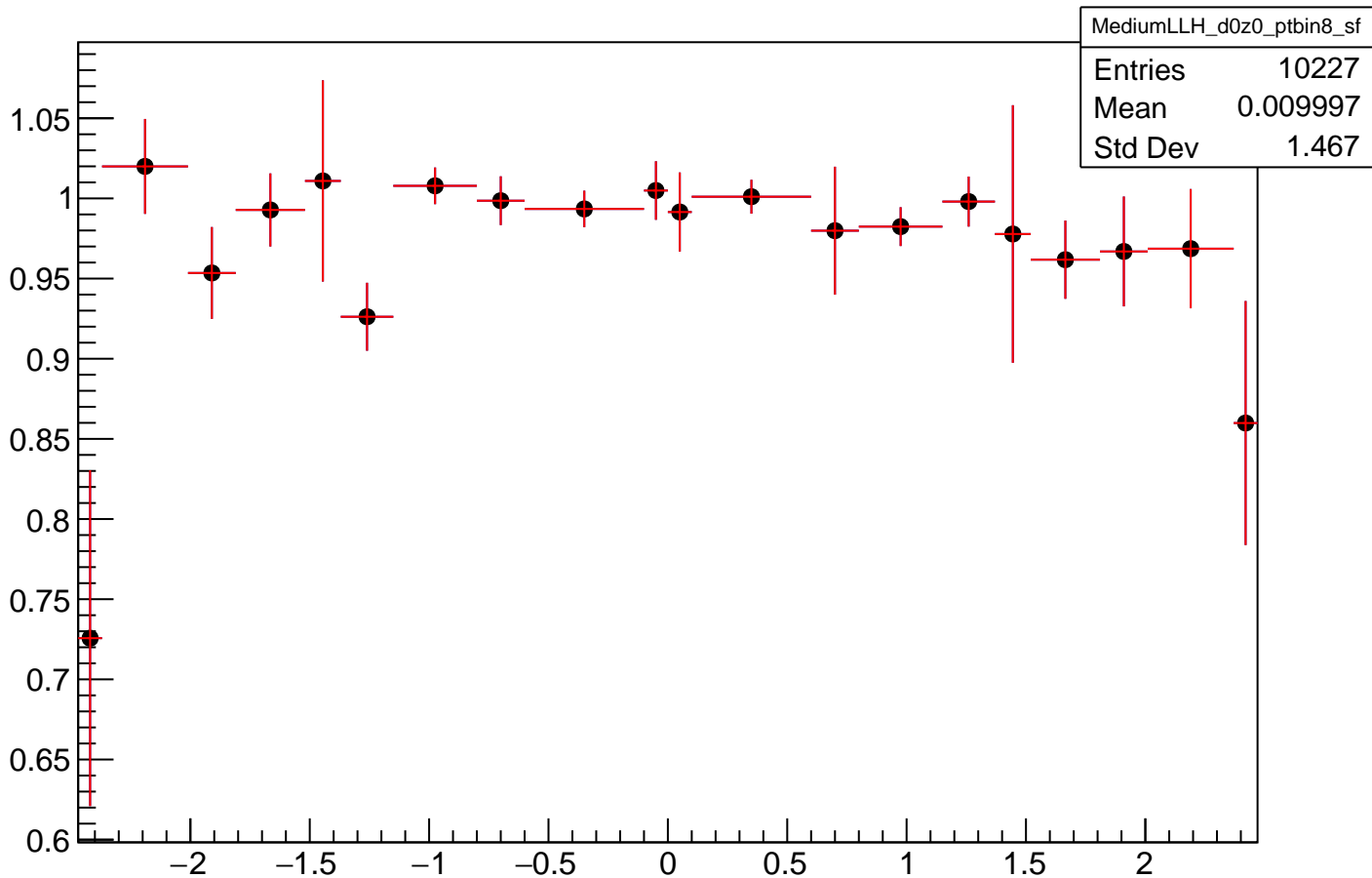


# FullSim\_MediumLLH\_d0z0\_sf

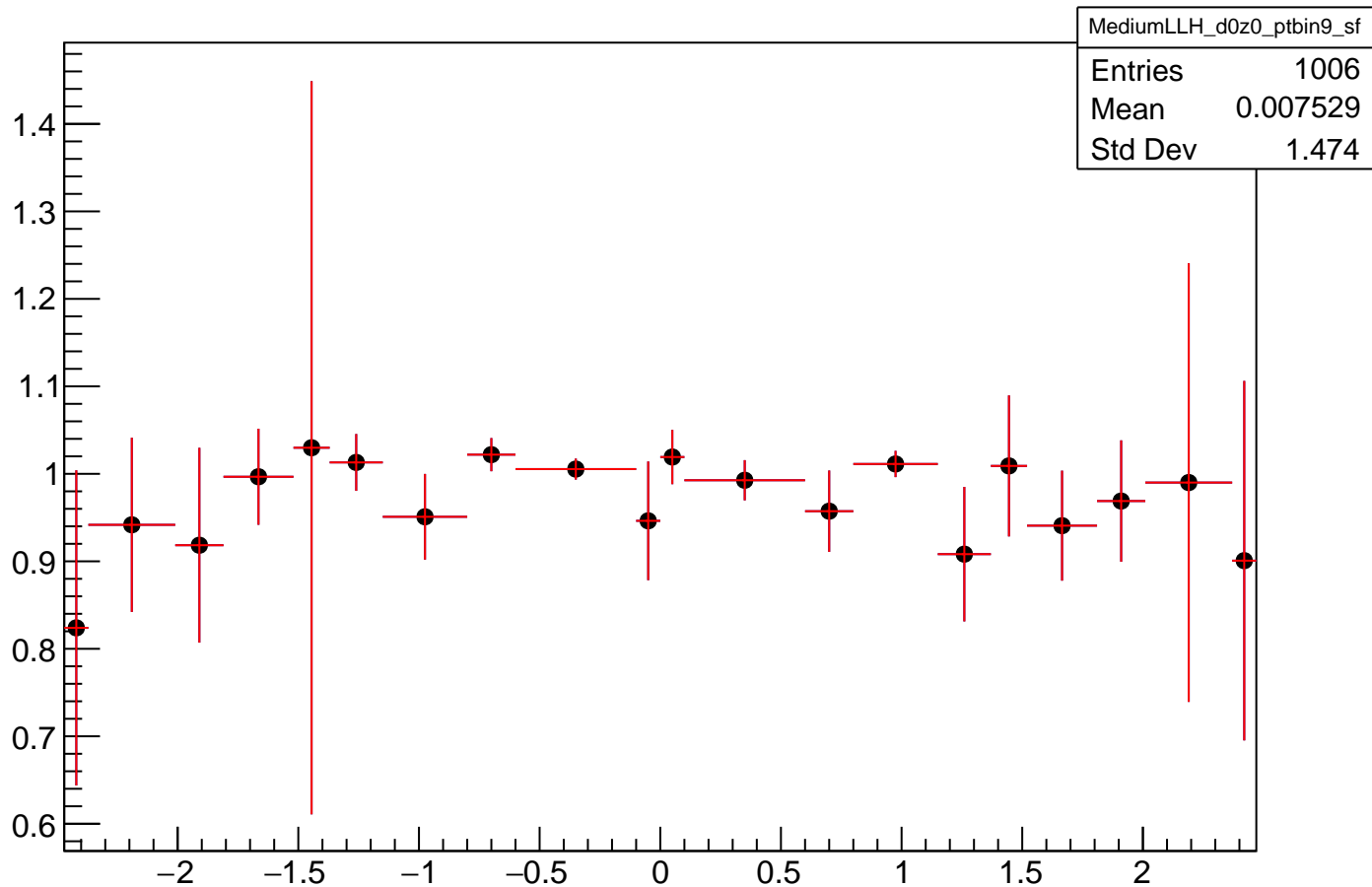




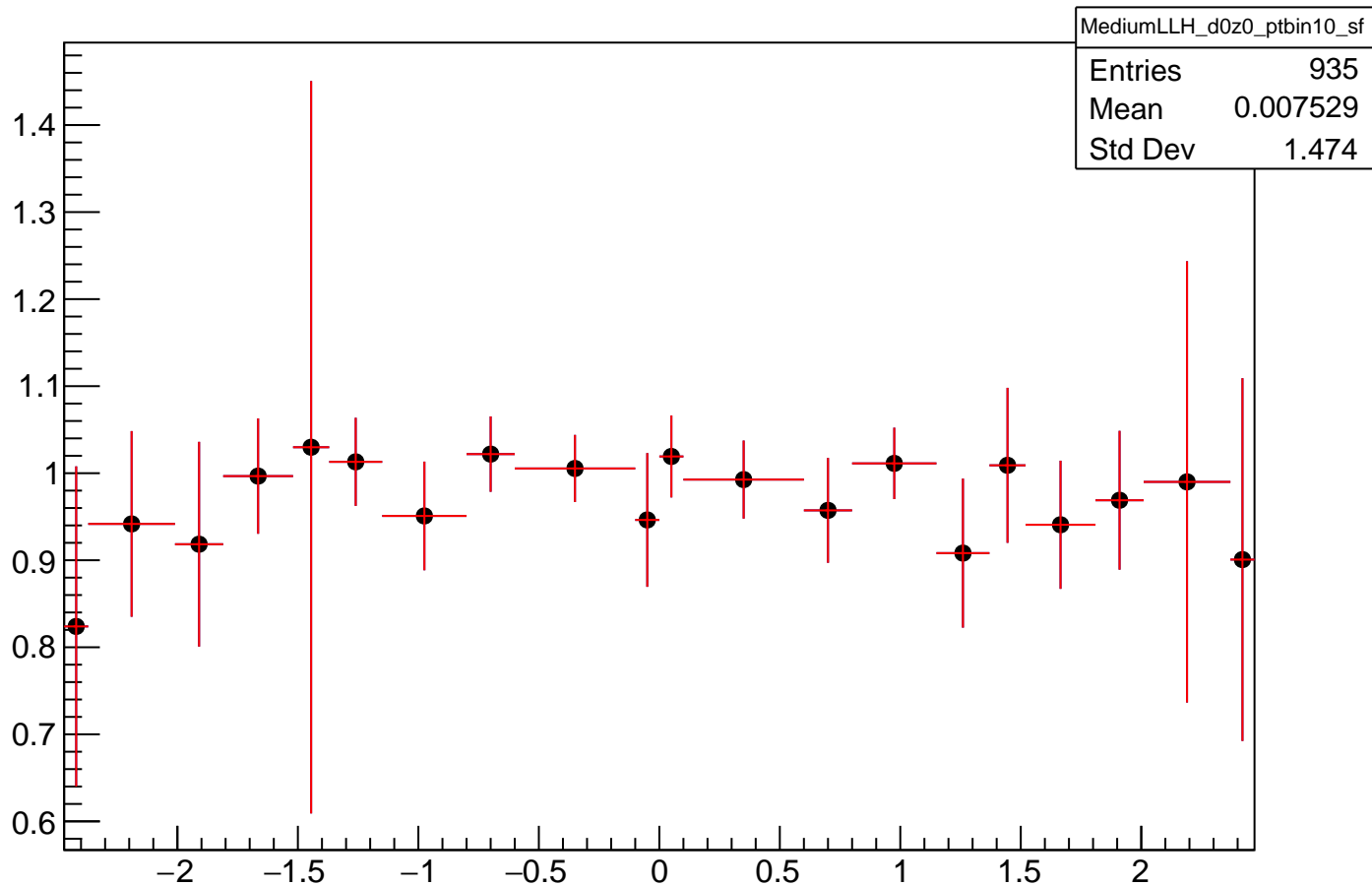
# FullSim\_MediumLLH\_d0z0\_sf



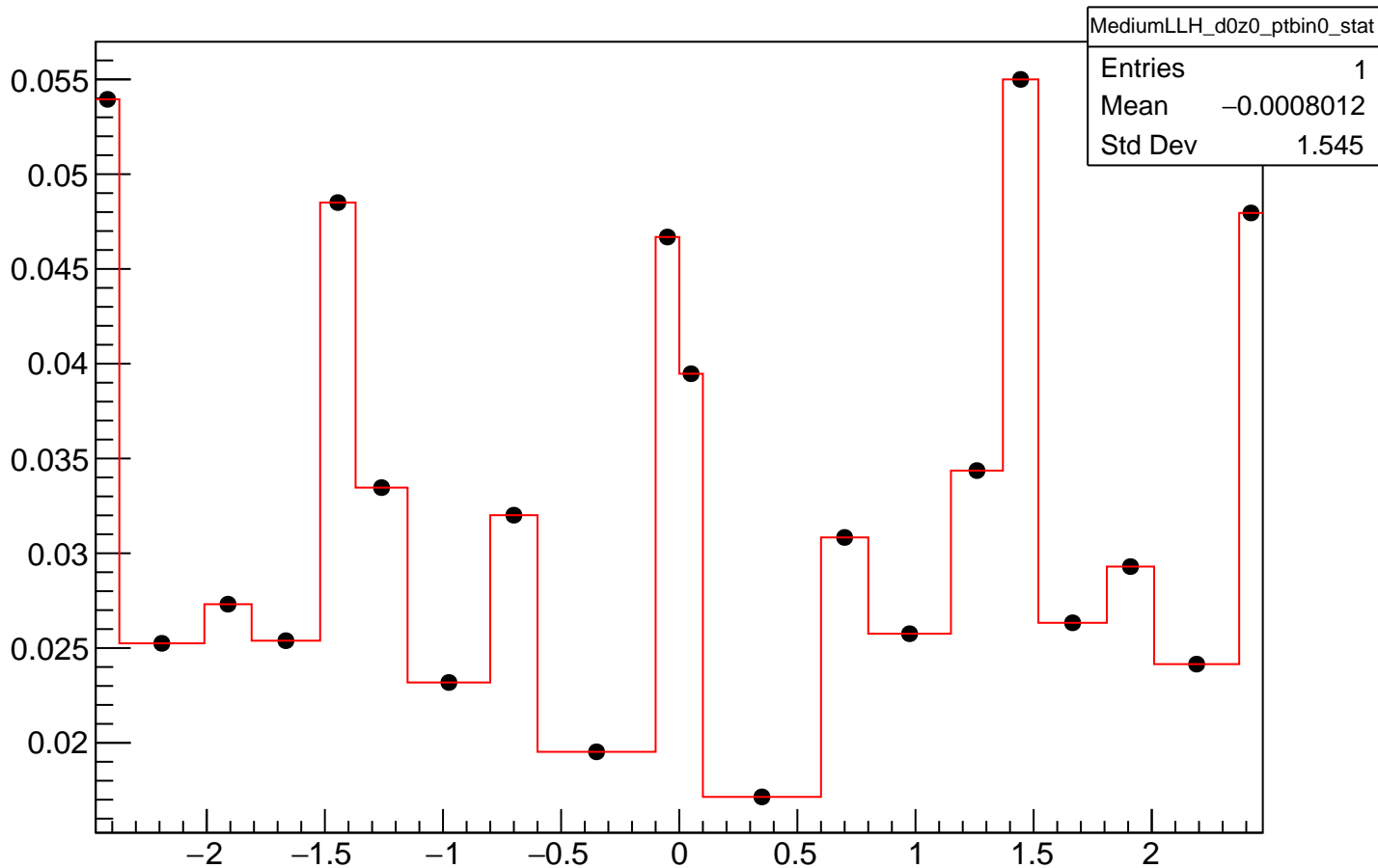
# FullSim\_MediumLLH\_d0z0\_sf



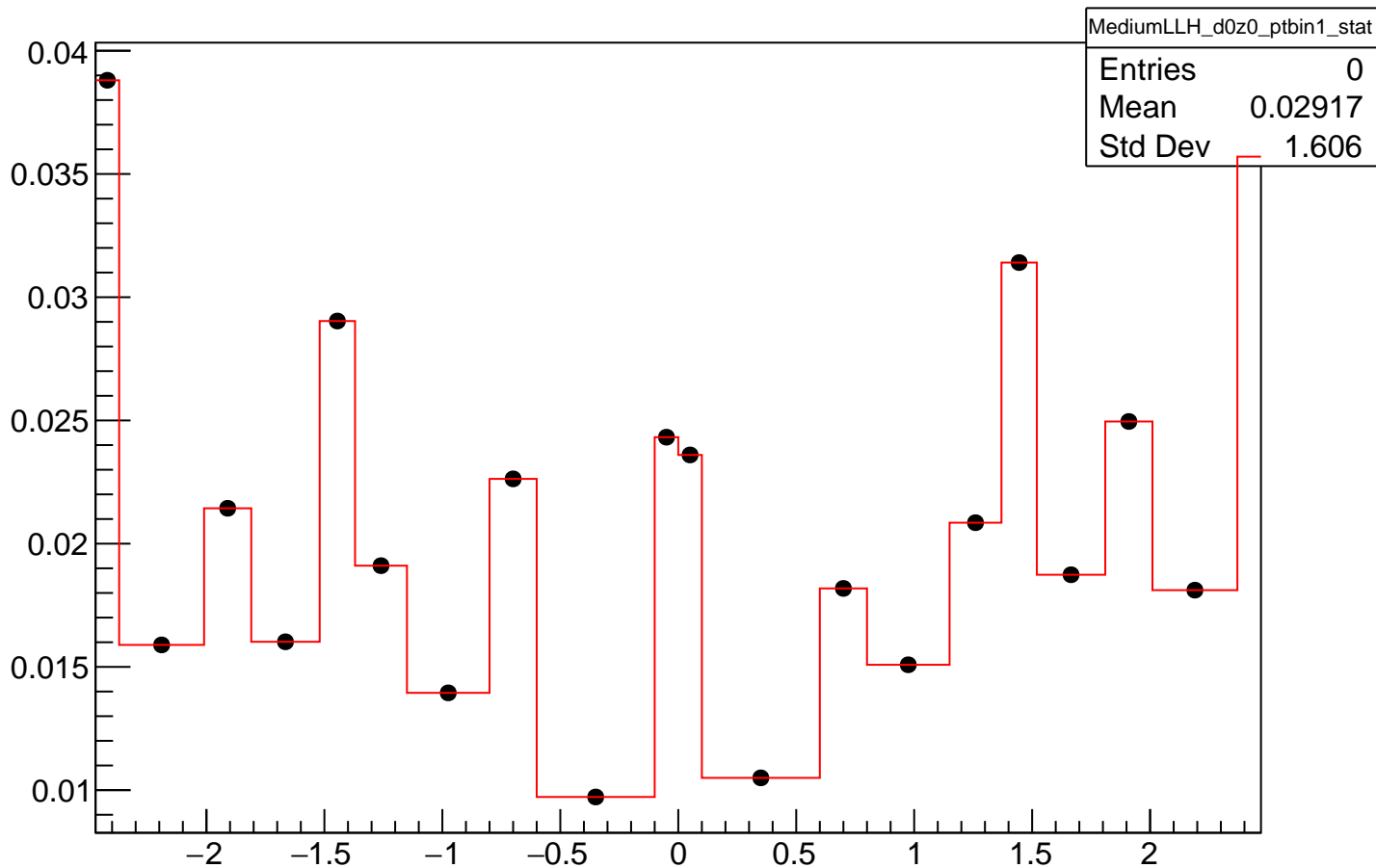
# FullSim\_MediumLLH\_d0z0\_sf



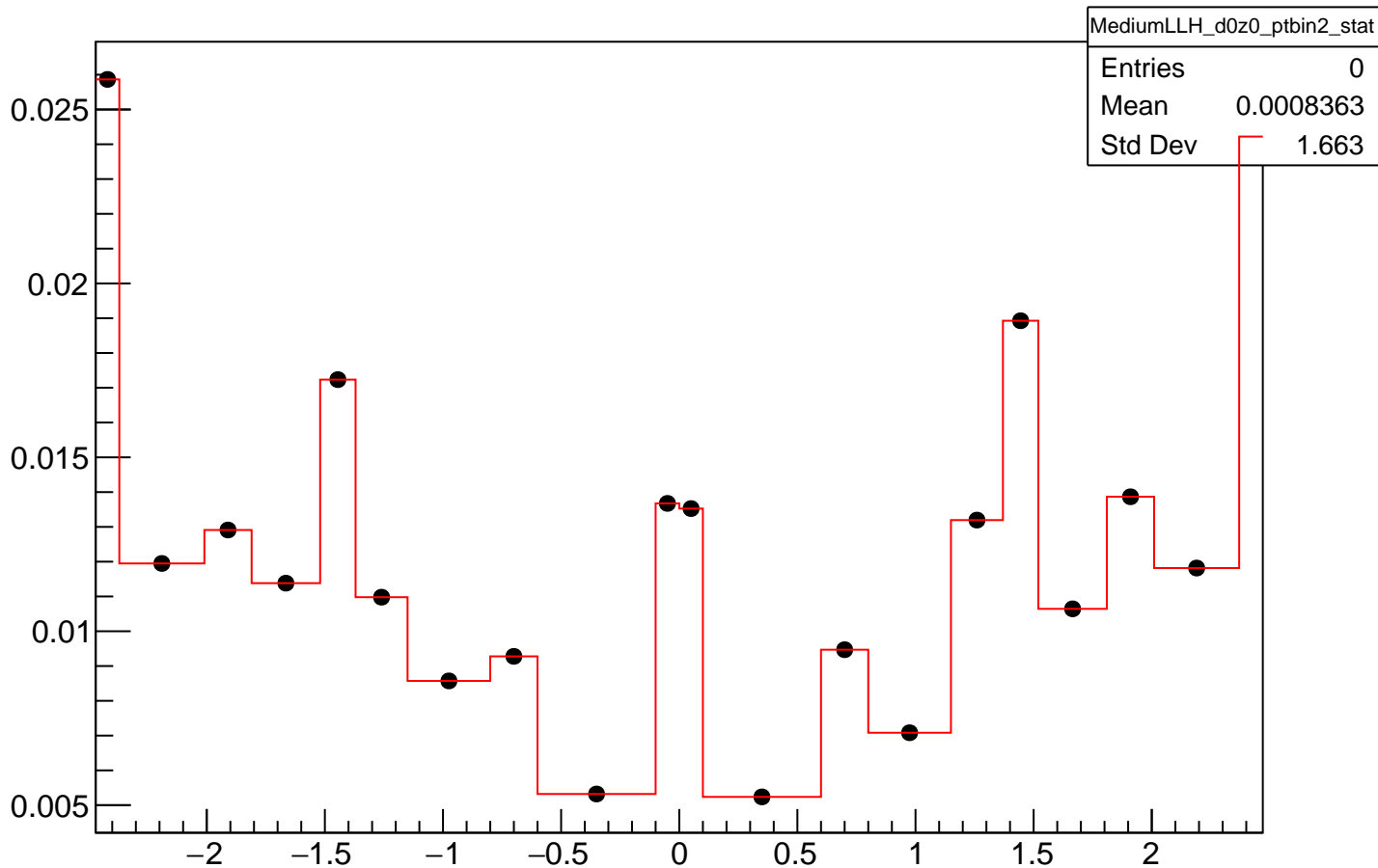
# FullSim\_MediumLLH\_d0z0\_stat



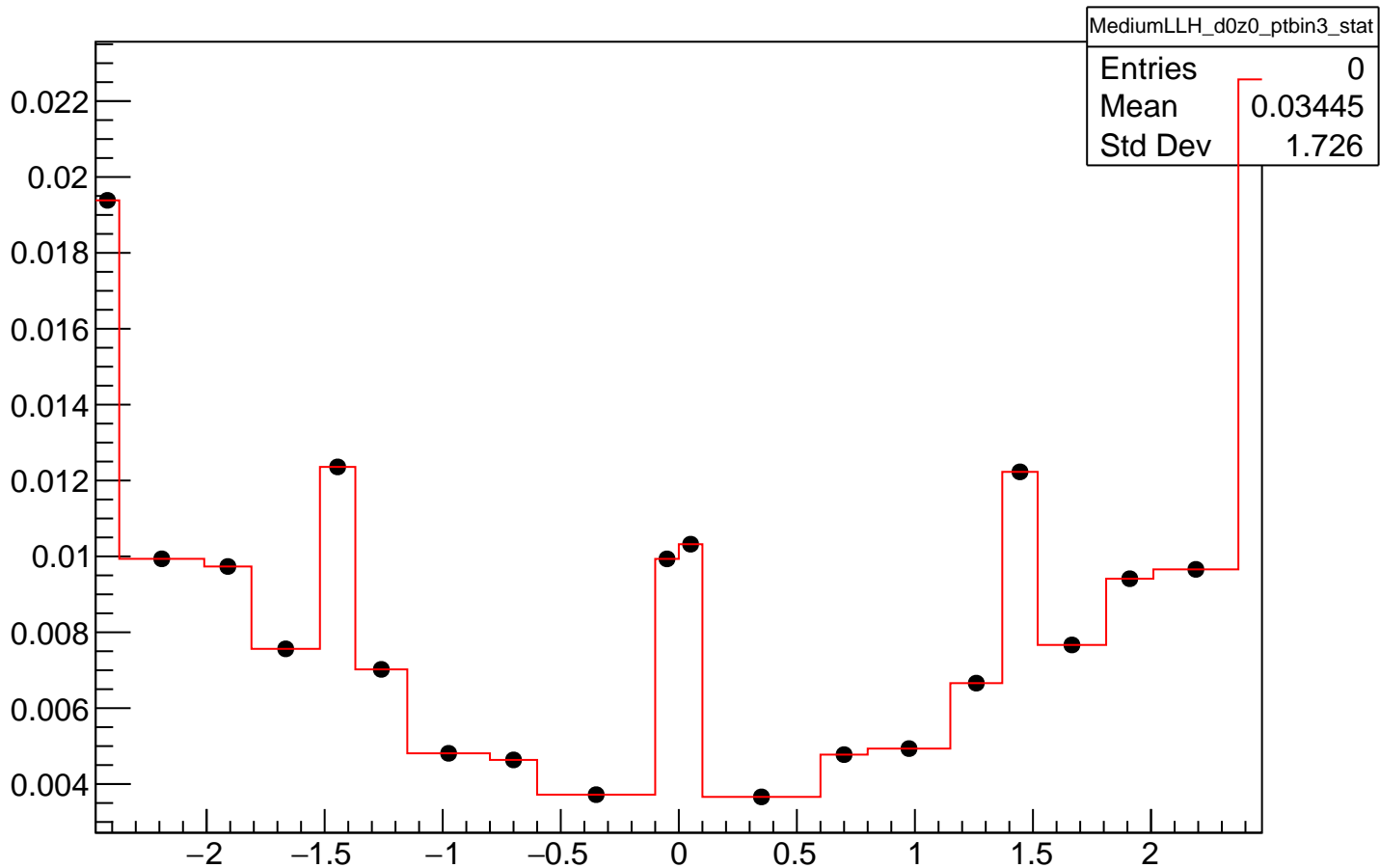
# FullSim\_MediumLLH\_d0z0\_stat



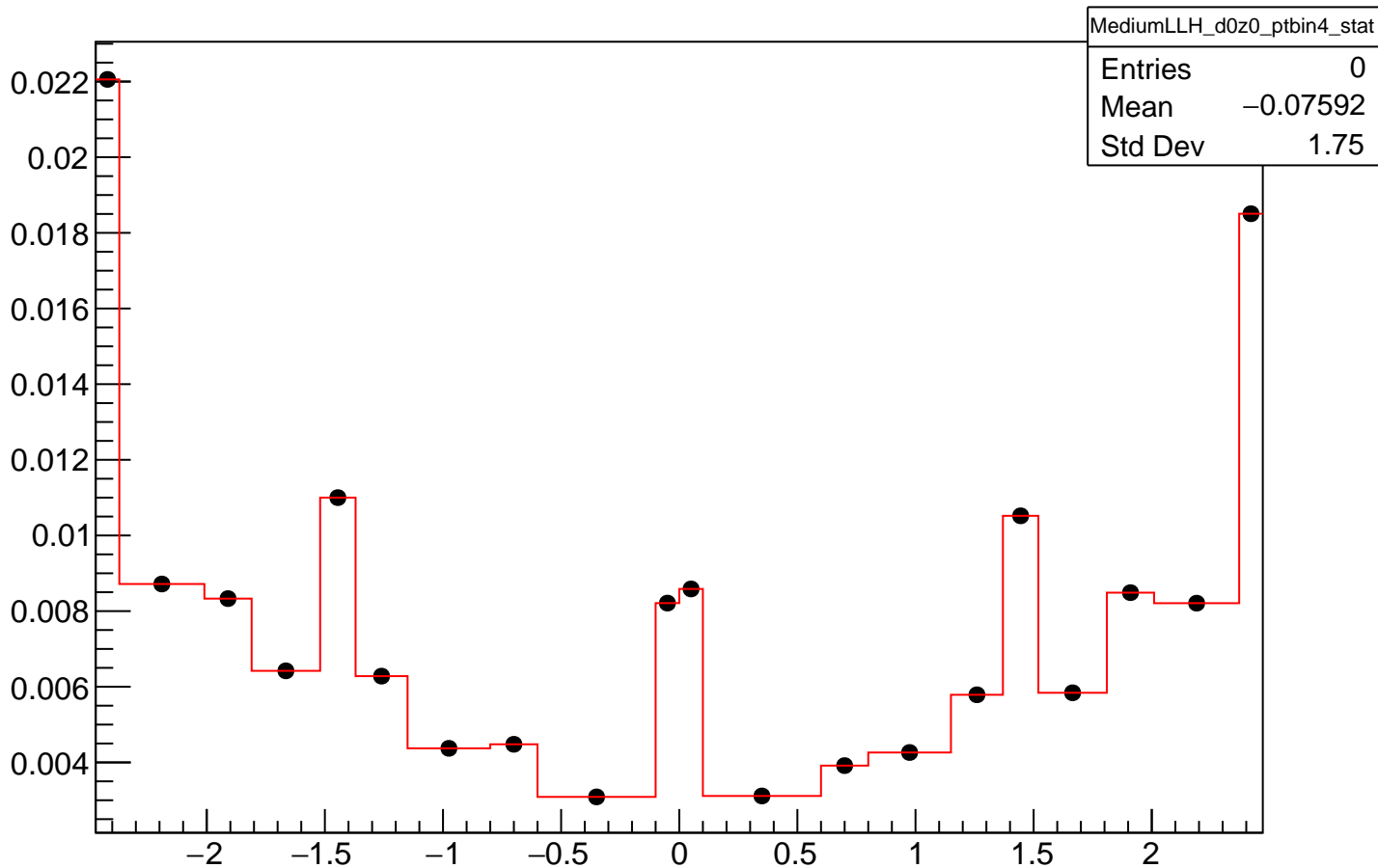
# FullSim\_MediumLLH\_d0z0\_stat



# FullSim\_MediumLLH\_d0z0\_stat

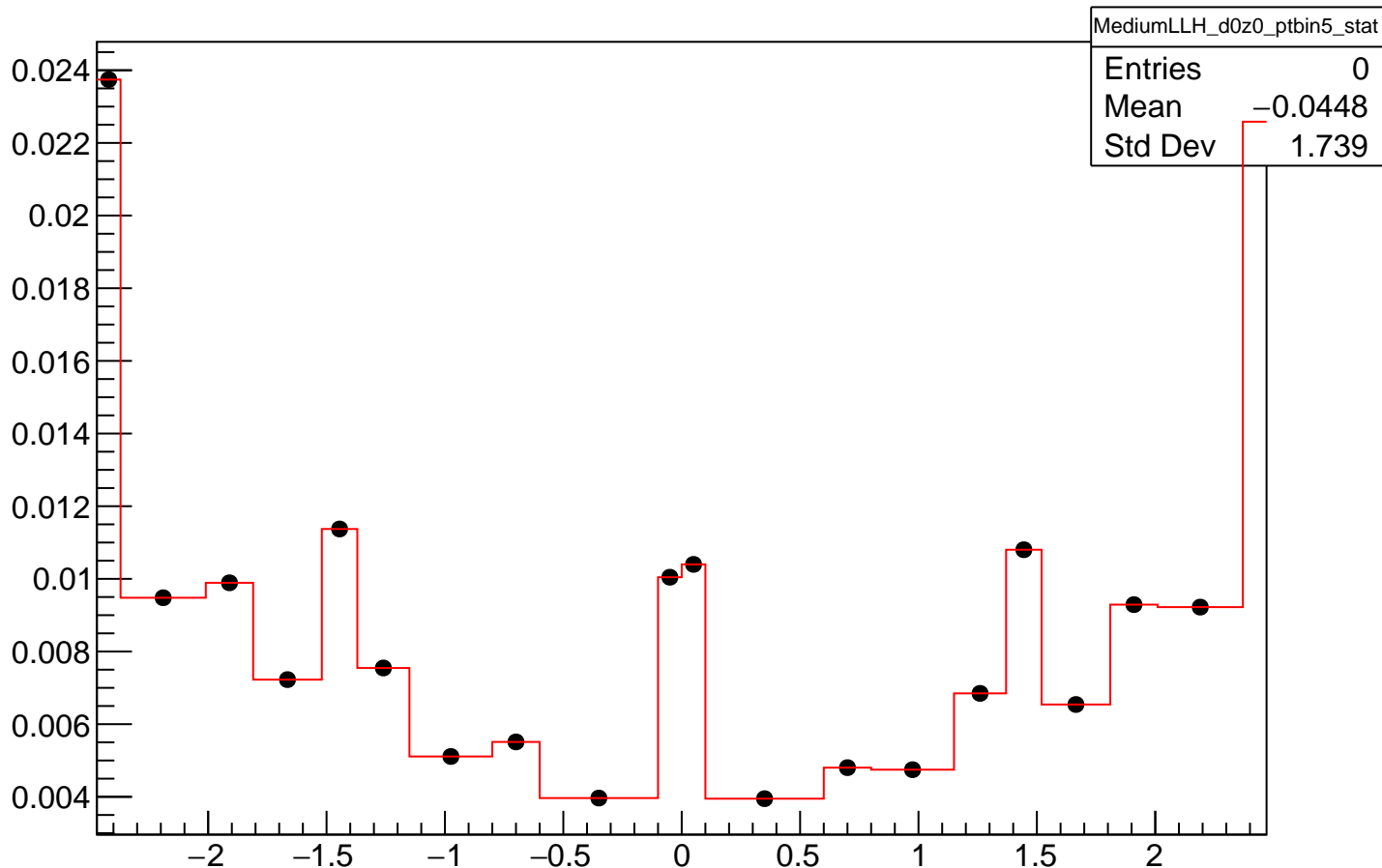


# FullSim\_MediumLLH\_d0z0\_stat

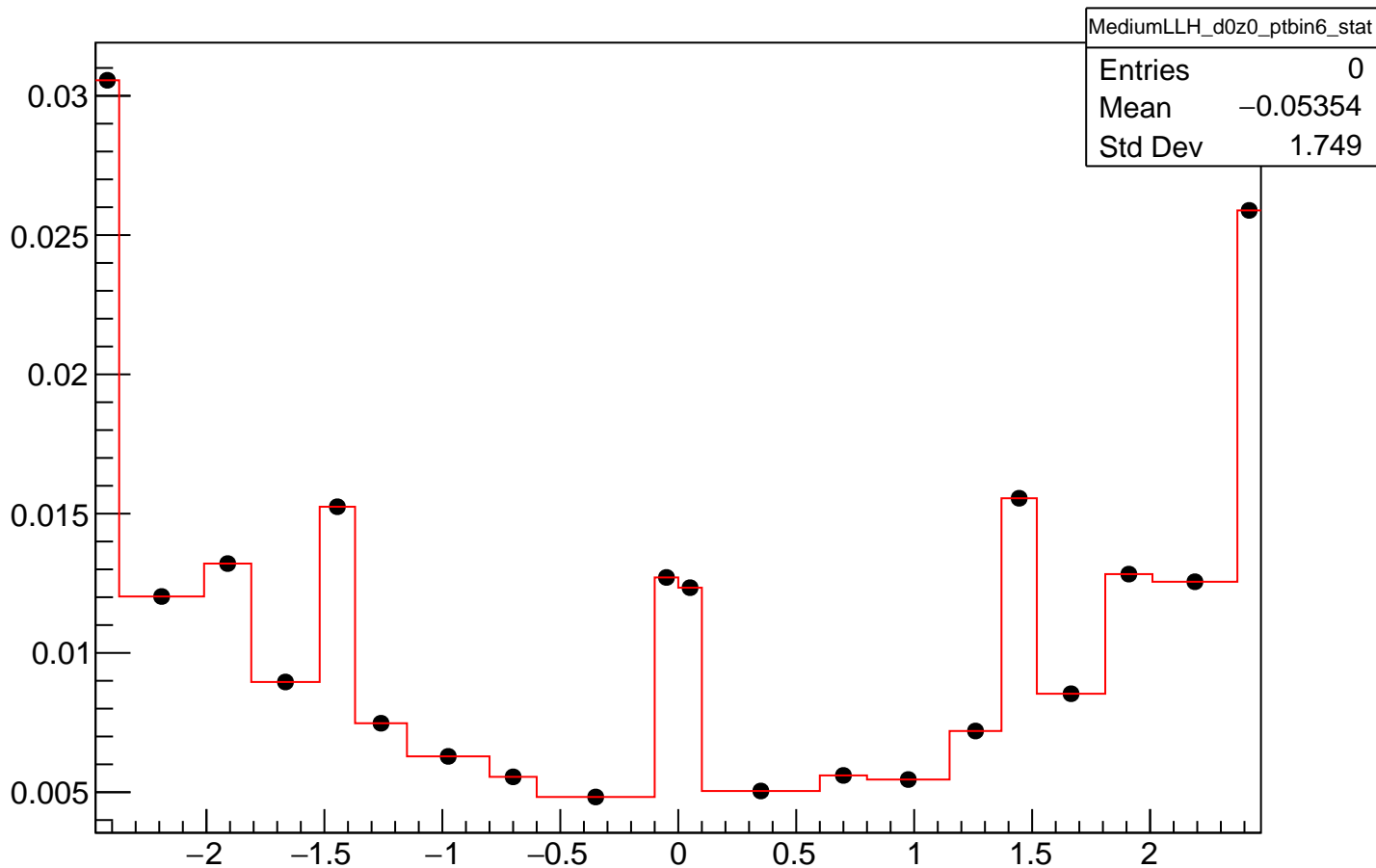




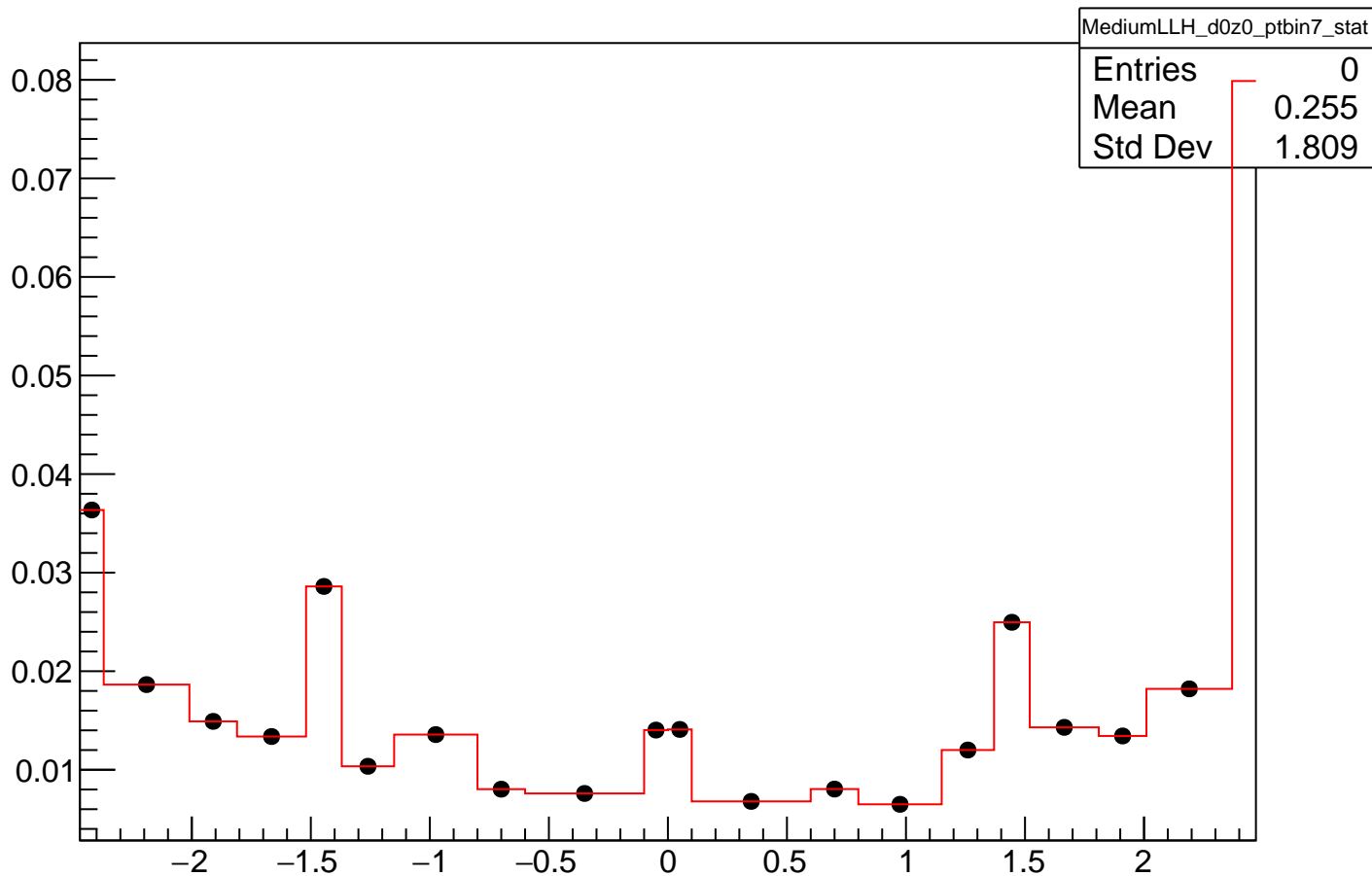
# FullSim\_MediumLLH\_d0z0\_stat



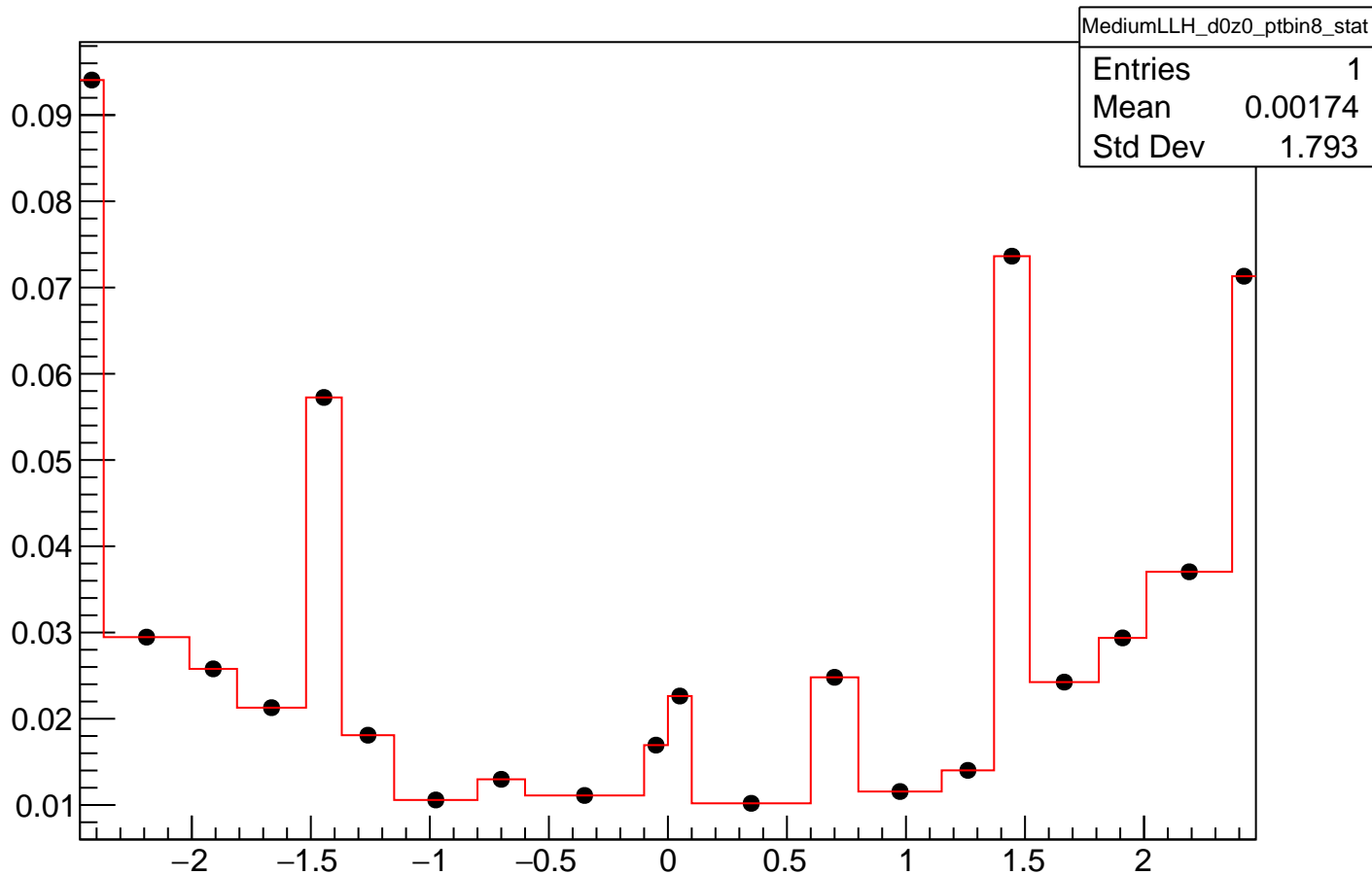
# FullSim\_MediumLLH\_d0z0\_stat



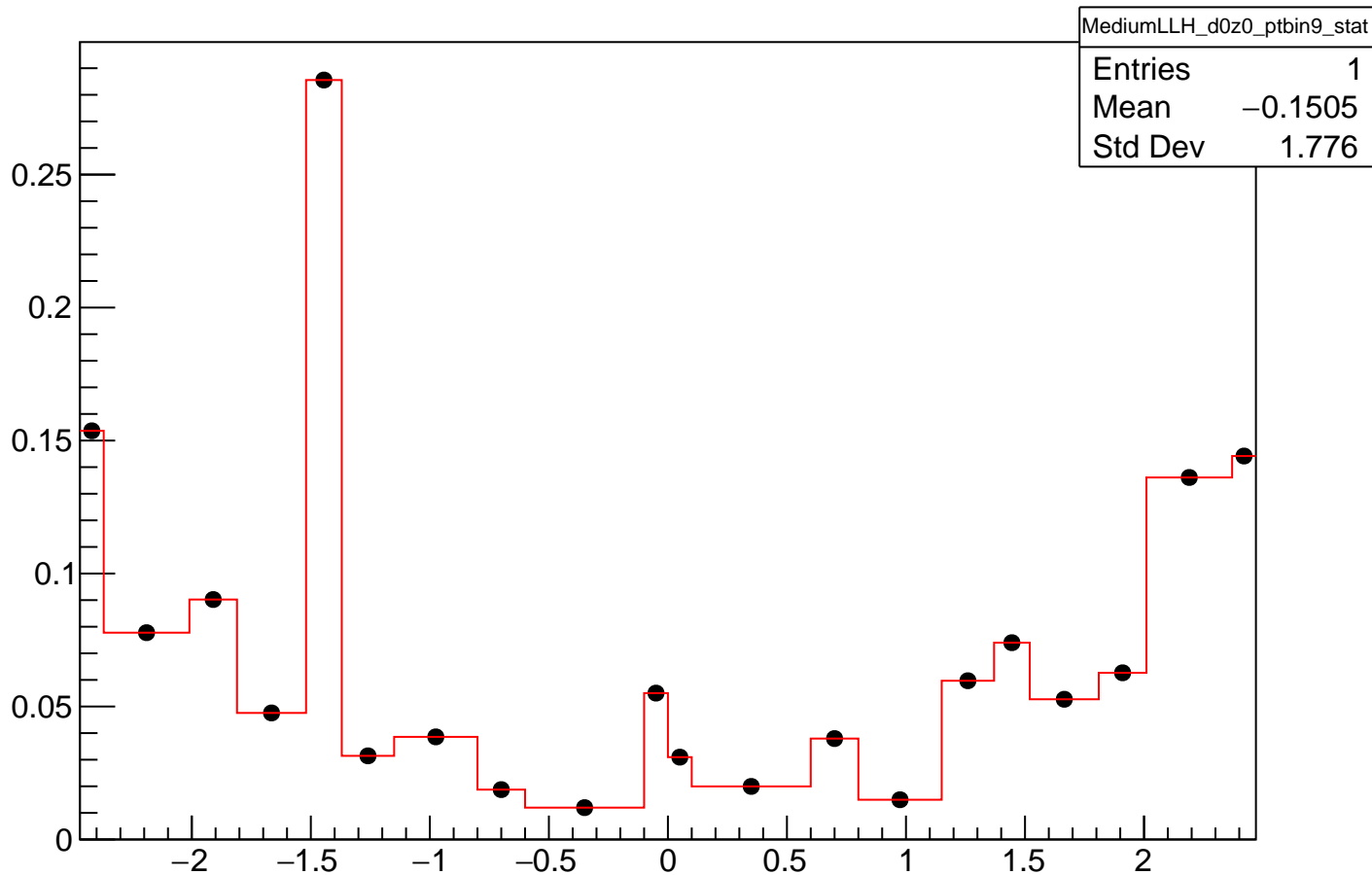
# FullSim\_MediumLLH\_d0z0\_stat



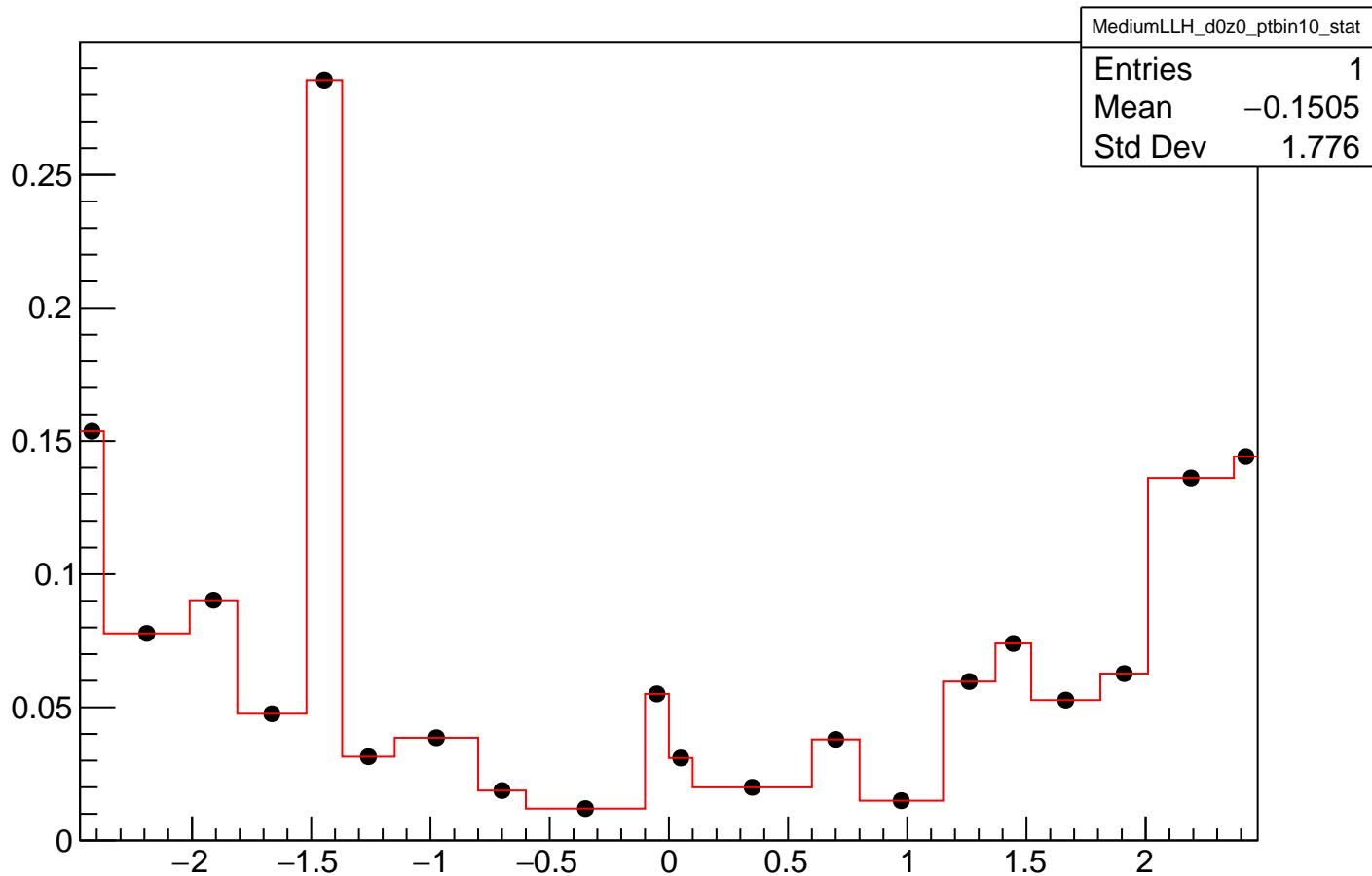
# FullSim\_MediumLLH\_d0z0\_stat



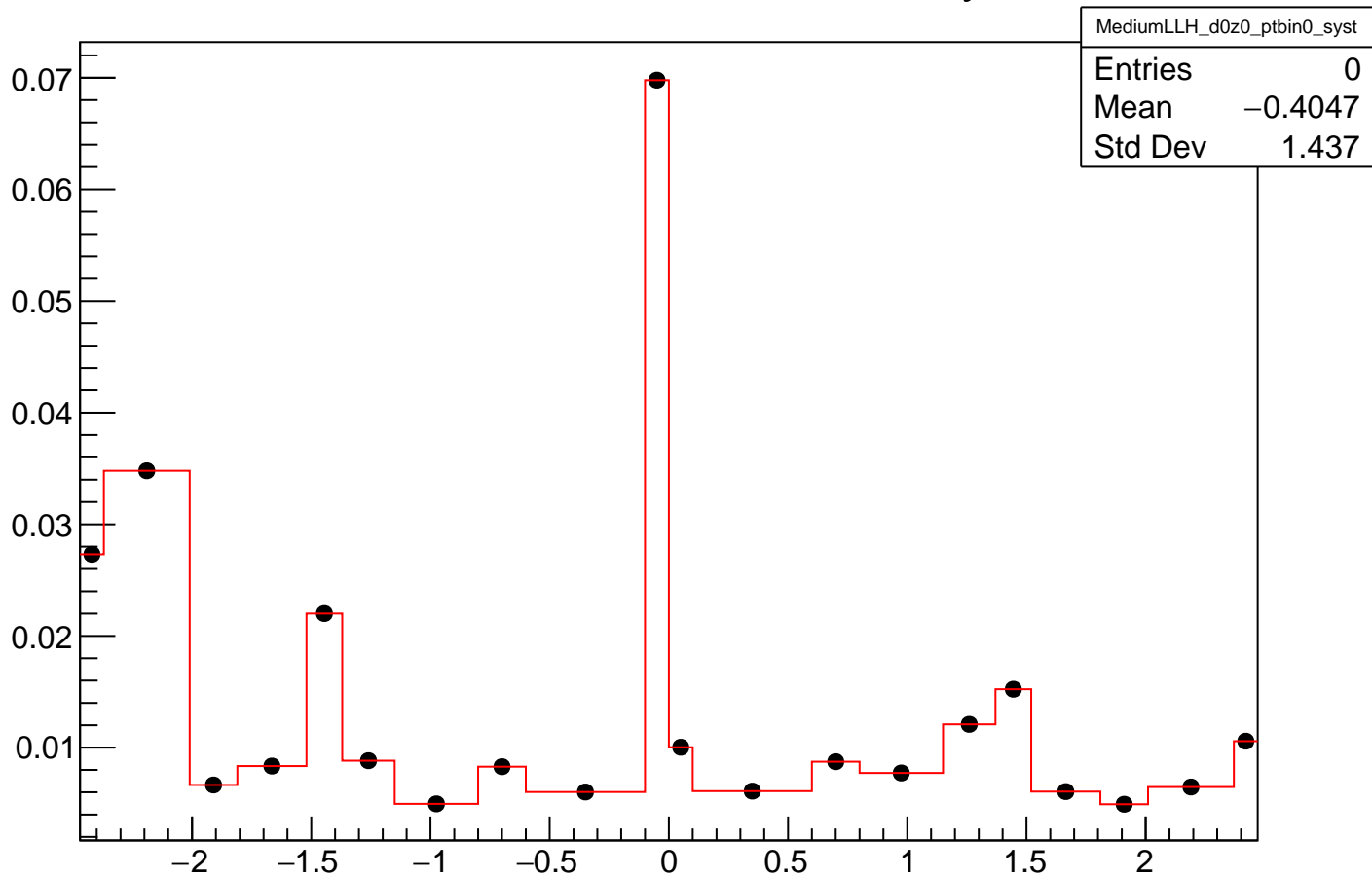
# FullSim\_MediumLLH\_d0z0\_stat



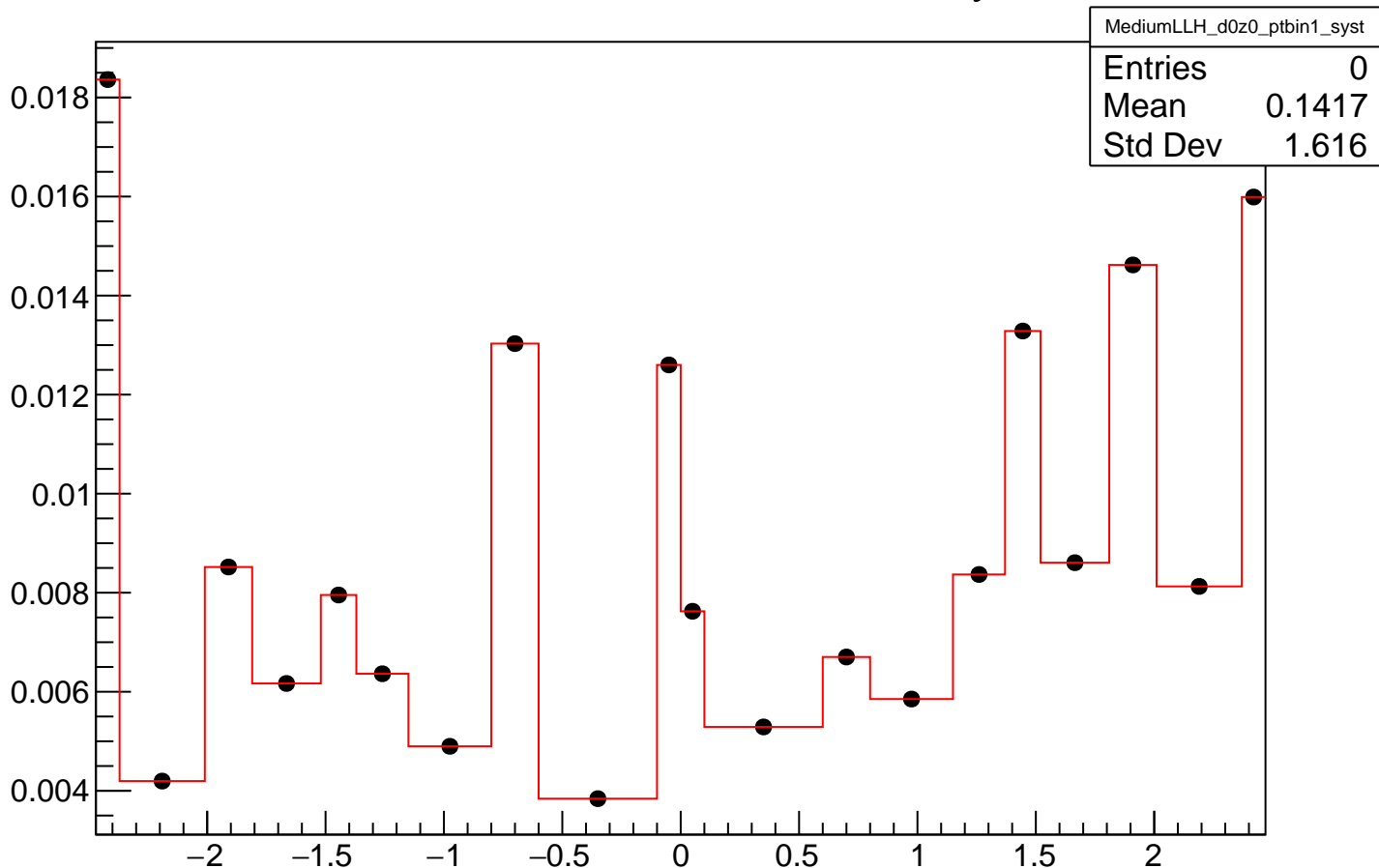
# FullSim\_MediumLLH\_d0z0\_stat



# FullSim\_MediumLLH\_d0z0\_sys

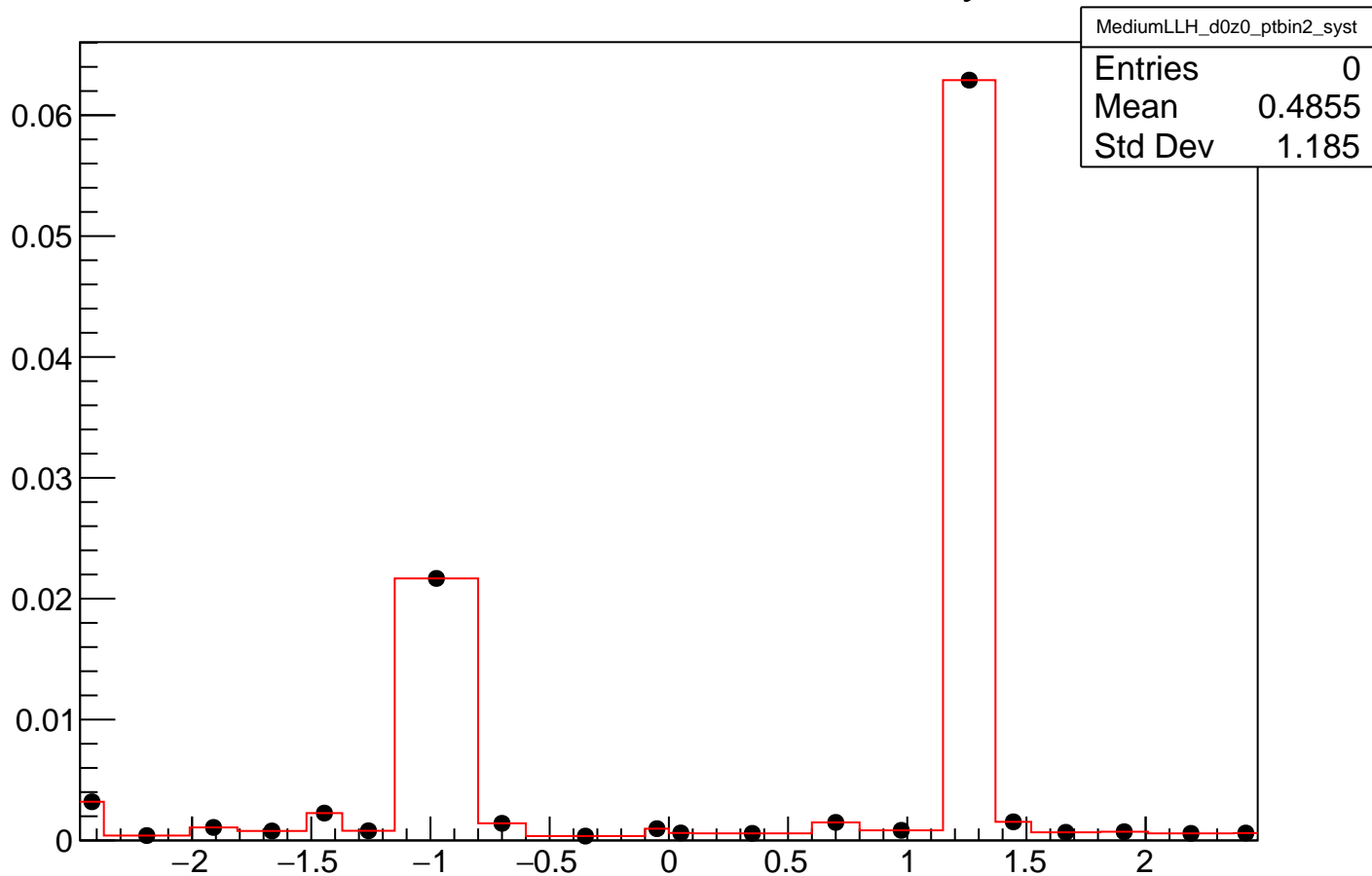


# FullSim\_MediumLLH\_d0z0\_sys

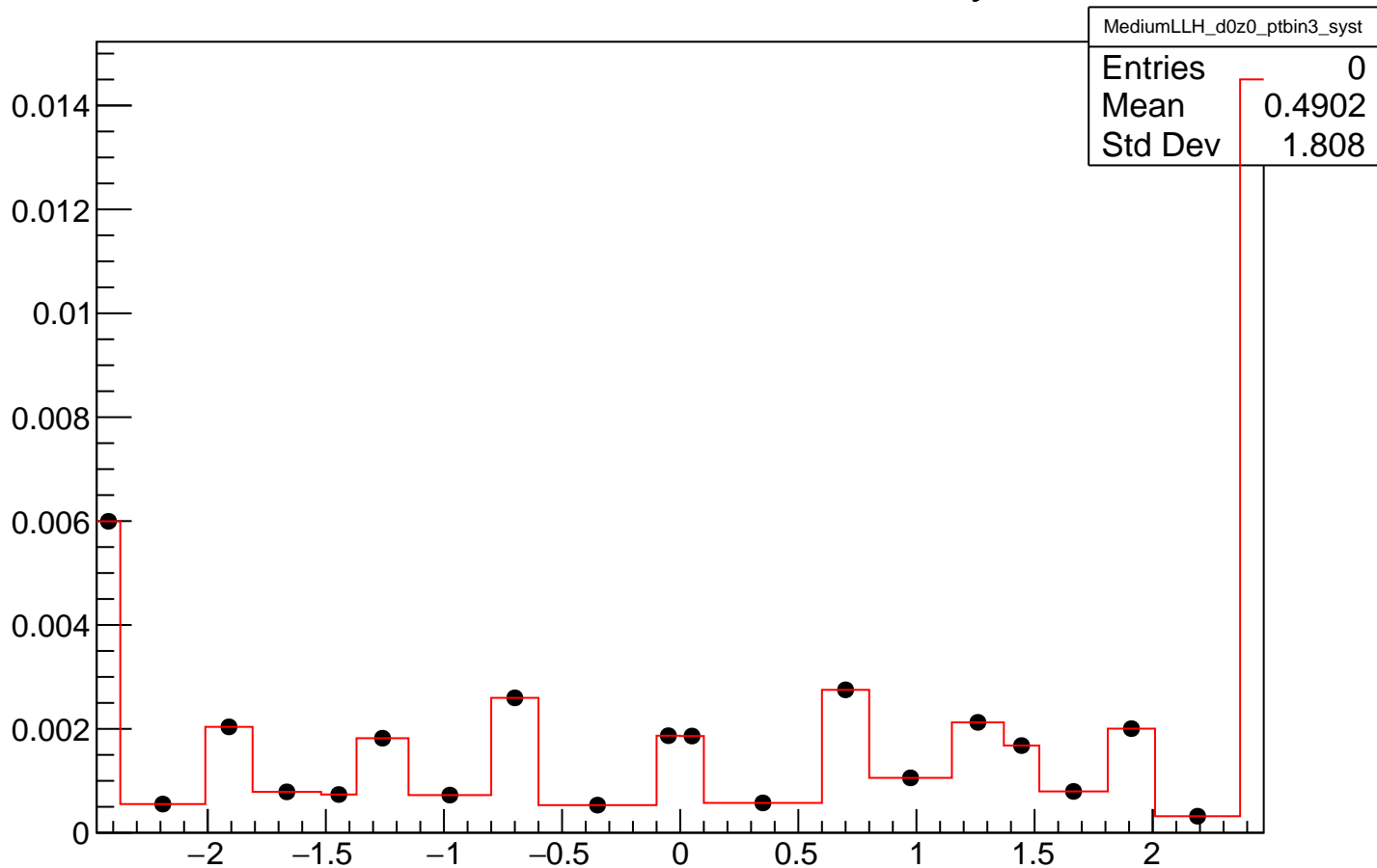




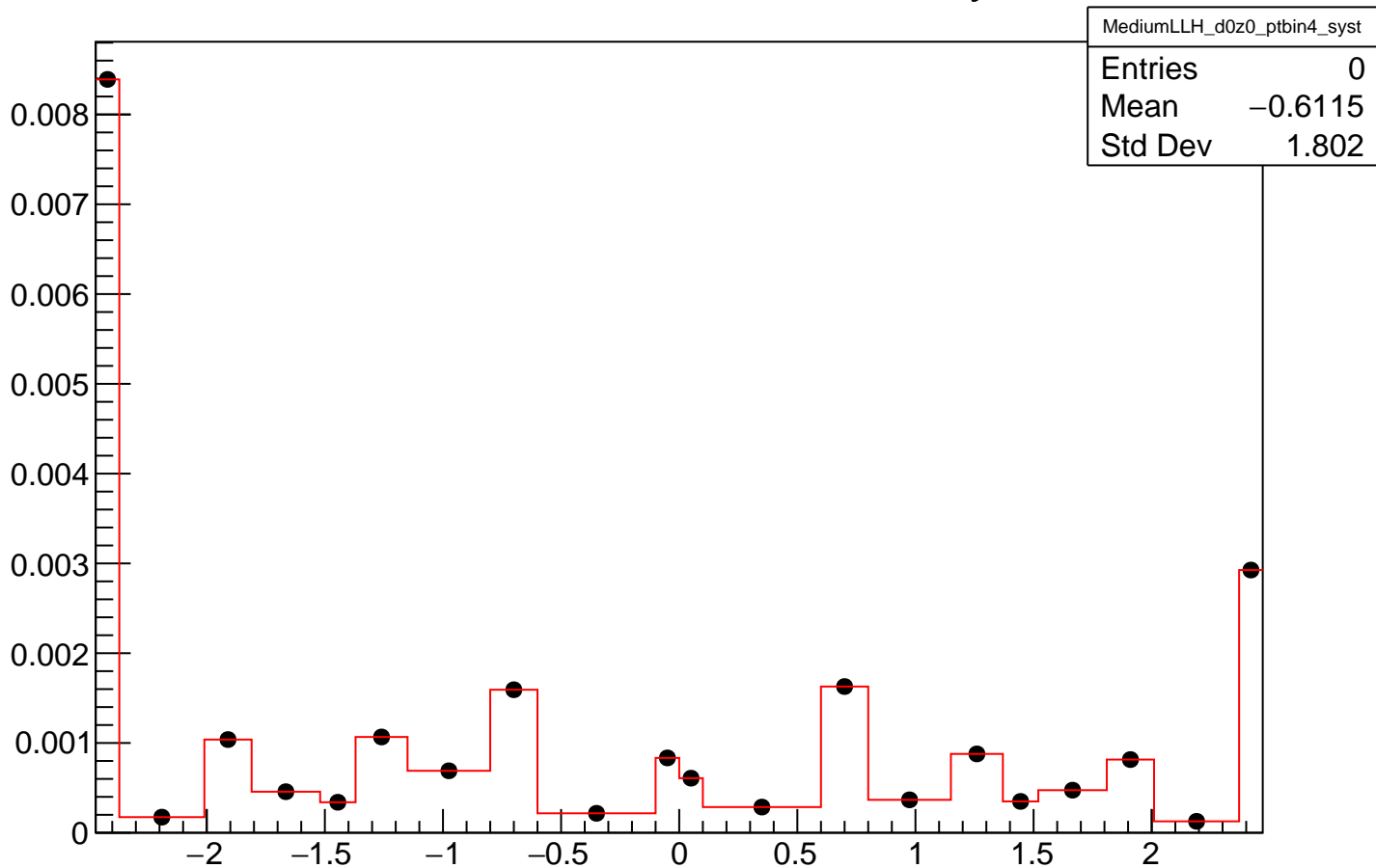
# FullSim\_MediumLLH\_d0z0\_sys



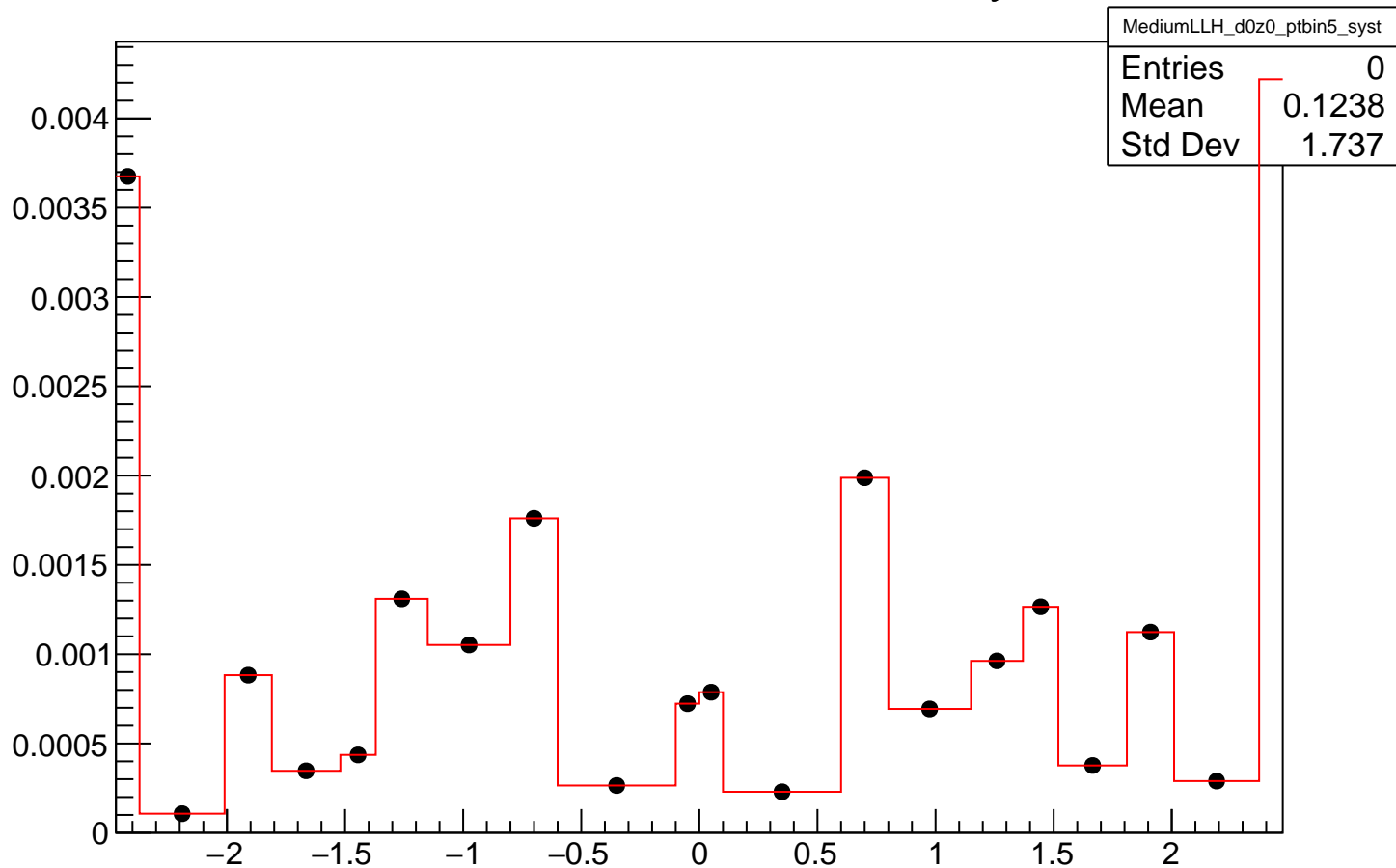
# FullSim\_MediumLLH\_d0z0\_sys



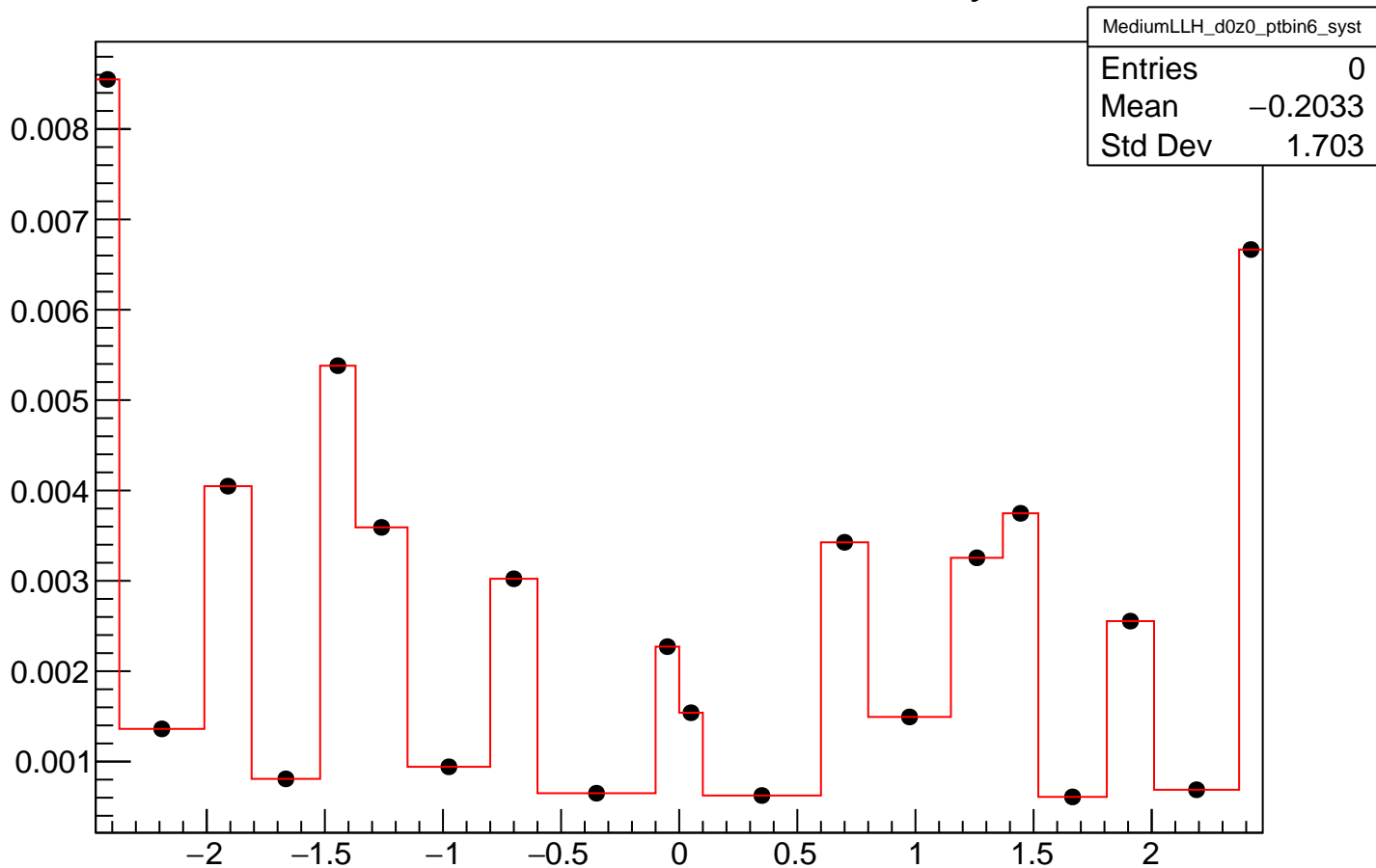
# FullSim\_MediumLLH\_d0z0\_sys



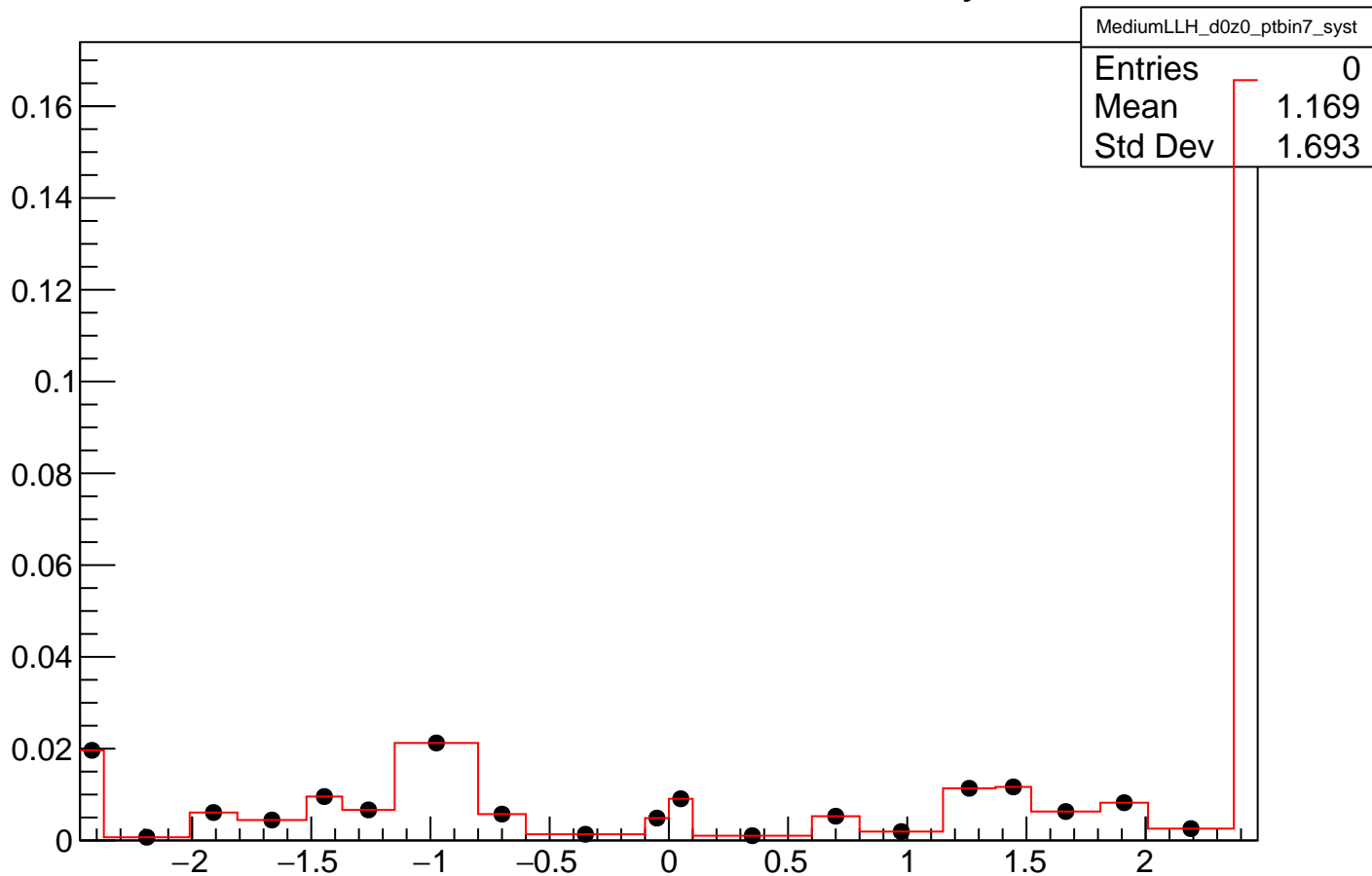
# FullSim\_MediumLLH\_d0z0\_sys



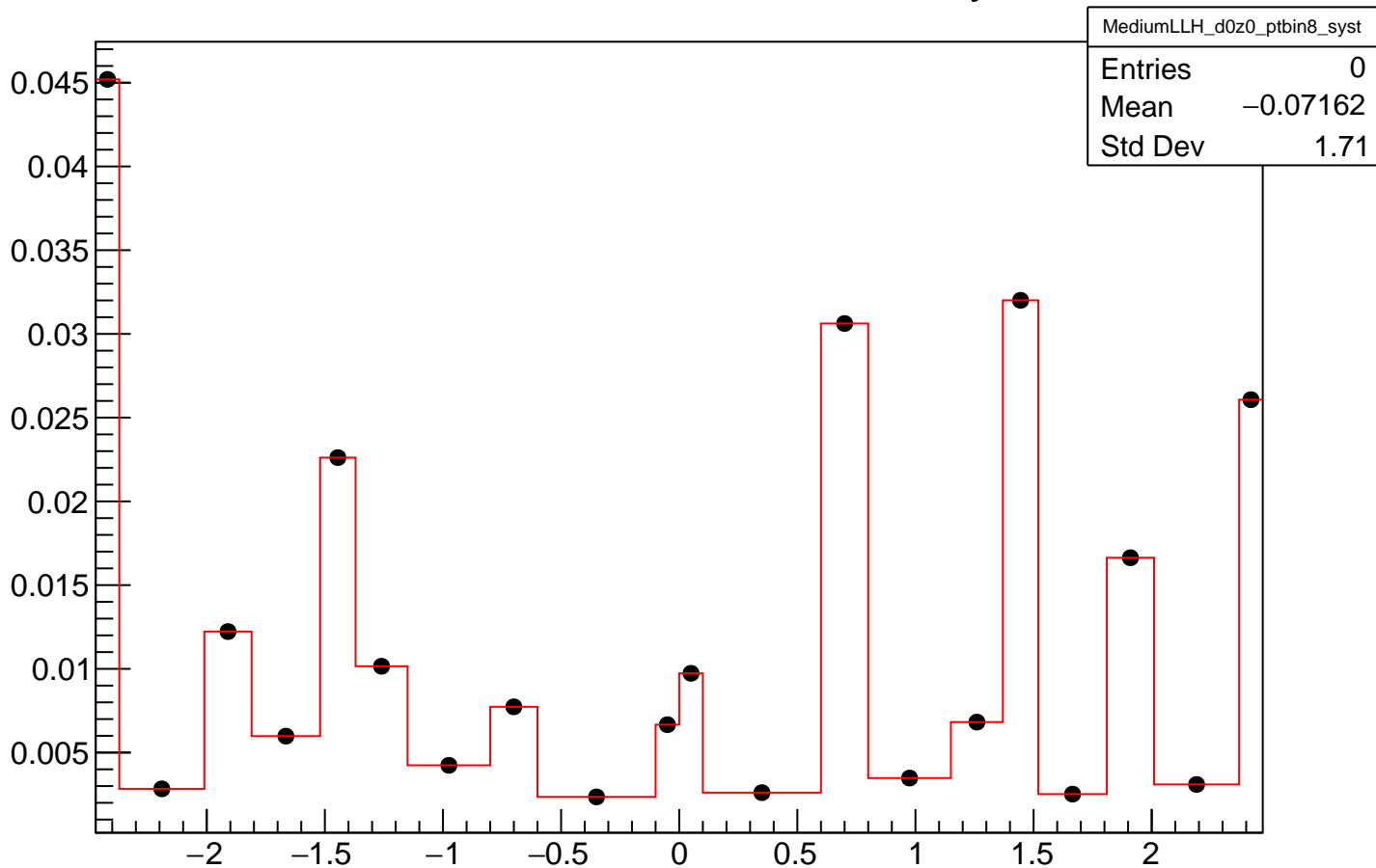
# FullSim\_MediumLLH\_d0z0\_sys



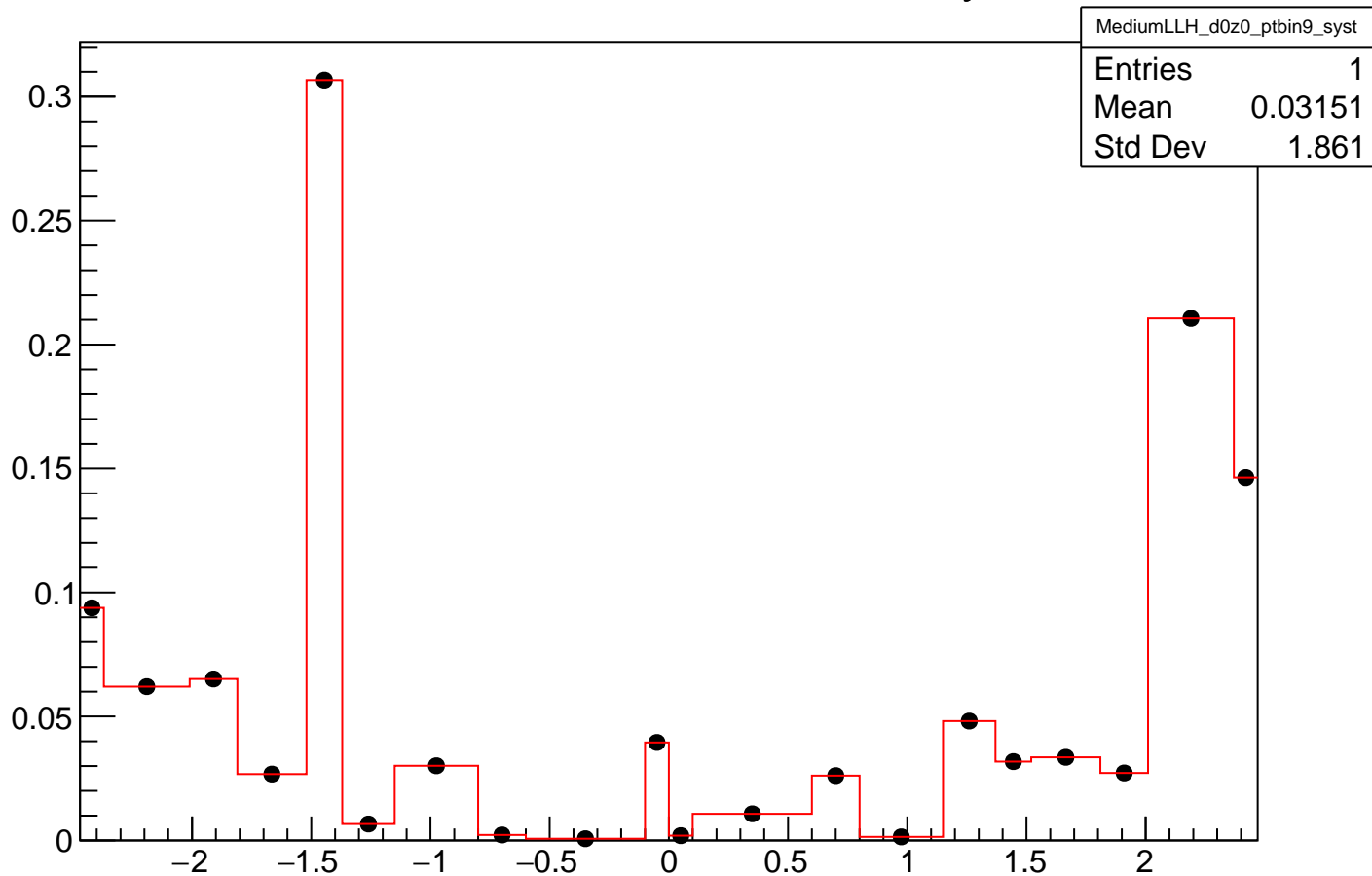
# FullSim\_MediumLLH\_d0z0\_sys



# FullSim\_MediumLLH\_d0z0\_sys

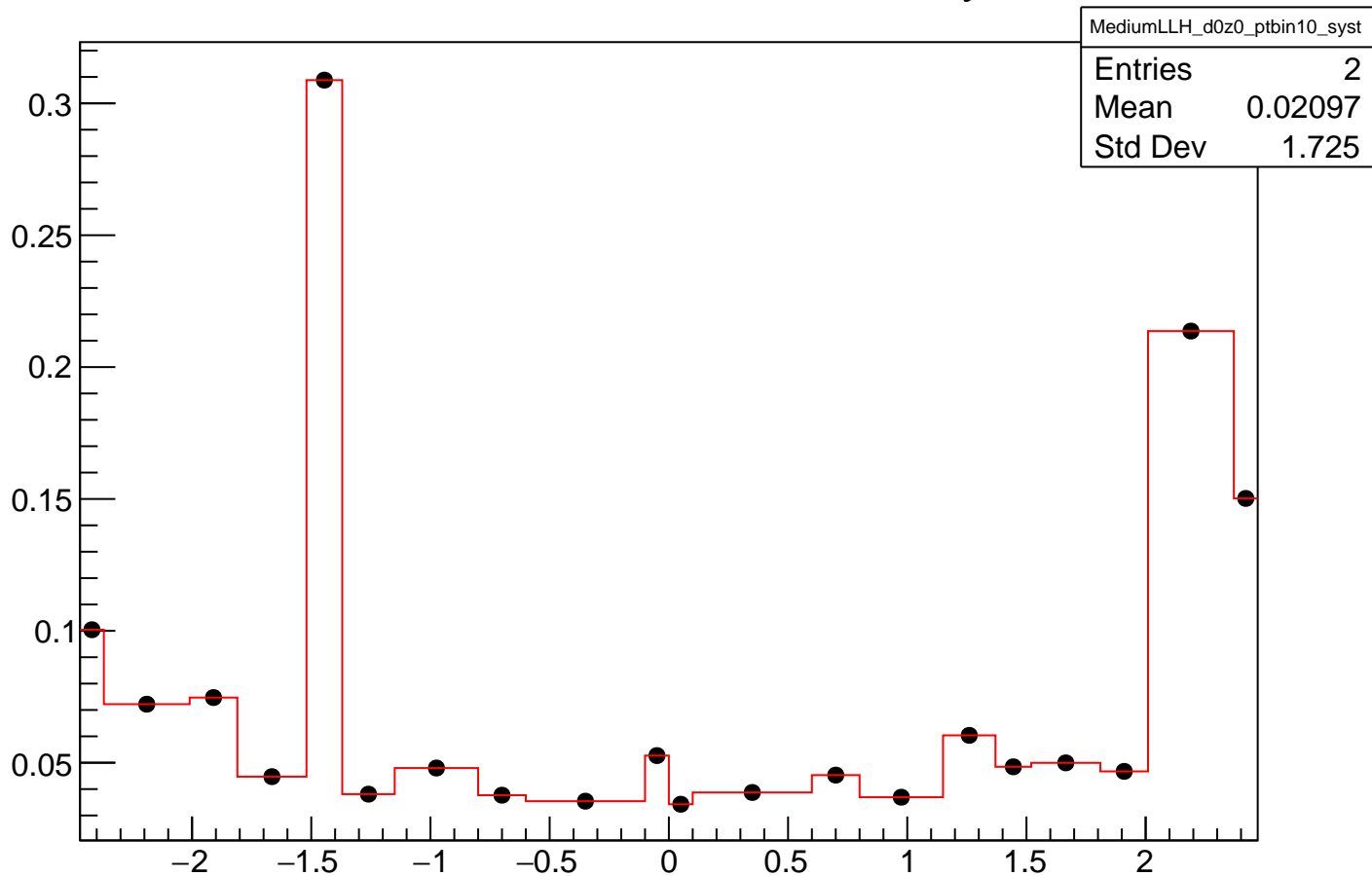


# FullSim\_MediumLLH\_d0z0\_sys

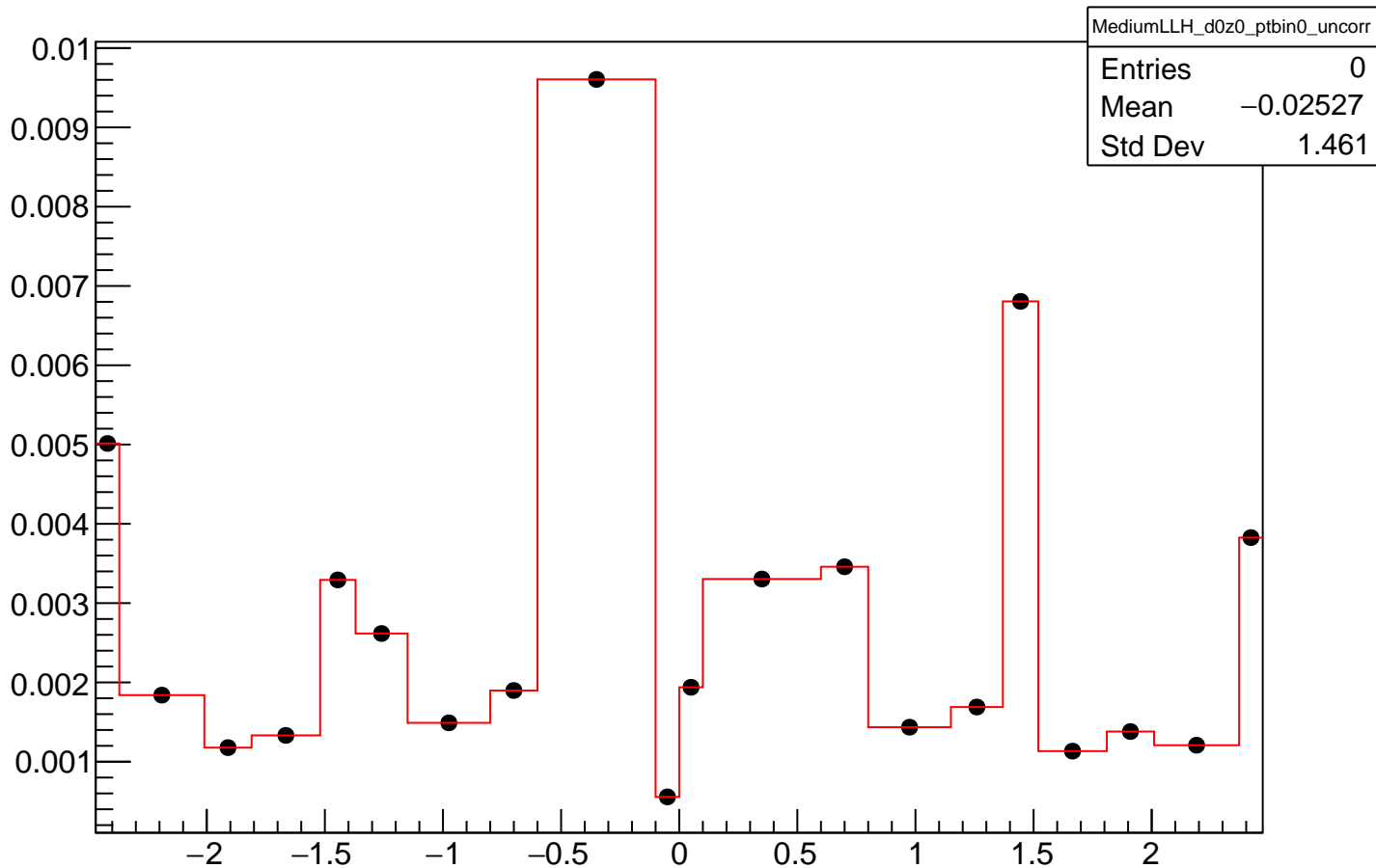




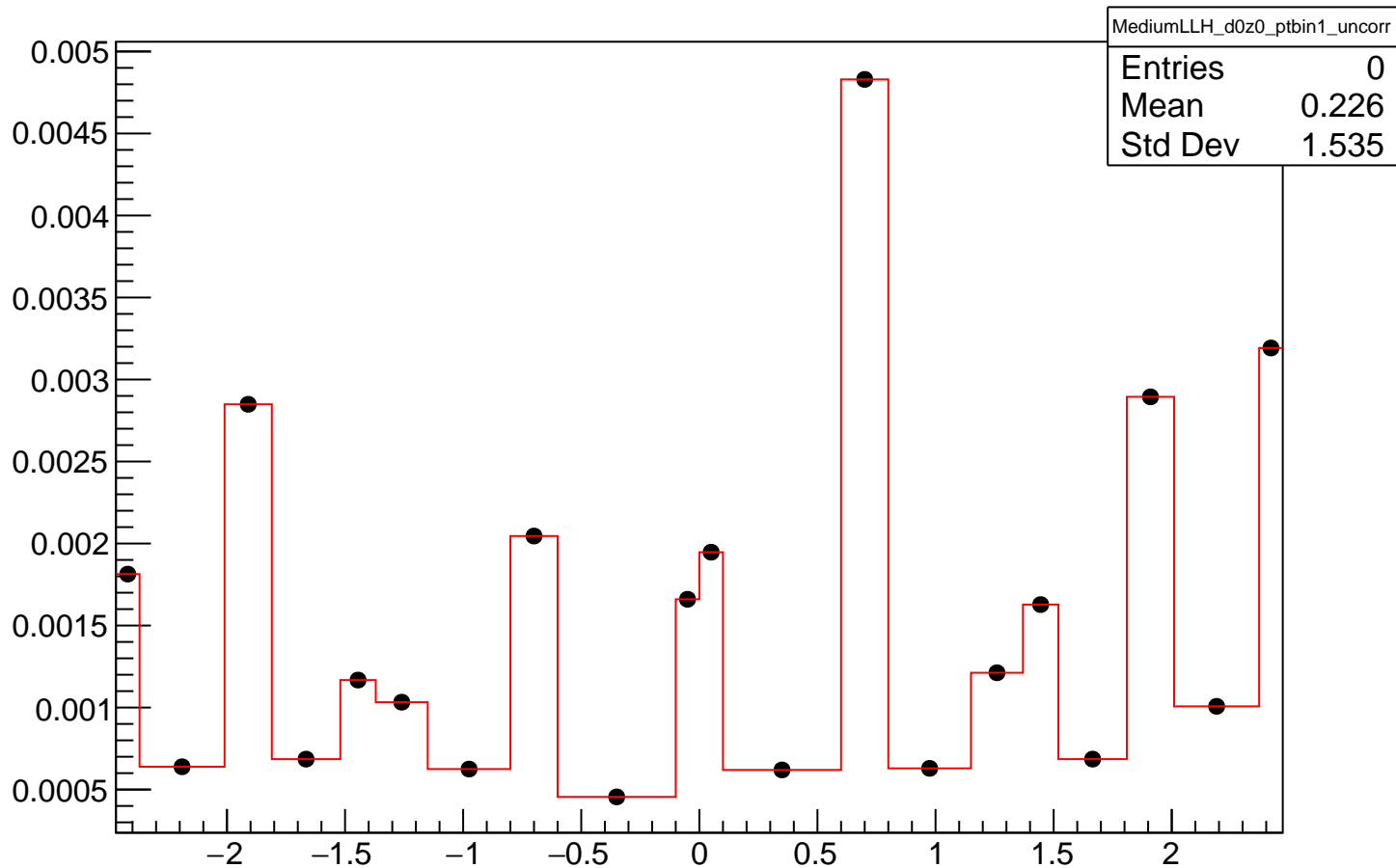
# FullSim\_MediumLLH\_d0z0\_sys



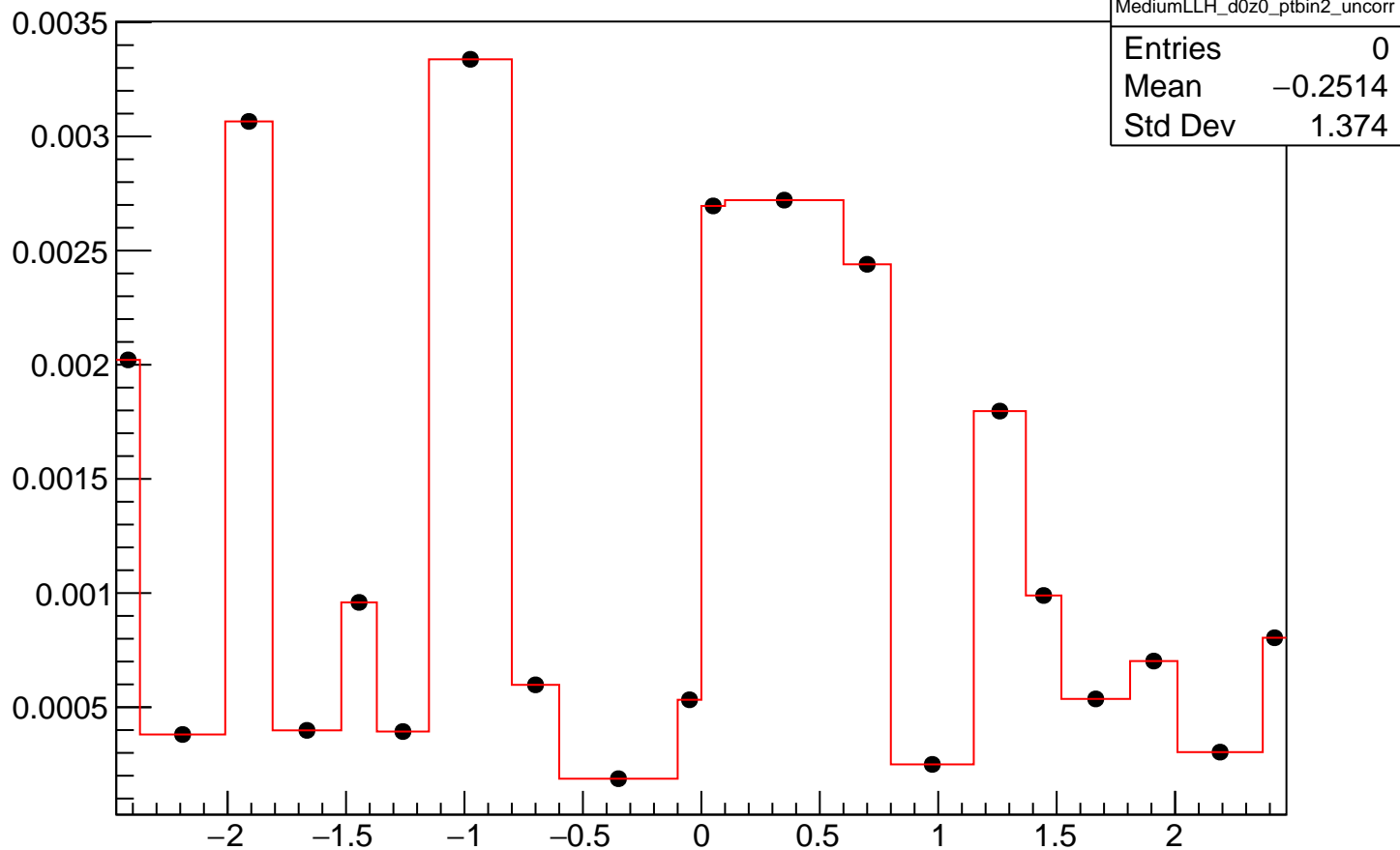
# FullSim\_MediumLLH\_d0z0\_uncorr



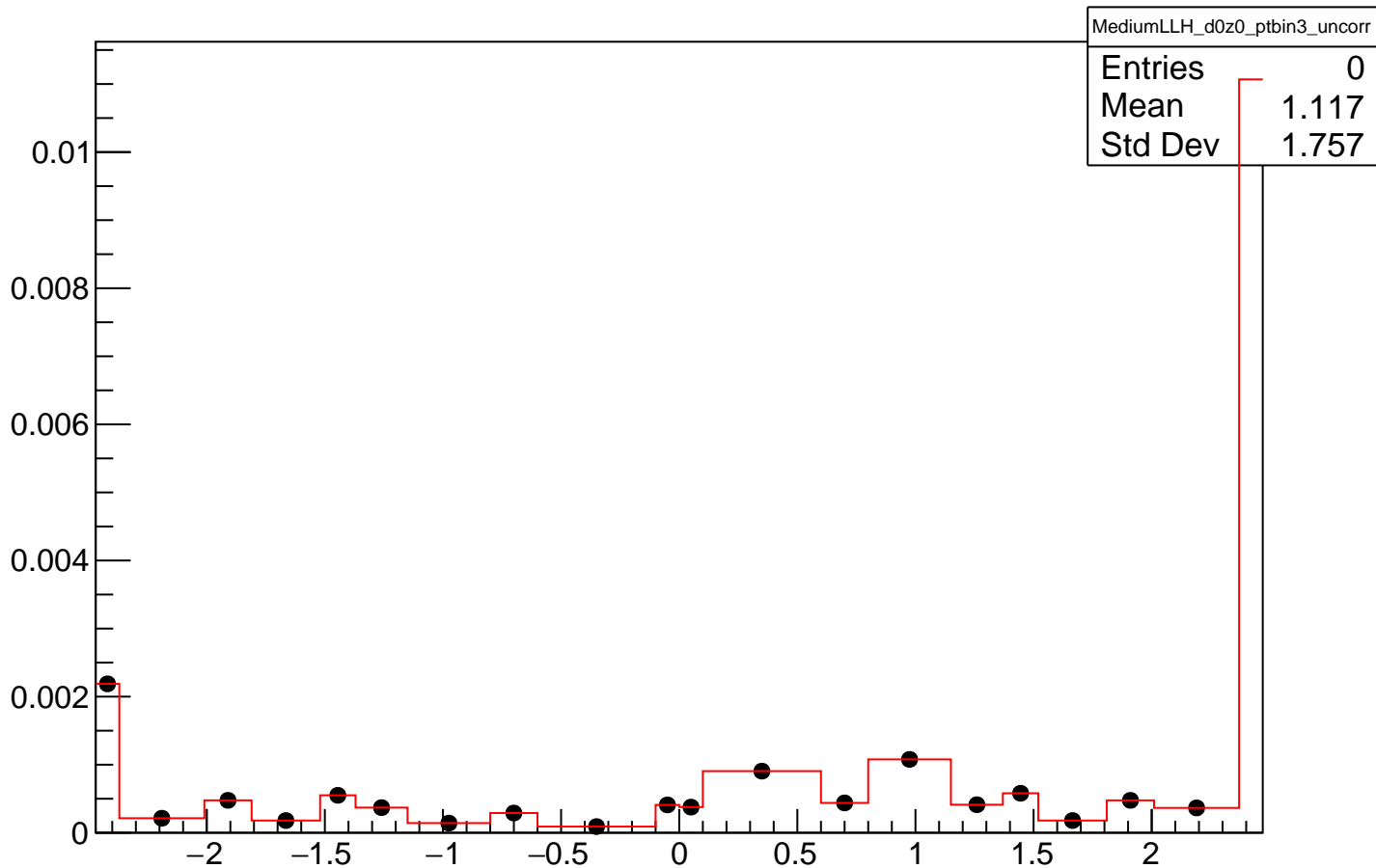
# FullSim\_MediumLLH\_d0z0\_uncorr



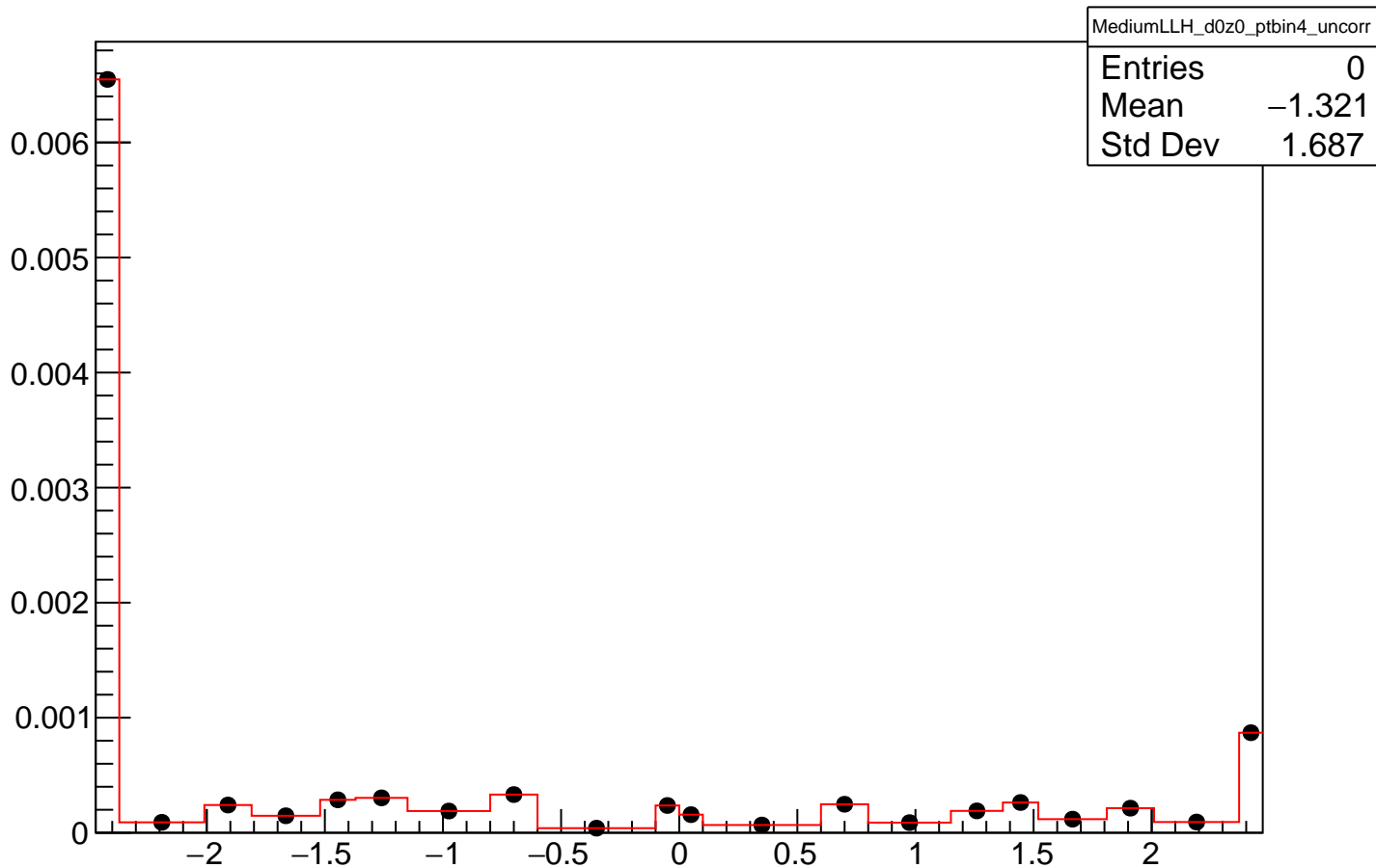
# FullSim\_MediumLLH\_d0z0\_uncorr



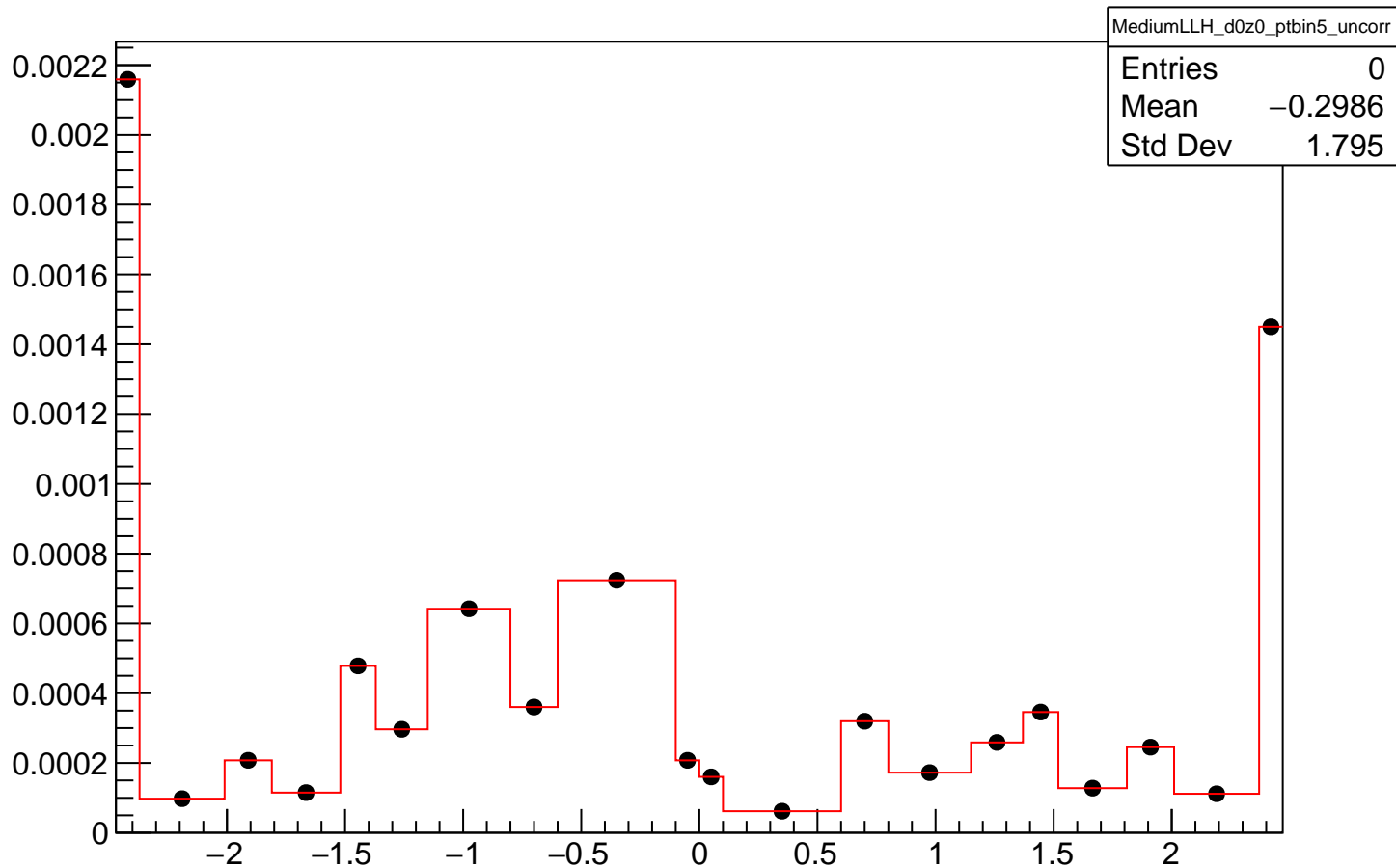
# FullSim\_MediumLLH\_d0z0\_uncorr



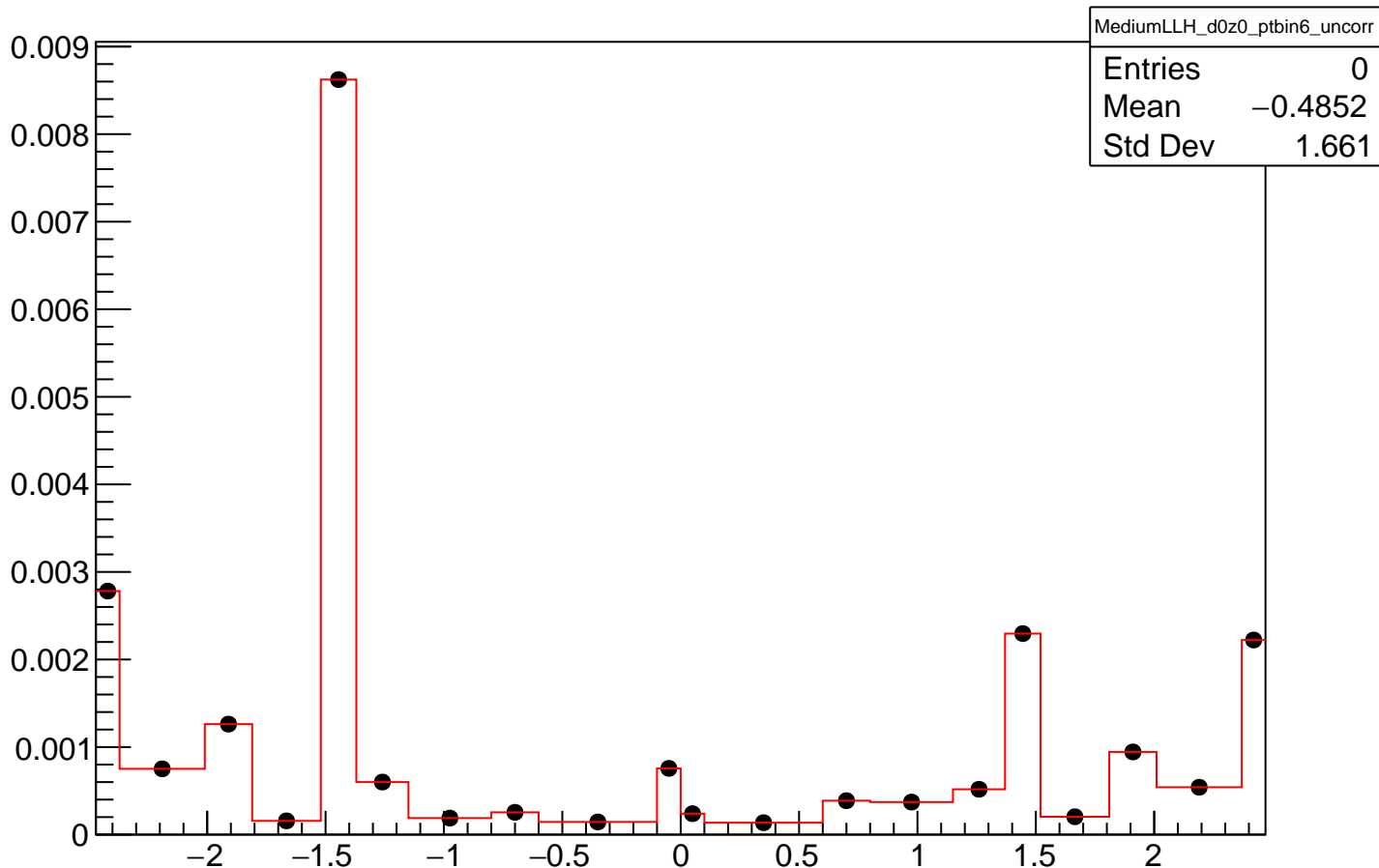
# FullSim\_MediumLLH\_d0z0\_uncorr



# FullSim\_MediumLLH\_d0z0\_uncorr

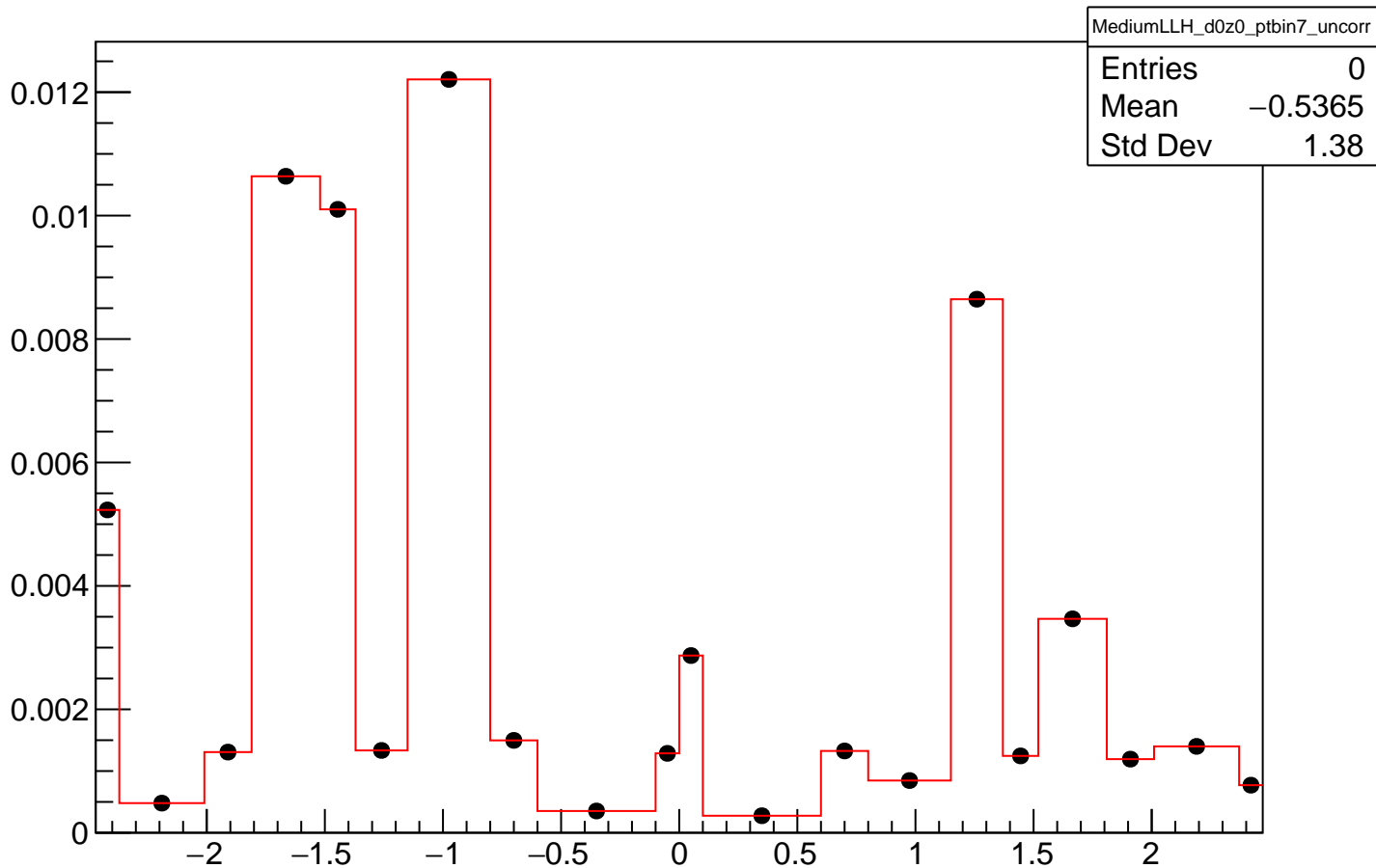


# FullSim\_MediumLLH\_d0z0\_uncorr

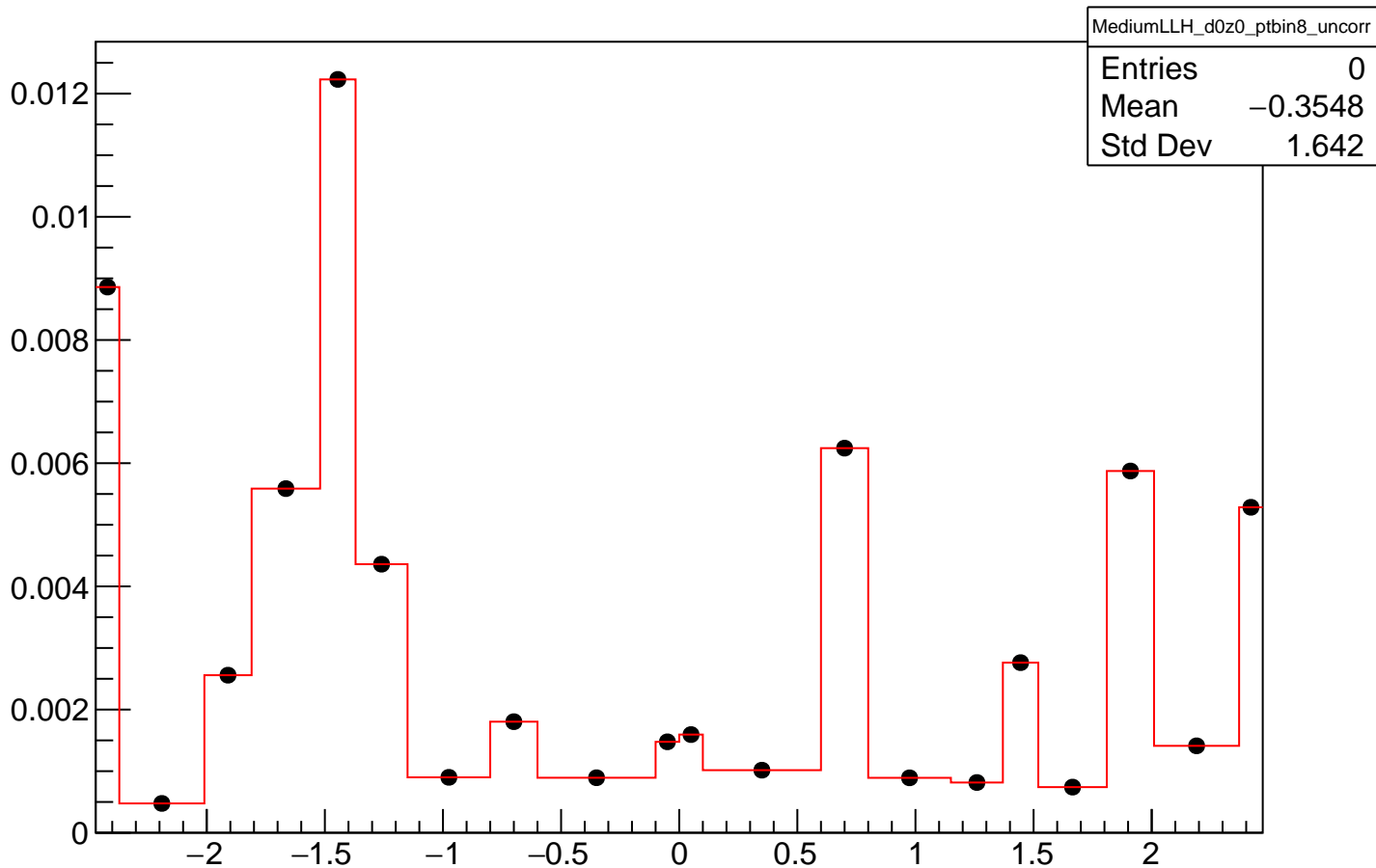




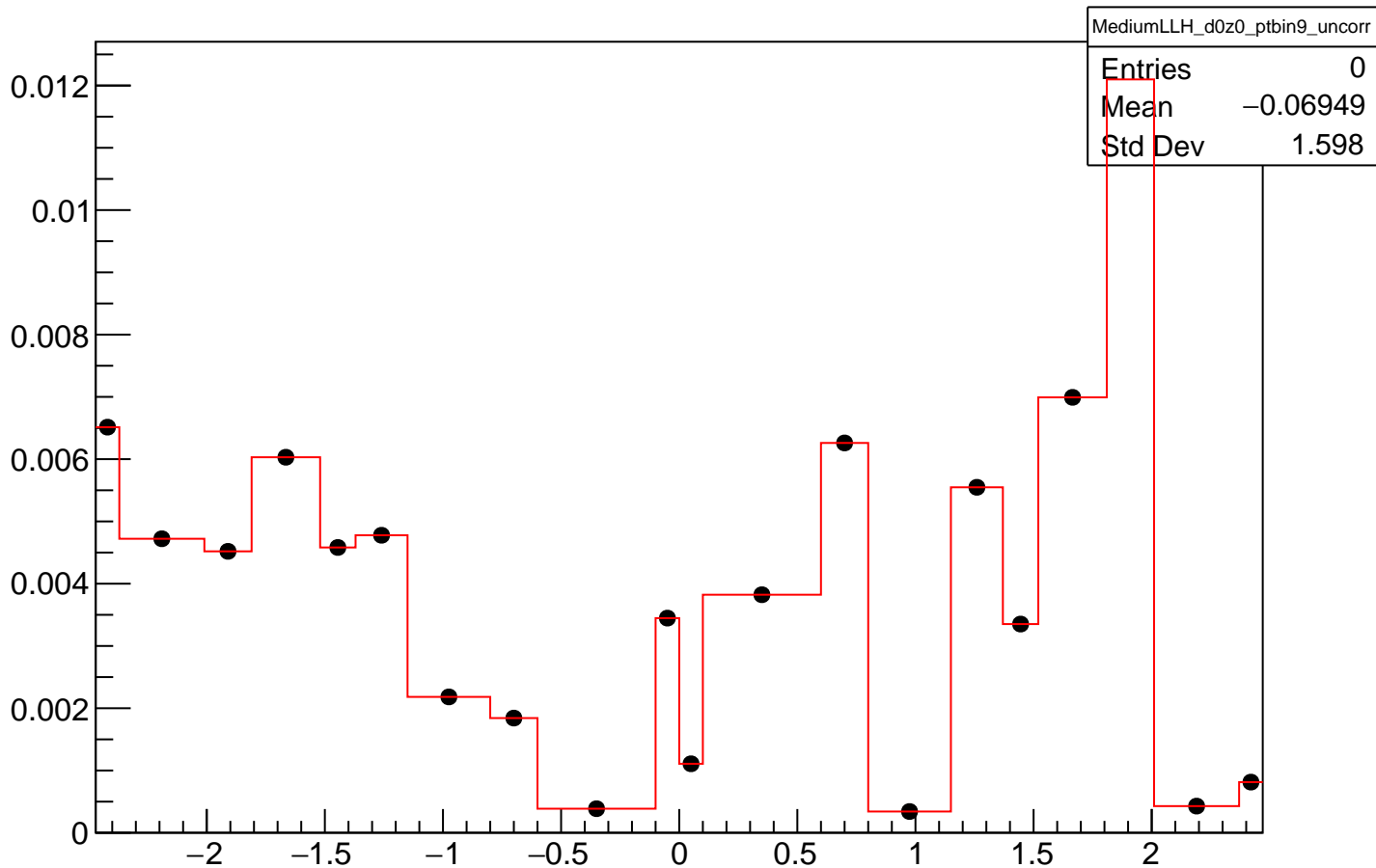
# FullSim\_MediumLLH\_d0z0\_uncorr



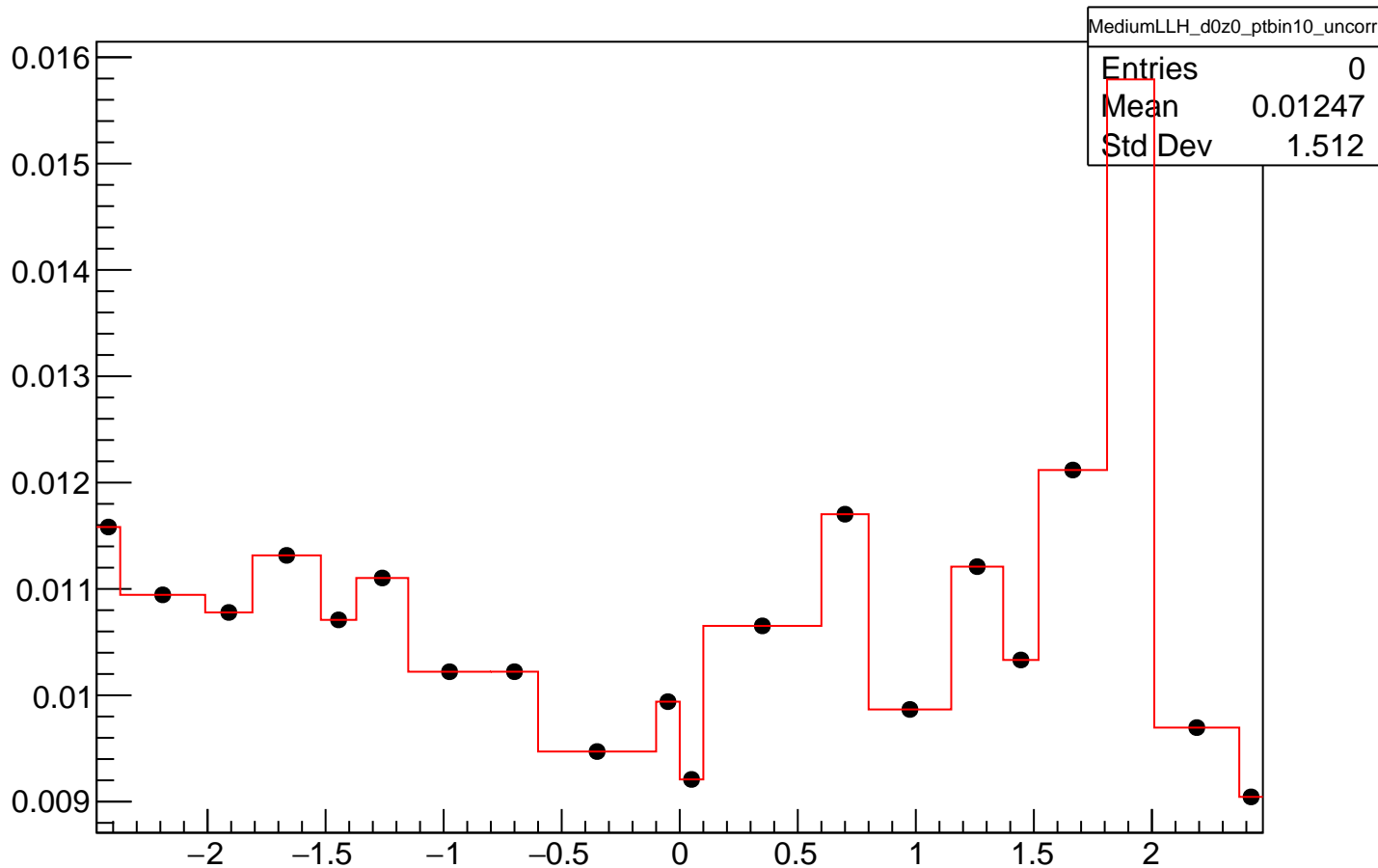
# FullSim\_MediumLLH\_d0z0\_uncorr



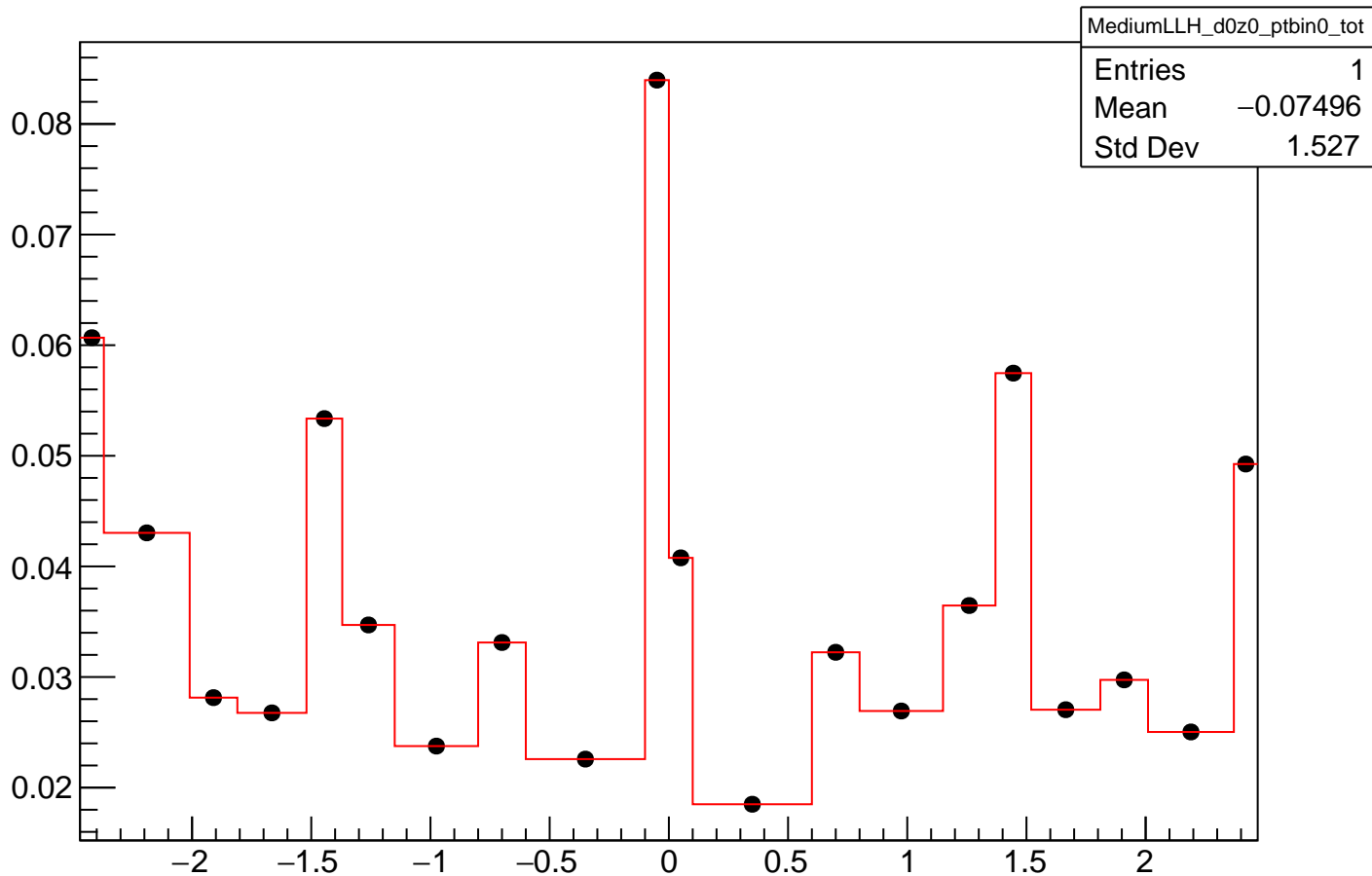
# FullSim\_MediumLLH\_d0z0\_uncorr



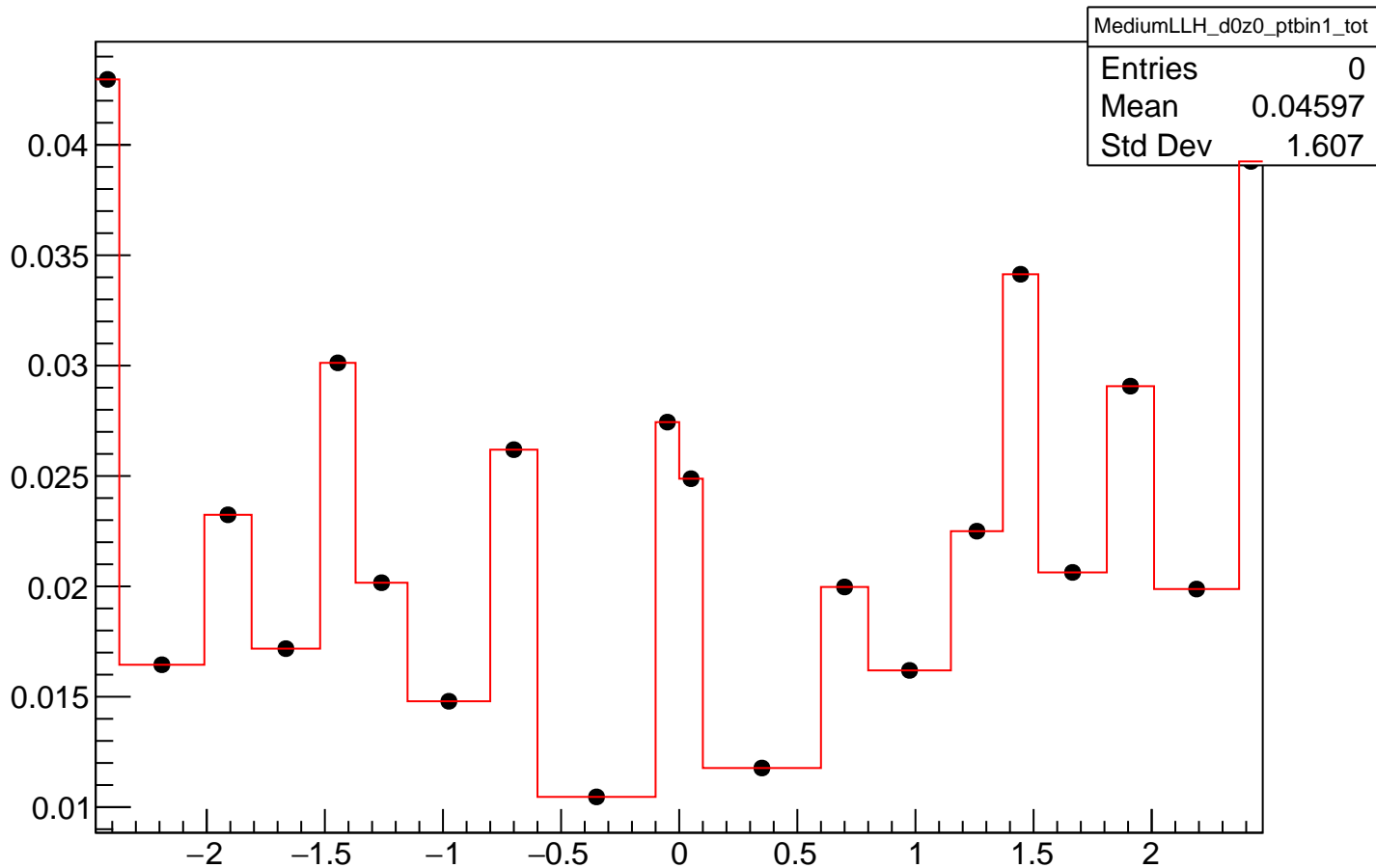
# FullSim\_MediumLLH\_d0z0\_uncorr



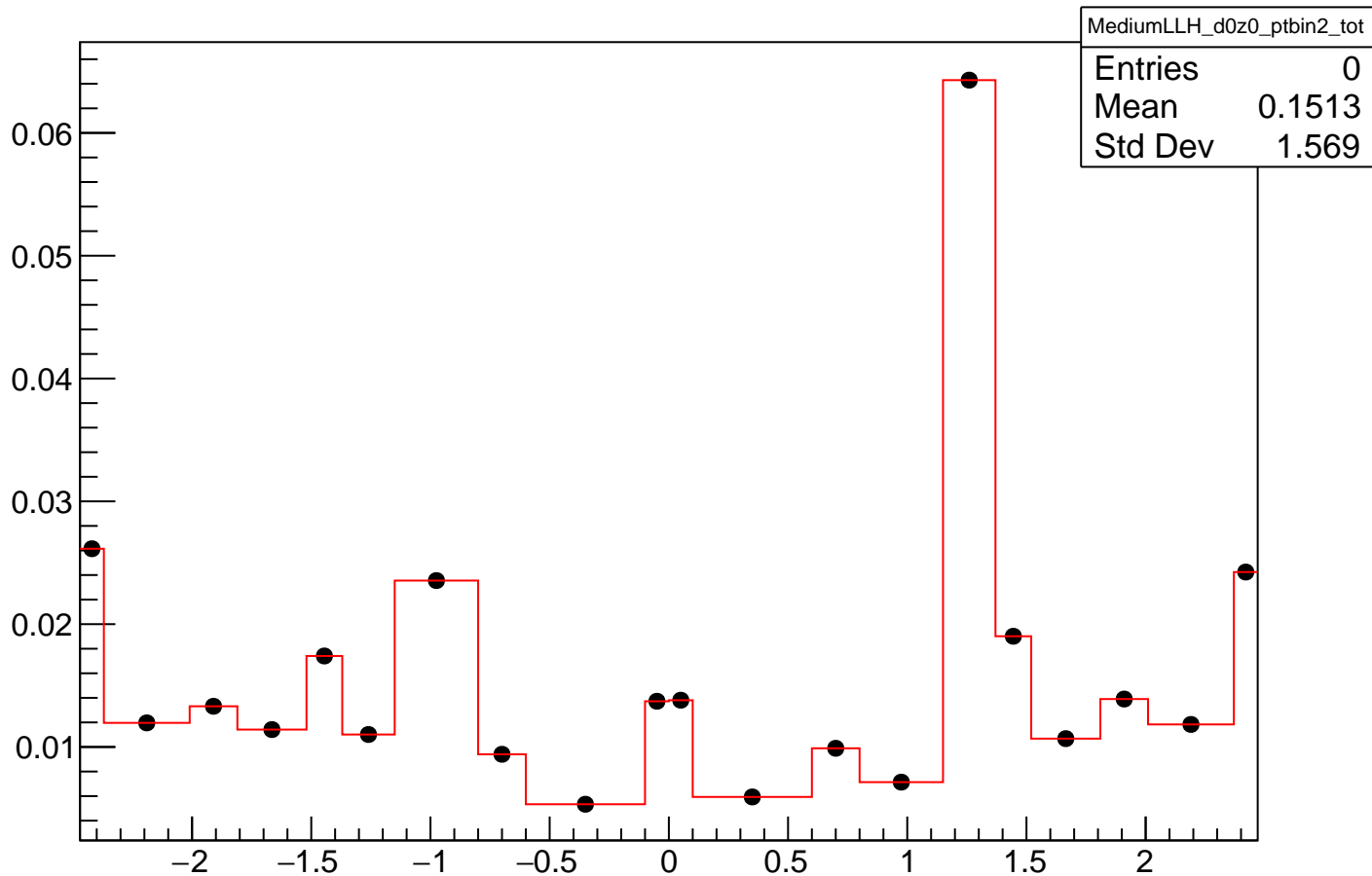
# FullSim\_MediumLLH\_d0z0\_tot



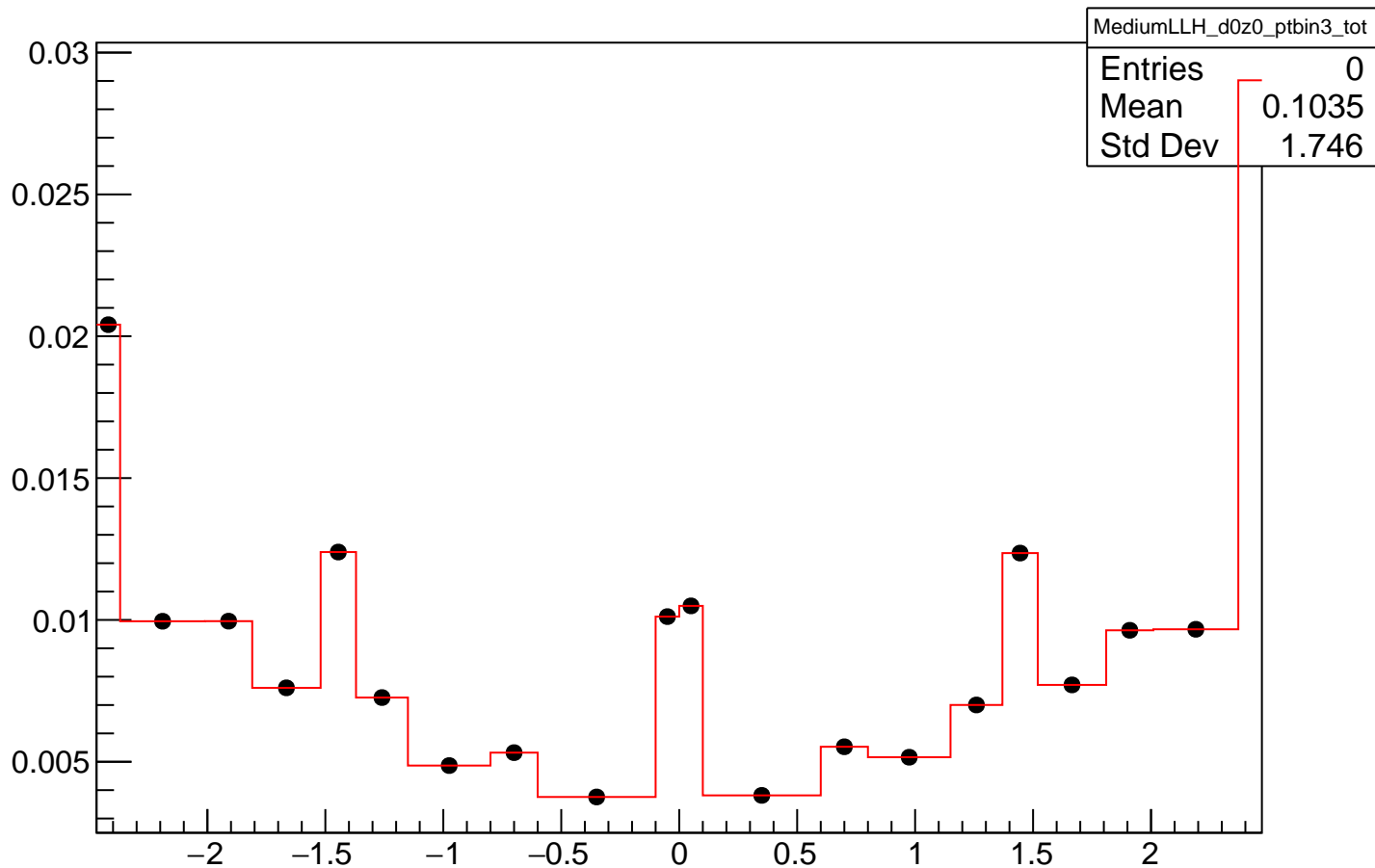
# FullSim\_MediumLLH\_d0z0\_tot



# FullSim\_MediumLLH\_d0z0\_tot

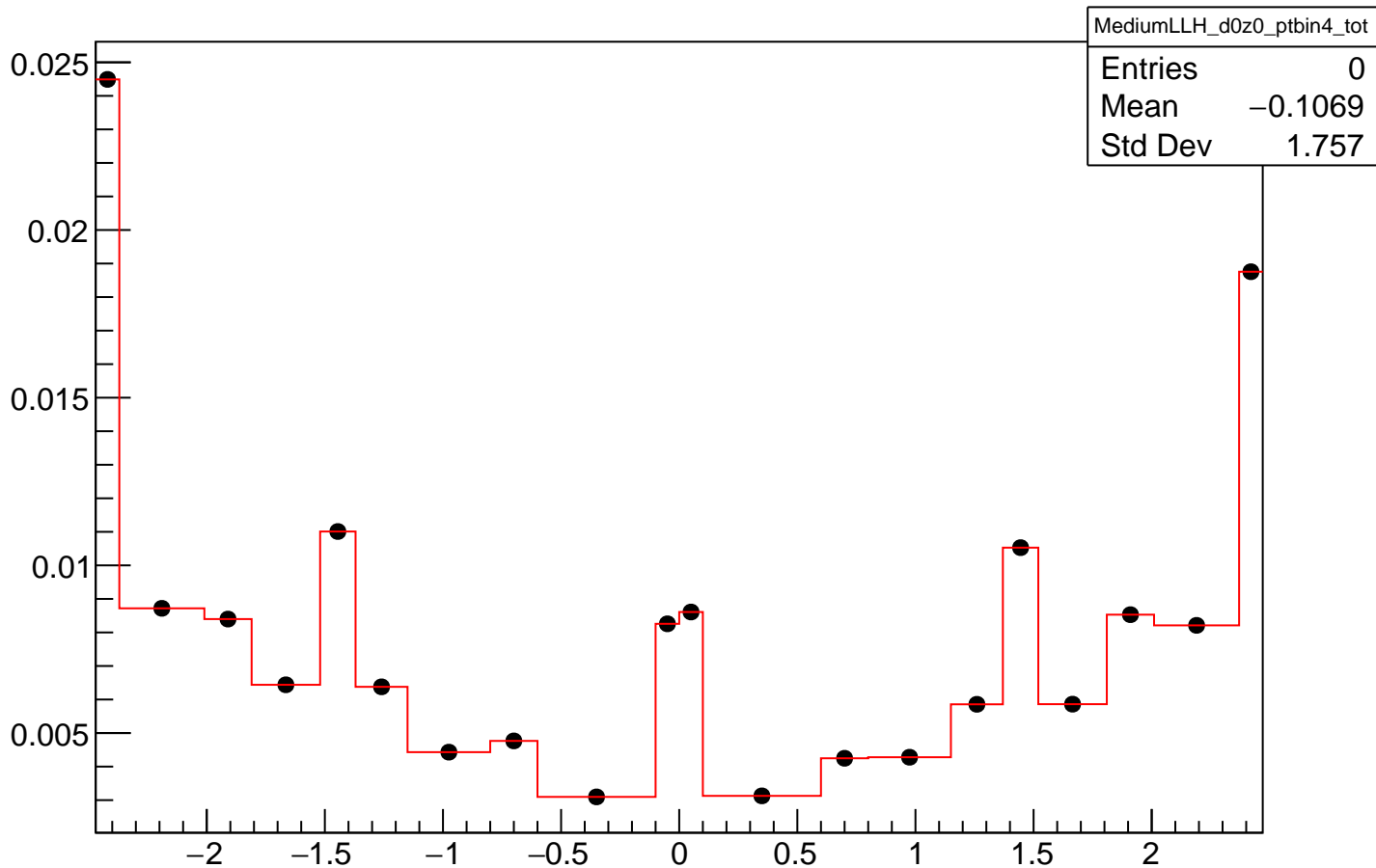


# FullSim\_MediumLLH\_d0z0\_tot

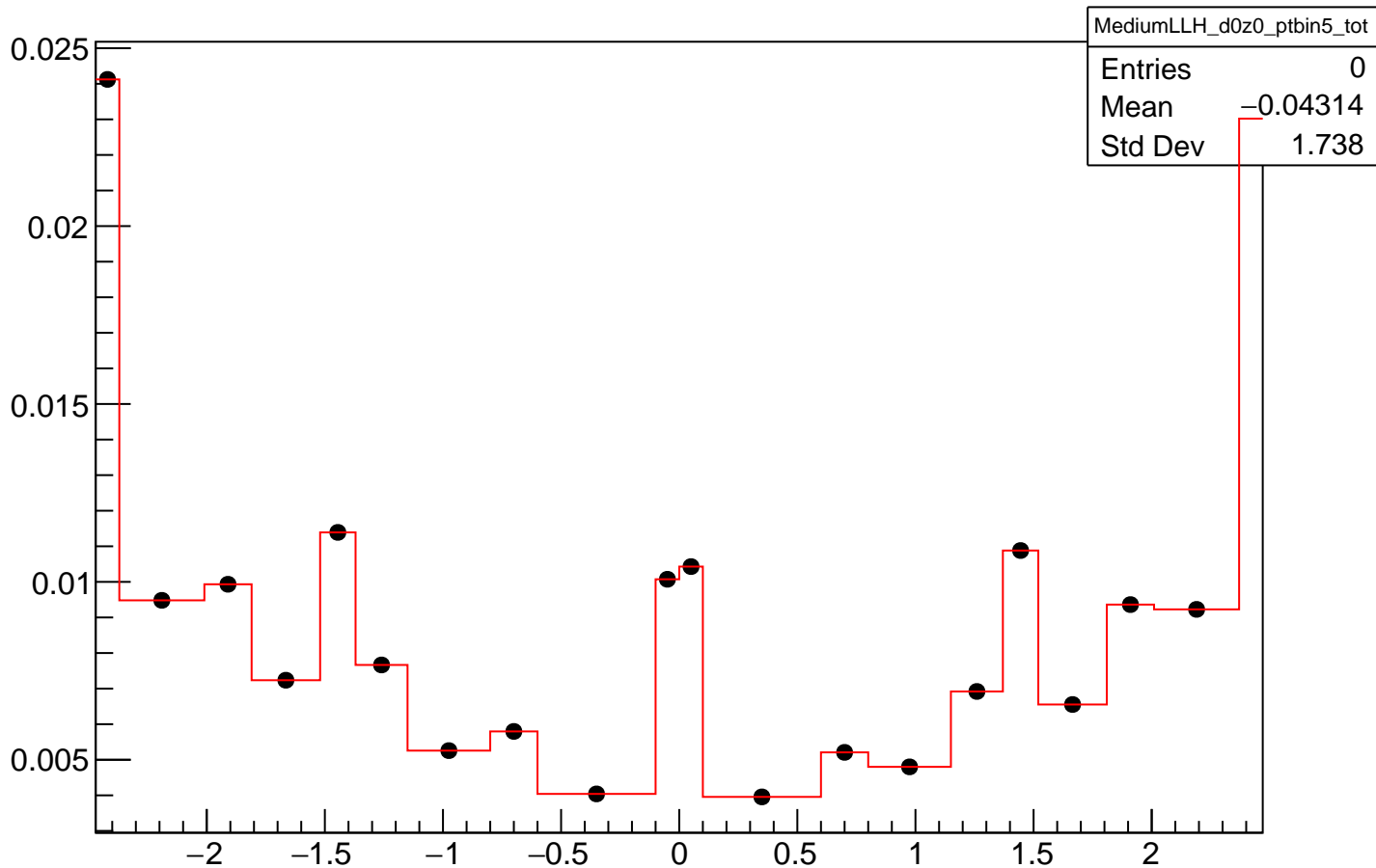




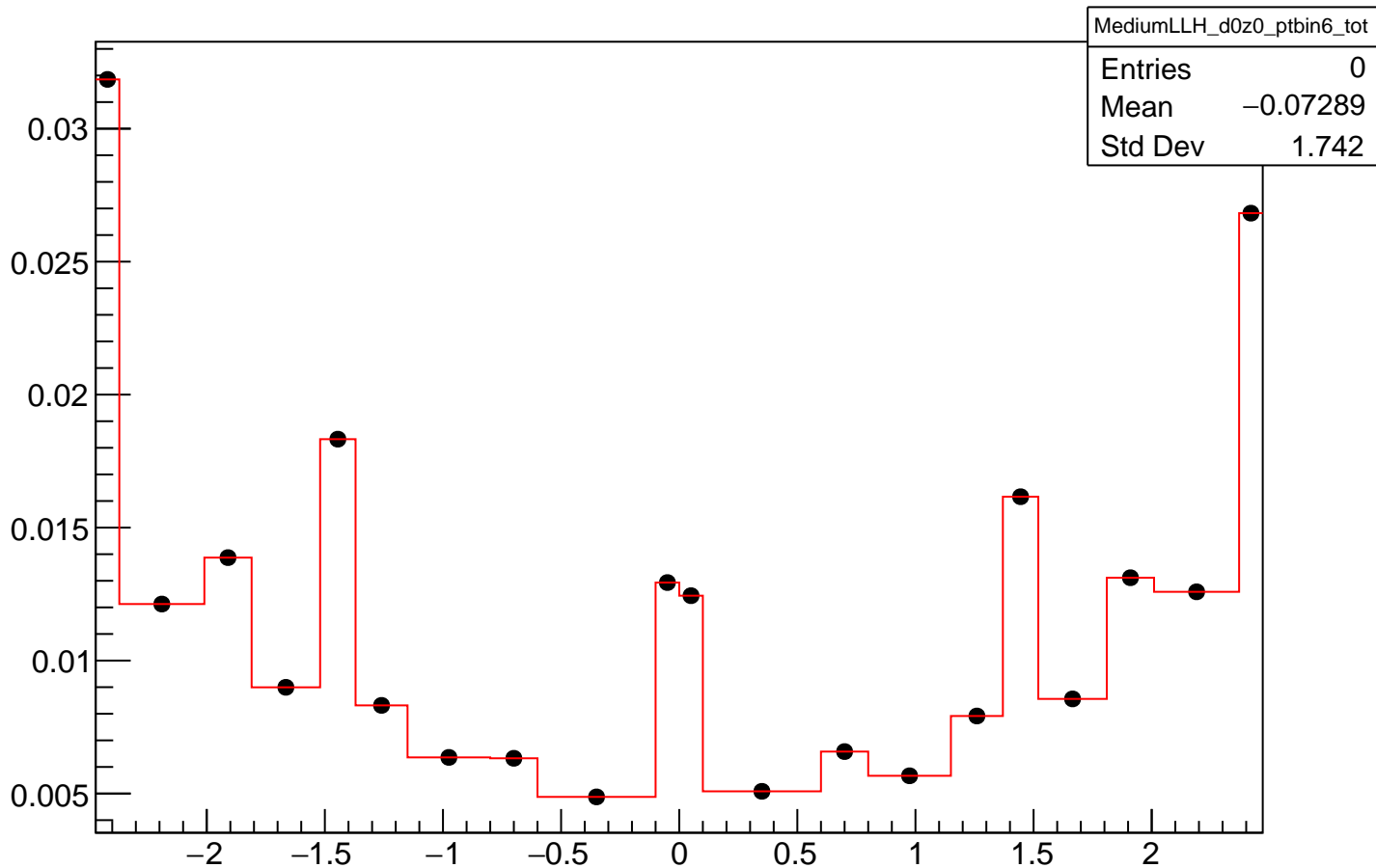
# FullSim\_MediumLLH\_d0z0\_tot



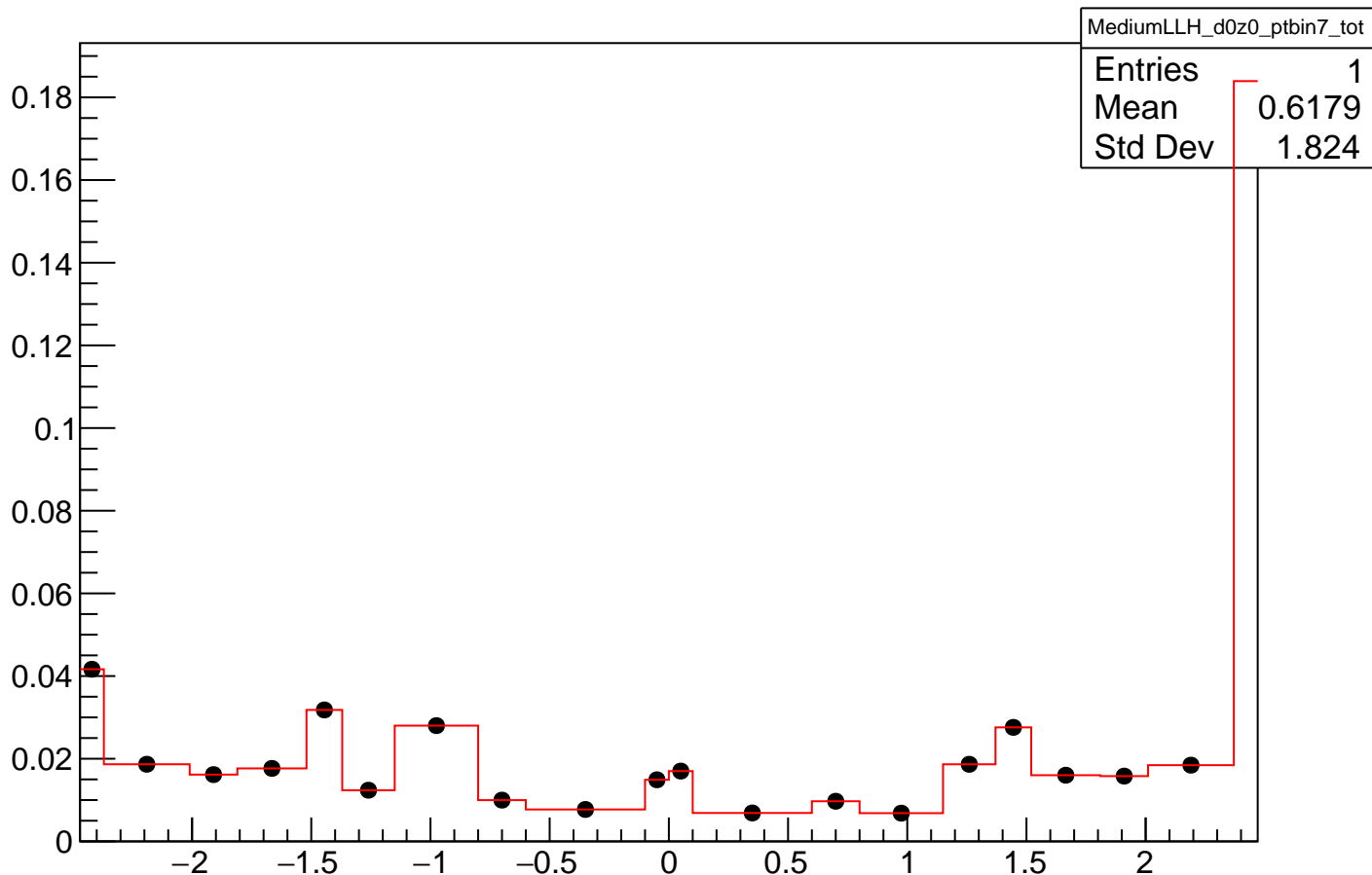
# FullSim\_MediumLLH\_d0z0\_tot



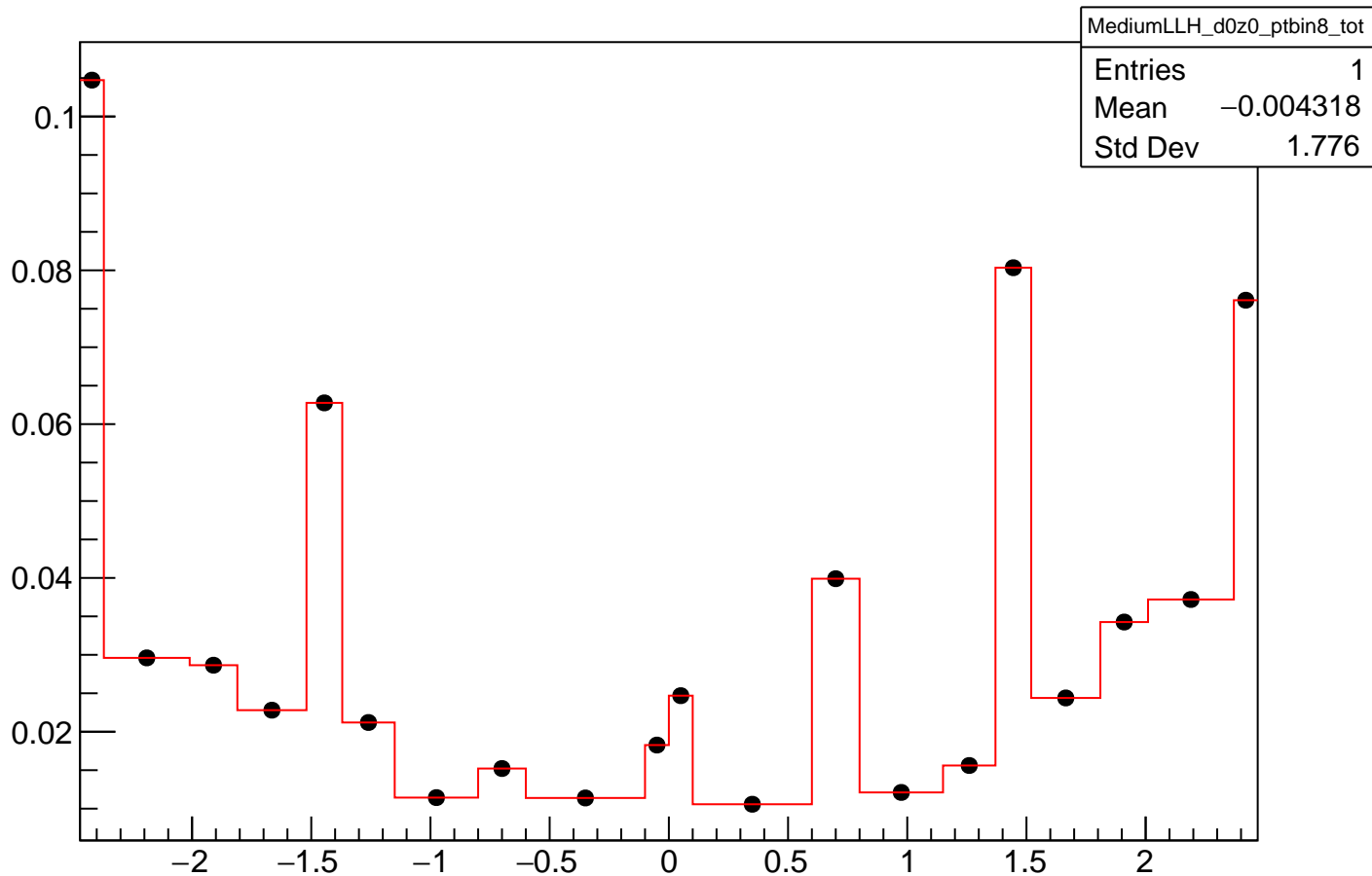
# FullSim\_MediumLLH\_d0z0\_tot



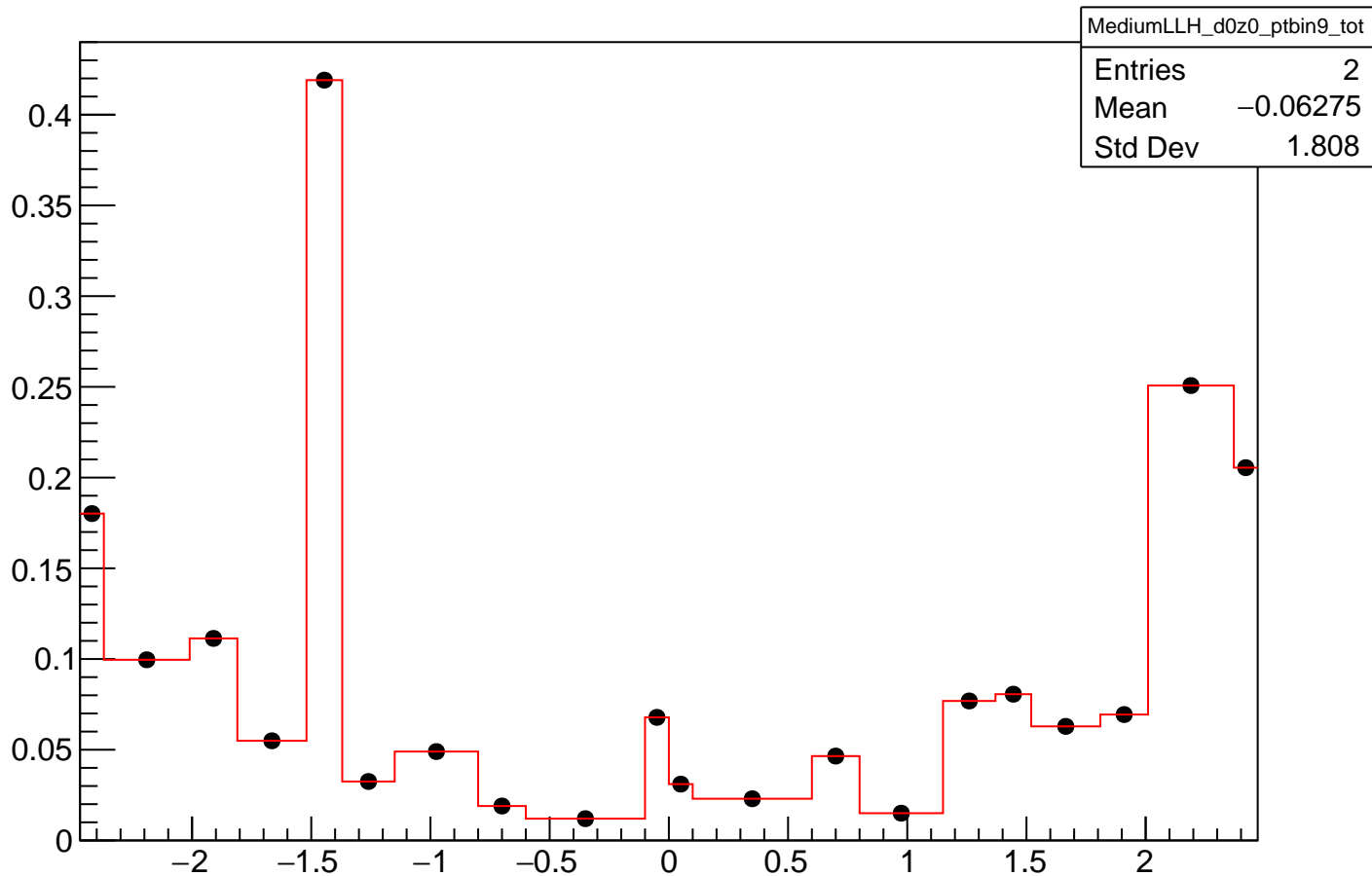
# FullSim\_MediumLLH\_d0z0\_tot



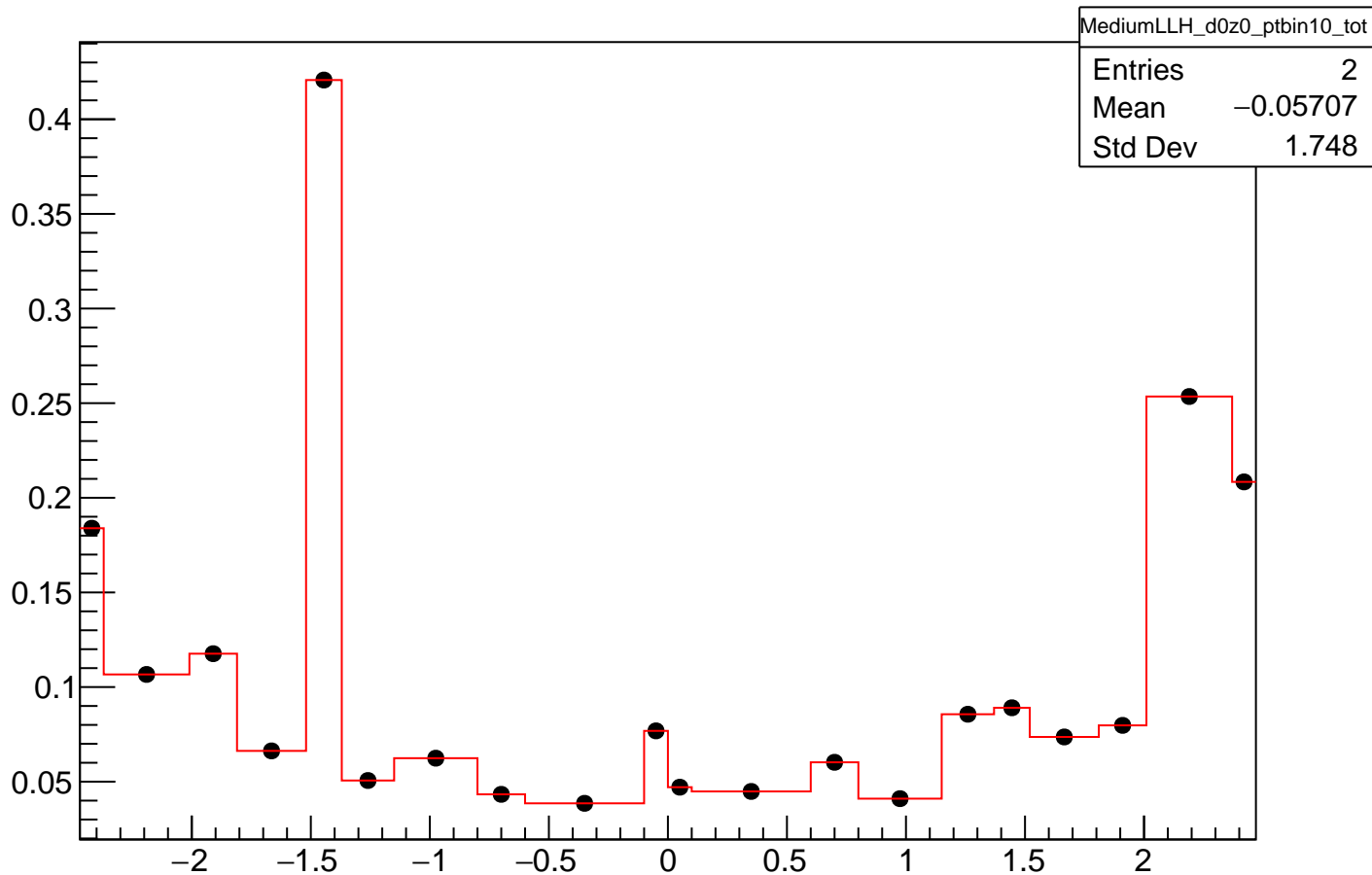
# FullSim\_MediumLLH\_d0z0\_tot



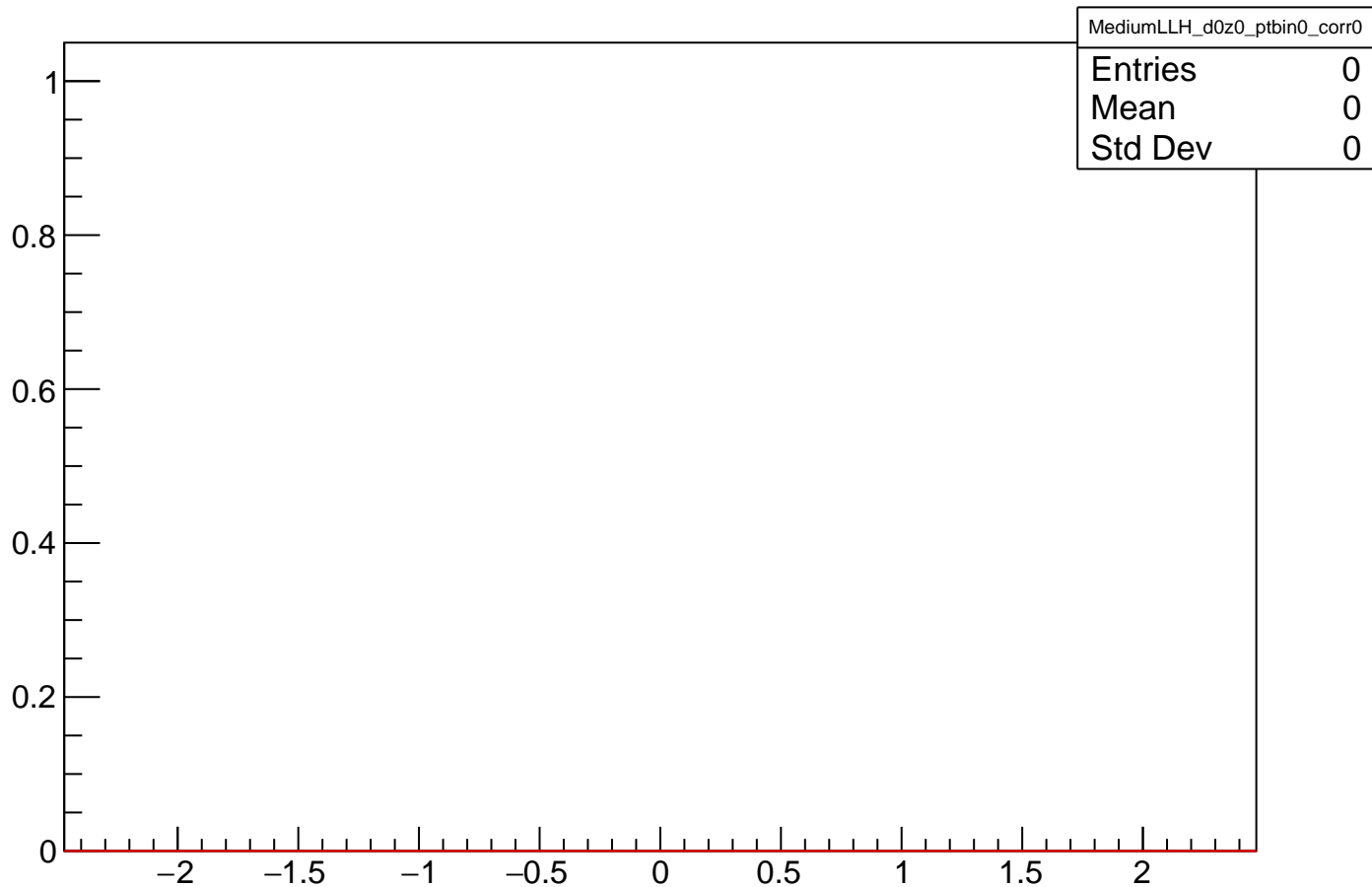
# FullSim\_MediumLLH\_d0z0\_tot



# FullSim\_MediumLLH\_d0z0\_tot

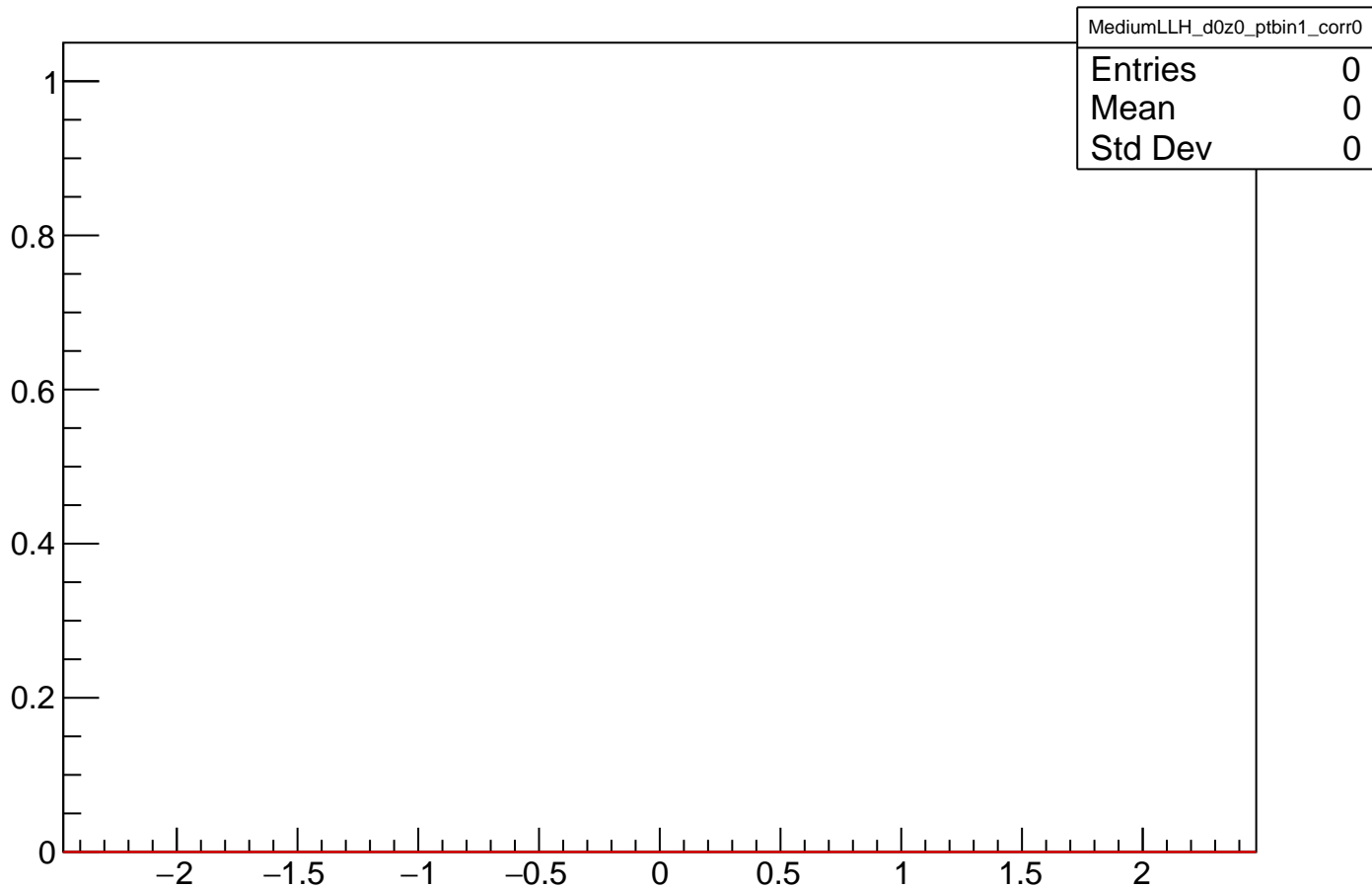


# FullSim\_MediumLLH\_d0z0\_corr0

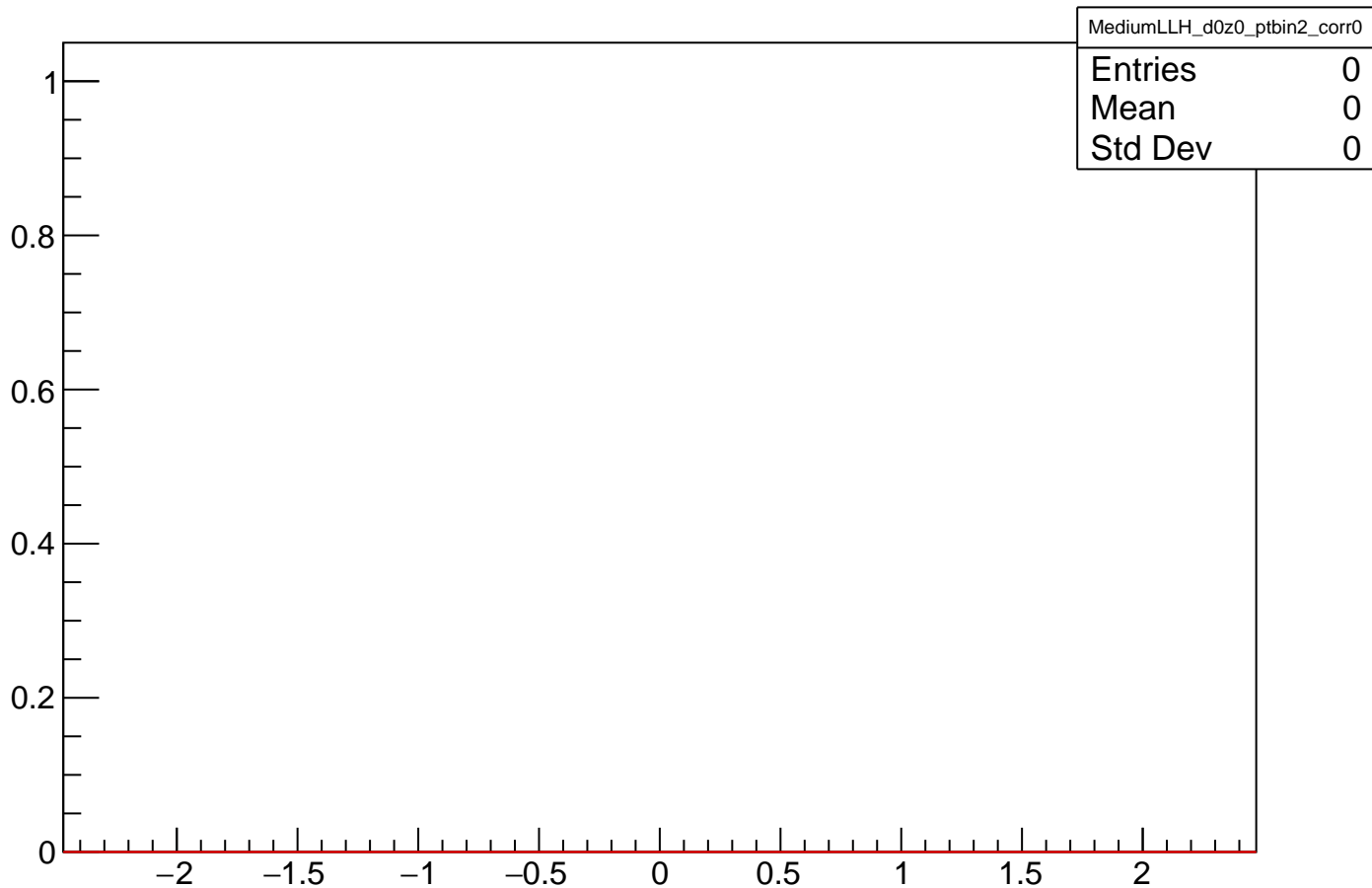




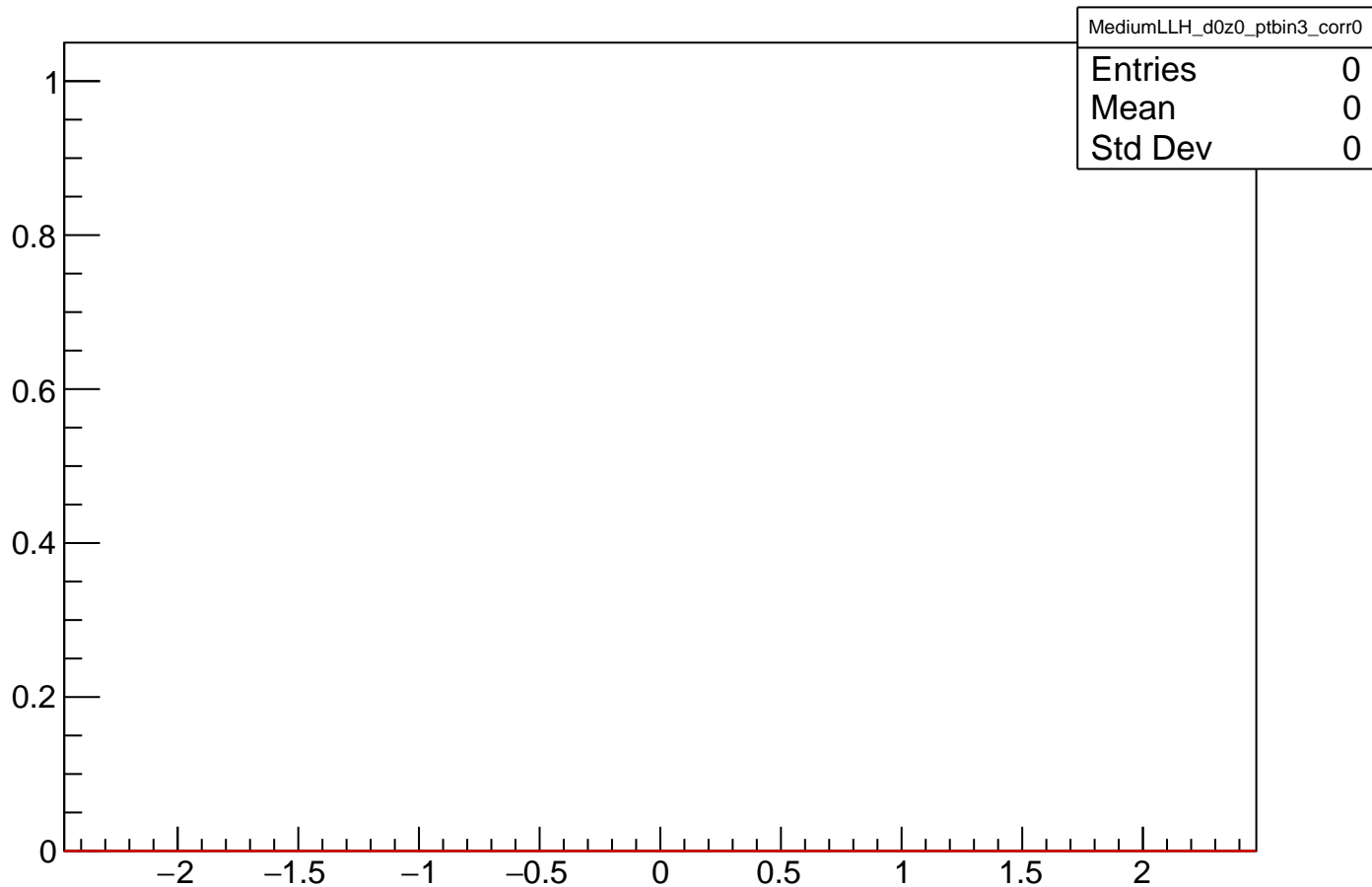
# FullSim\_MediumLLH\_d0z0\_corr0



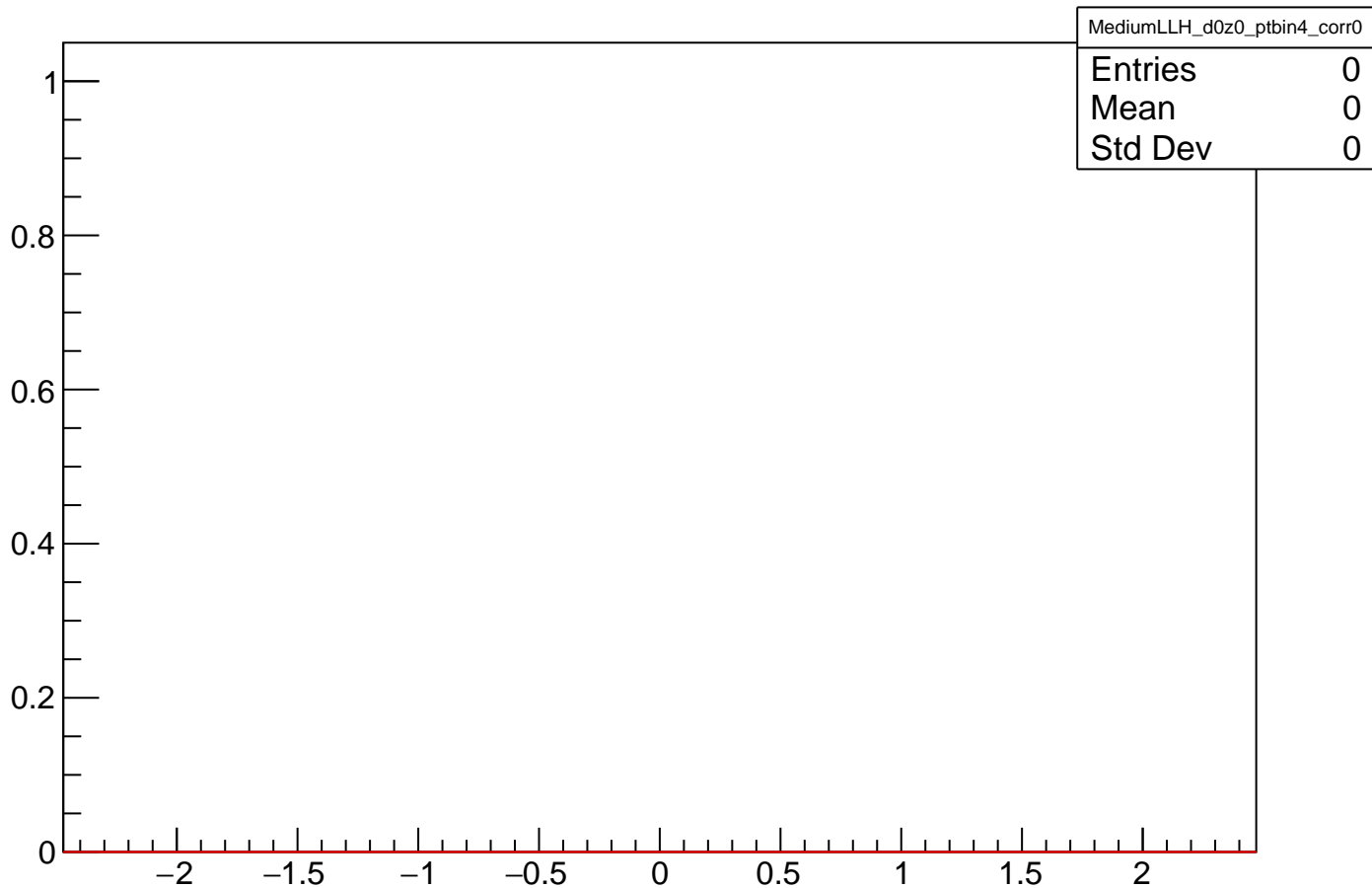
# FullSim\_MediumLLH\_d0z0\_corr0



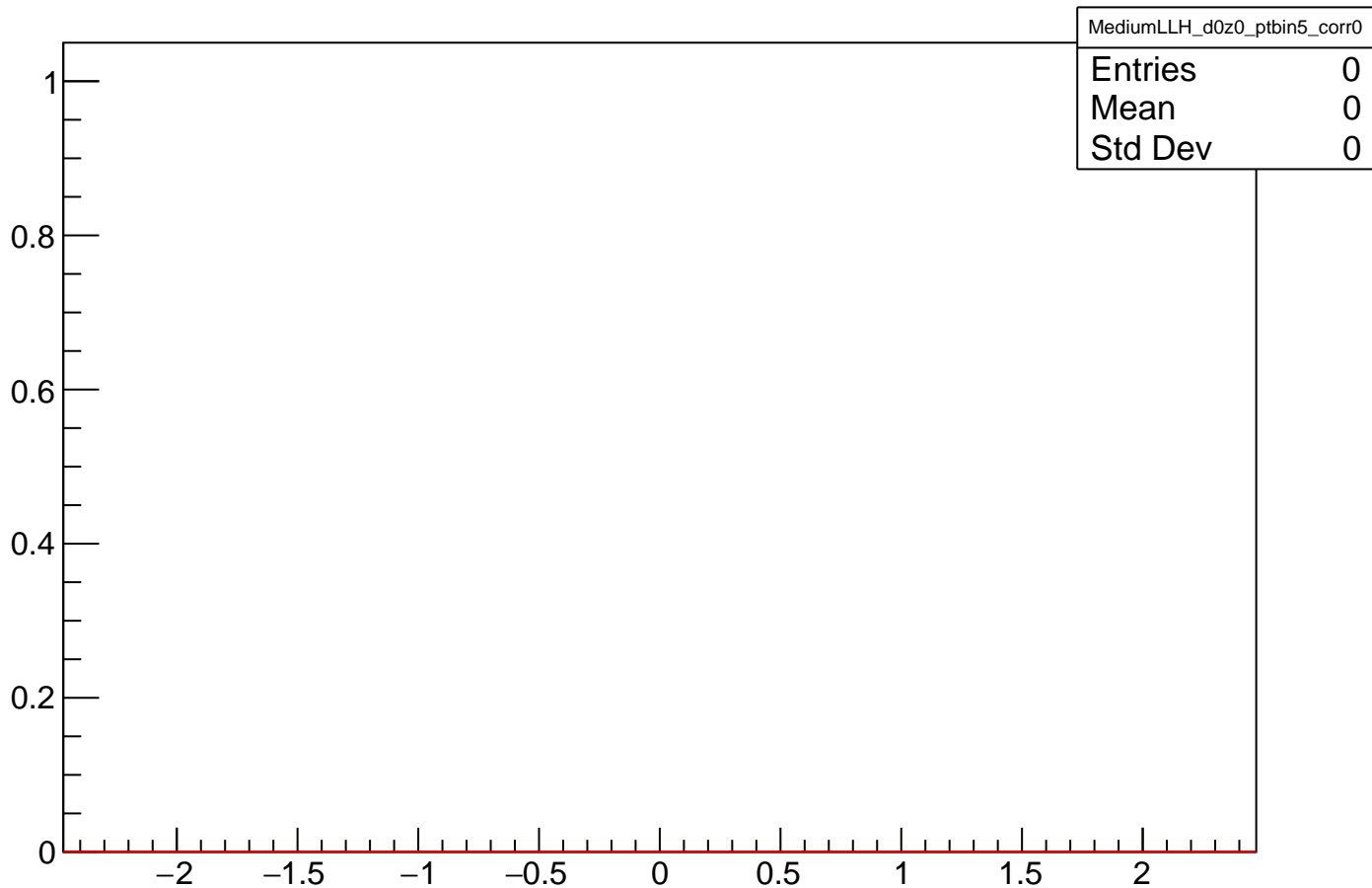
# FullSim\_MediumLLH\_d0z0\_corr0



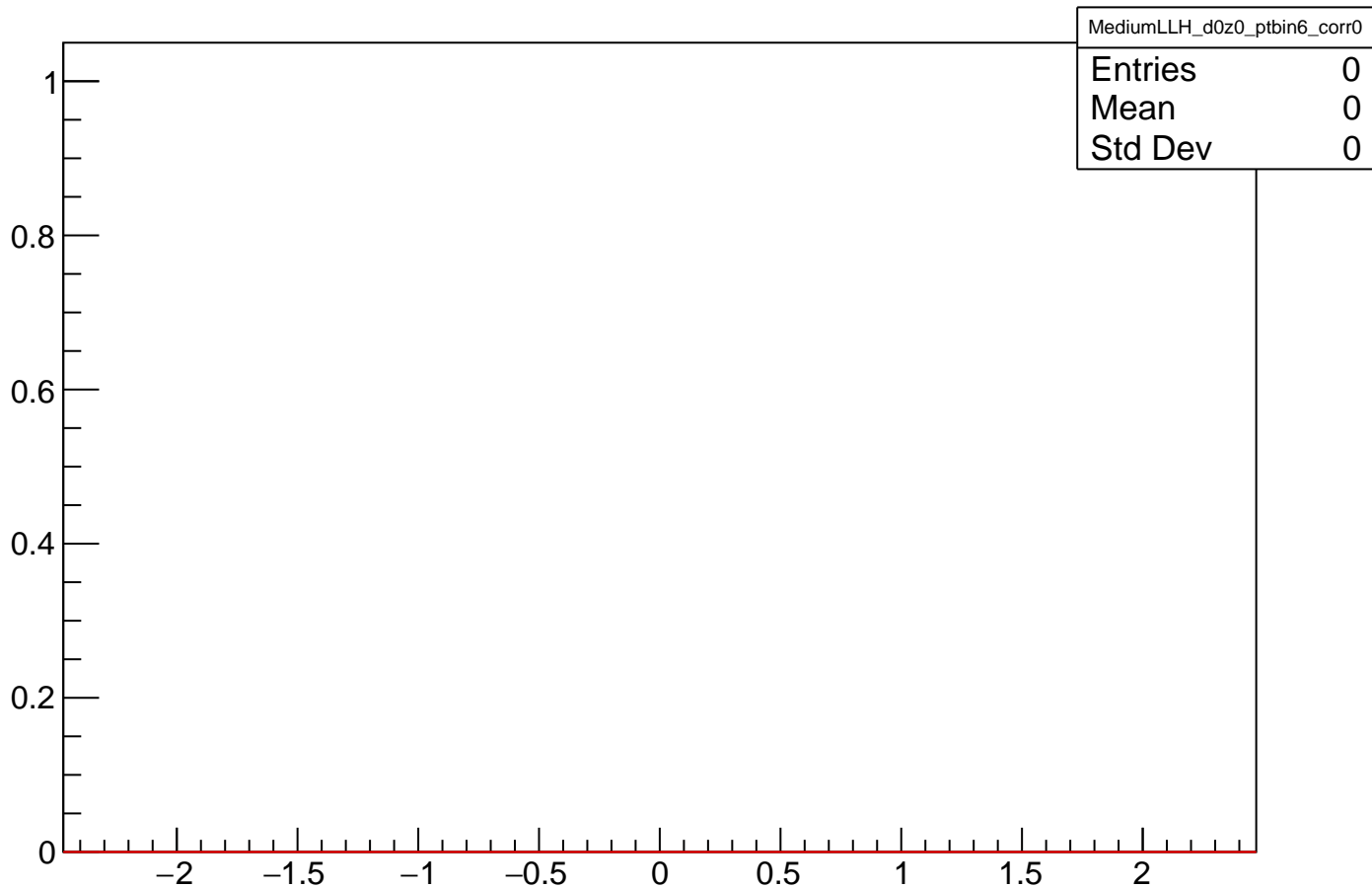
# FullSim\_MediumLLH\_d0z0\_corr0



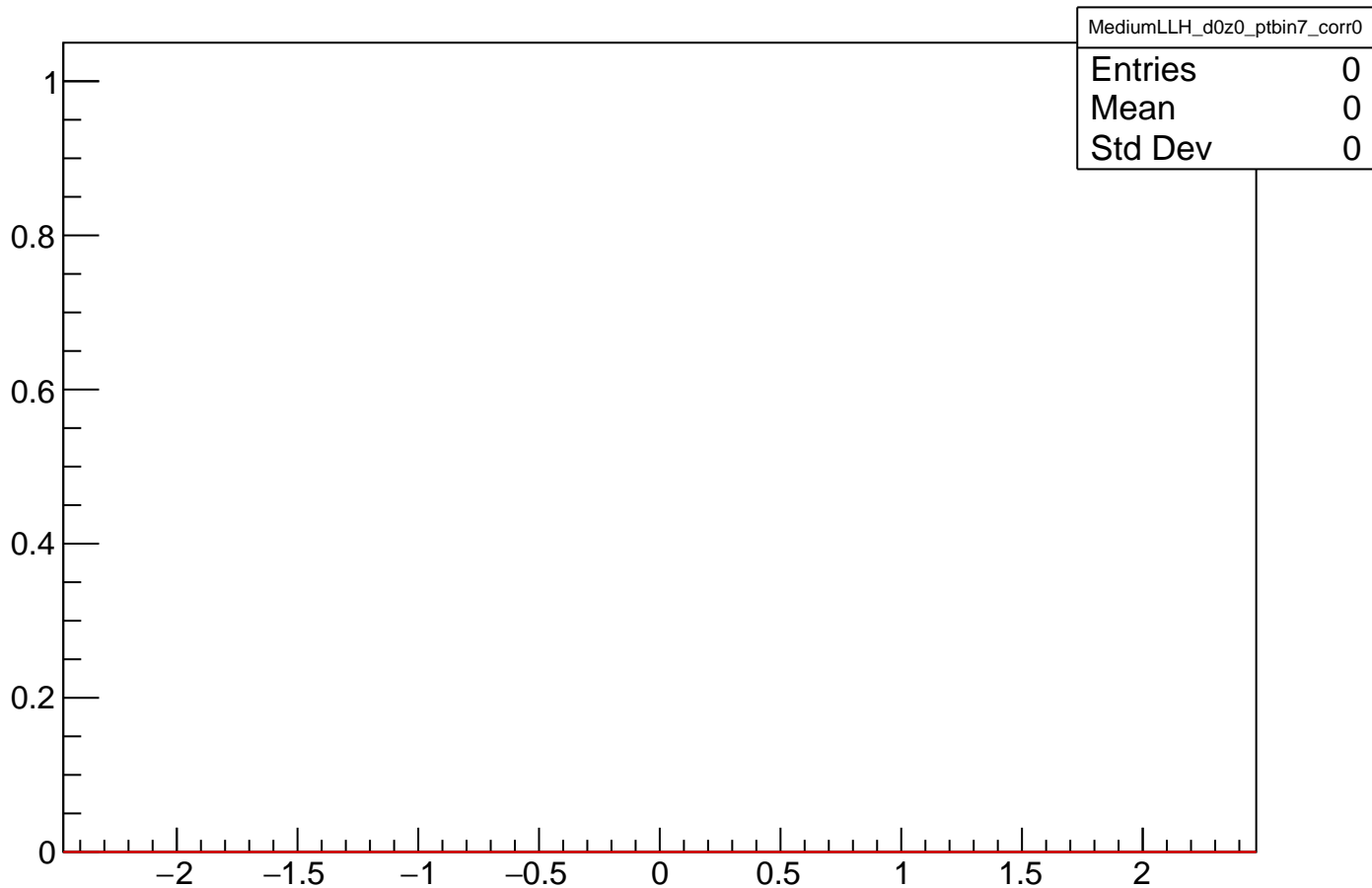
# FullSim\_MediumLLH\_d0z0\_corr0



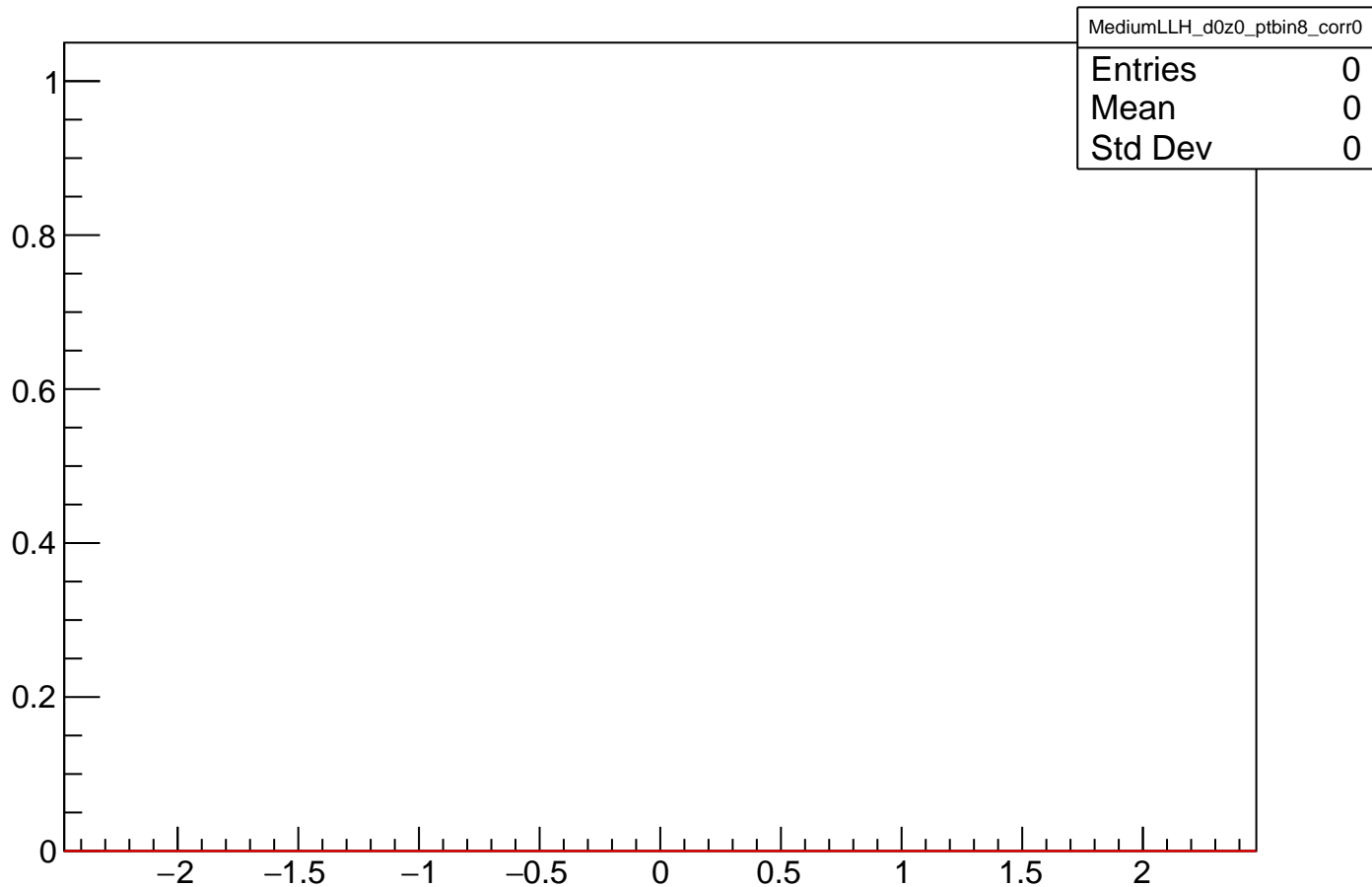
# FullSim\_MediumLLH\_d0z0\_corr0



# FullSim\_MediumLLH\_d0z0\_corr0

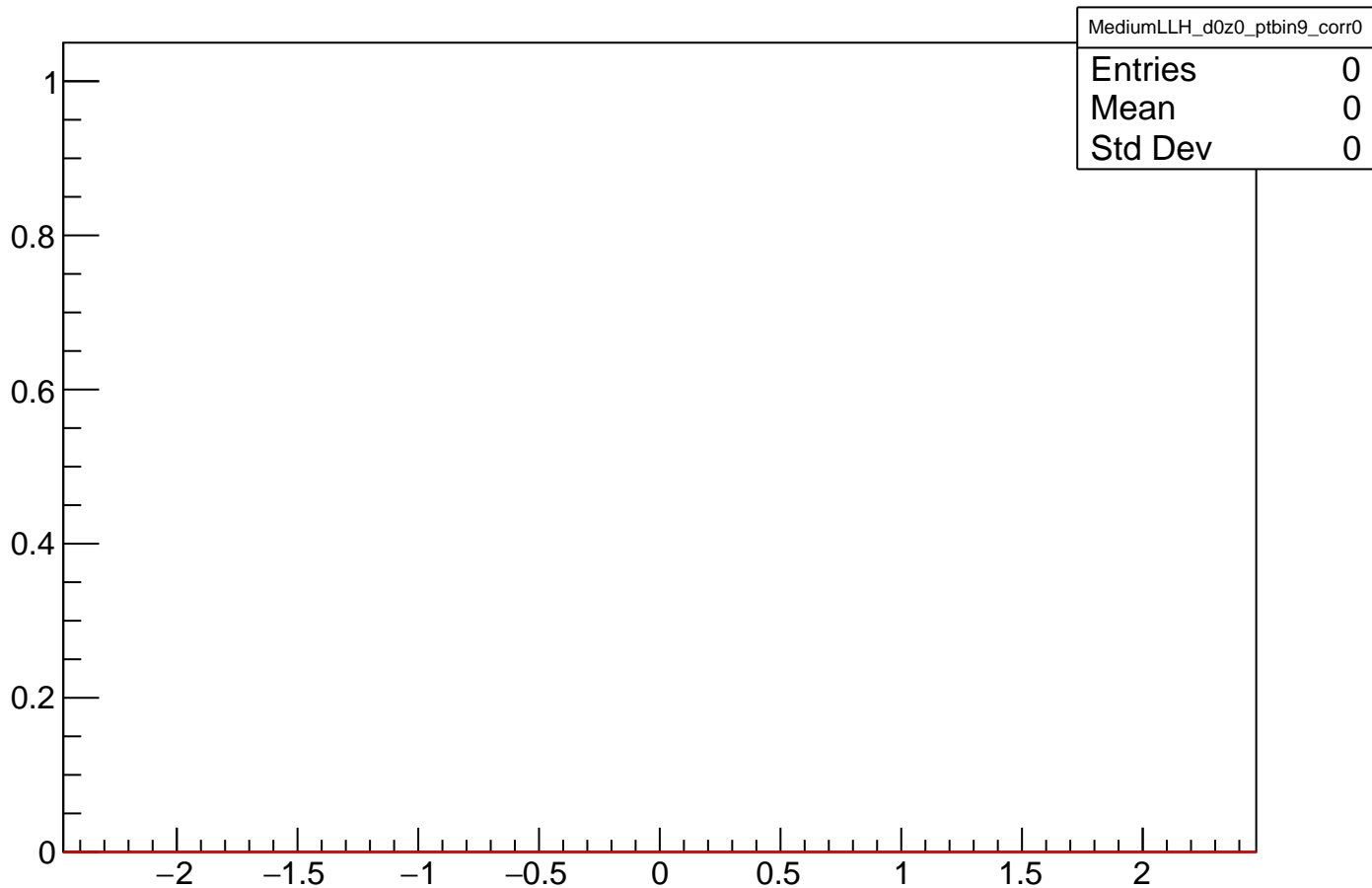


# FullSim\_MediumLLH\_d0z0\_corr0

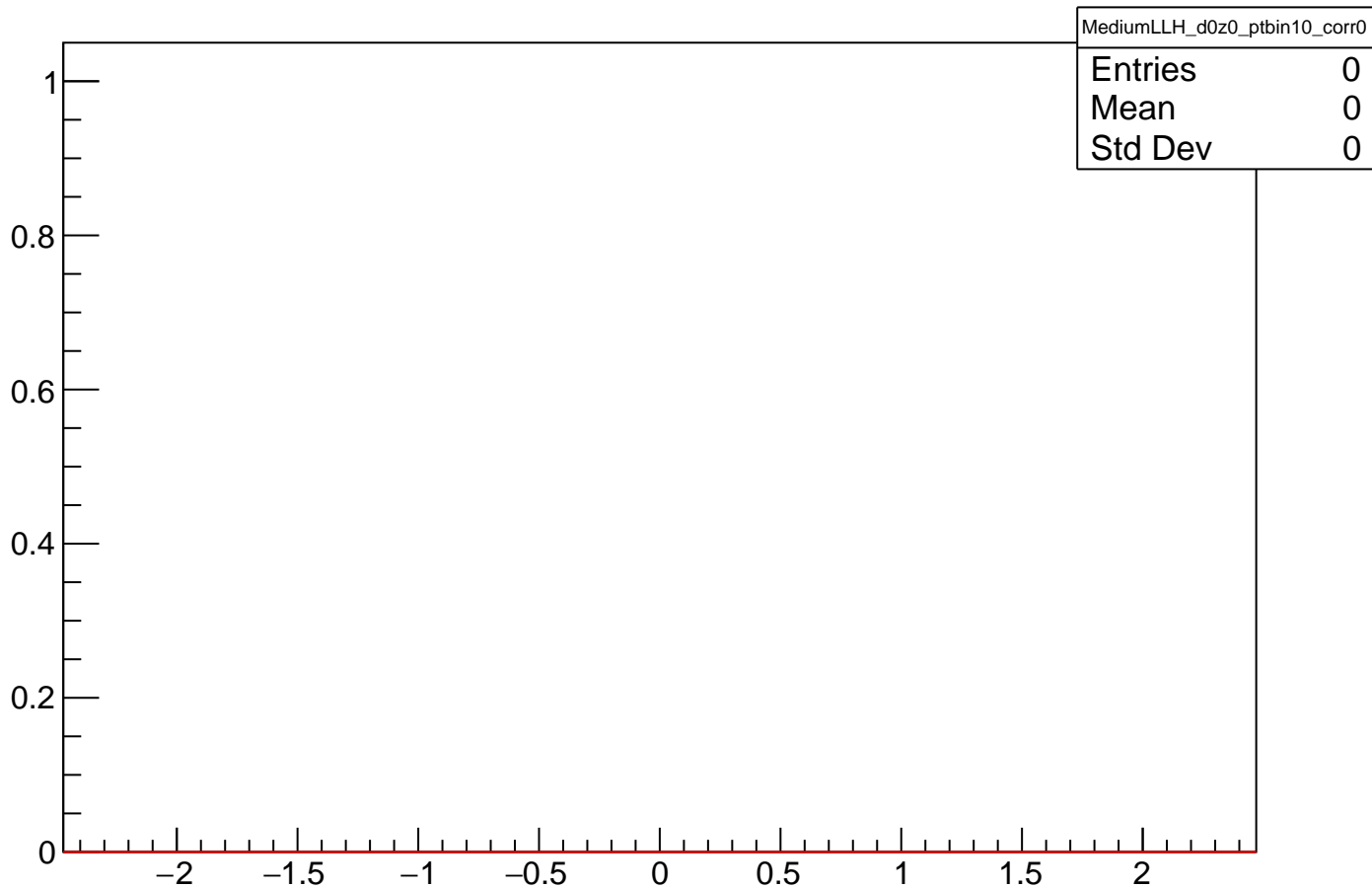




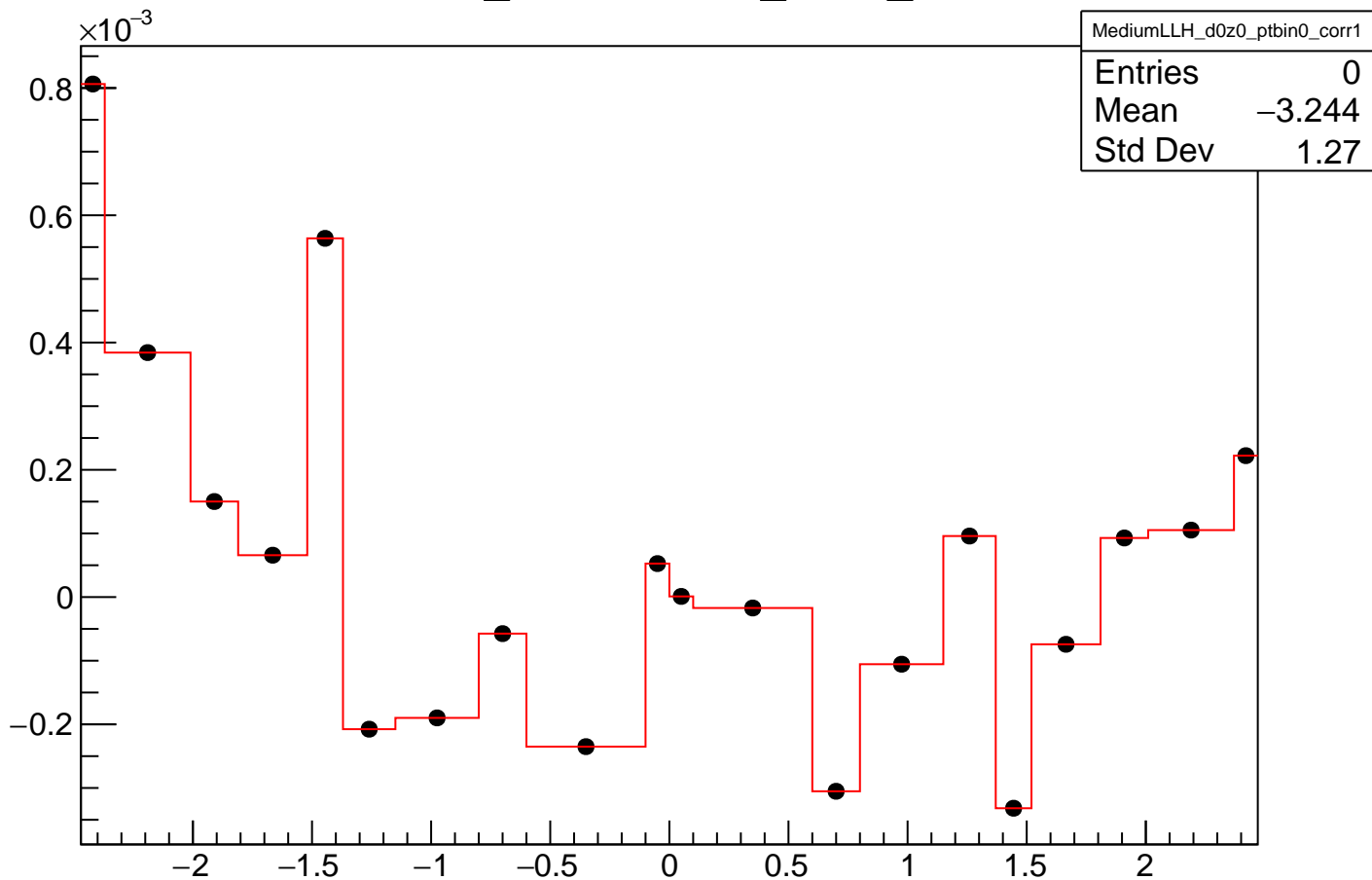
# FullSim\_MediumLLH\_d0z0\_corr0



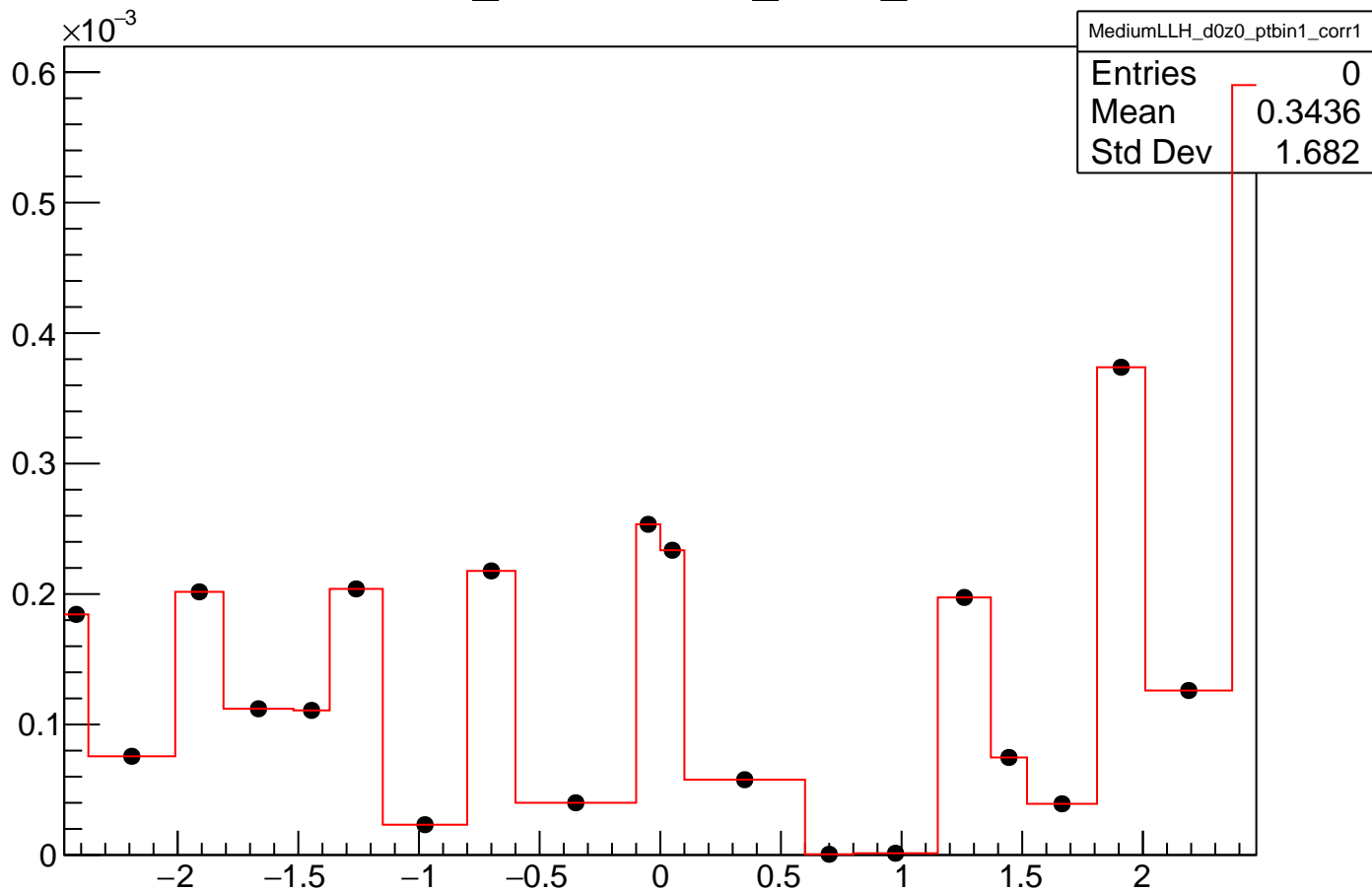
# FullSim\_MediumLLH\_d0z0\_corr0



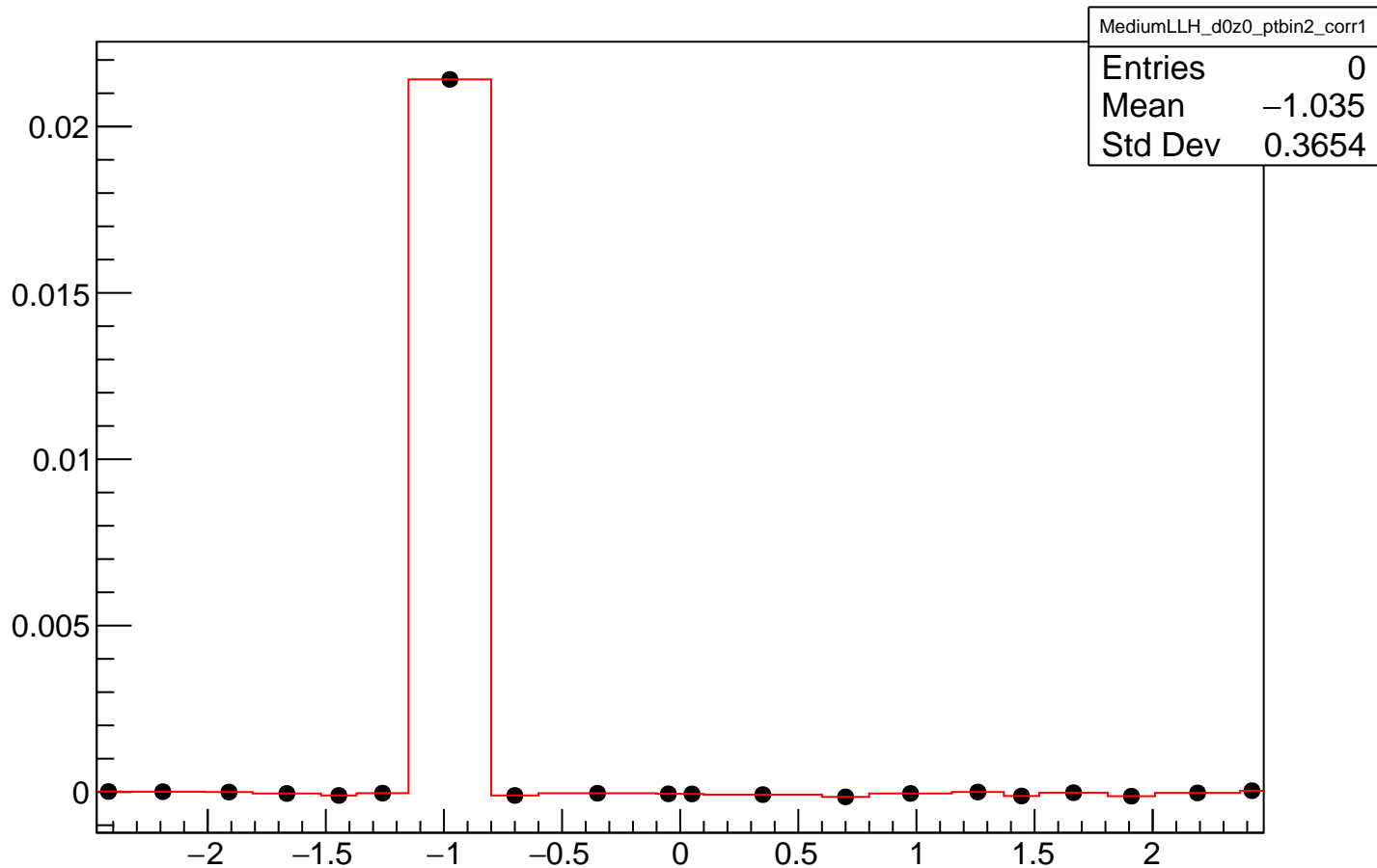
# FullSim\_MediumLLH\_d0z0\_corr1



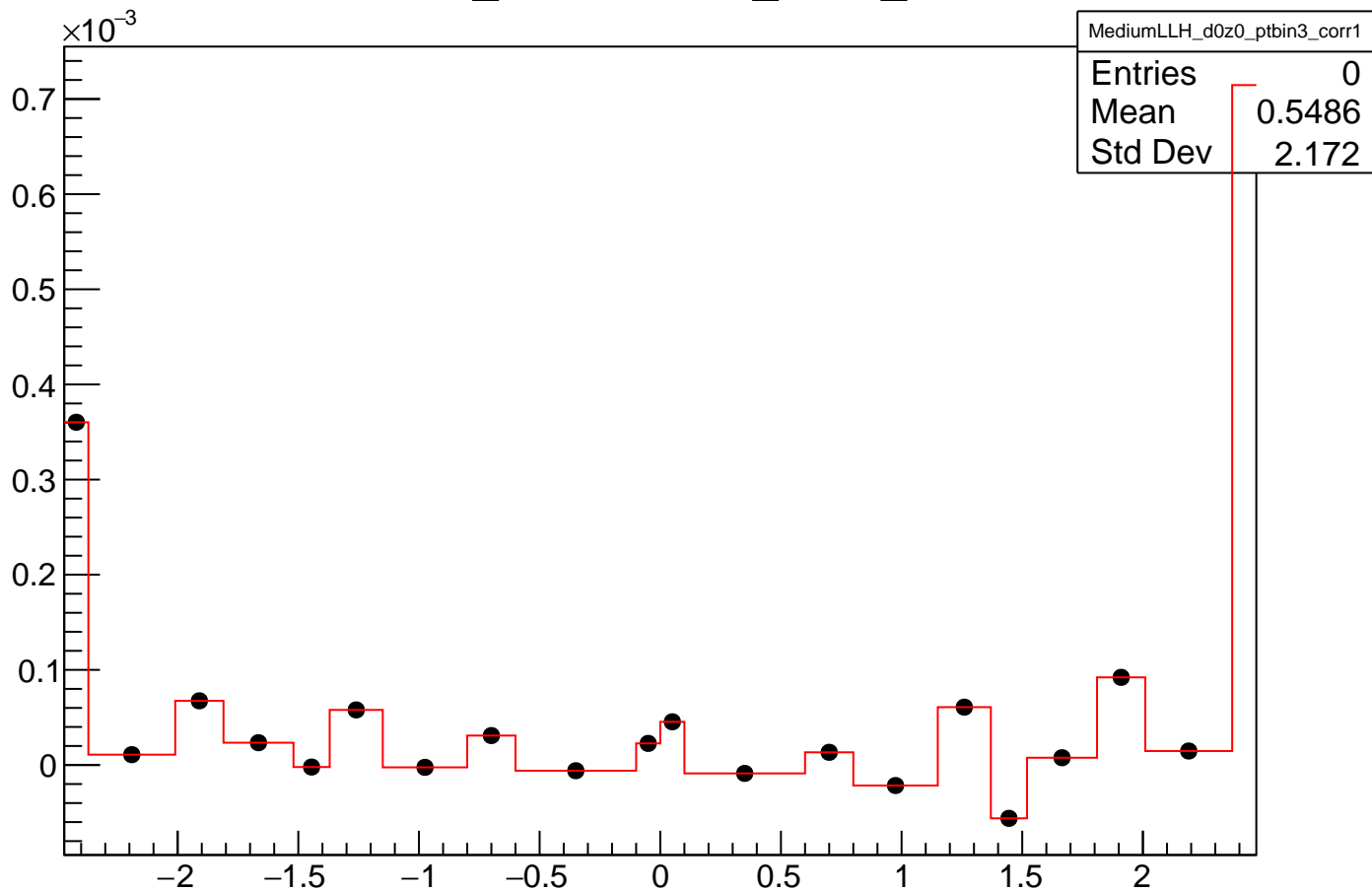
# FullSim\_MediumLLH\_d0z0\_corr1



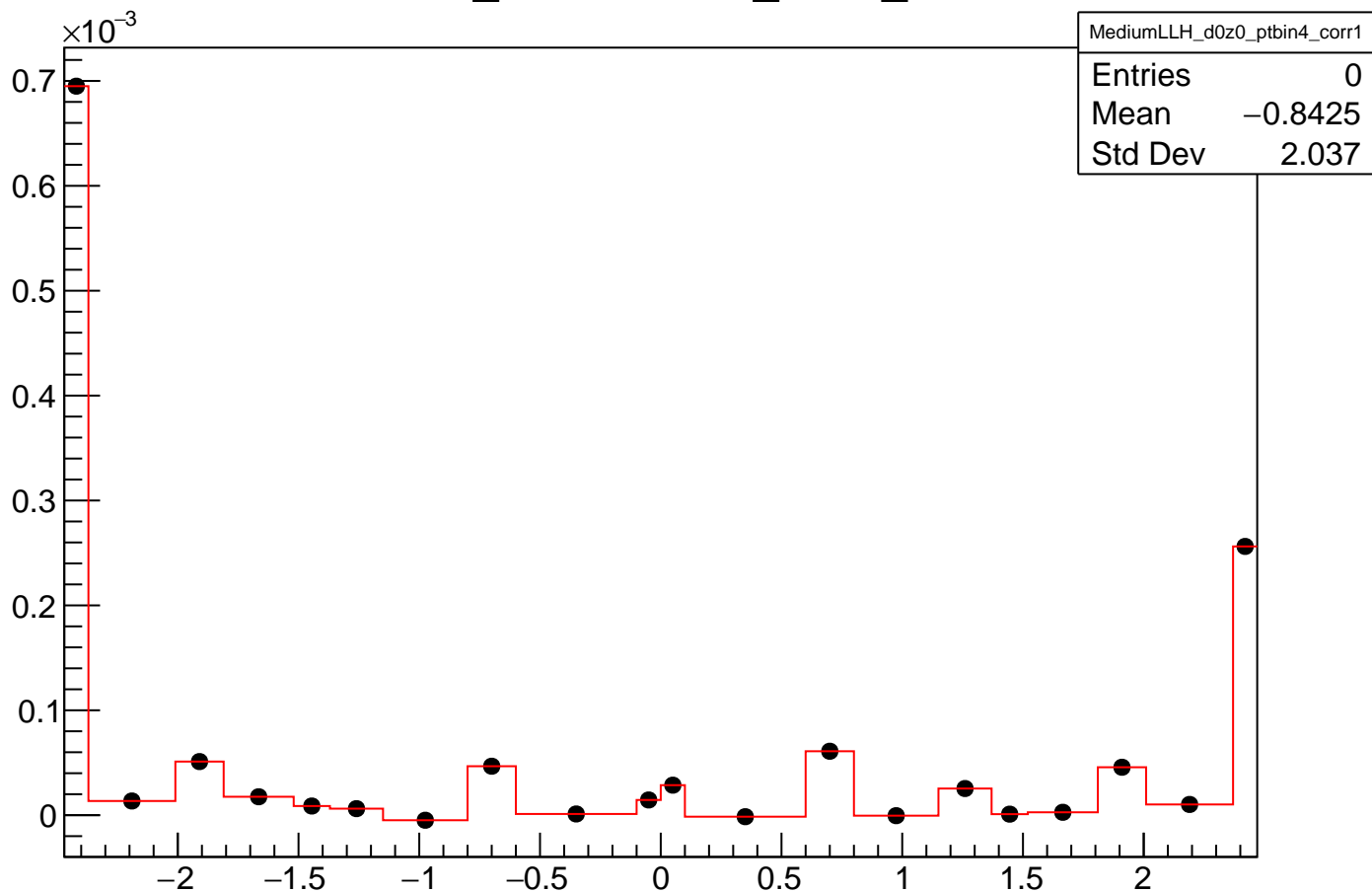
# FullSim\_MediumLLH\_d0z0\_corr1



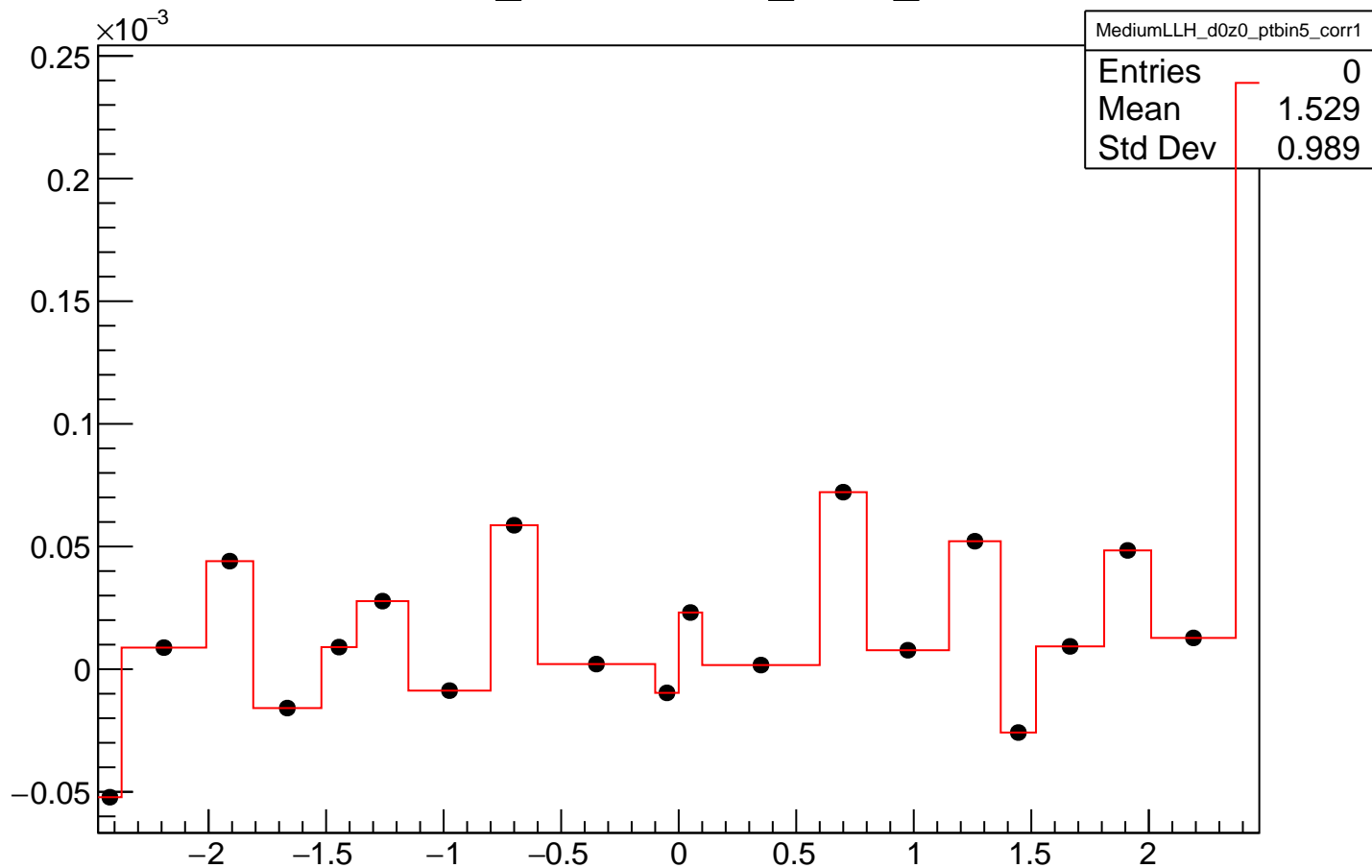
# FullSim\_MediumLLH\_d0z0\_corr1



# FullSim\_MediumLLH\_d0z0\_corr1

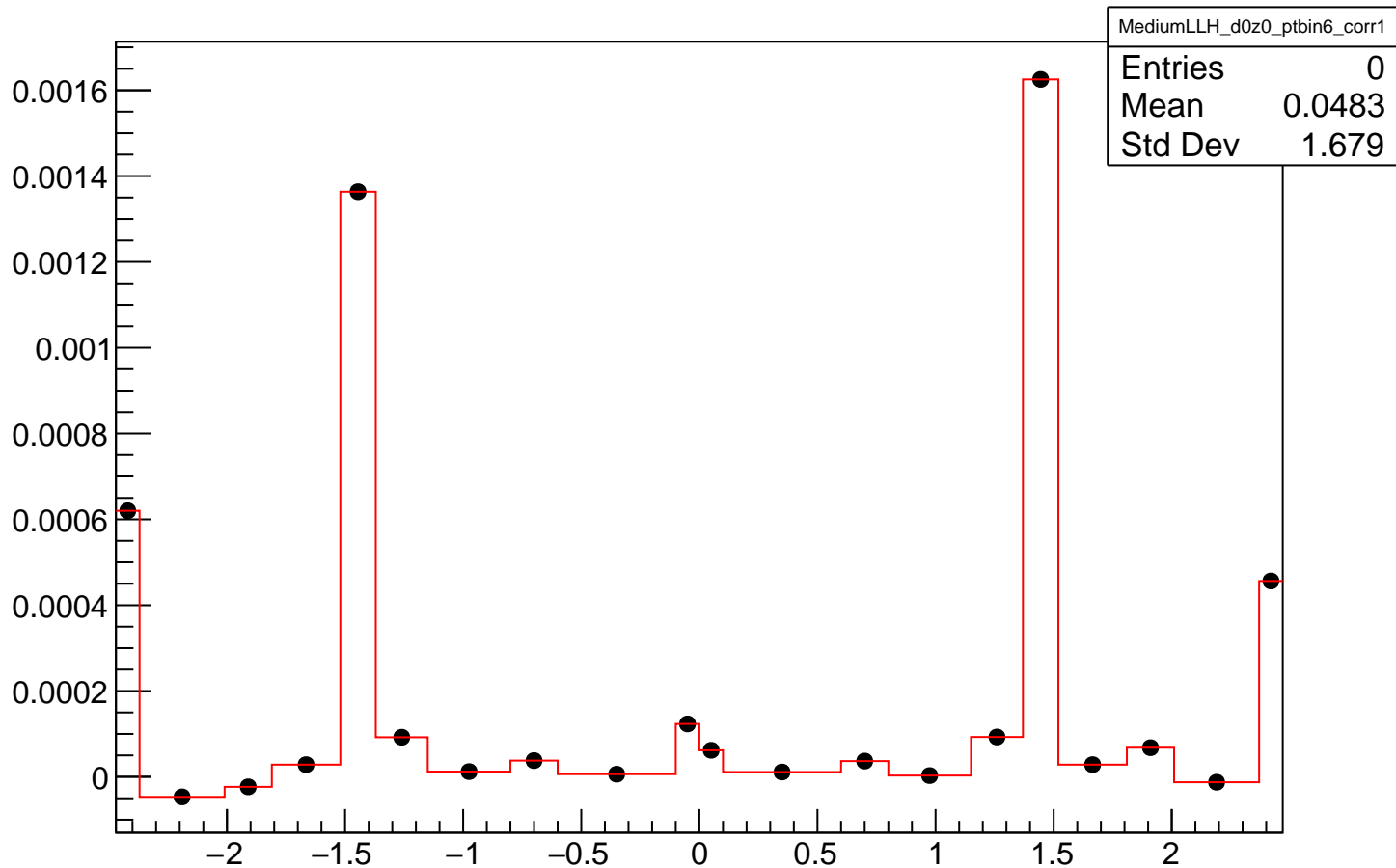


# FullSim\_MediumLLH\_d0z0\_corr1

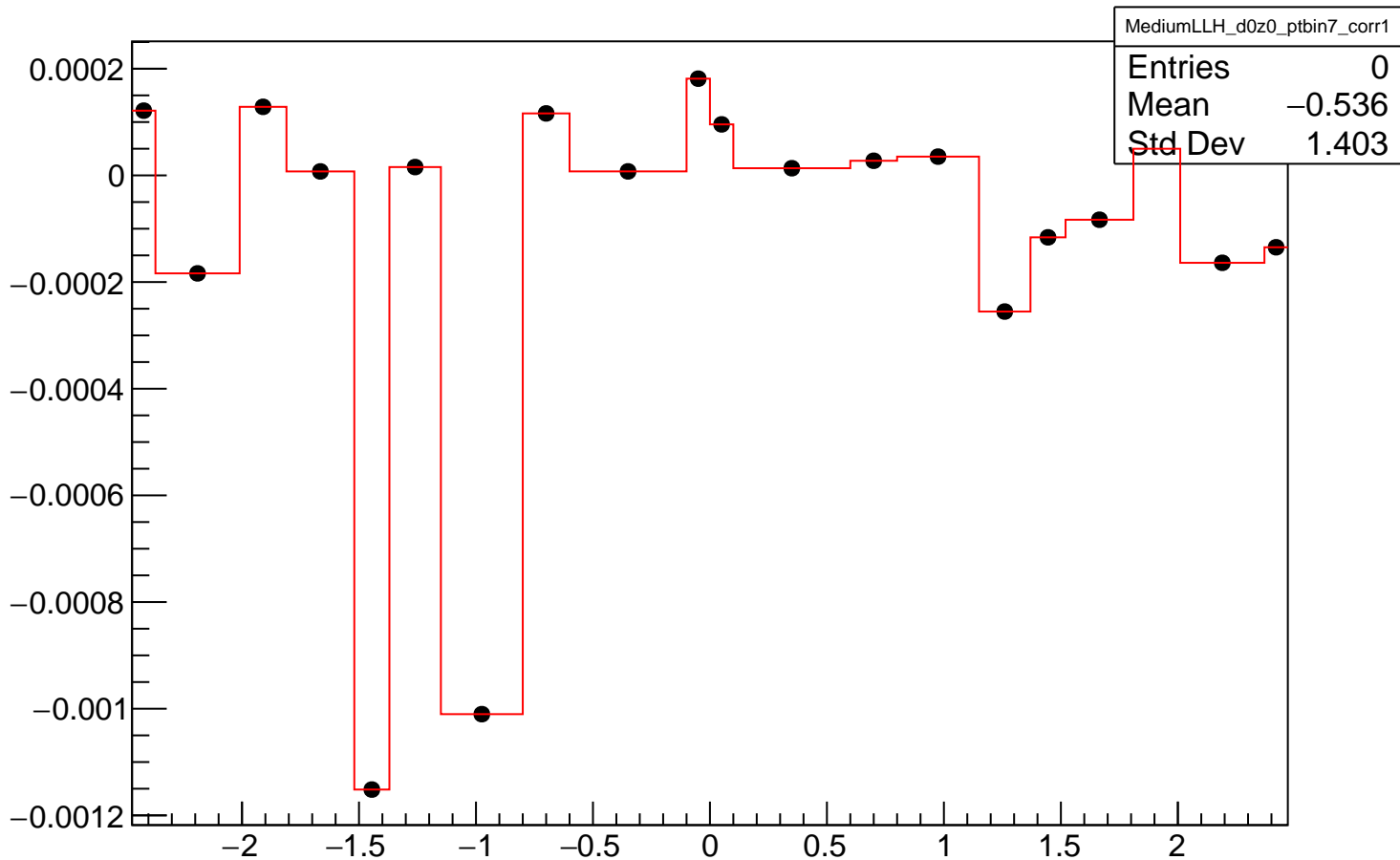




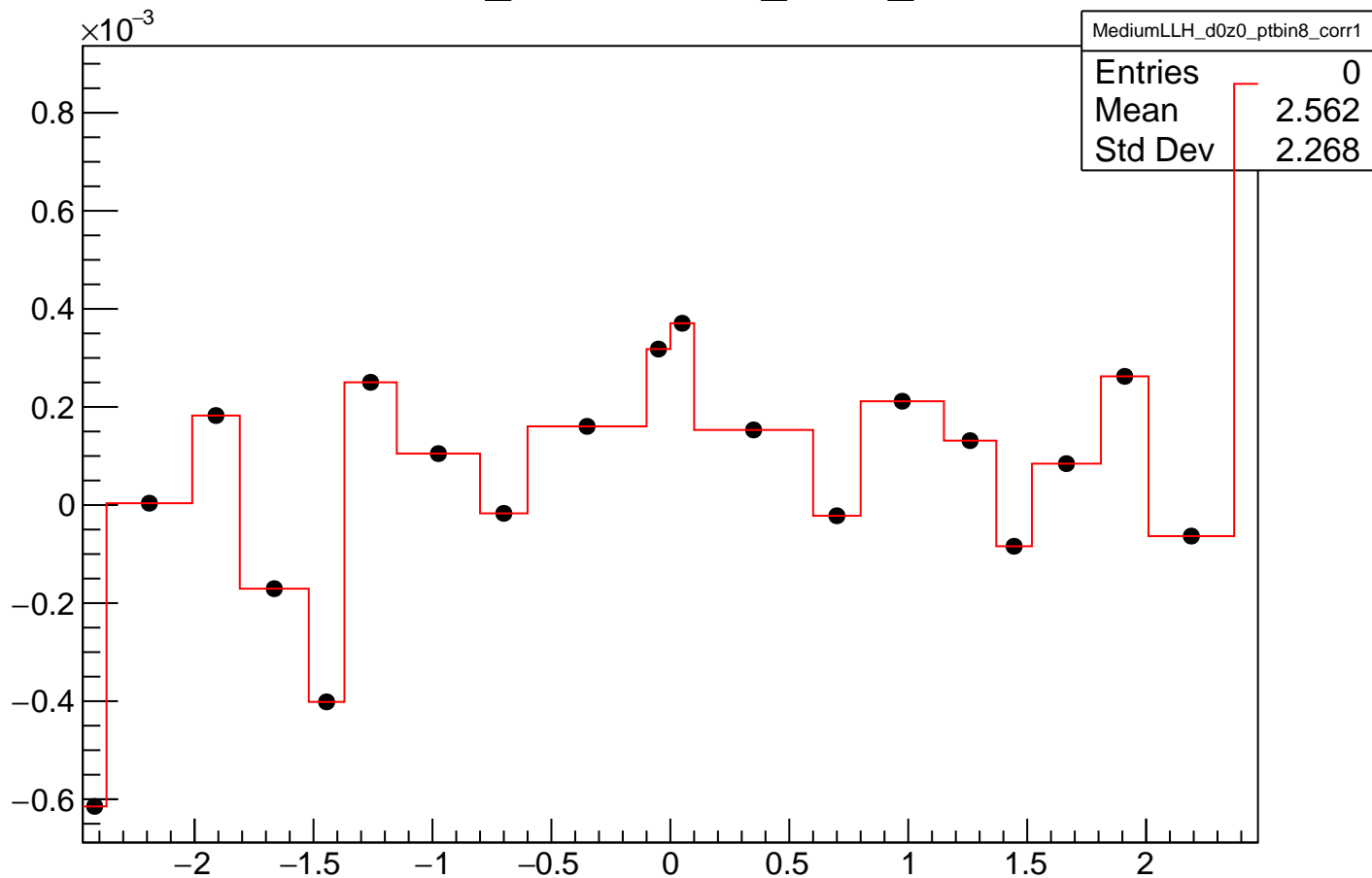
# FullSim\_MediumLLH\_d0z0\_corr1



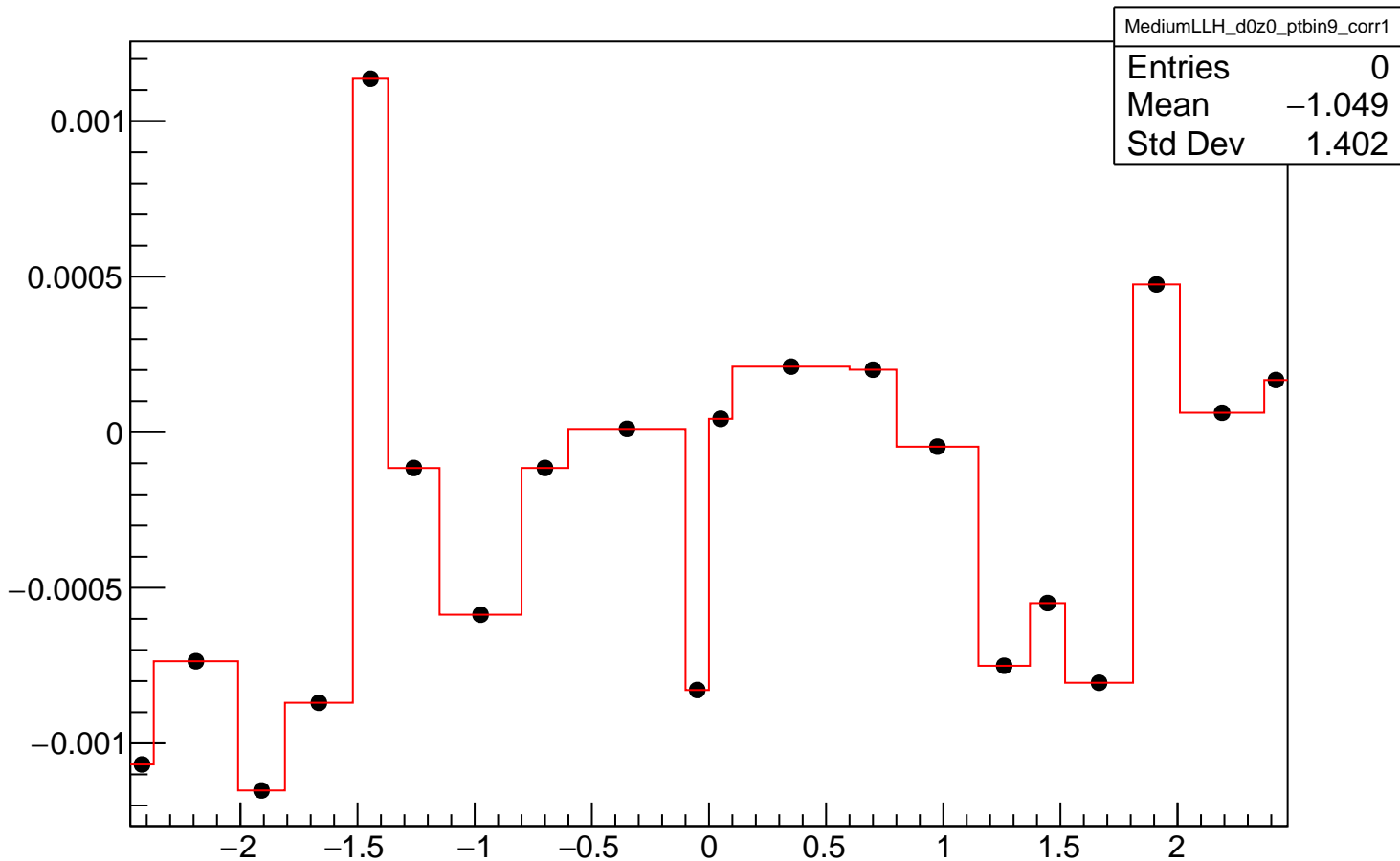
# FullSim\_MediumLLH\_d0z0\_corr1



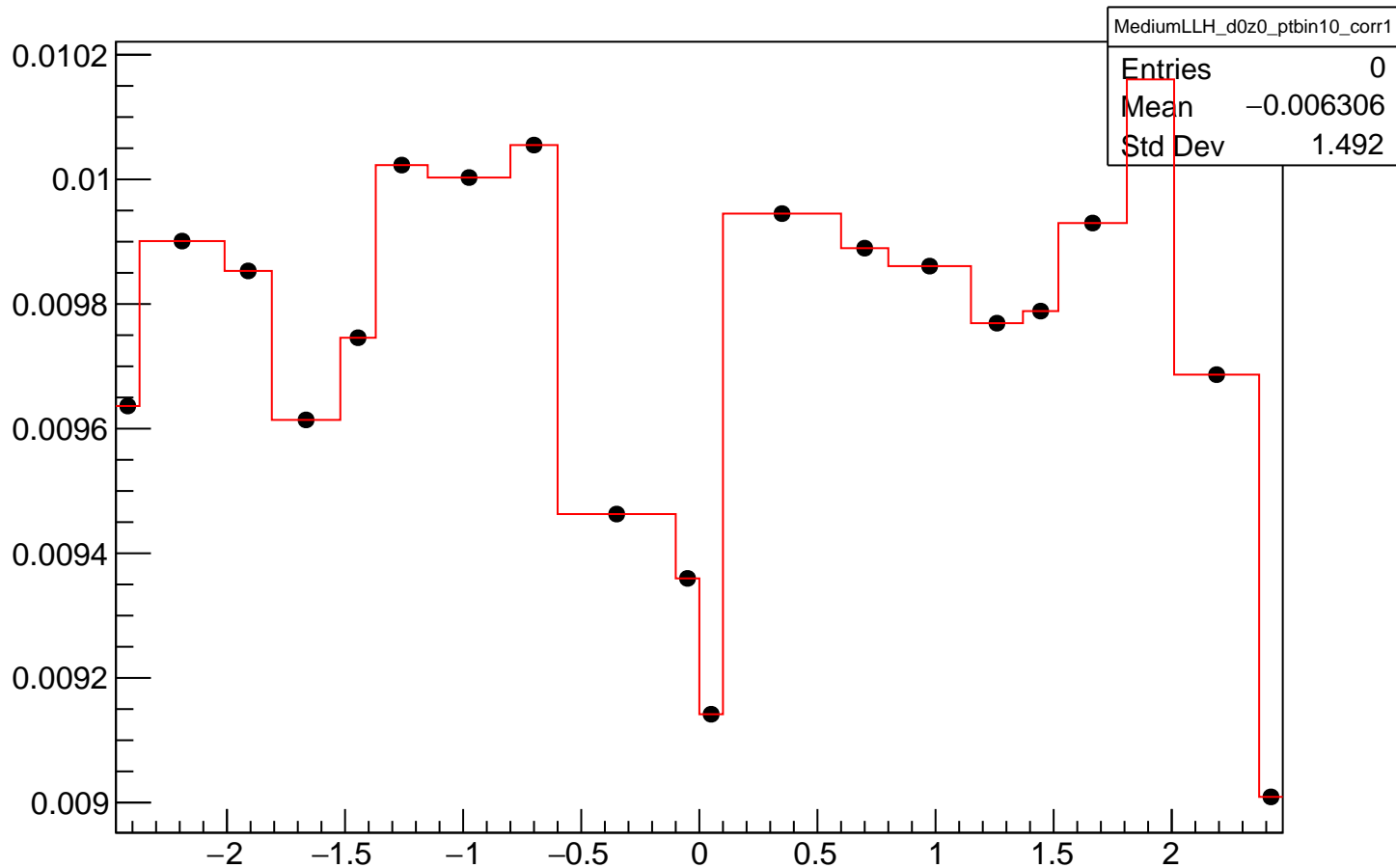
# FullSim\_MediumLLH\_d0z0\_corr1



# FullSim\_MediumLLH\_d0z0\_corr1



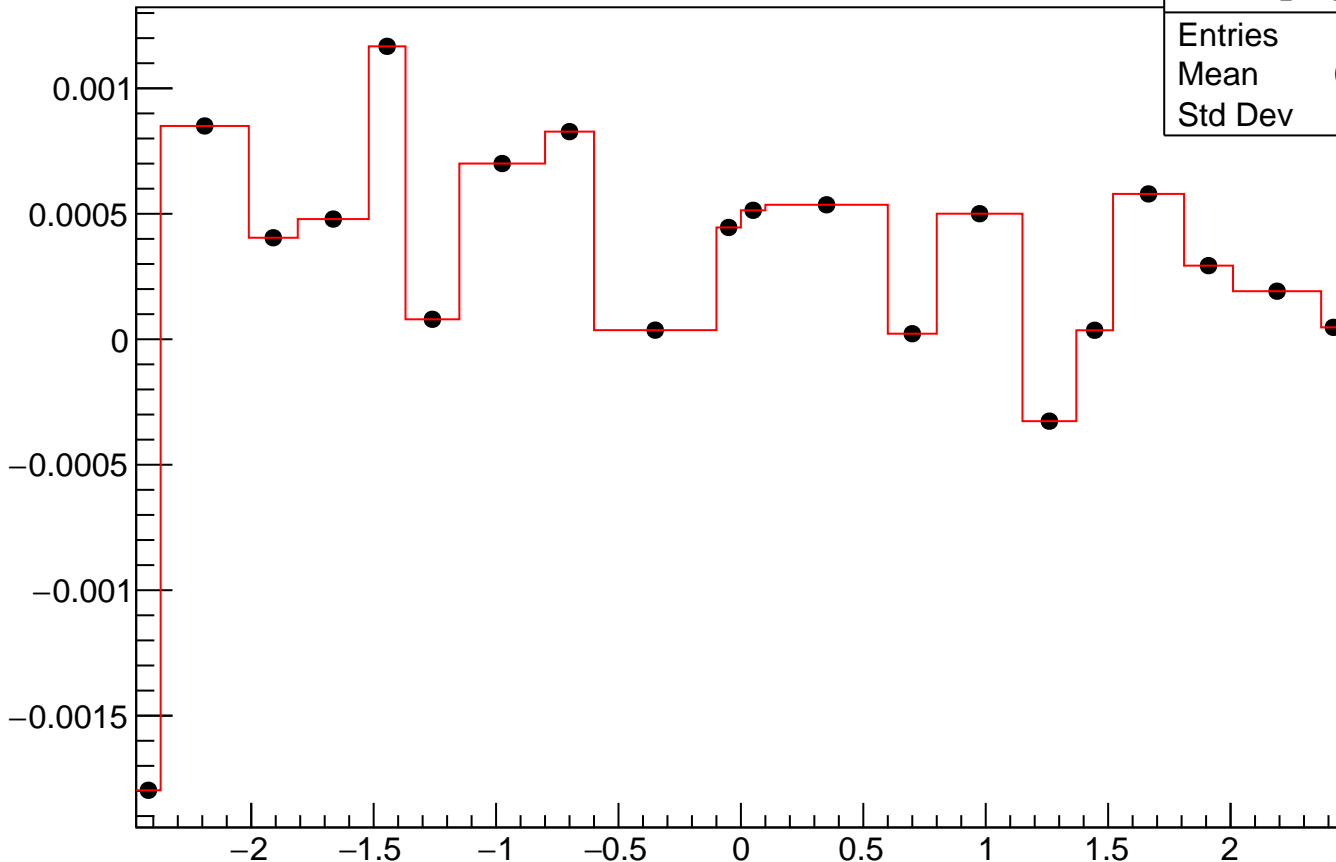
# FullSim\_MediumLLH\_d0z0\_corr1



# FullSim\_MediumLLH\_d0z0\_corr2

MediumLLH\_d0z0\_ptbin0\_corr2

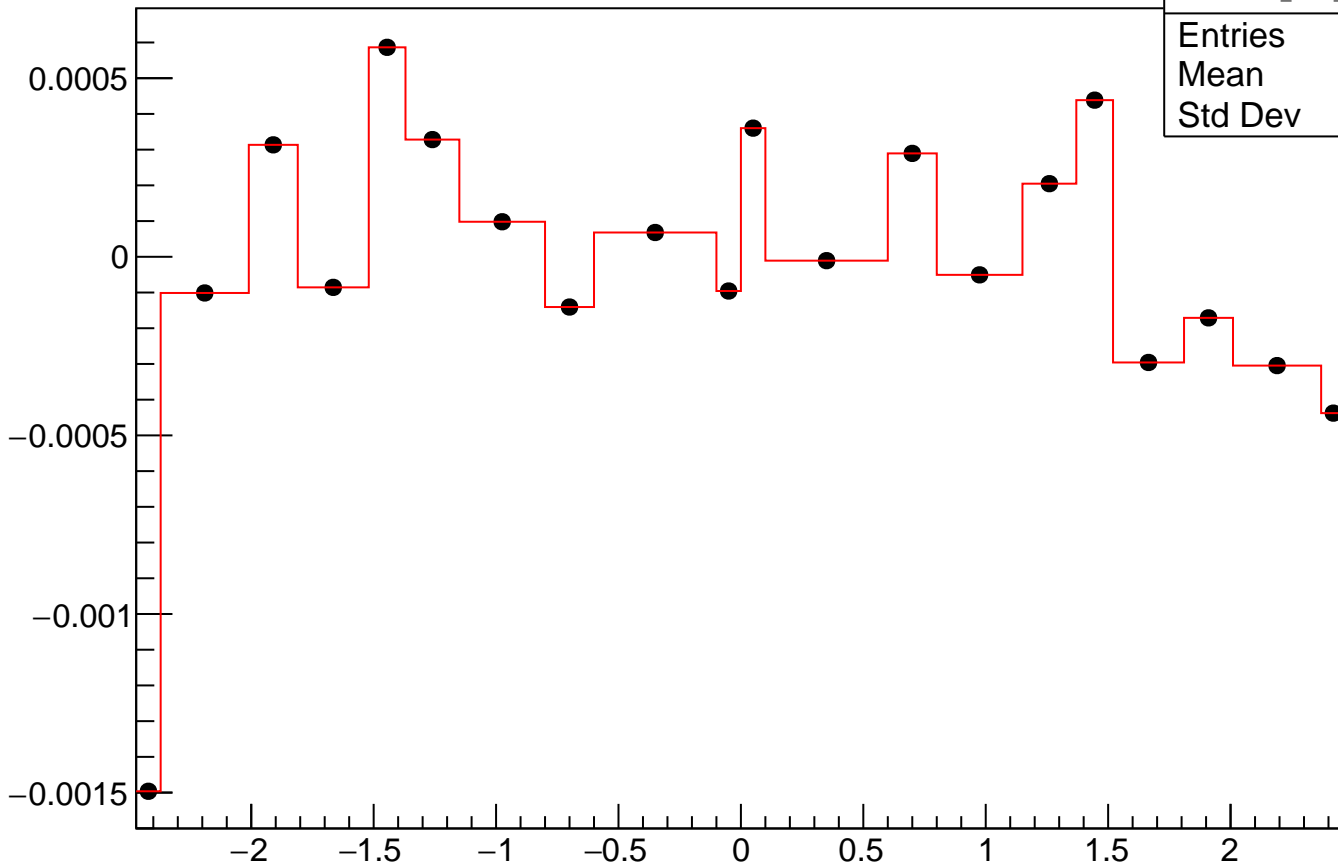
Entries	0
Mean	0.04474
Std Dev	0.8427



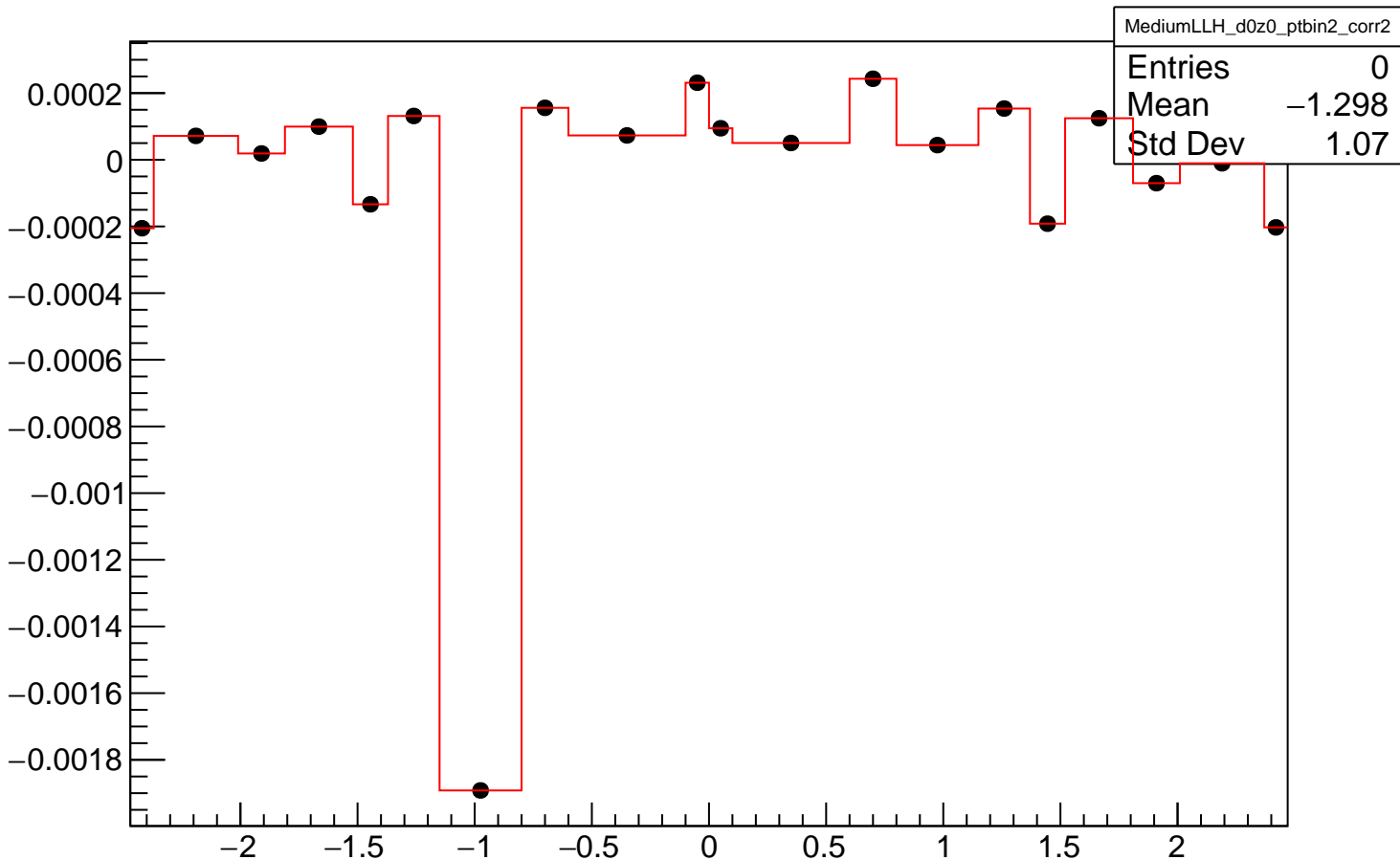
# FullSim\_MediumLLH\_d0z0\_corr2

MediumLLH\_d0z0\_ptbin1\_corr2

Entries	0
Mean	-1.24
Std Dev	4.442



# FullSim\_MediumLLH\_d0z0\_corr2

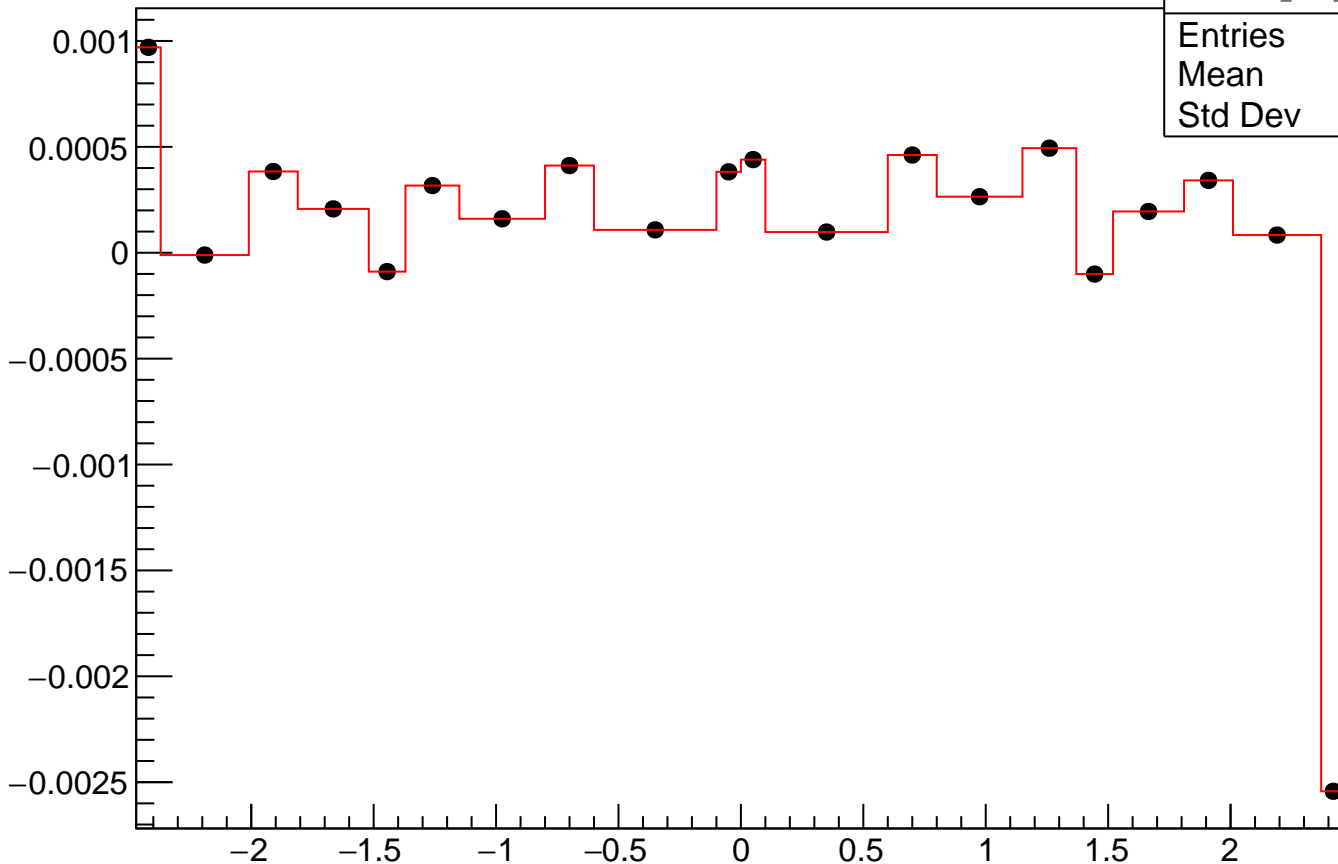




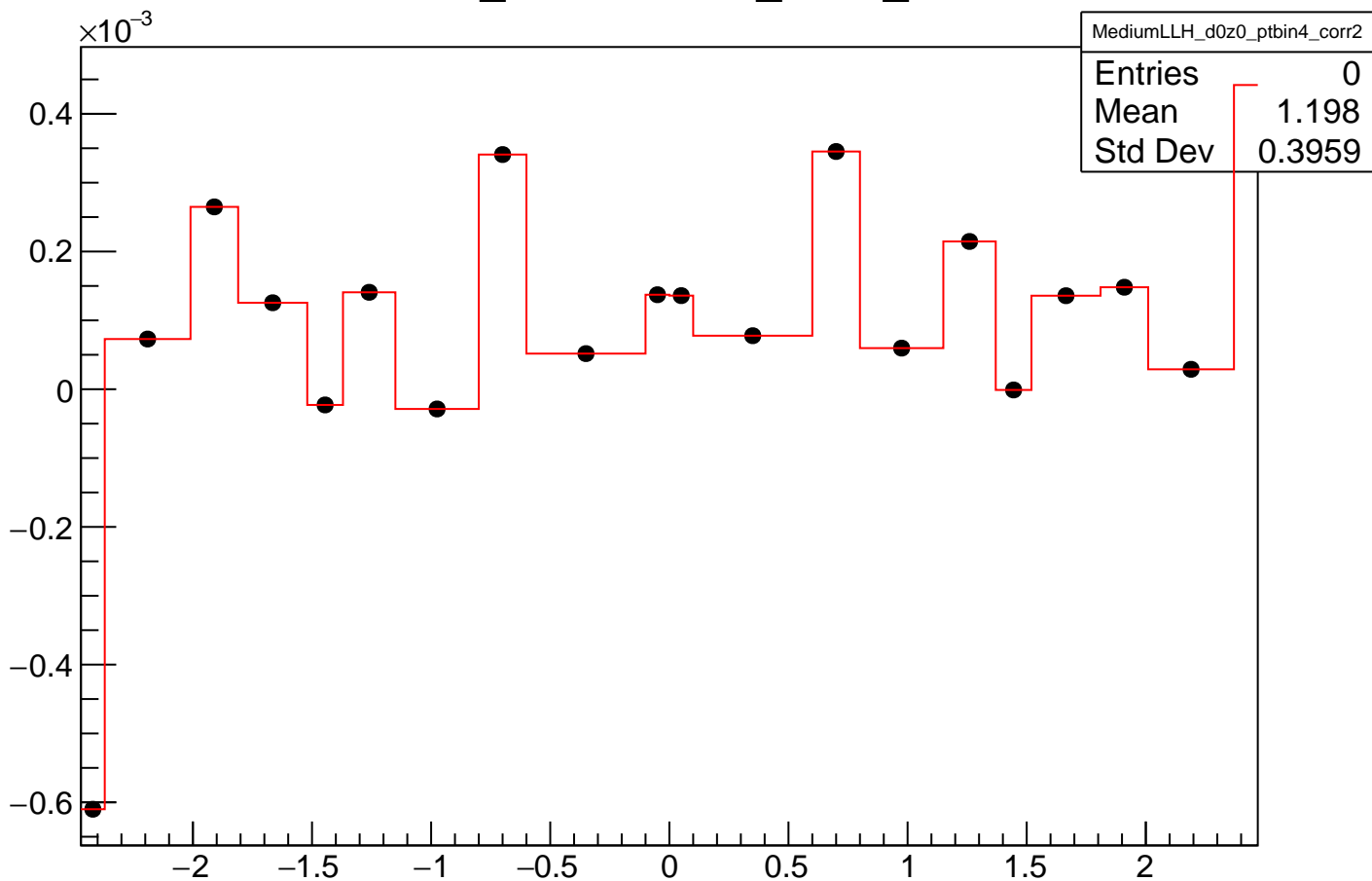
# FullSim\_MediumLLH\_d0z0\_corr2

MediumLLH\_d0z0\_ptbin3\_corr2

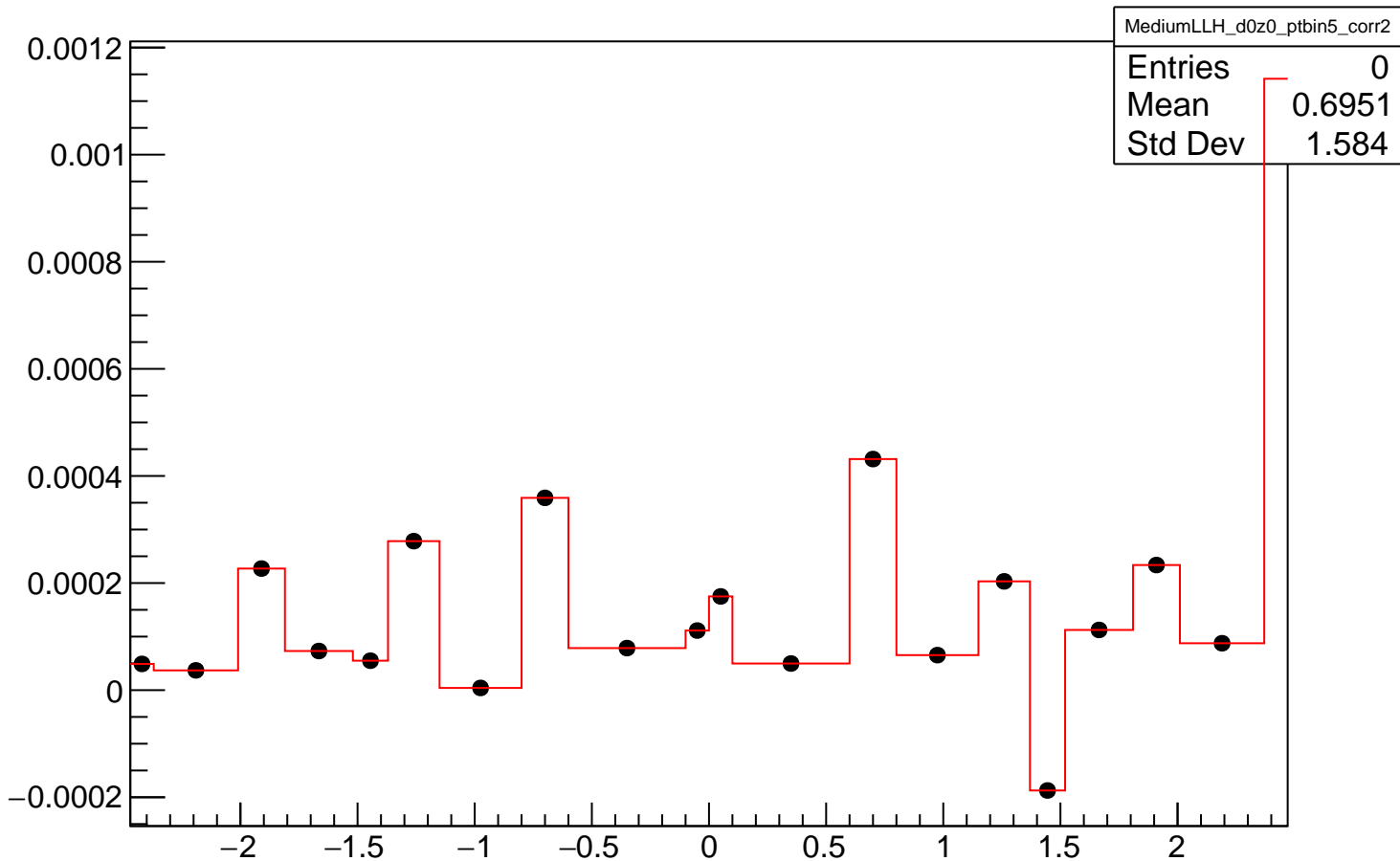
Entries	0
Mean	-3.128
Std Dev	3.33



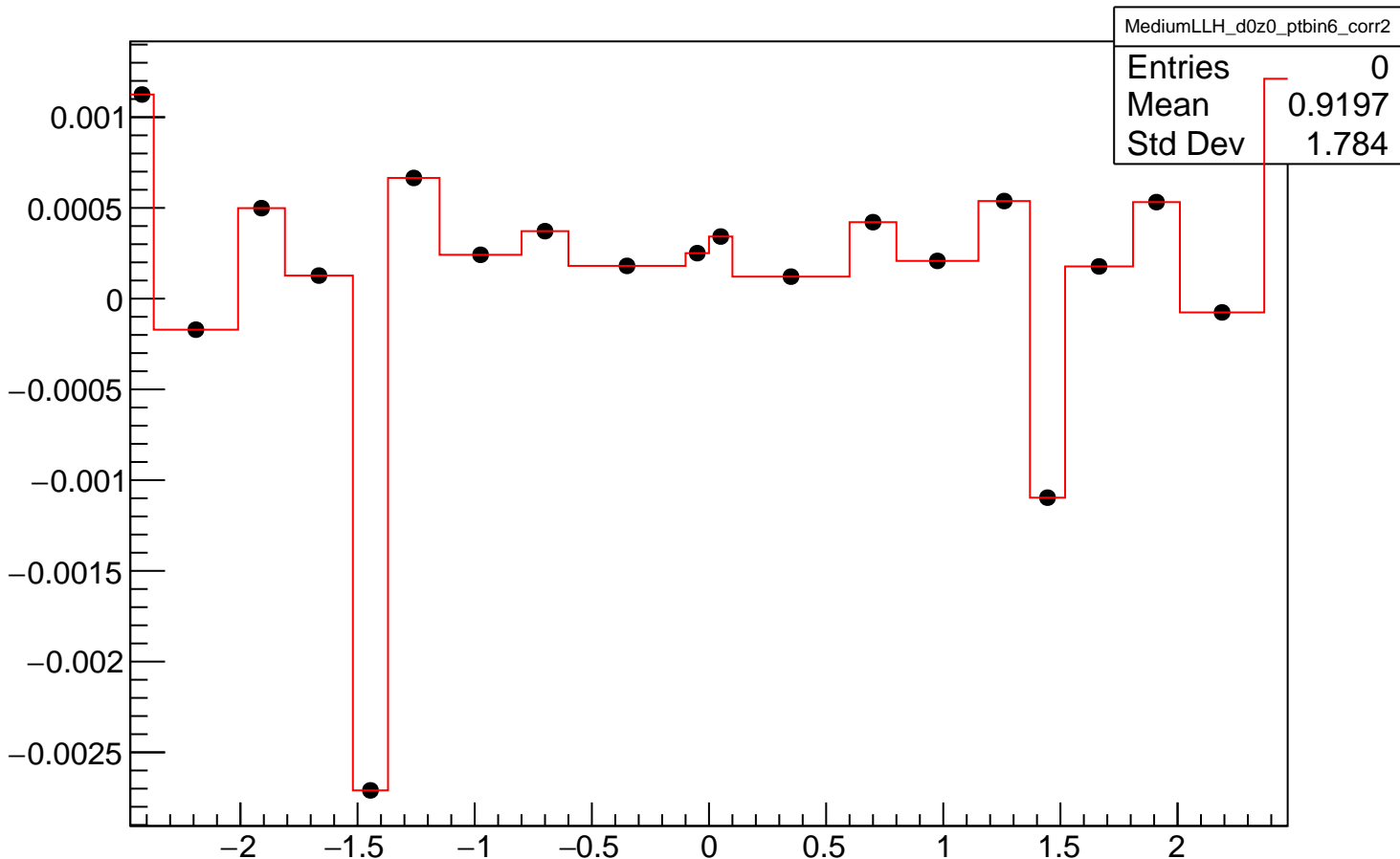
# FullSim\_MediumLLH\_d0z0\_corr2



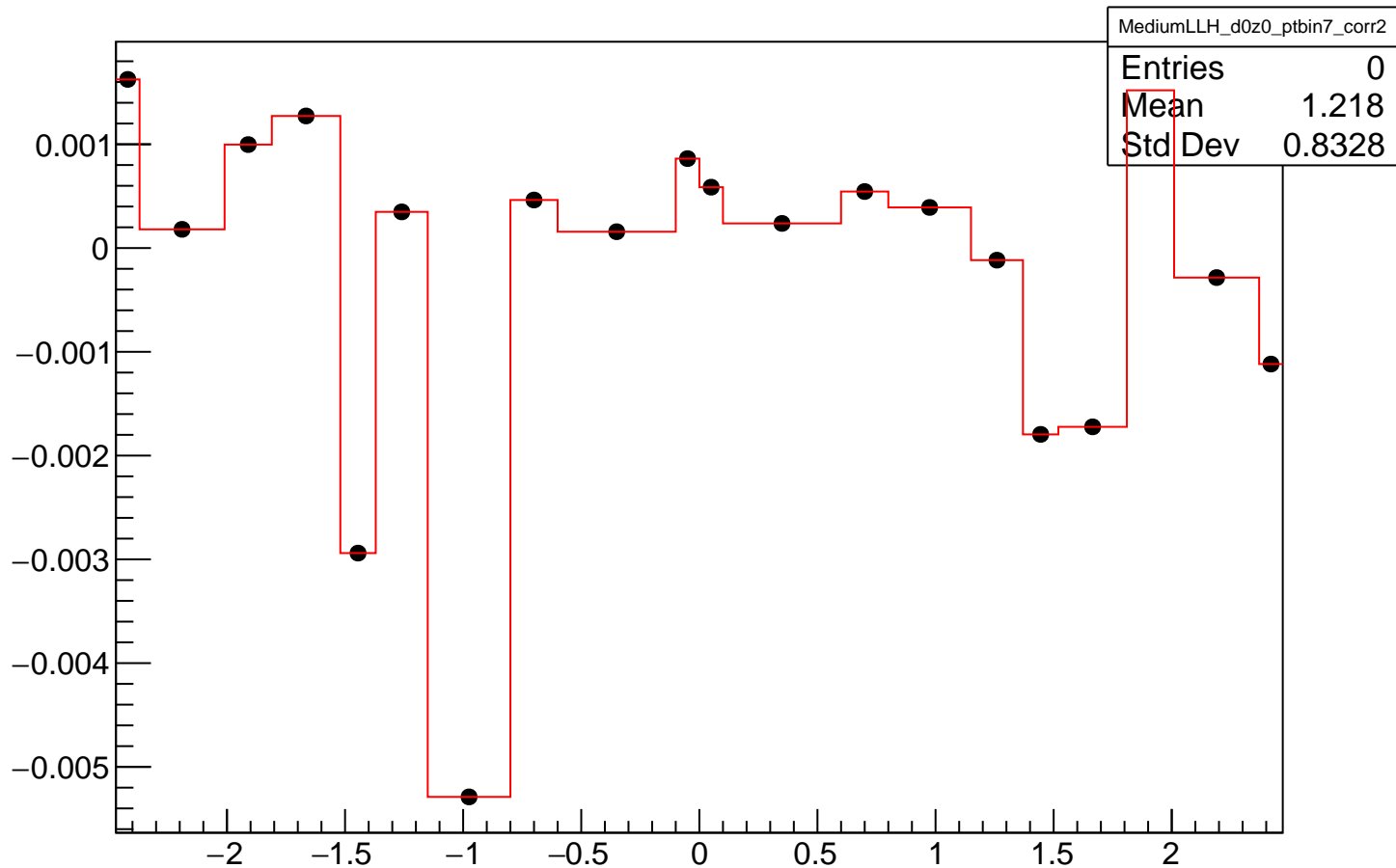
# FullSim\_MediumLLH\_d0z0\_corr2



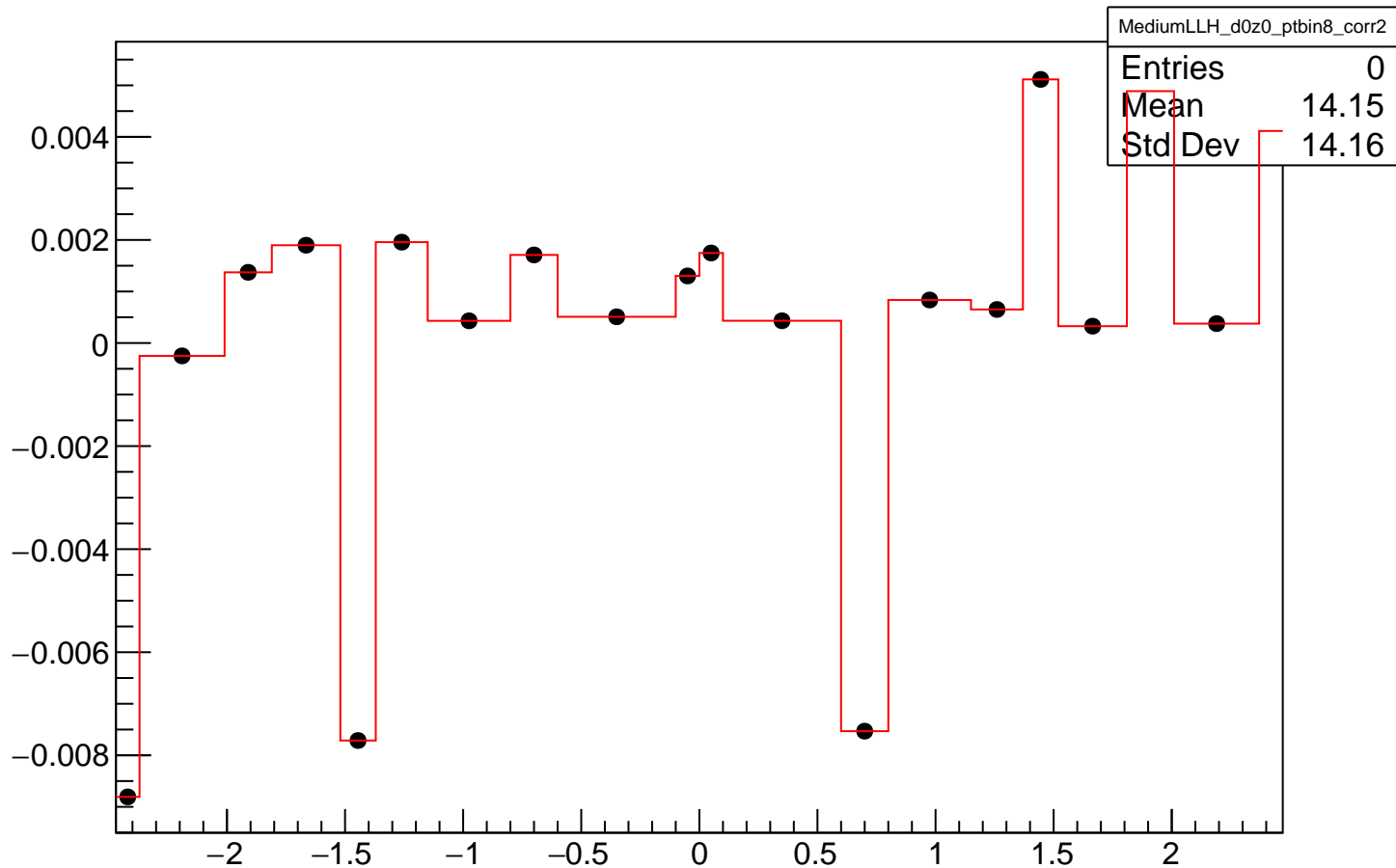
# FullSim\_MediumLLH\_d0z0\_corr2



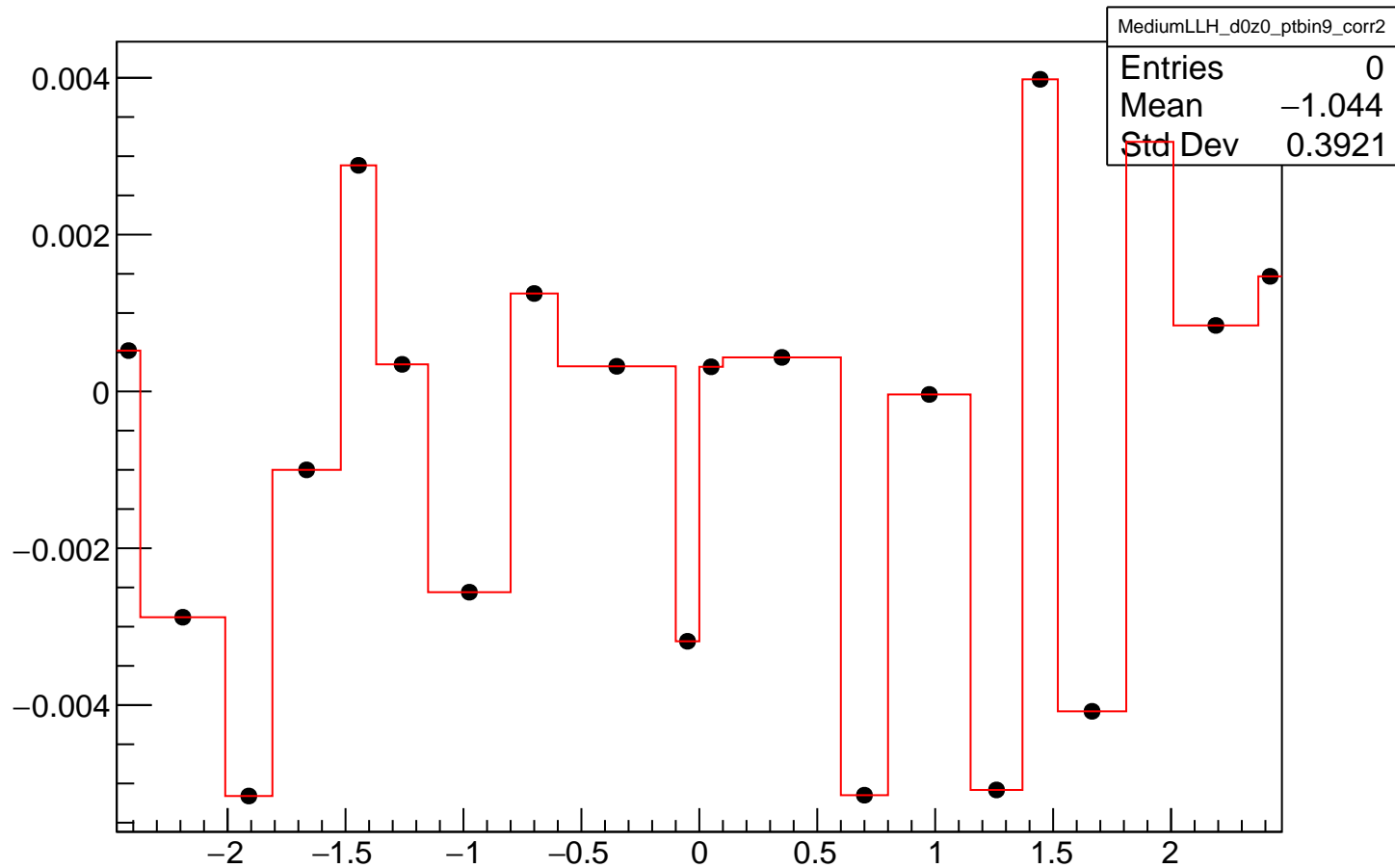
# FullSim\_MediumLLH\_d0z0\_corr2



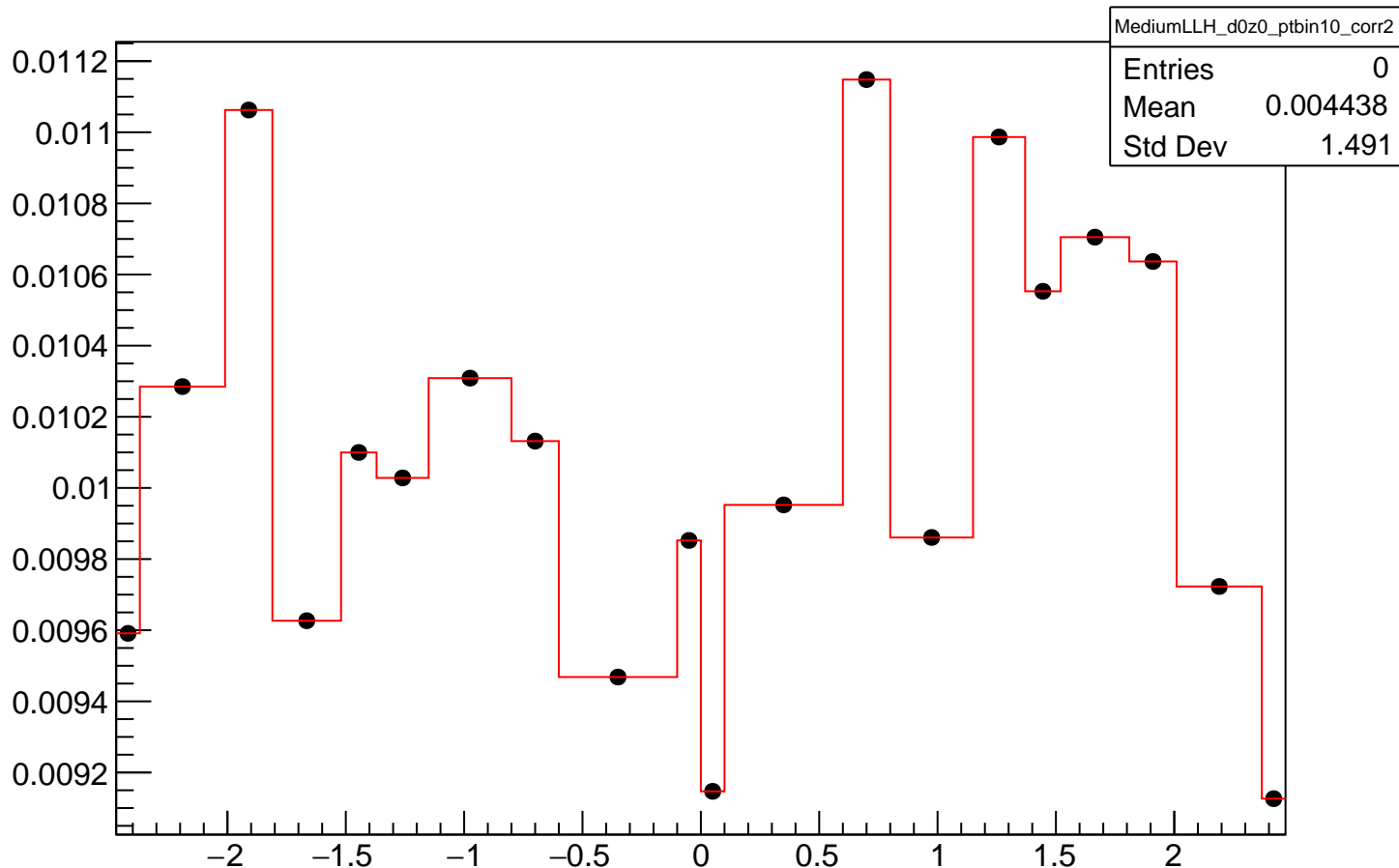
# FullSim\_MediumLLH\_d0z0\_corr2



# FullSim\_MediumLLH\_d0z0\_corr2

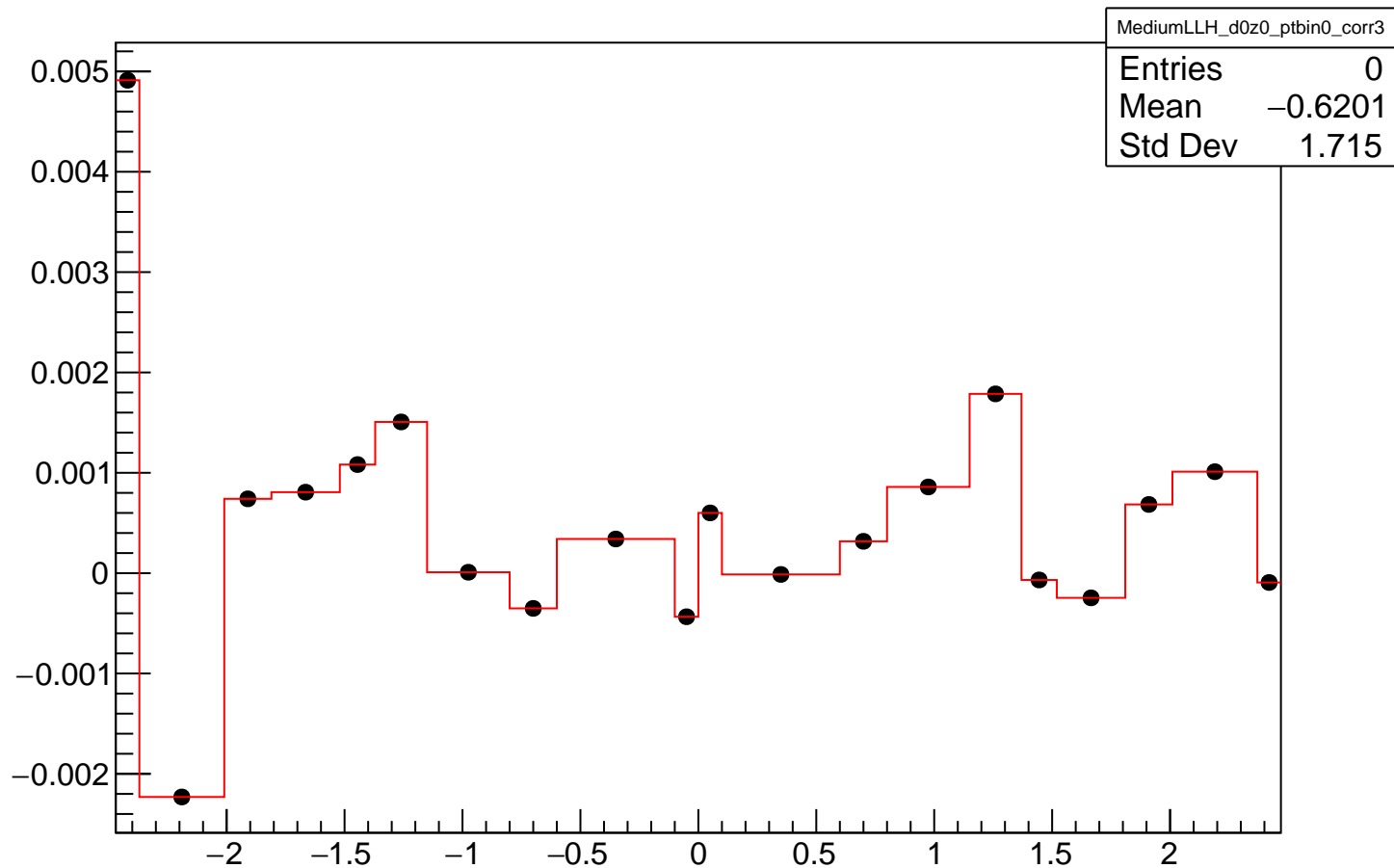


# FullSim\_MediumLLH\_d0z0\_corr2

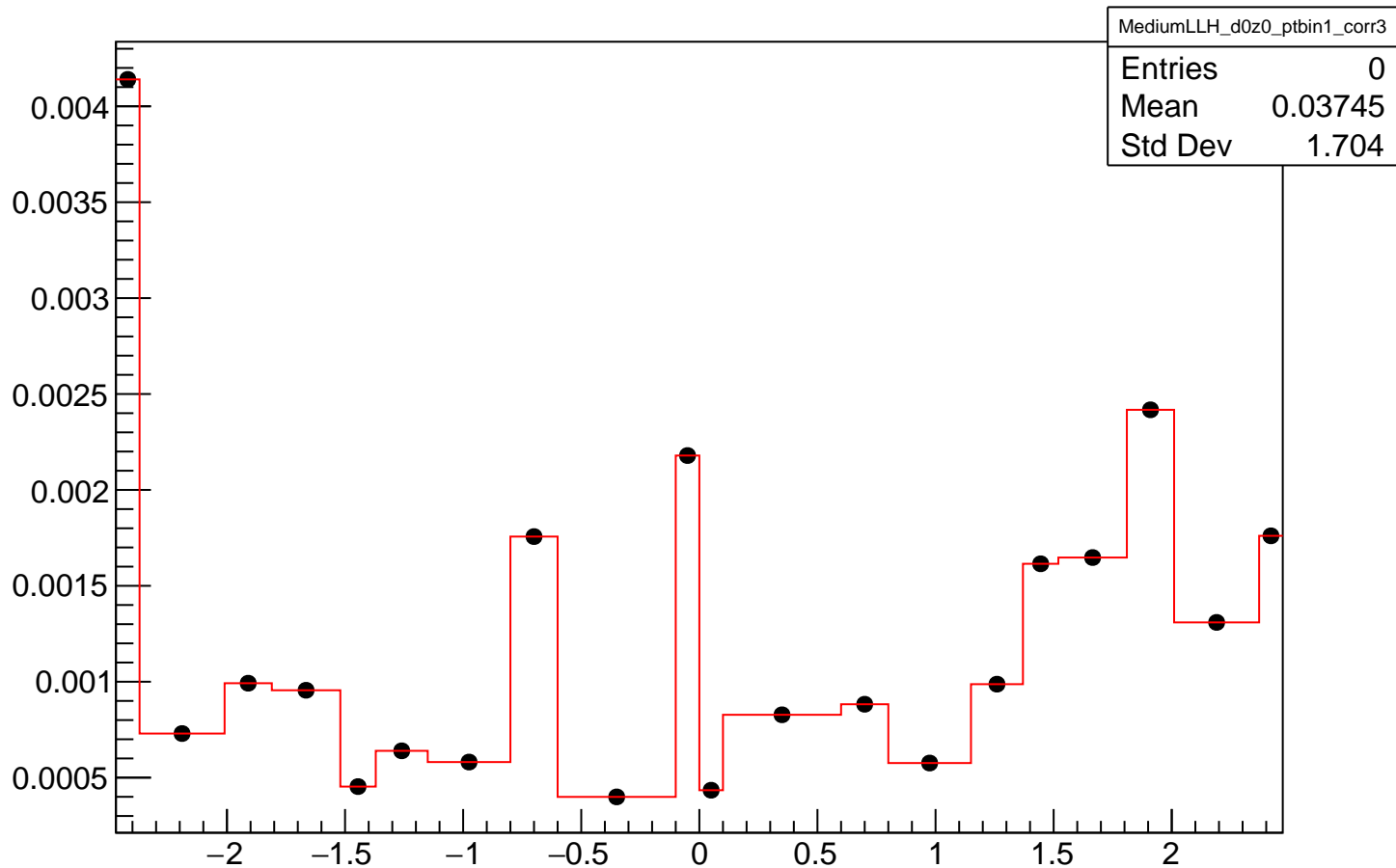




# FullSim\_MediumLLH\_d0z0\_corr3



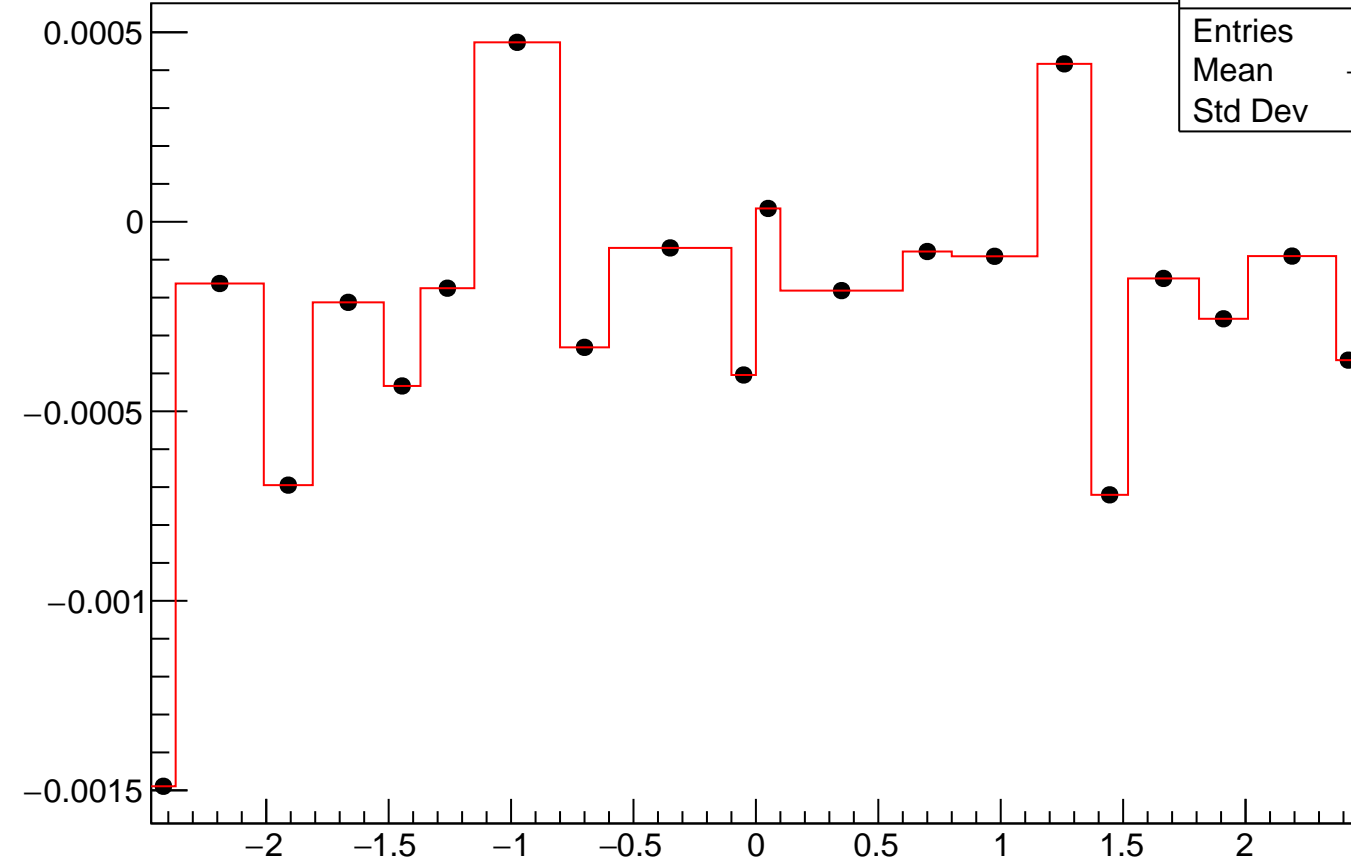
# FullSim\_MediumLLH\_d0z0\_corr3



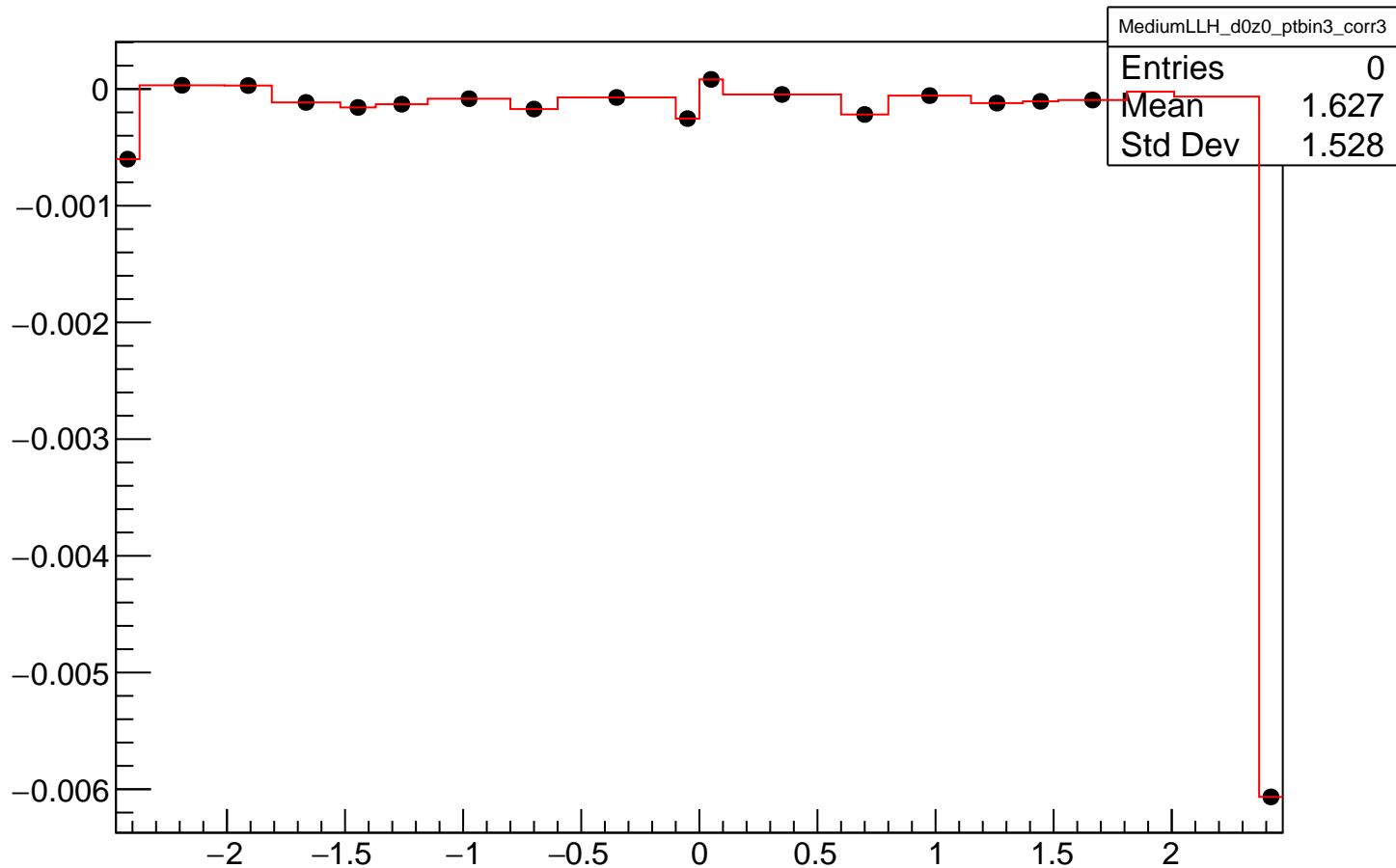
# FullSim\_MediumLLH\_d0z0\_corr3

MediumLLH\_d0z0\_ptbin2\_corr3

Entries	0
Mean	-0.7556
Std Dev	1.77



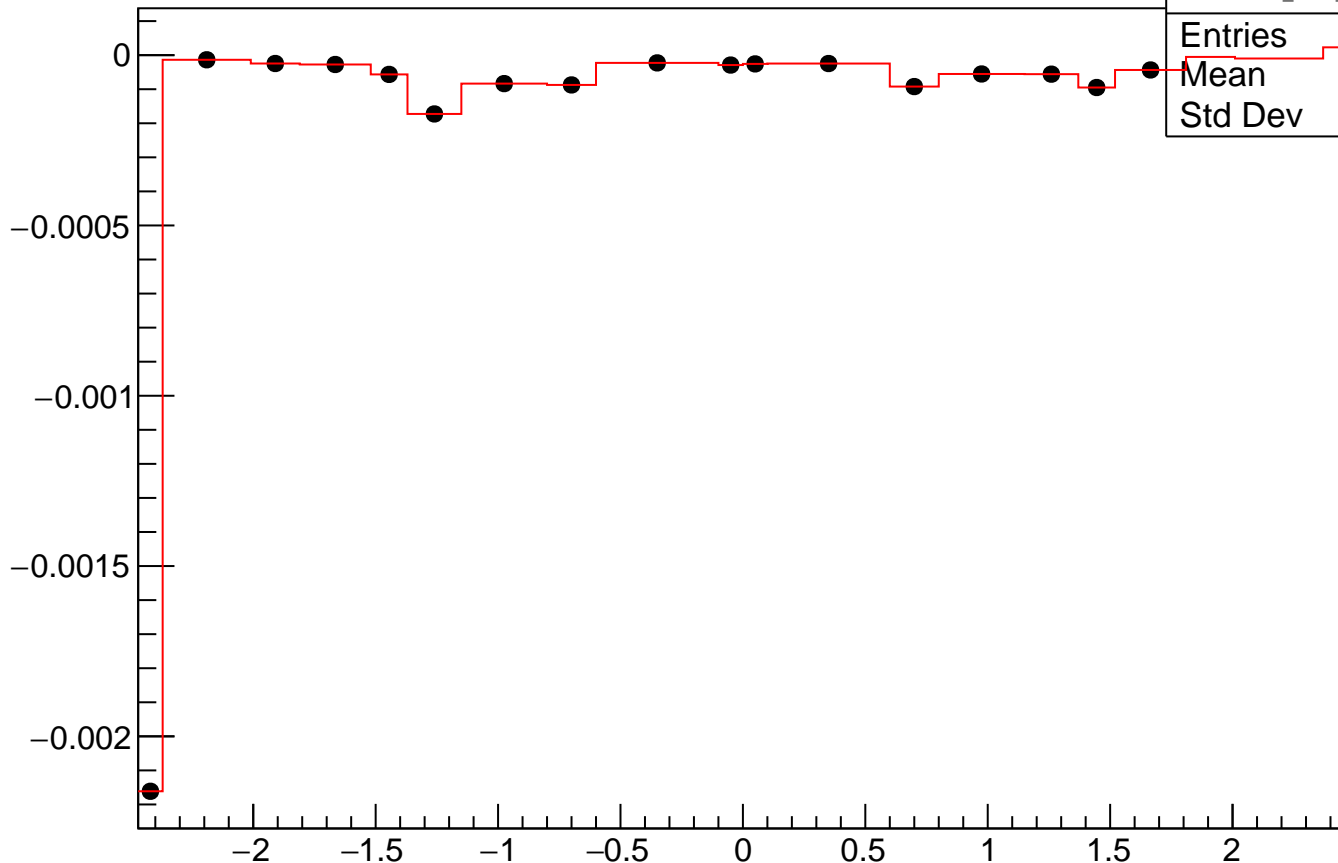
# FullSim\_MediumLLH\_d0z0\_corr3



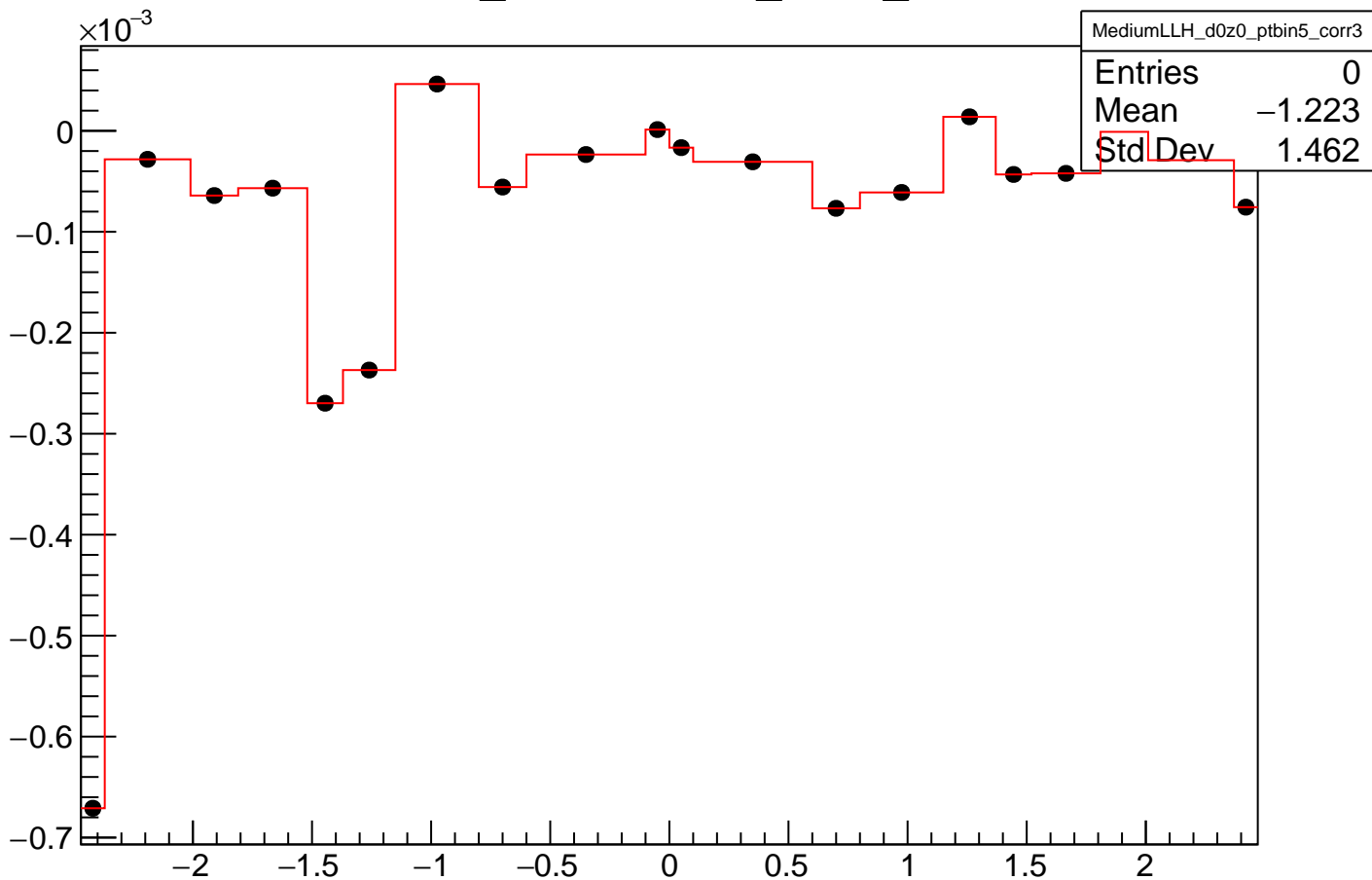
# FullSim\_MediumLLH\_d0z0\_corr3

MediumLLH\_d0z0\_ptbin4\_corr3

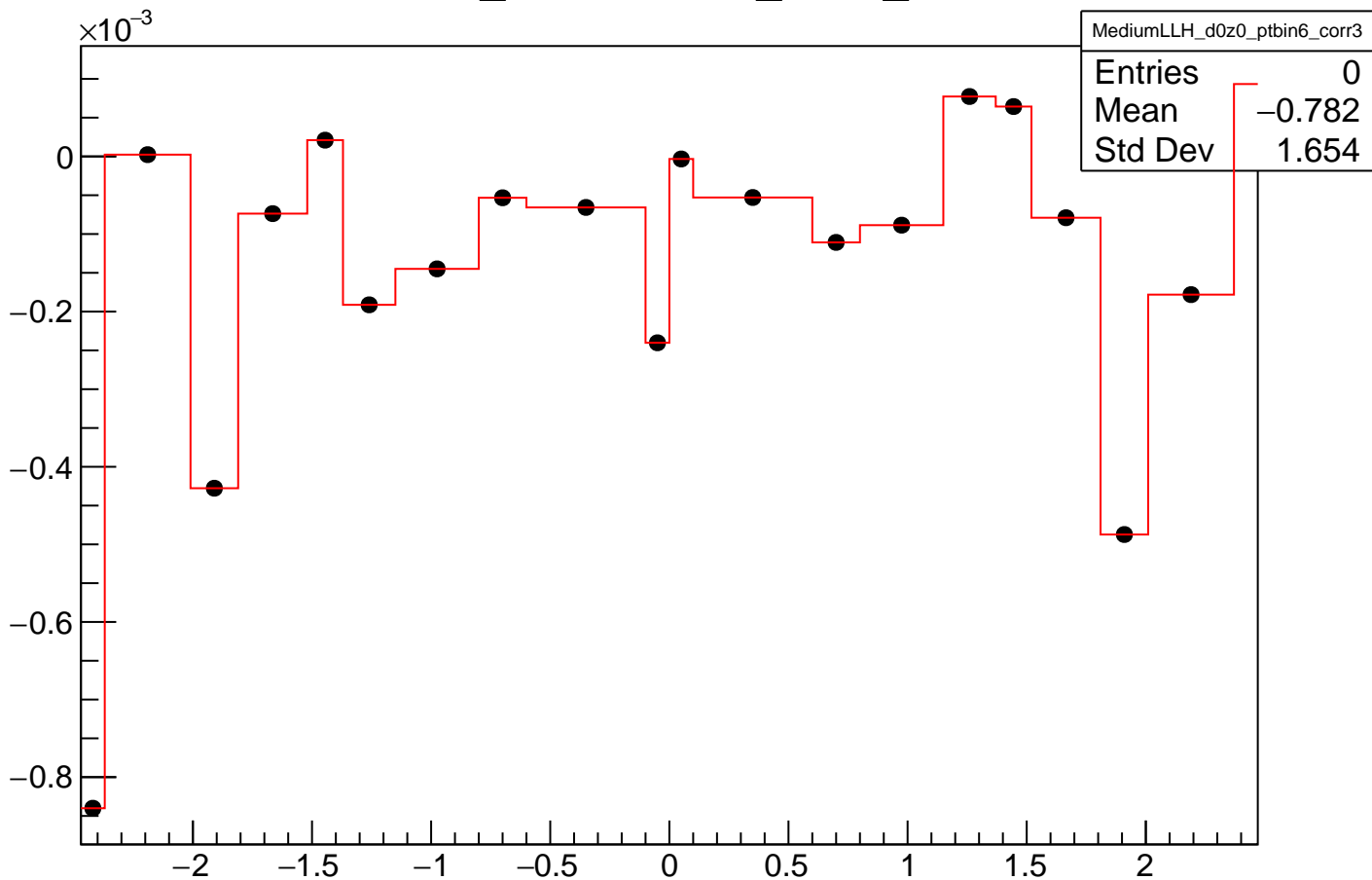
Entries	0
Mean	-1.77
Std Dev	1.179



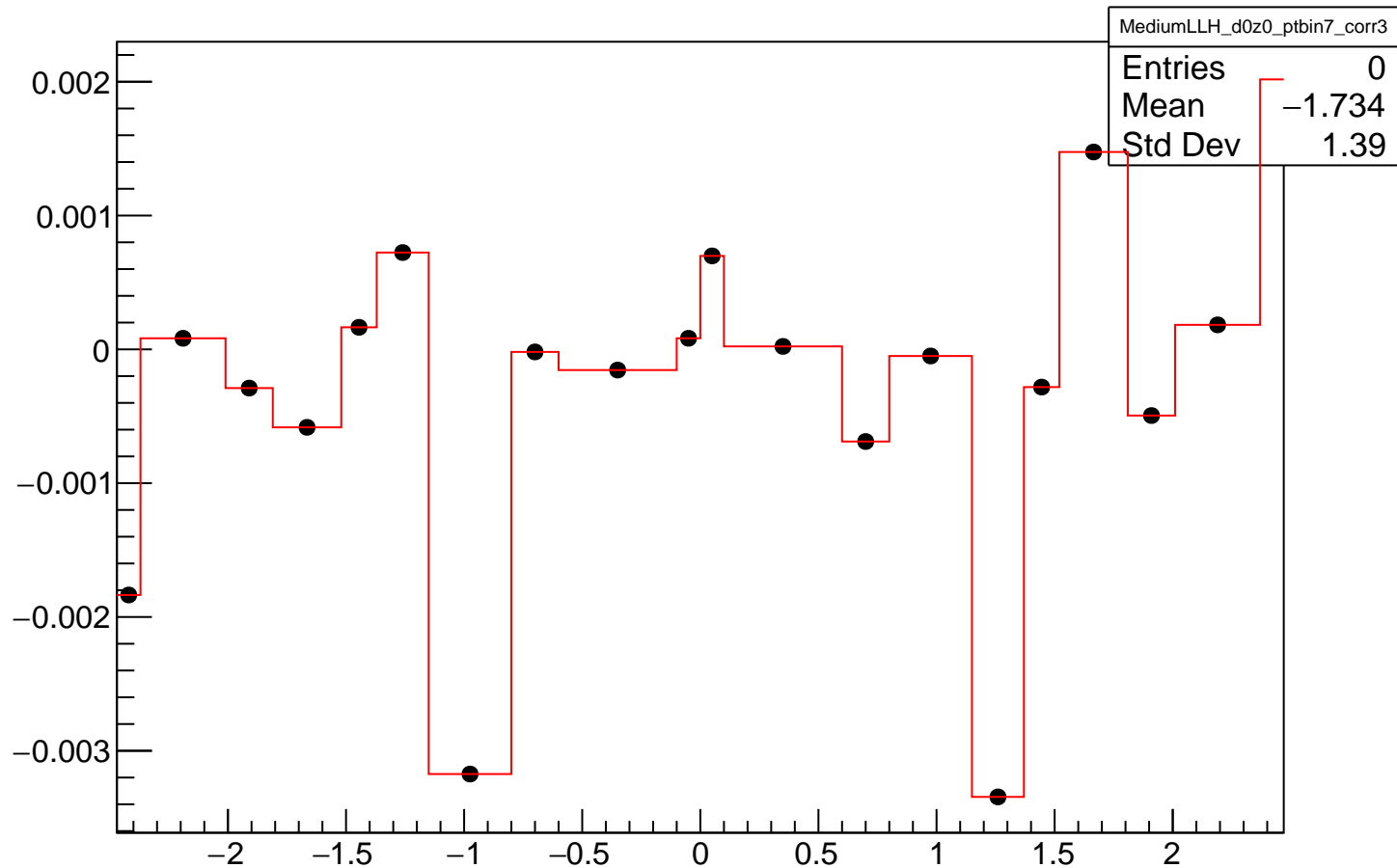
# FullSim\_MediumLLH\_d0z0\_corr3



# FullSim\_MediumLLH\_d0z0\_corr3

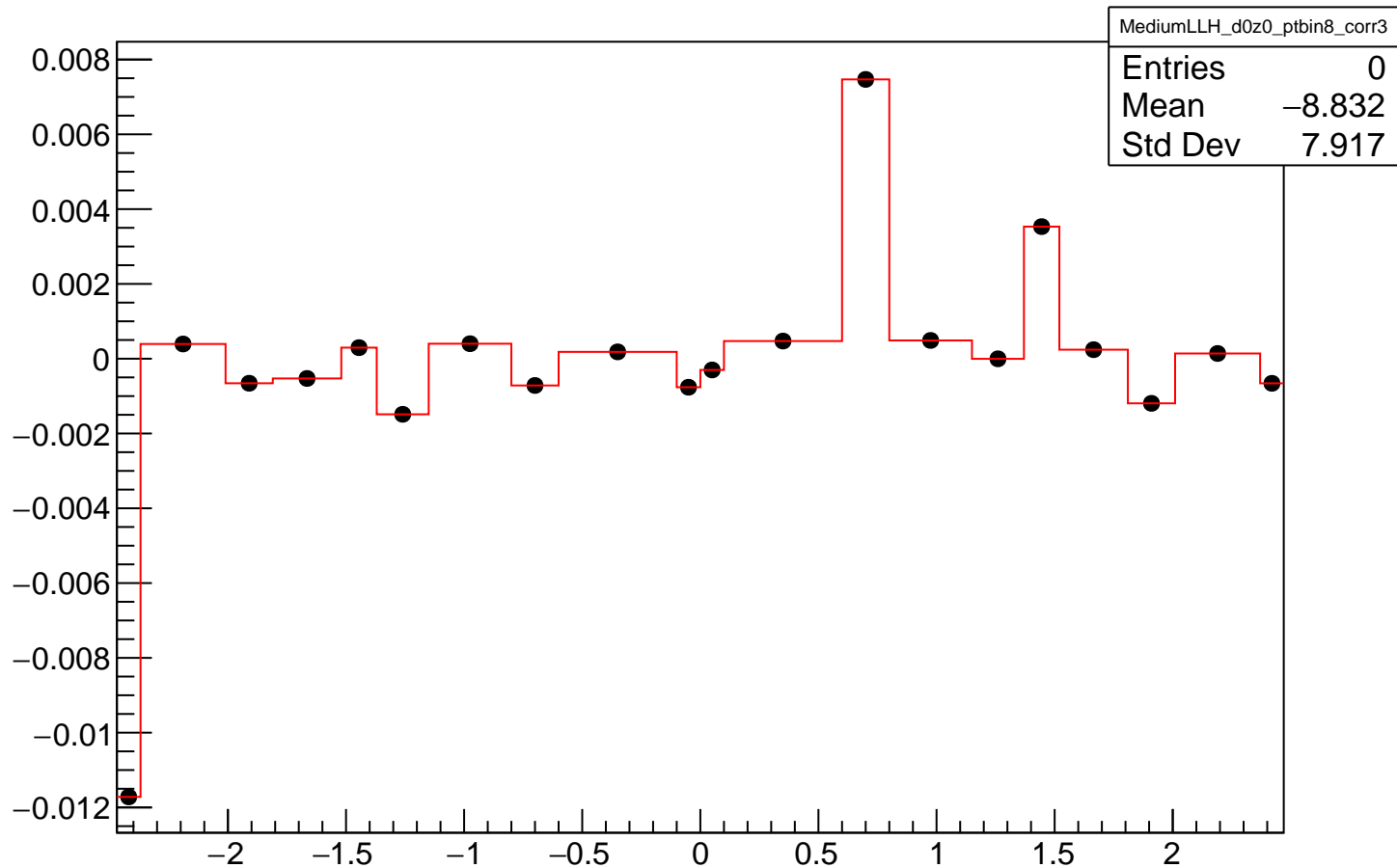


# FullSim\_MediumLLH\_d0z0\_corr3

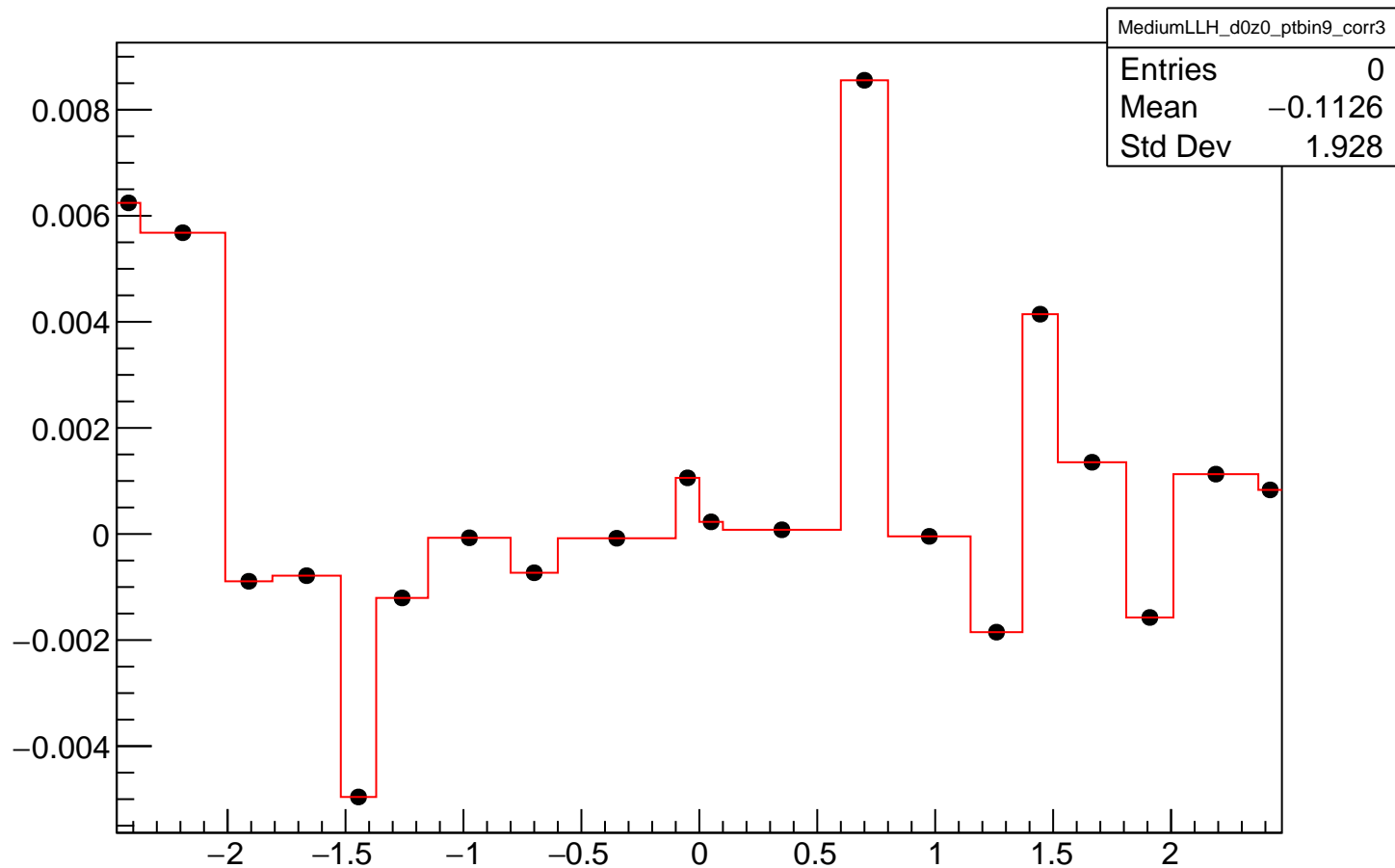




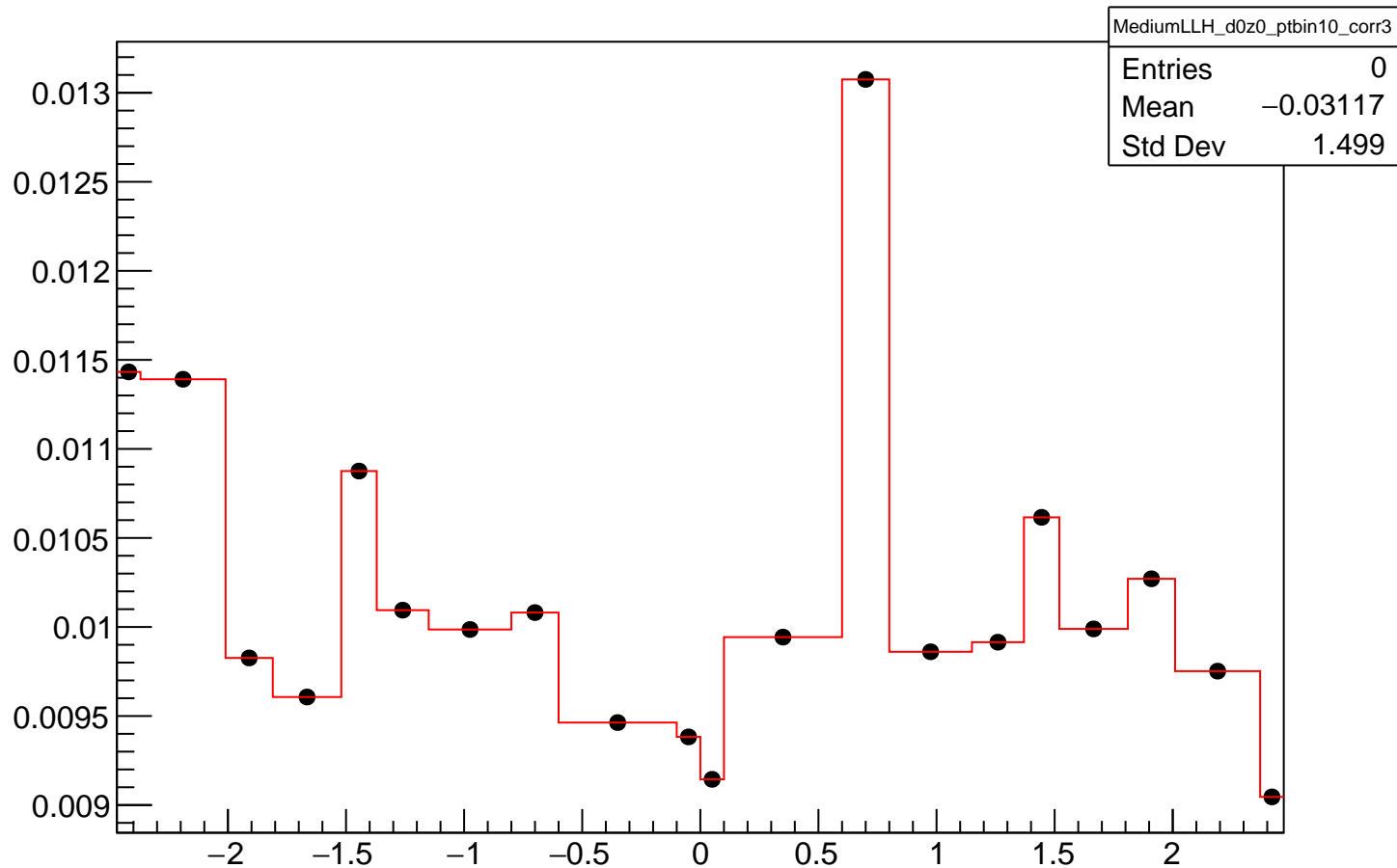
# FullSim\_MediumLLH\_d0z0\_corr3



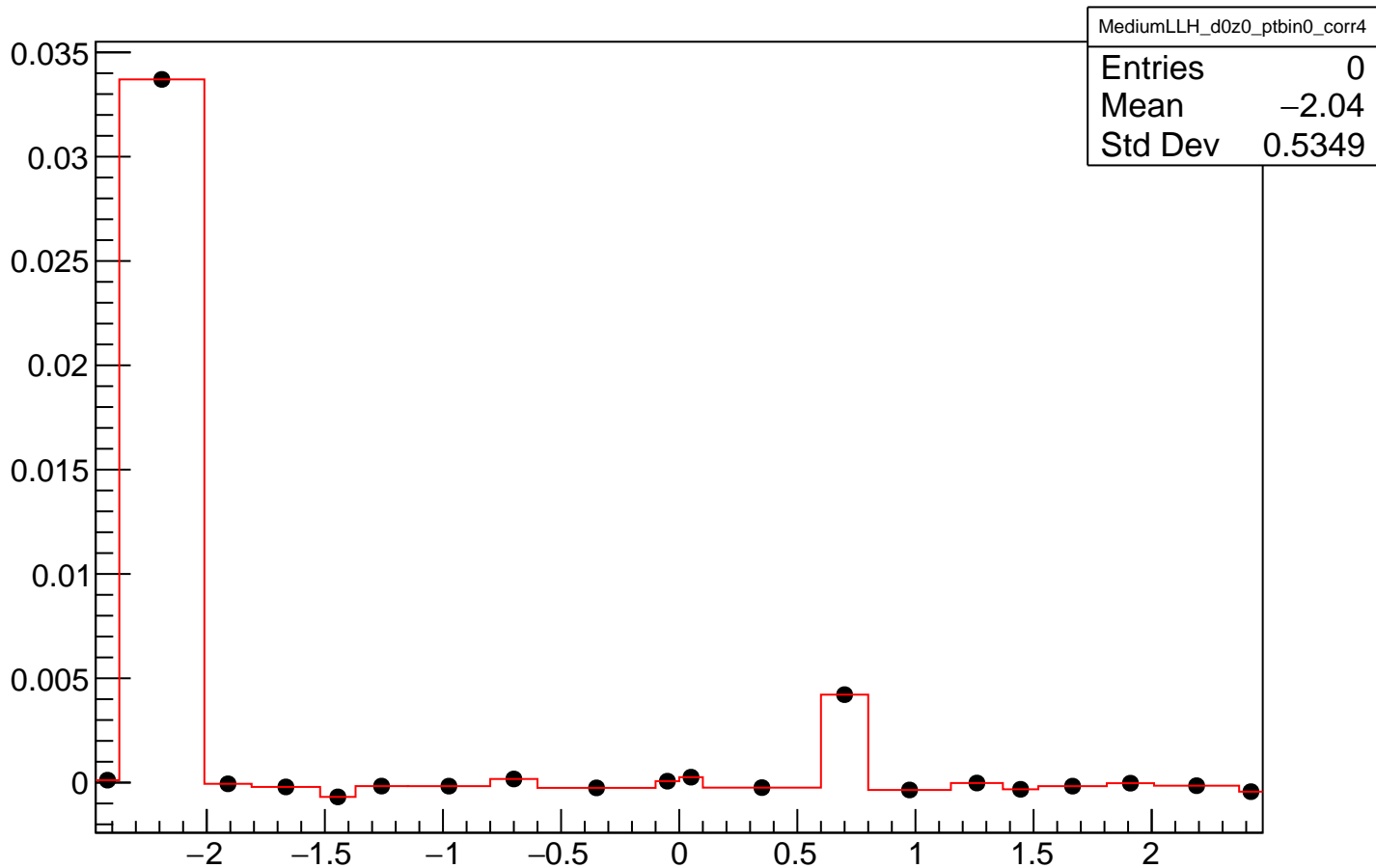
# FullSim\_MediumLLH\_d0z0\_corr3



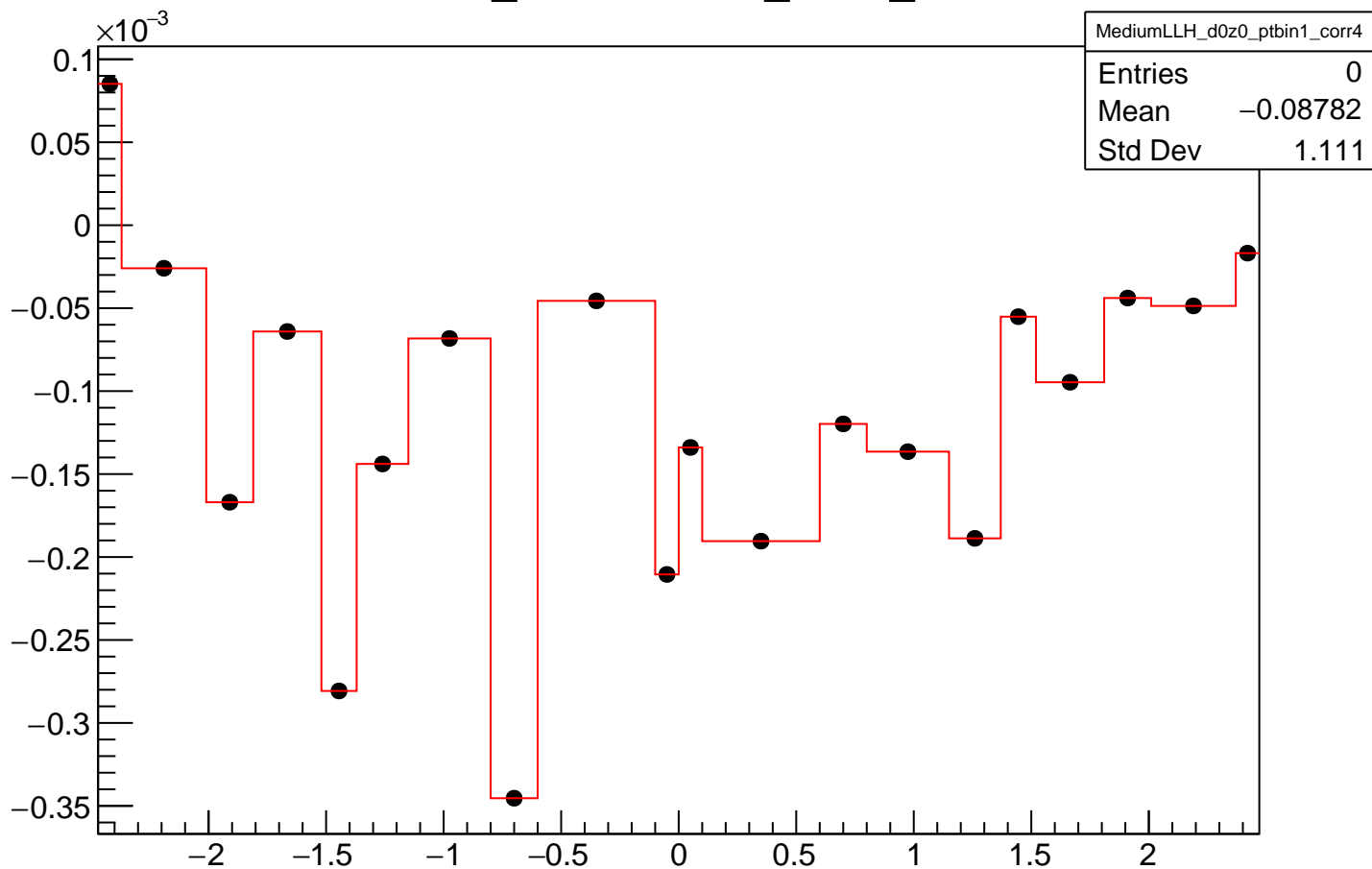
# FullSim\_MediumLLH\_d0z0\_corr3



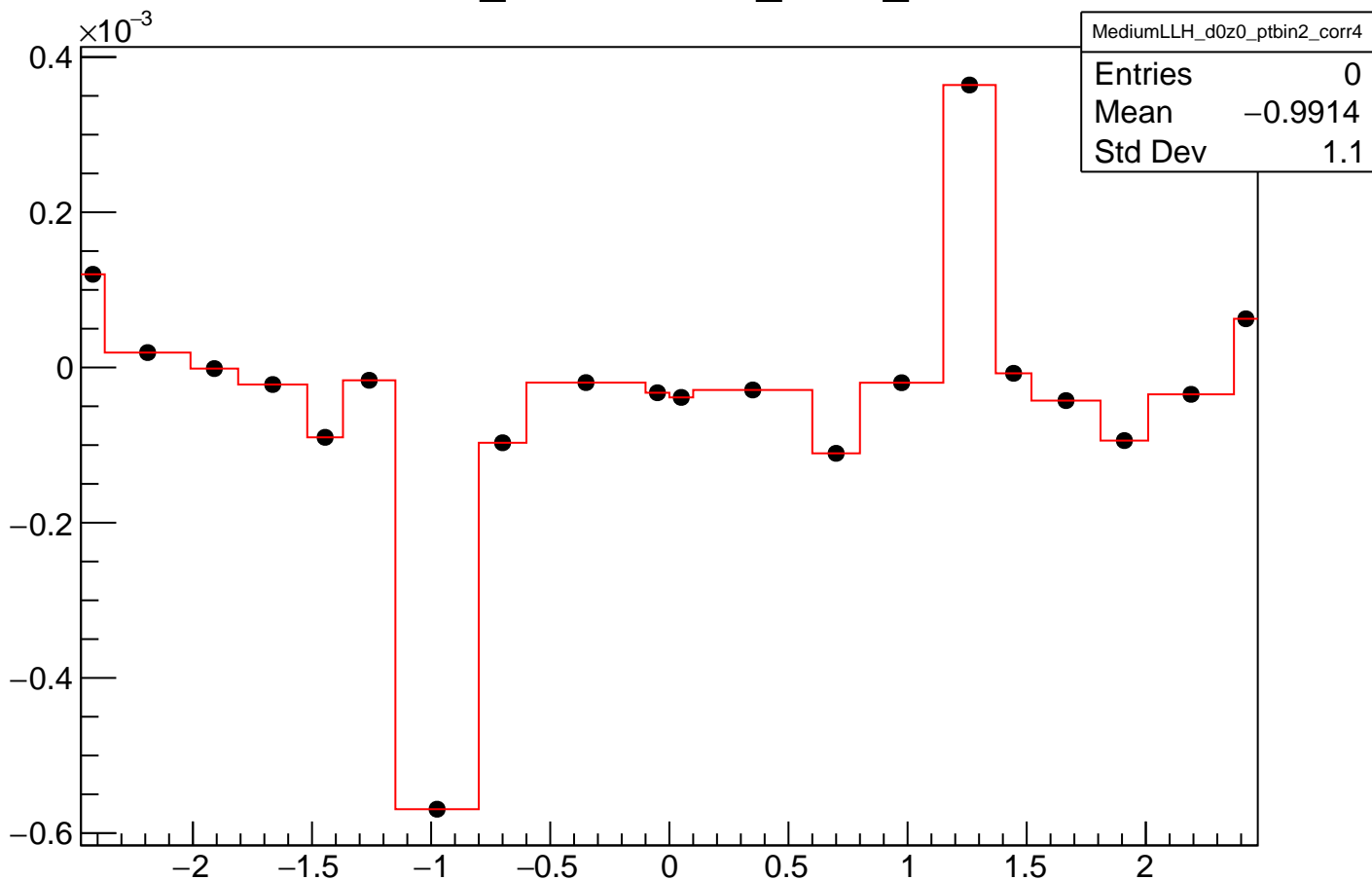
# FullSim\_MediumLLH\_d0z0\_corr4



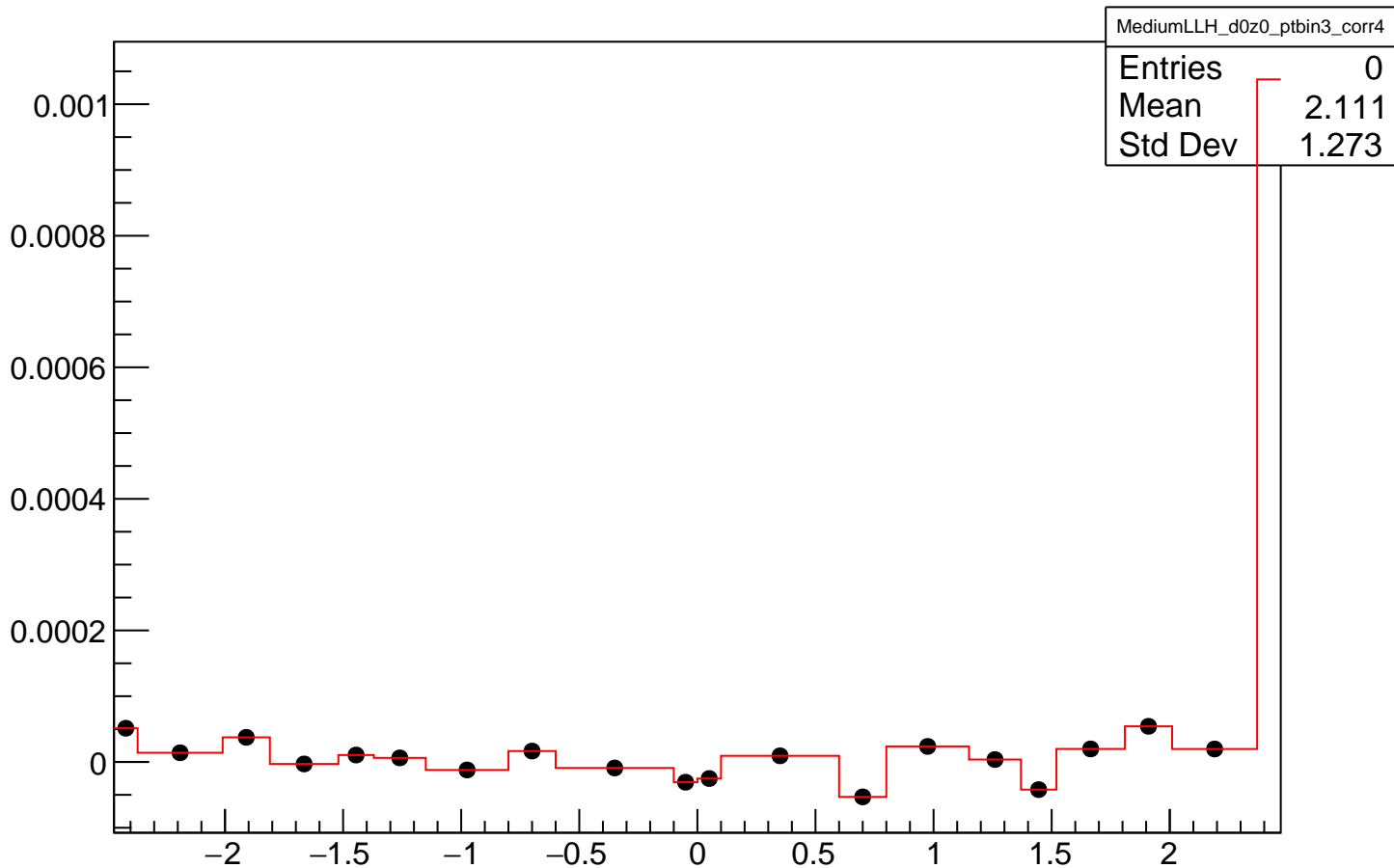
# FullSim\_MediumLLH\_d0z0\_corr4



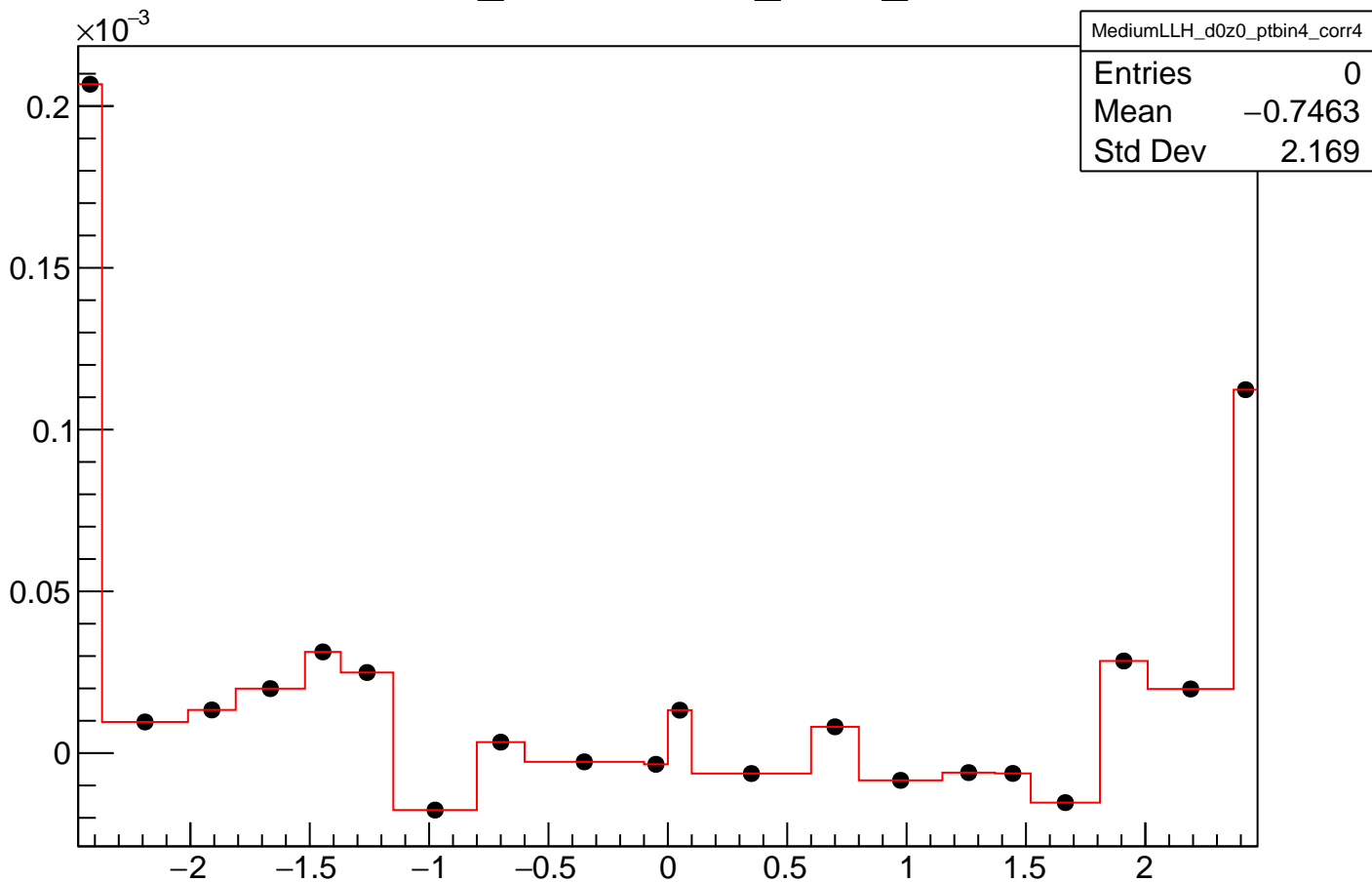
# FullSim\_MediumLLH\_d0z0\_corr4



# FullSim\_MediumLLH\_d0z0\_corr4

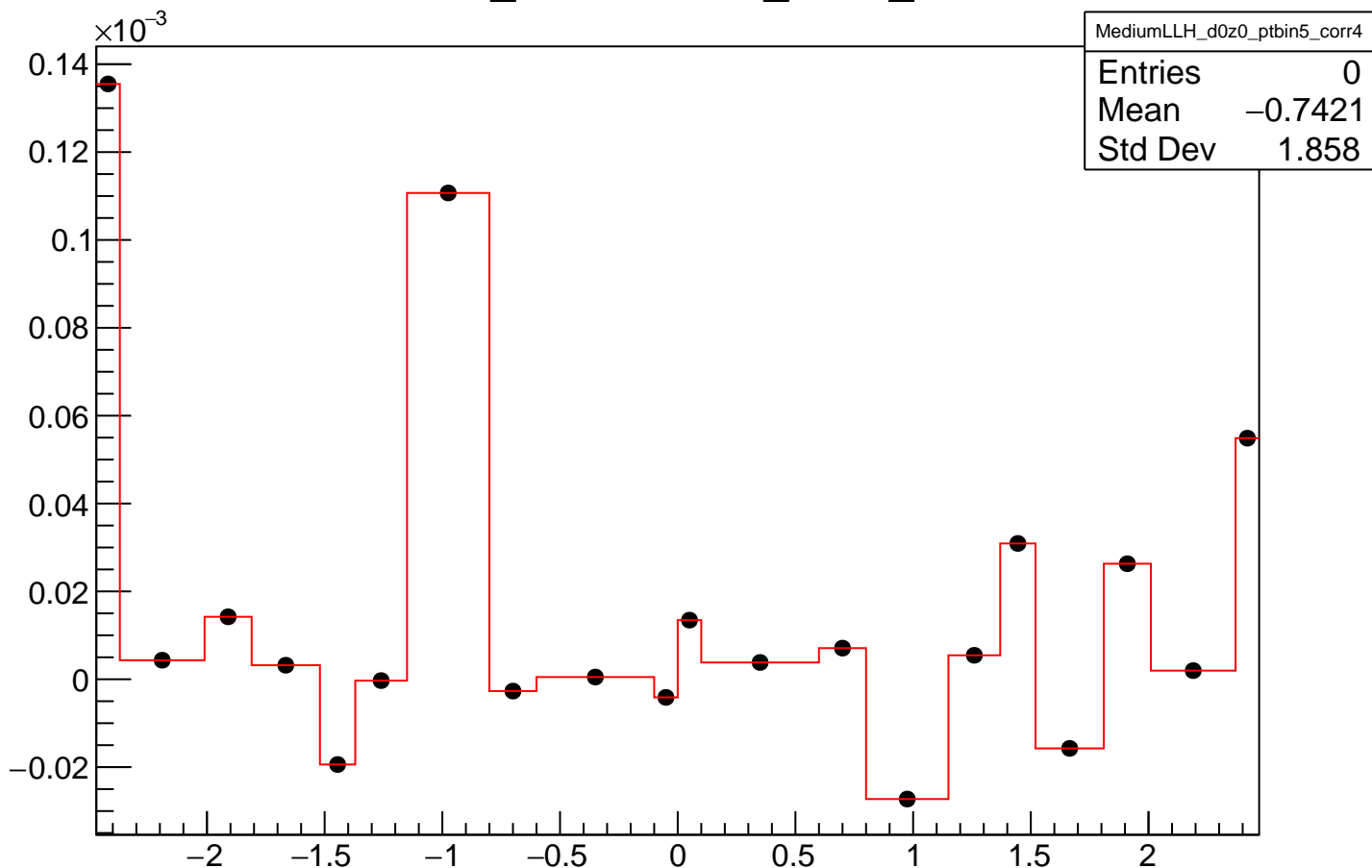


# FullSim\_MediumLLH\_d0z0\_corr4

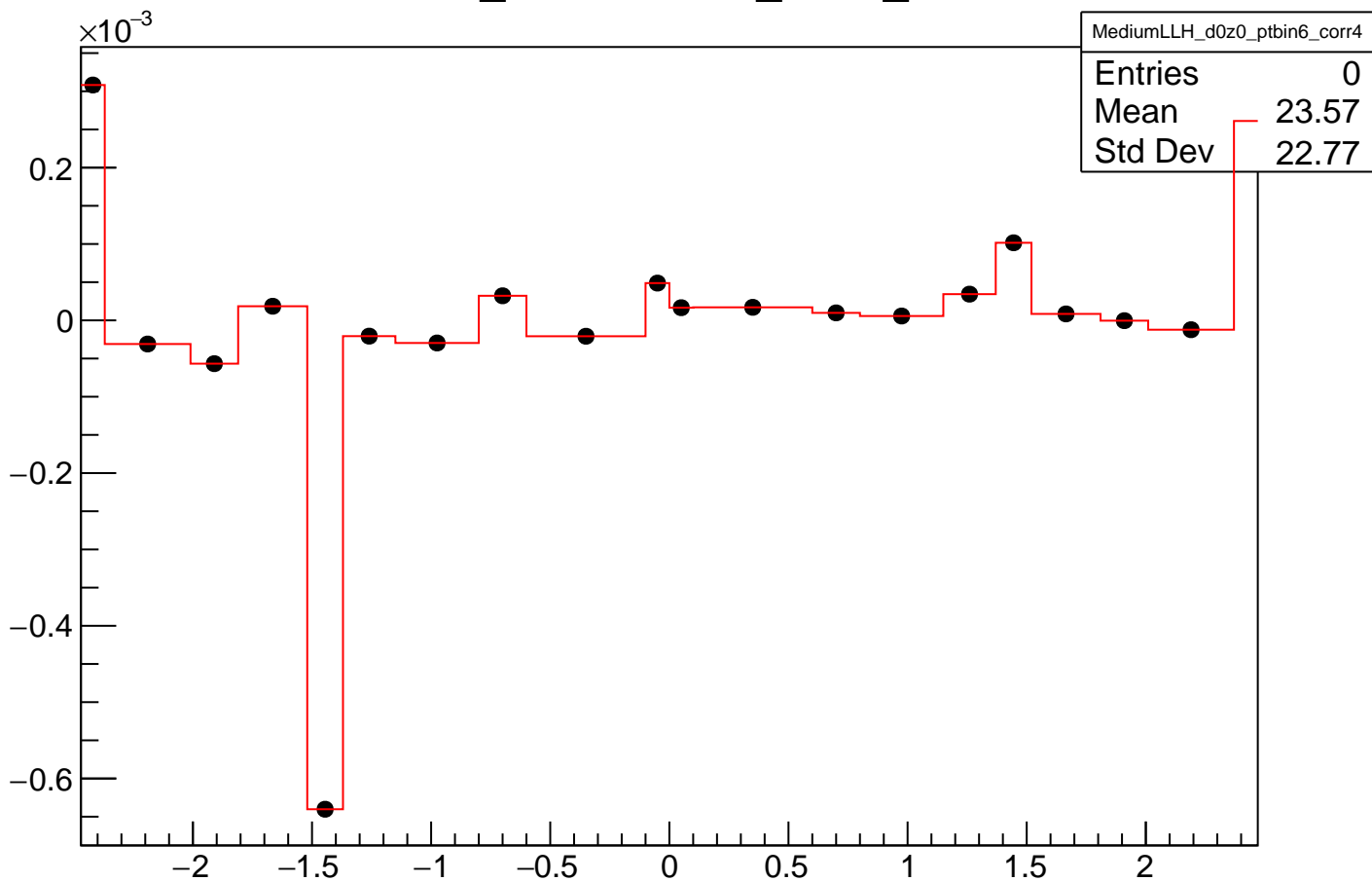




# FullSim\_MediumLLH\_d0z0\_corr4



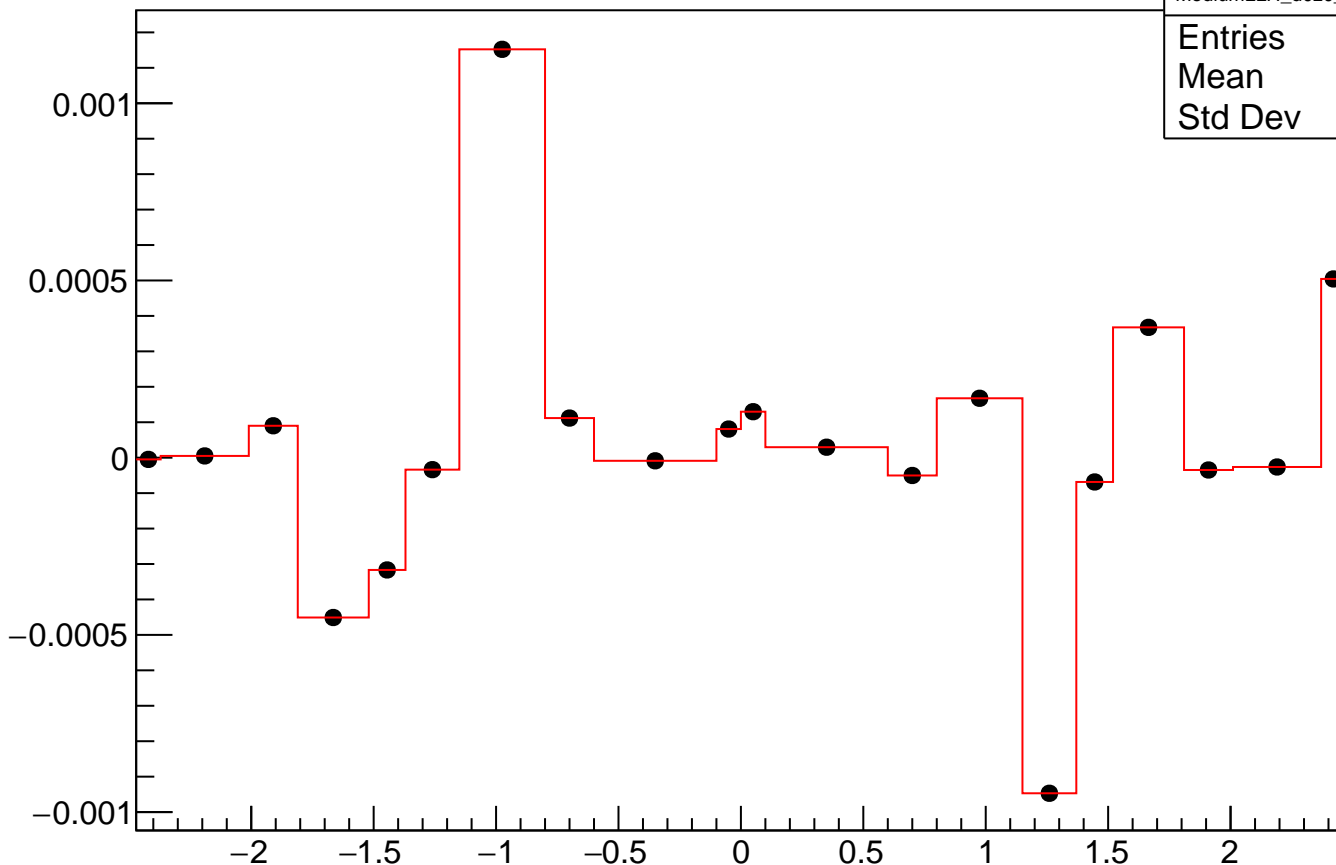
# FullSim\_MediumLLH\_d0z0\_corr4



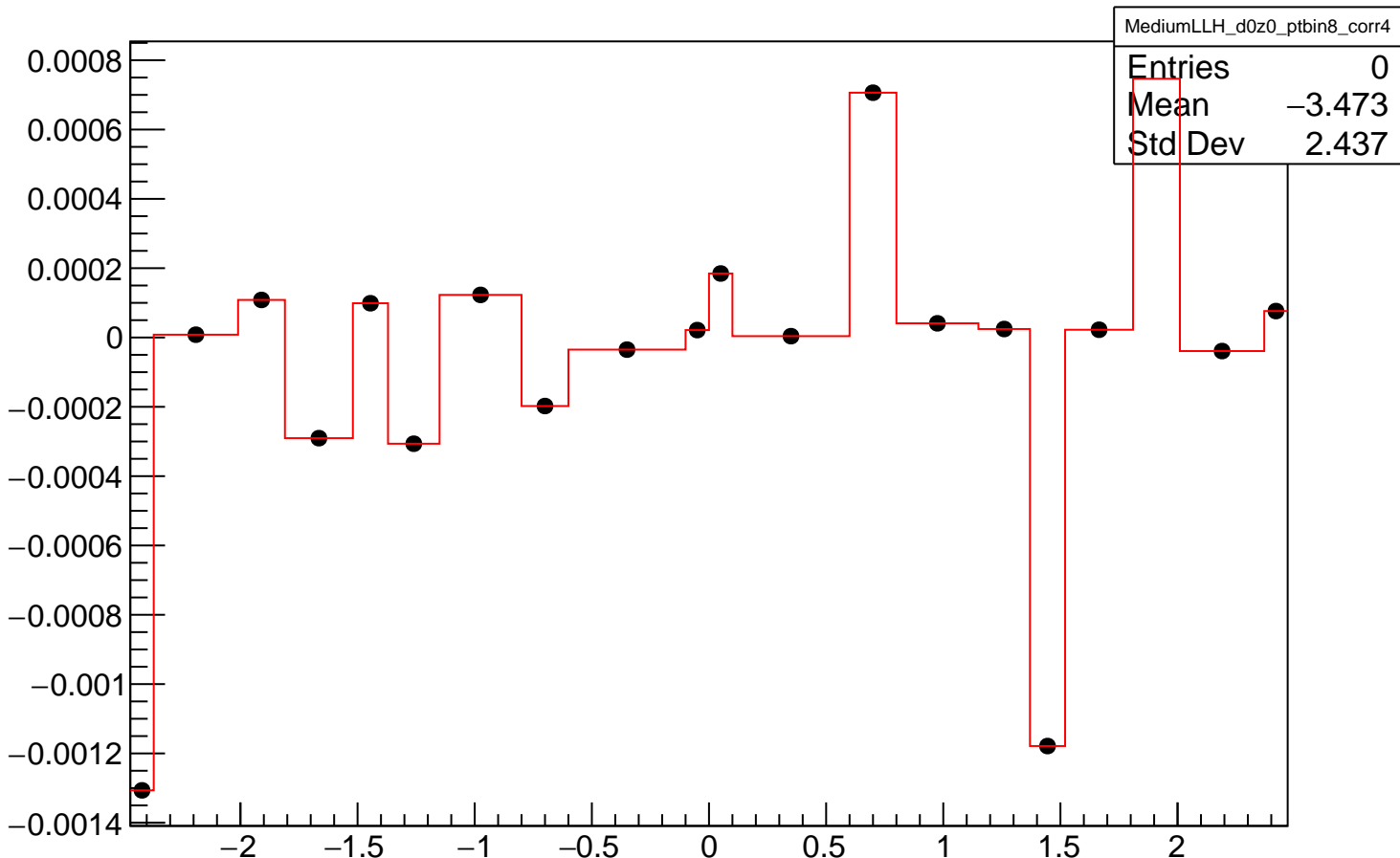
# FullSim\_MediumLLH\_d0z0\_corr4

MediumLLH\_d0z0\_ptbin7\_corr4

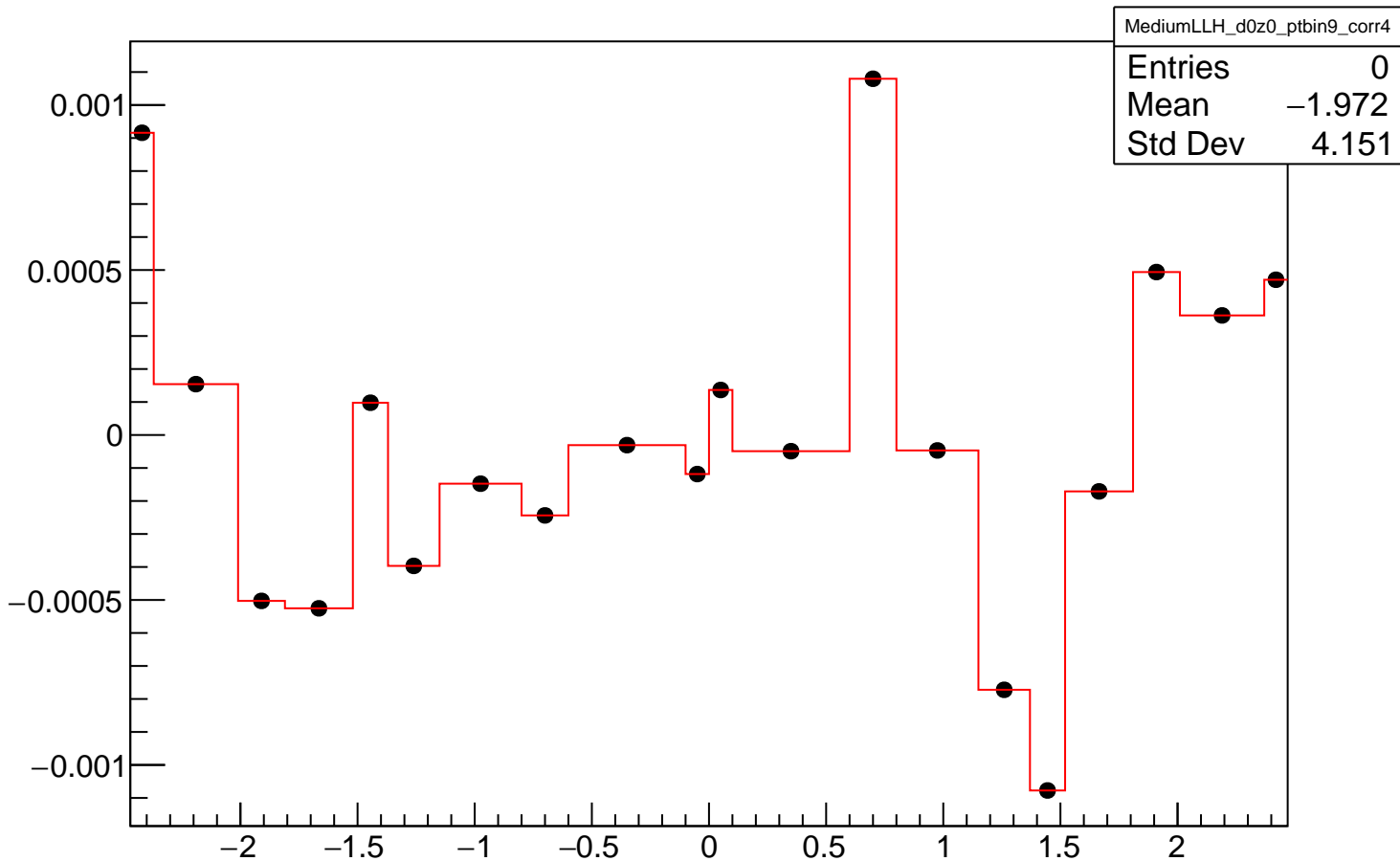
Entries	0
Mean	0.6303
Std Dev	1.439



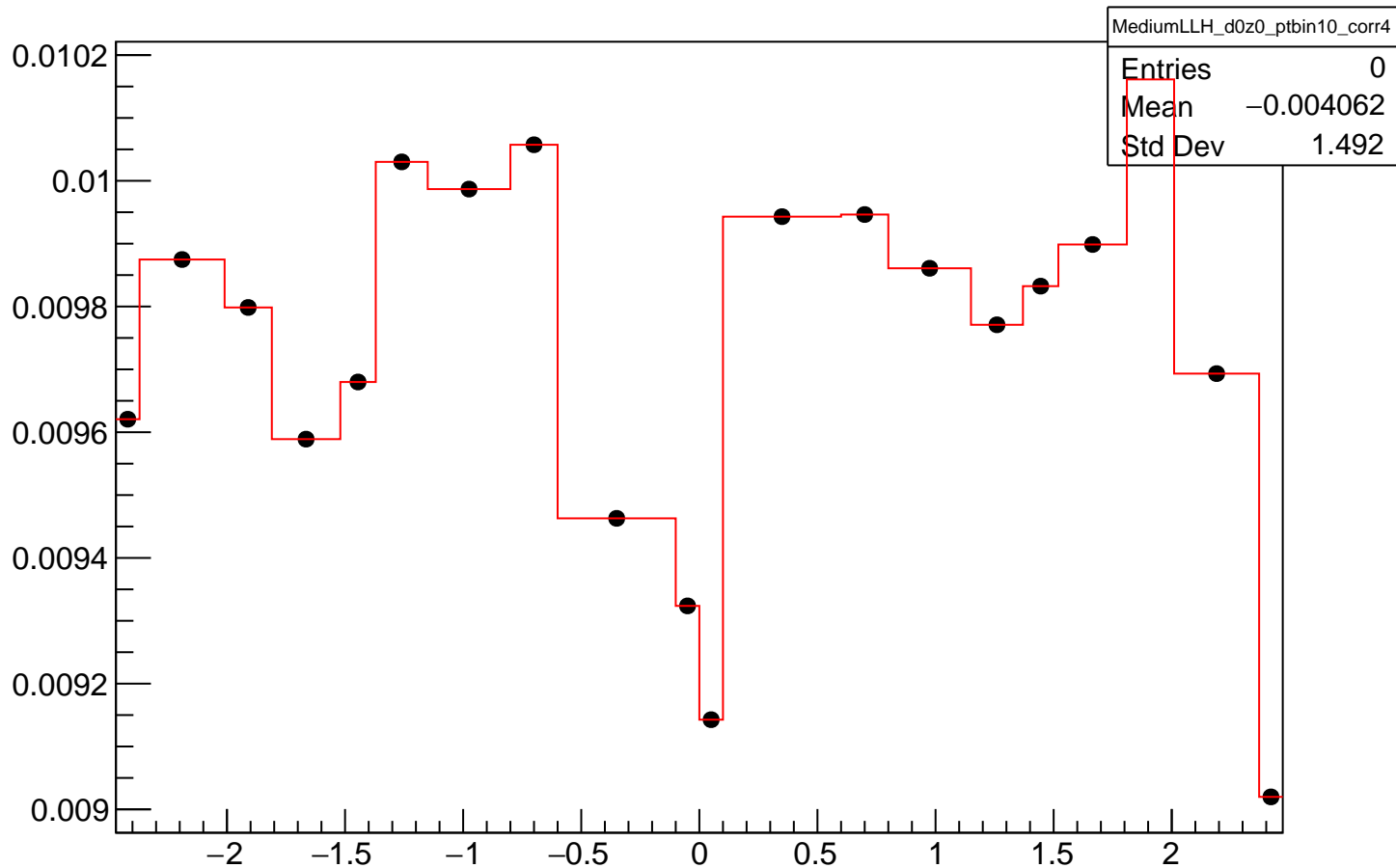
# FullSim\_MediumLLH\_d0z0\_corr4



# FullSim\_MediumLLH\_d0z0\_corr4



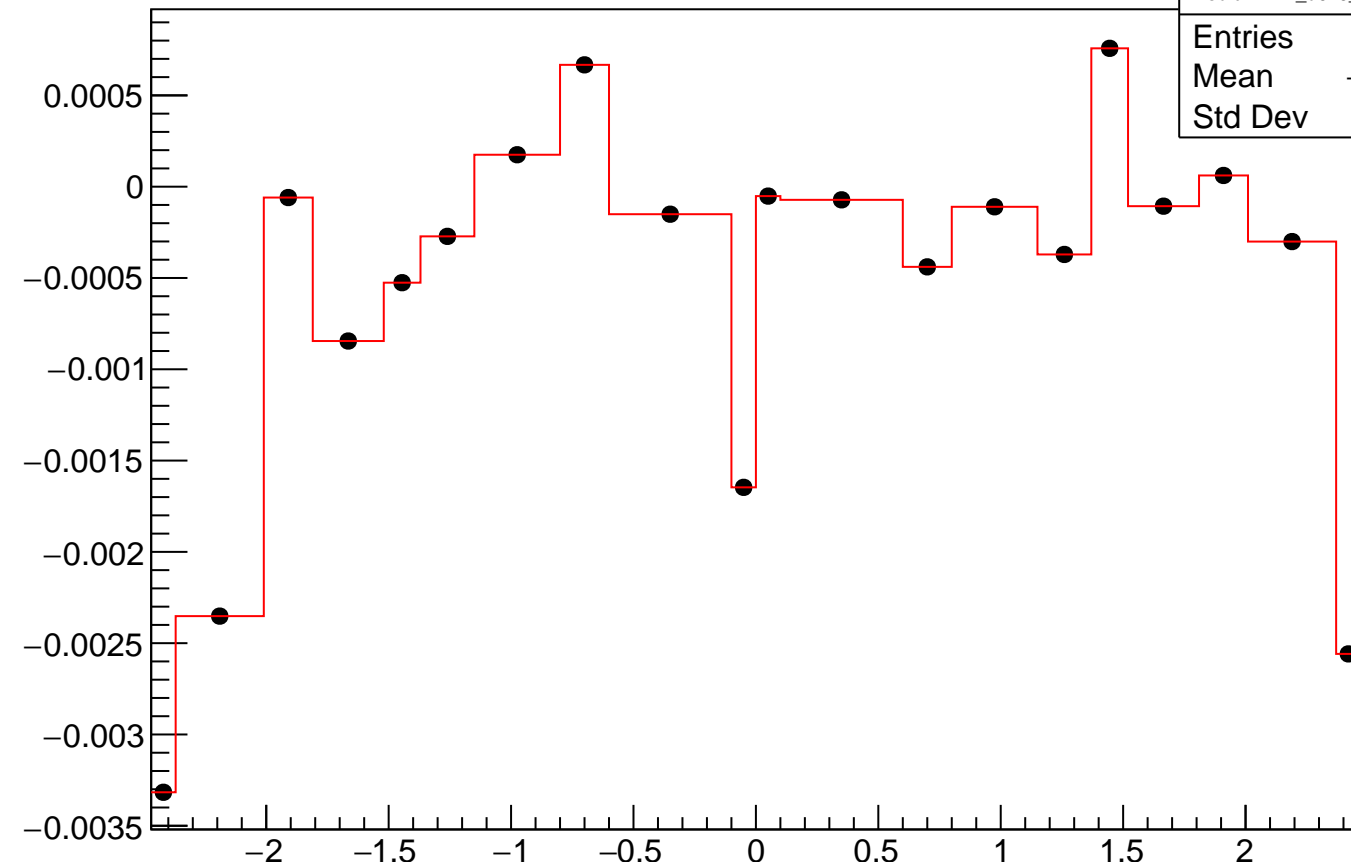
# FullSim\_MediumLLH\_d0z0\_corr4



# FullSim\_MediumLLH\_d0z0\_corr5

MediumLLH\_d0z0\_ptbin0\_corr5

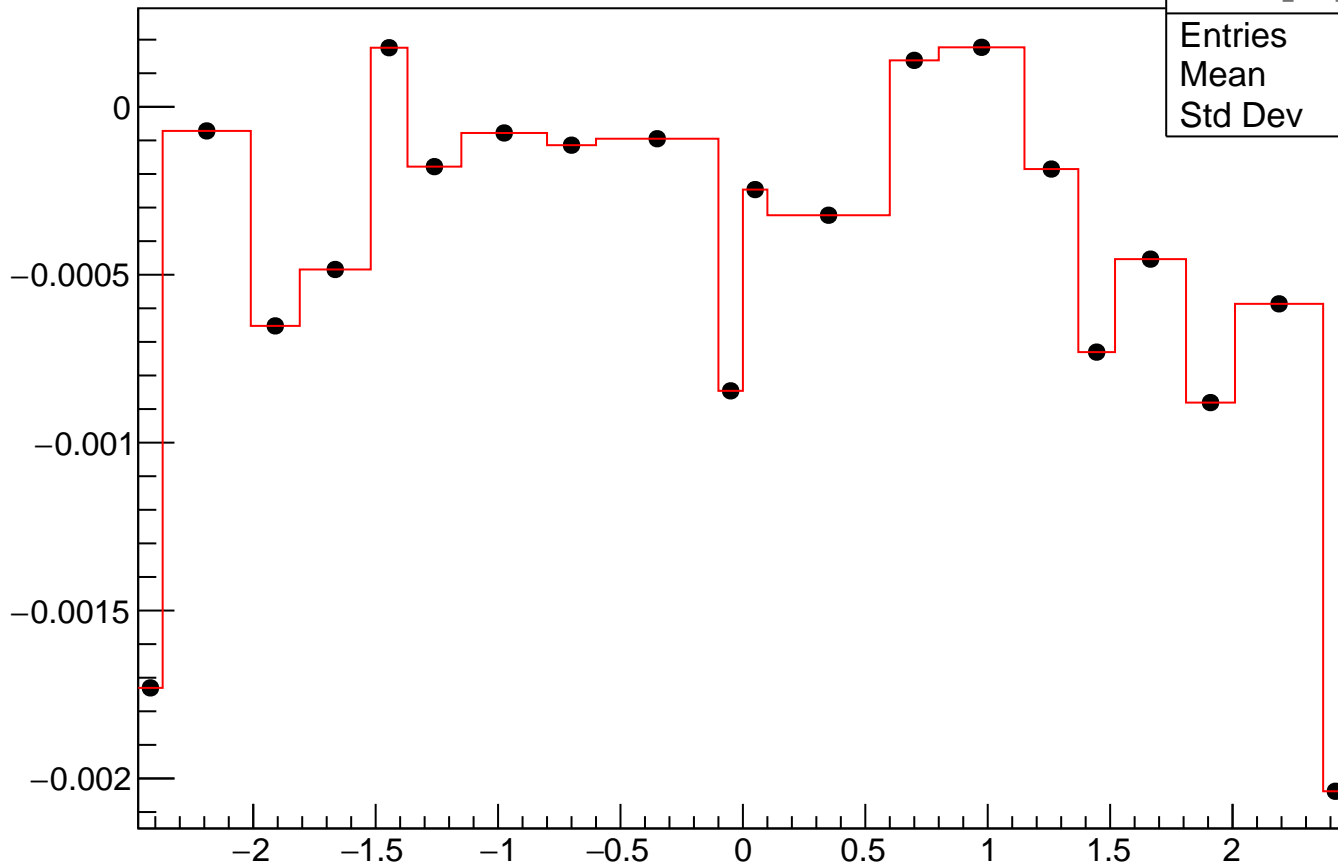
Entries	0
Mean	-0.7442
Std Dev	1.95



# FullSim\_MediumLLH\_d0z0\_corr5

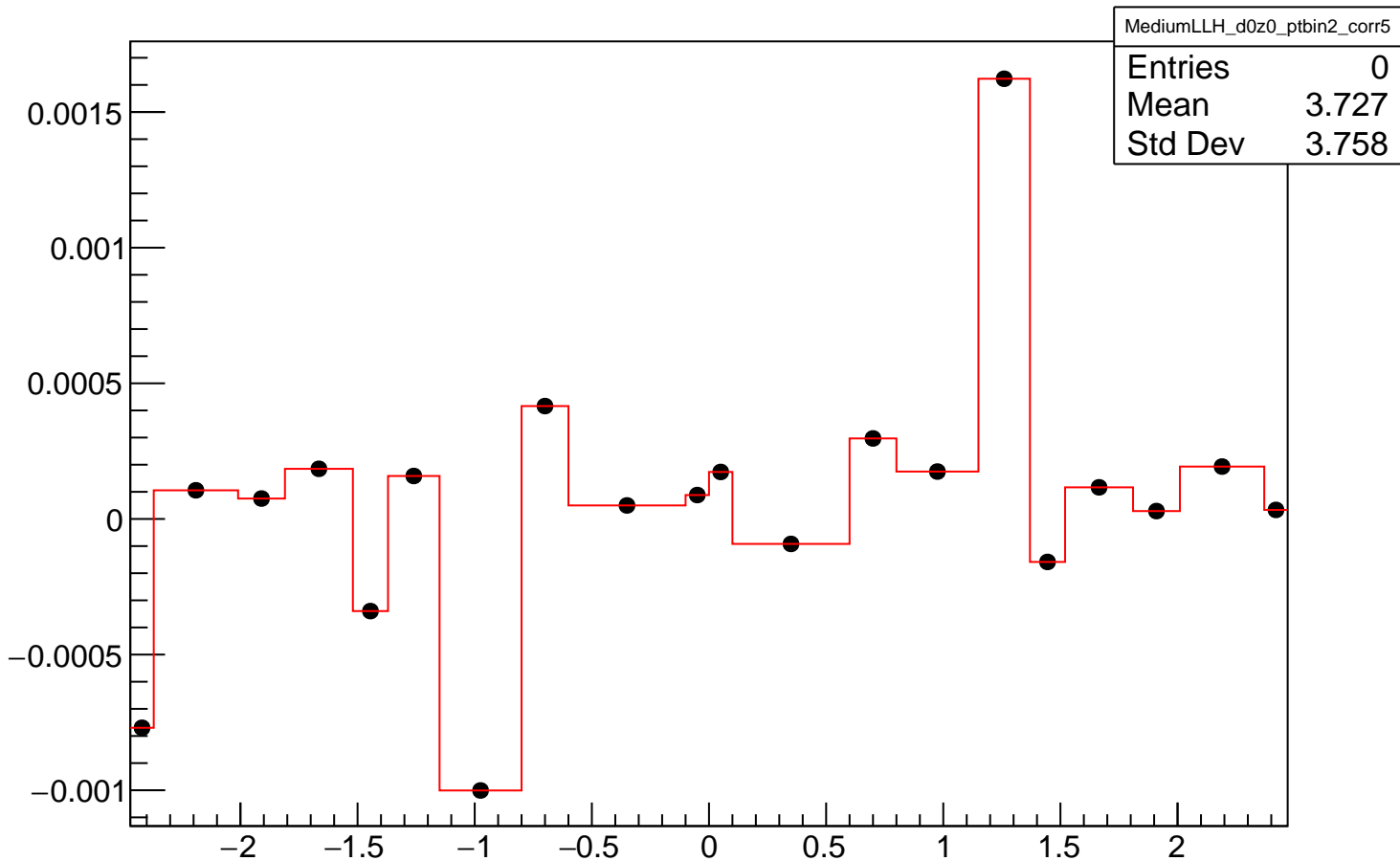
MediumLLH\_d0z0\_ptbin1\_corr5

Entries	0
Mean	0.3479
Std Dev	1.922

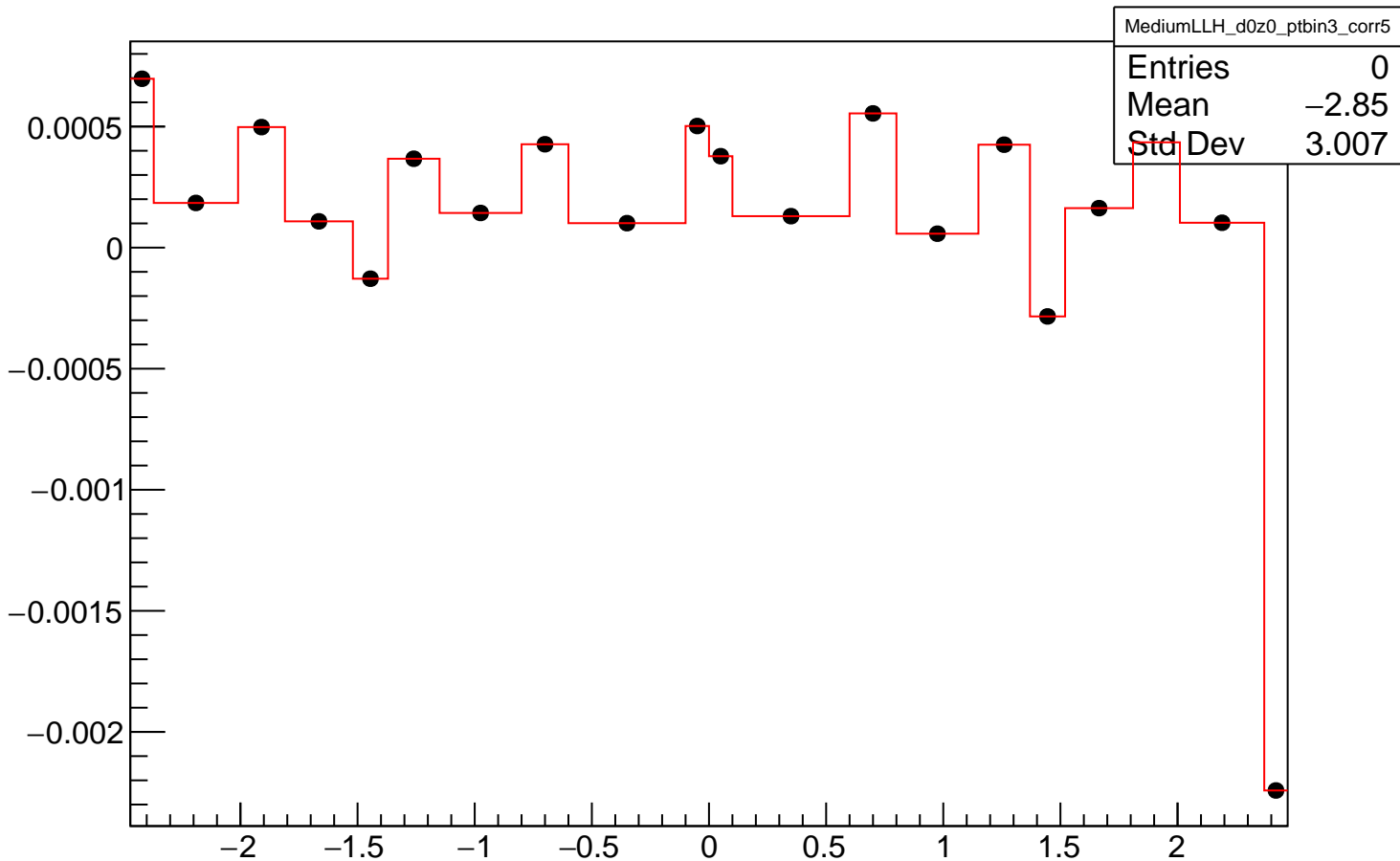




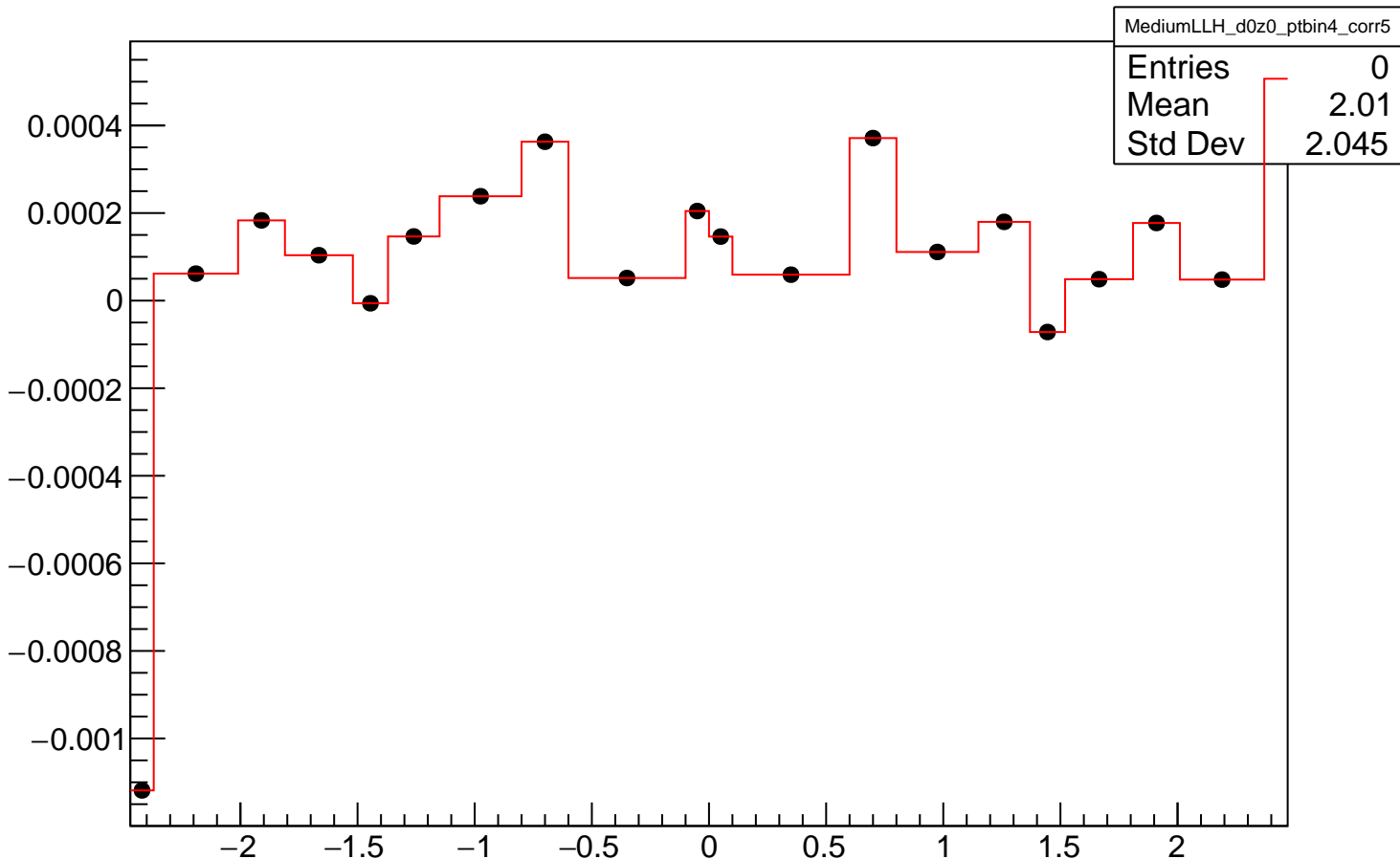
# FullSim\_MediumLLH\_d0z0\_corr5



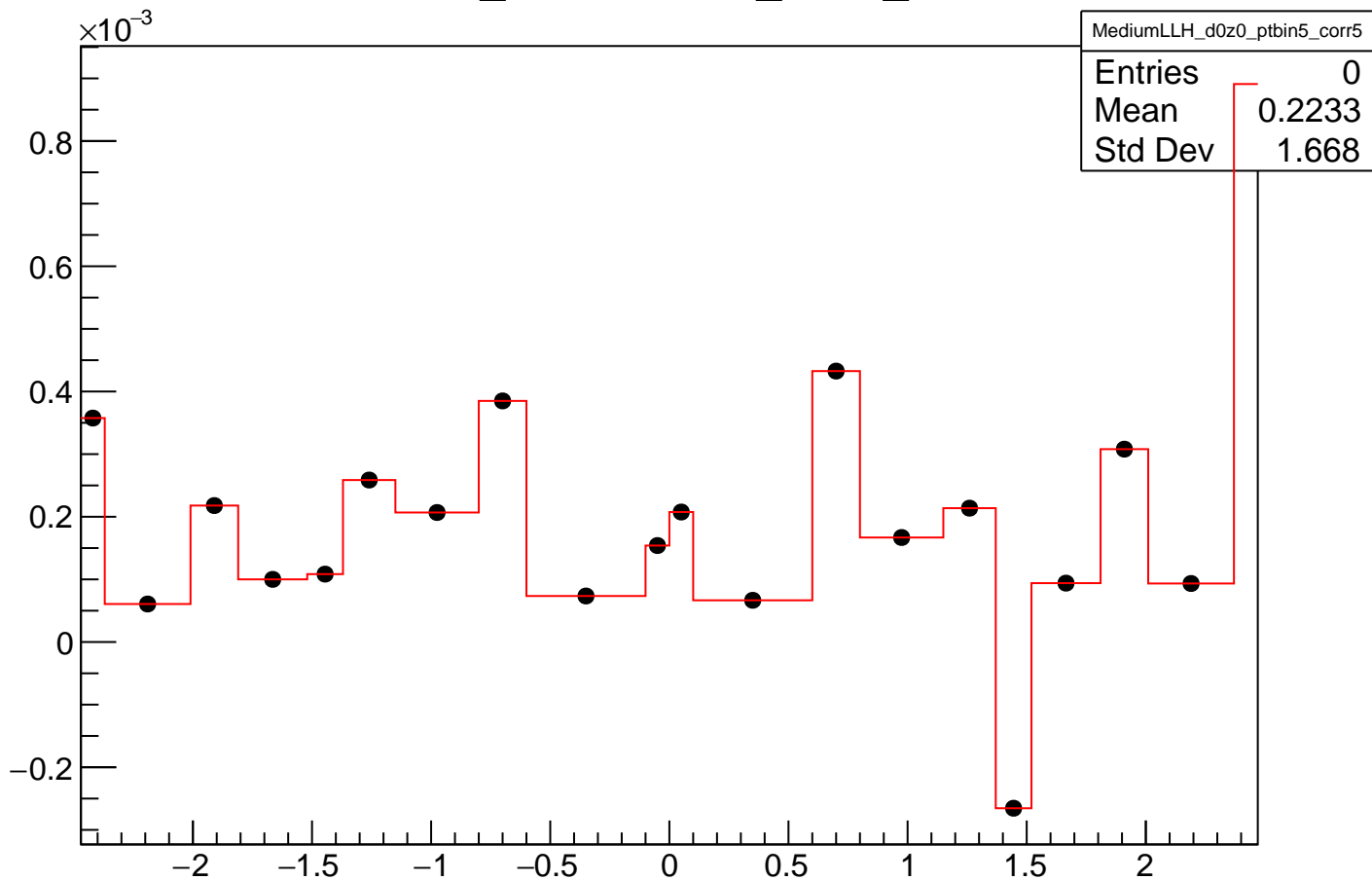
# FullSim\_MediumLLH\_d0z0\_corr5



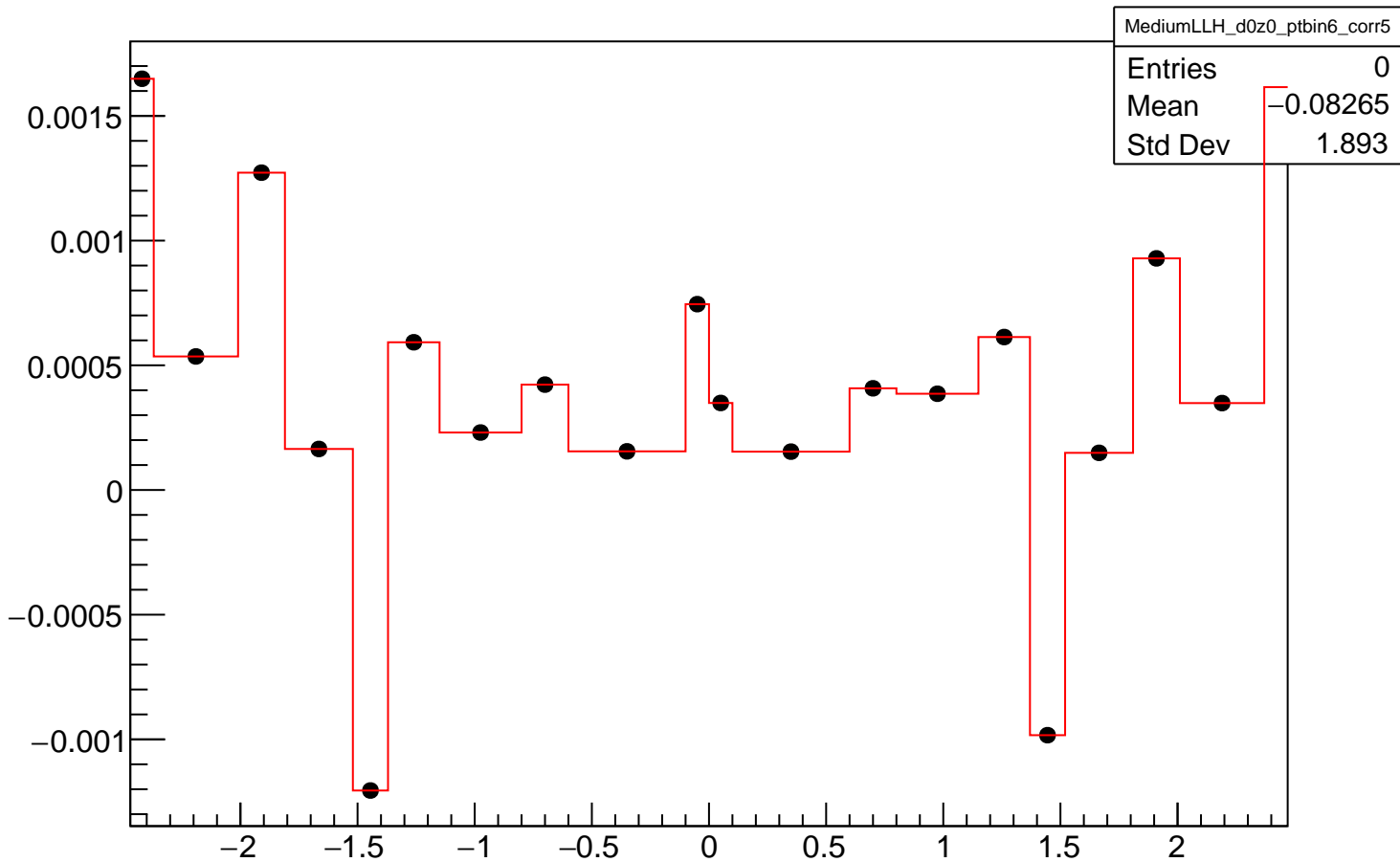
# FullSim\_MediumLLH\_d0z0\_corr5



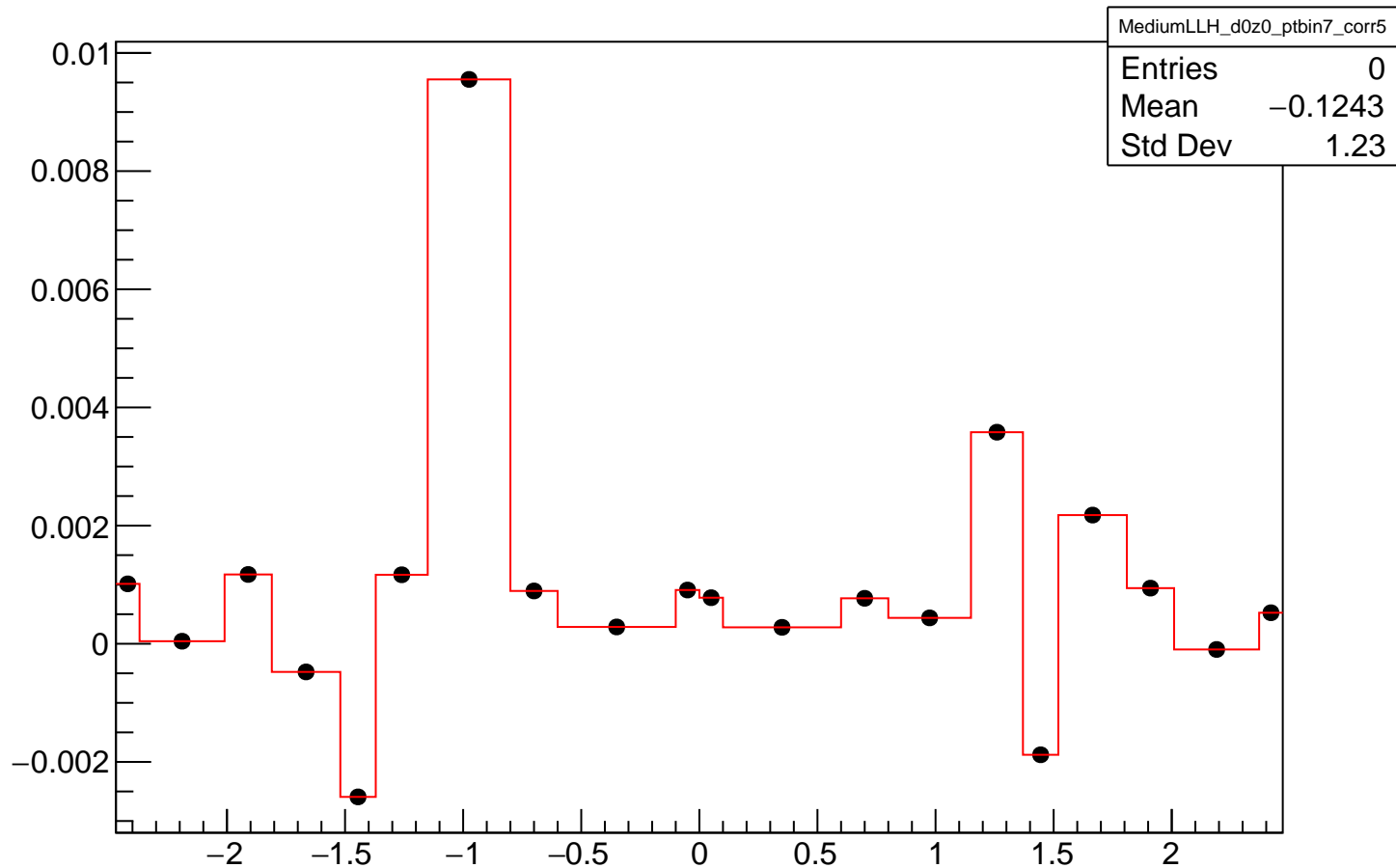
# FullSim\_MediumLLH\_d0z0\_corr5



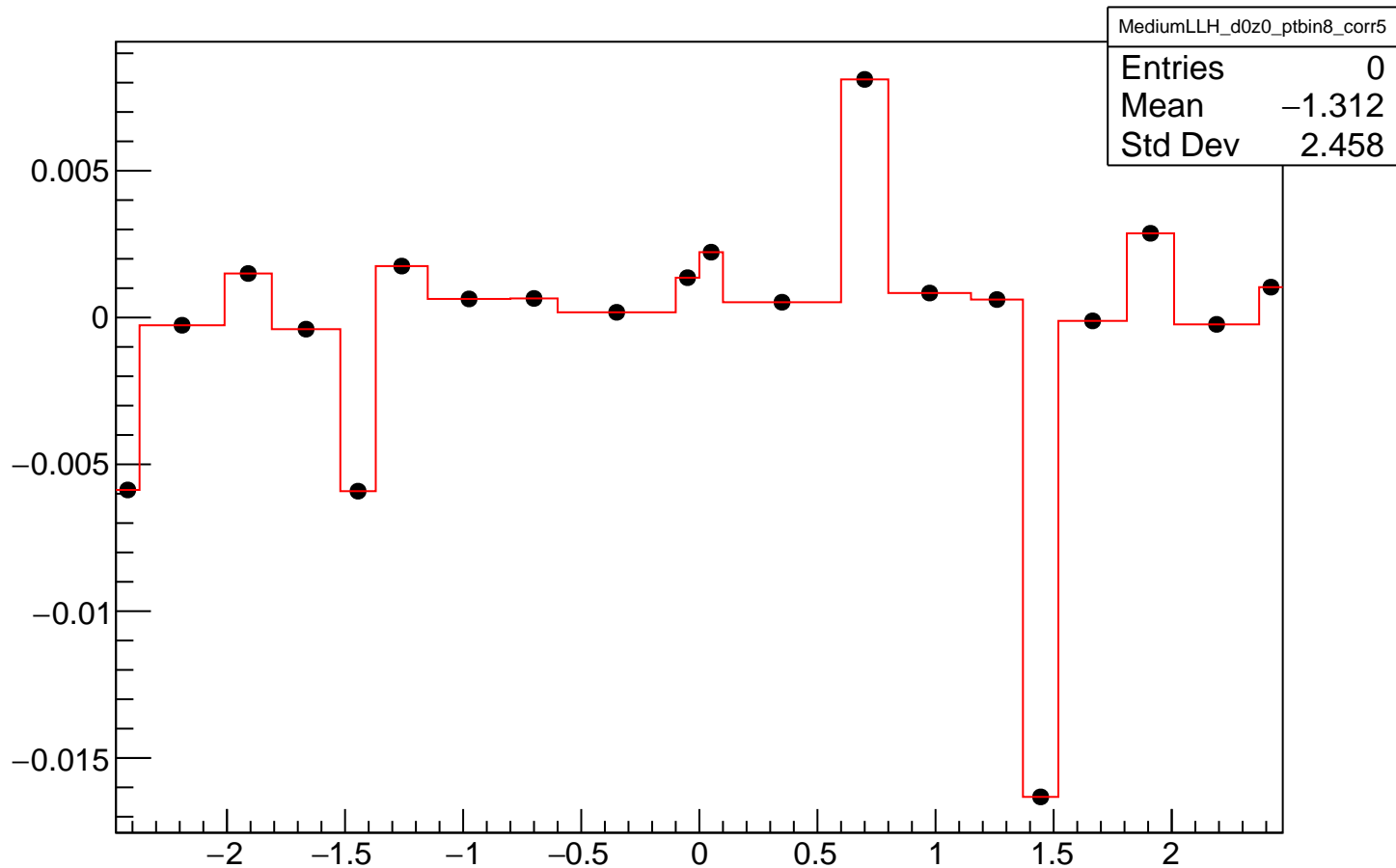
# FullSim\_MediumLLH\_d0z0\_corr5



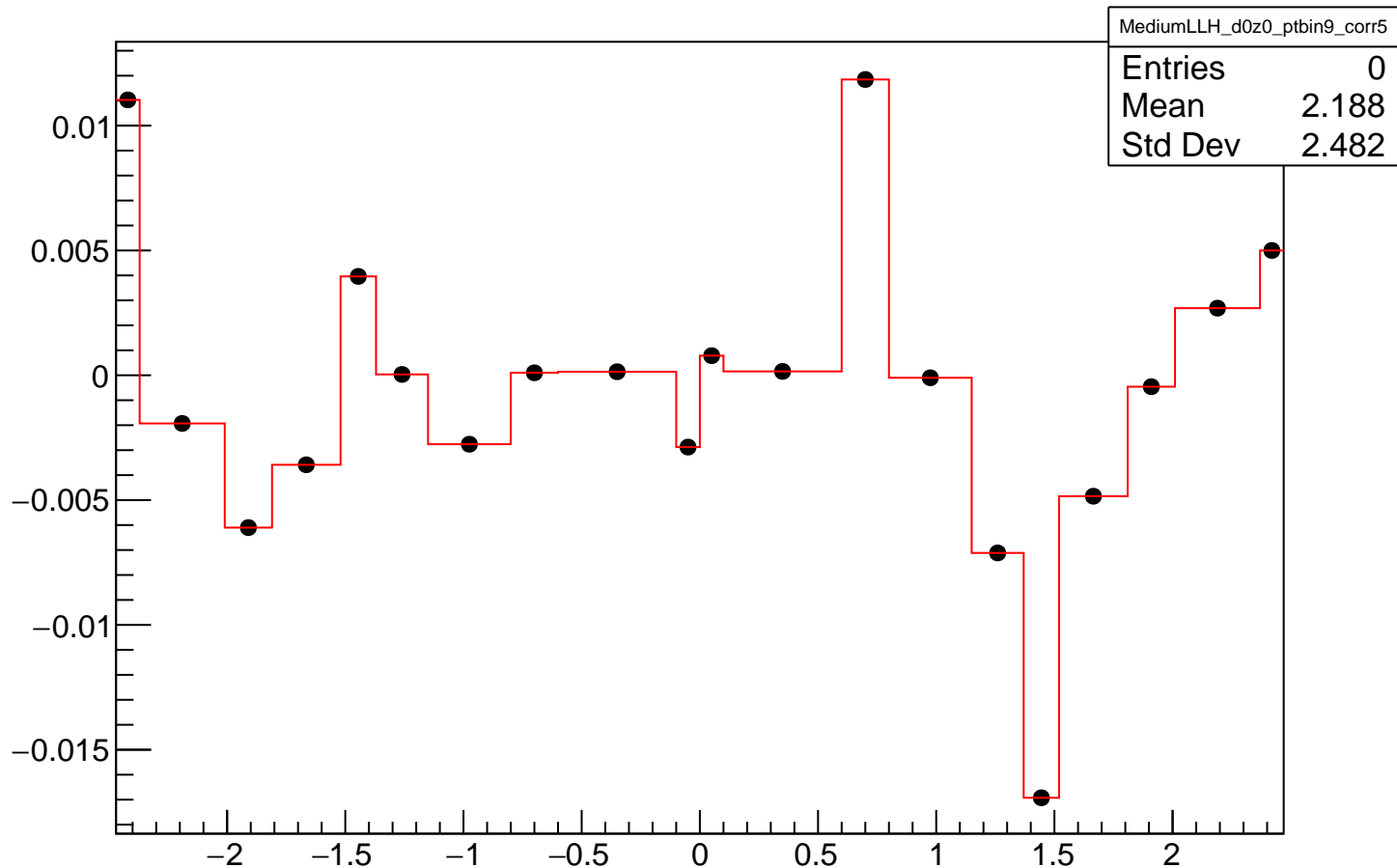
# FullSim\_MediumLLH\_d0z0\_corr5



# FullSim\_MediumLLH\_d0z0\_corr5

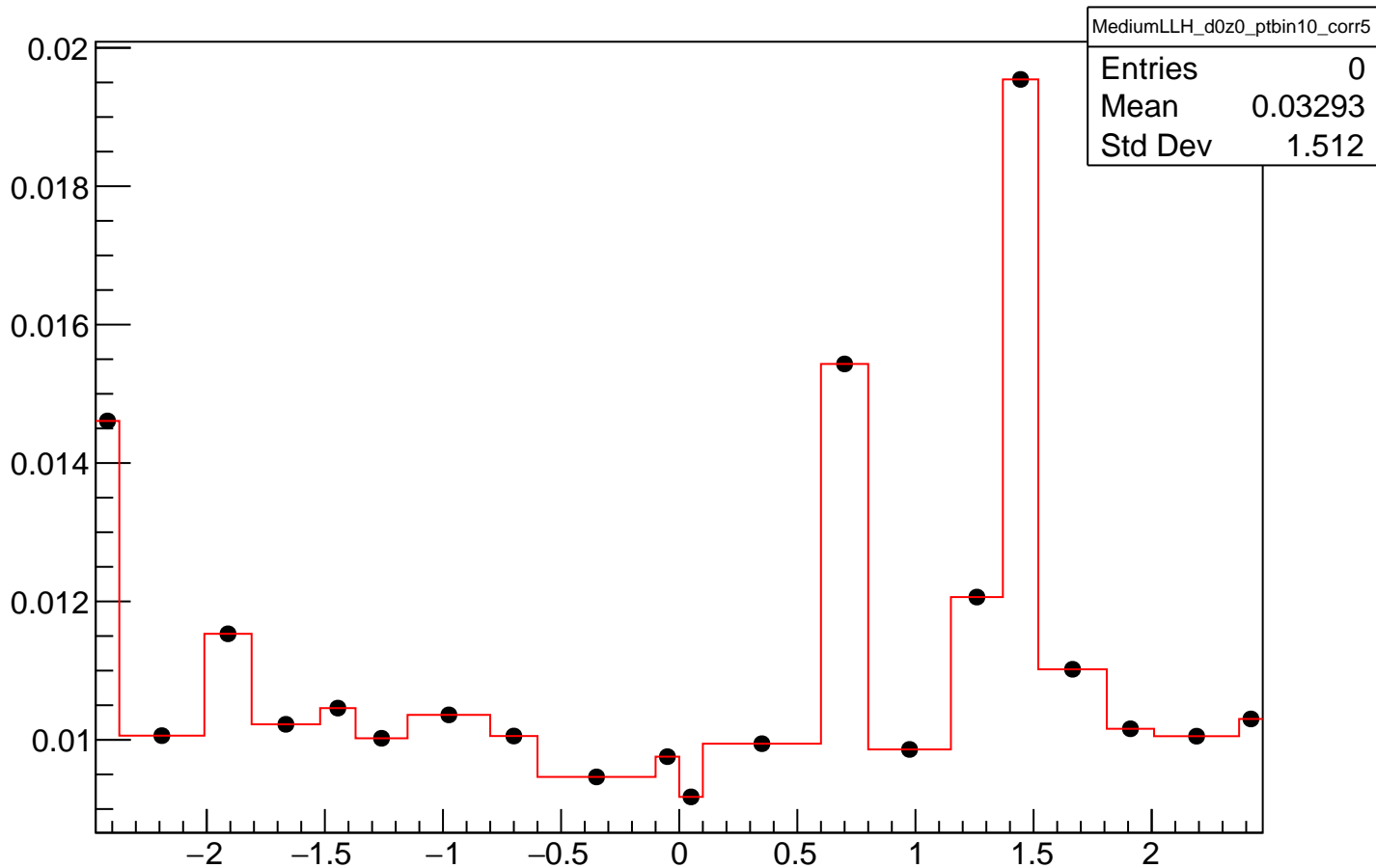


# FullSim\_MediumLLH\_d0z0\_corr5

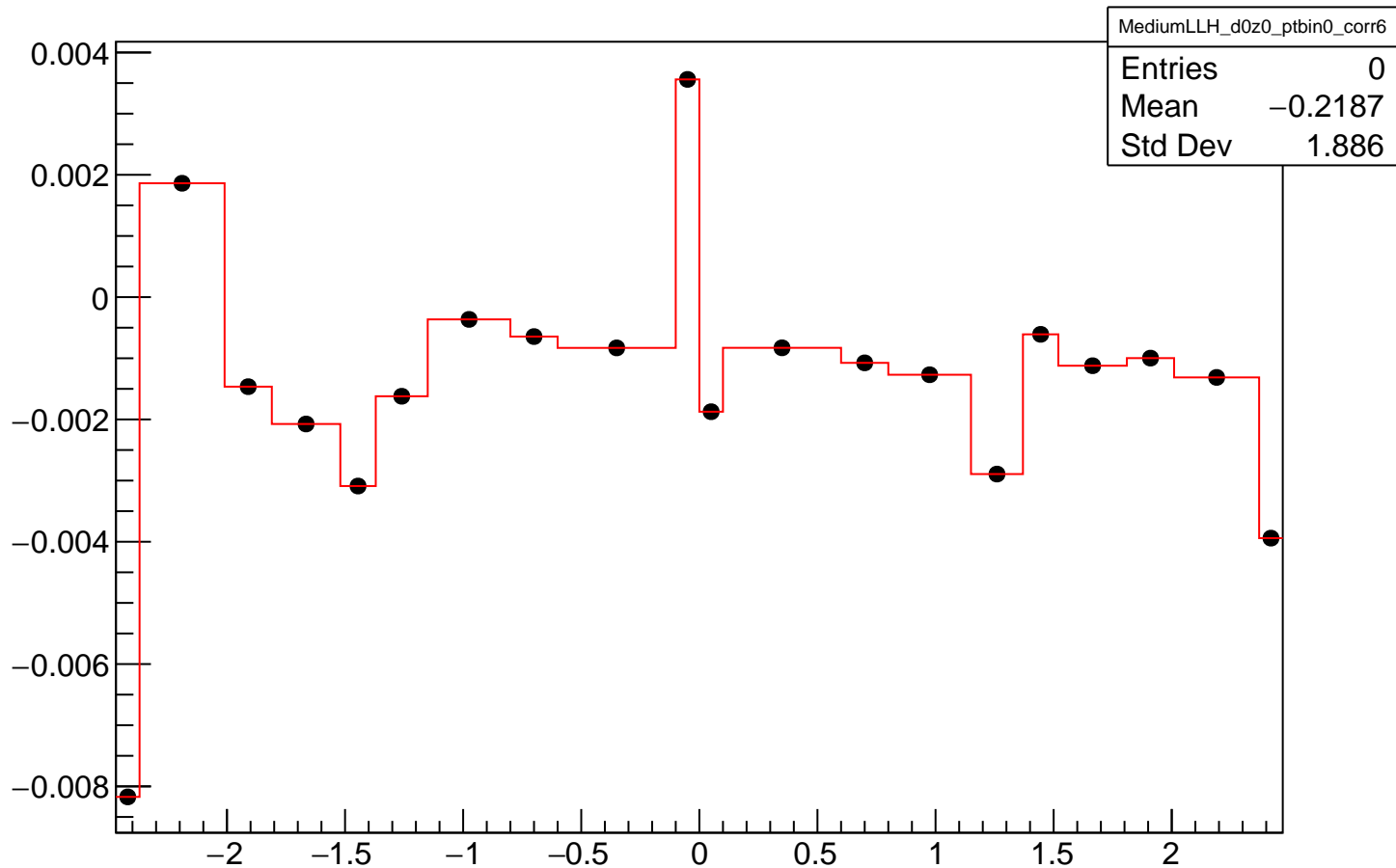




# FullSim\_MediumLLH\_d0z0\_corr5

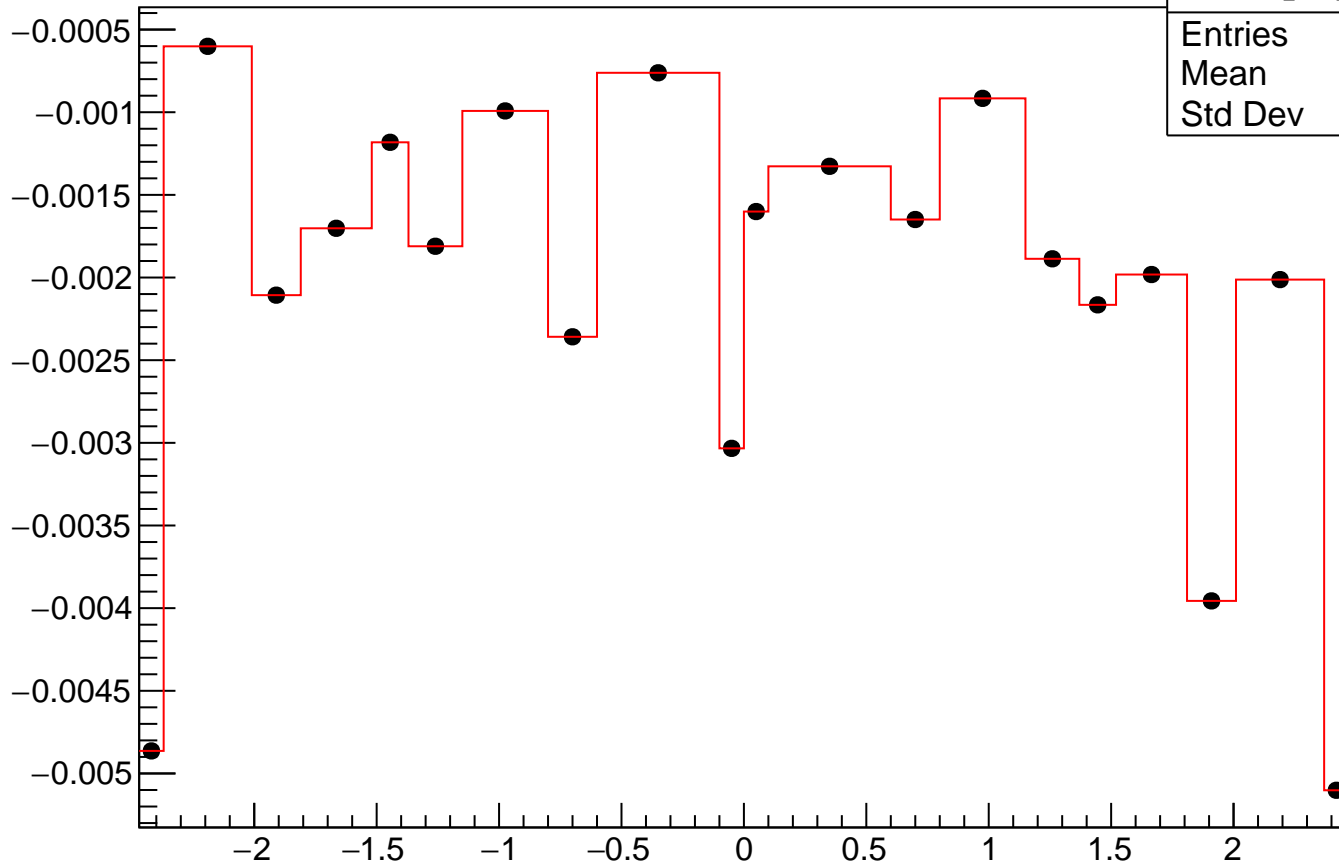


# FullSim\_MediumLLH\_d0z0\_corr6

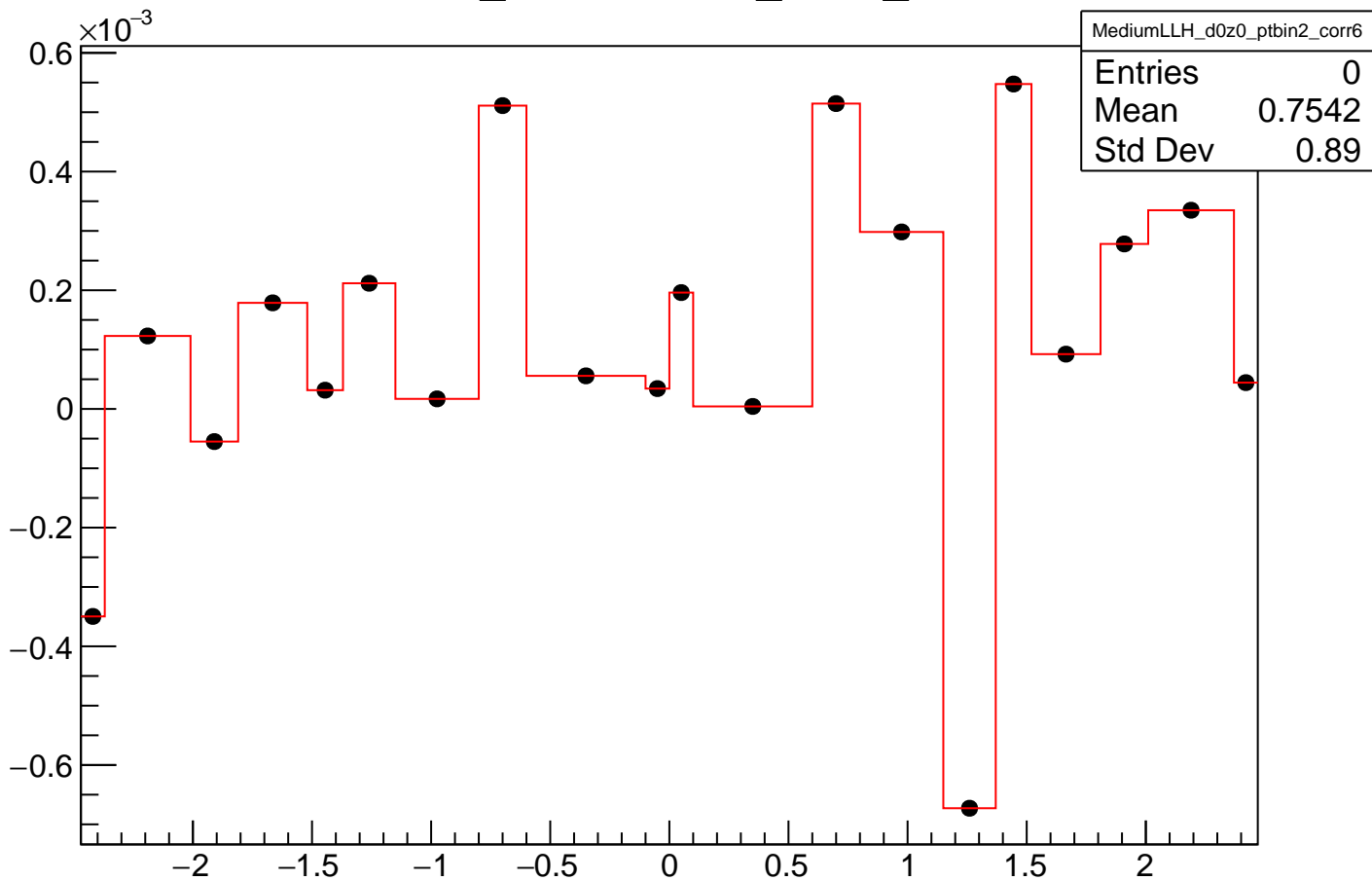


# FullSim\_MediumLLH\_d0z0\_corr6

MediumLLH_d0z0_ptbin1_corr6	
Entries	0
Mean	0.208
Std Dev	1.678



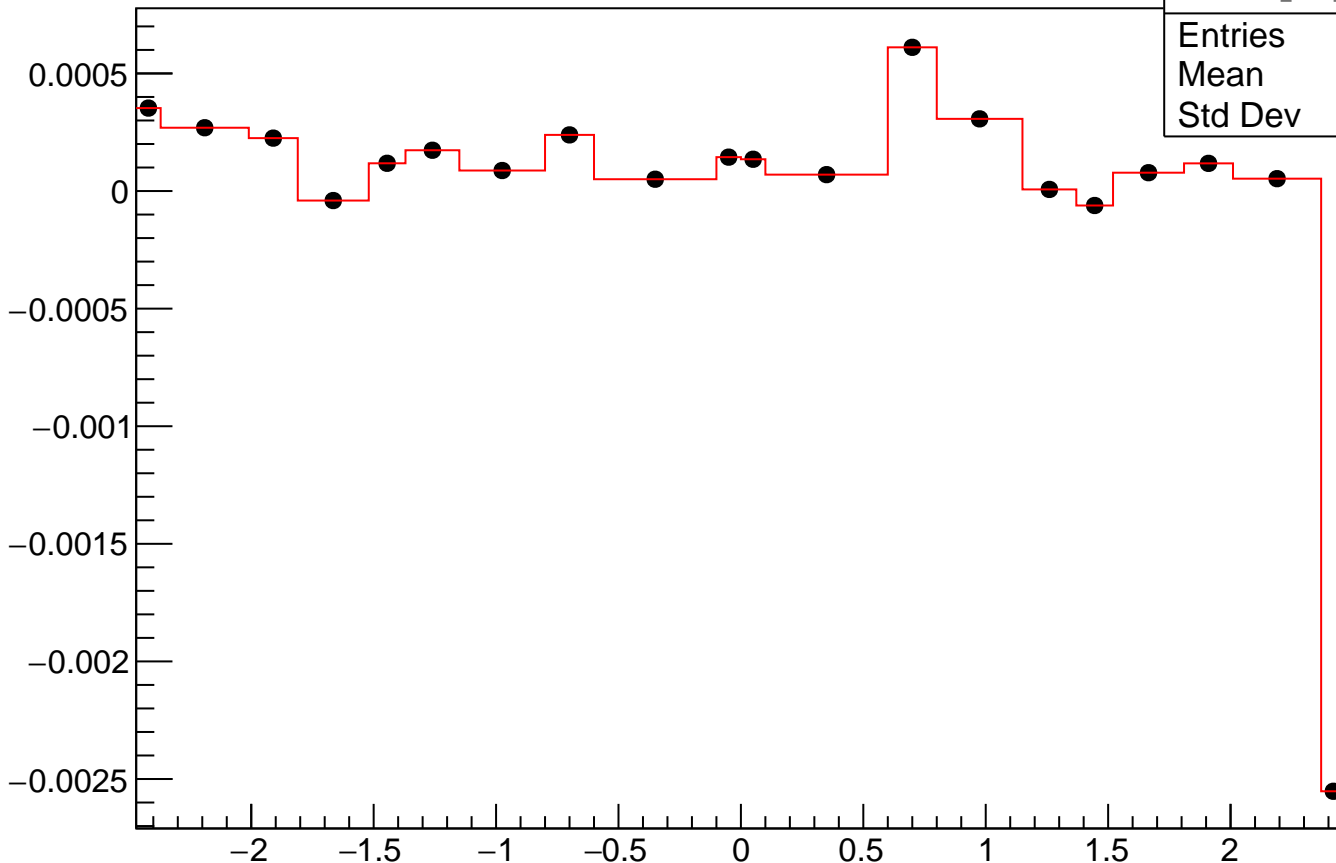
# FullSim\_MediumLLH\_d0z0\_corr6



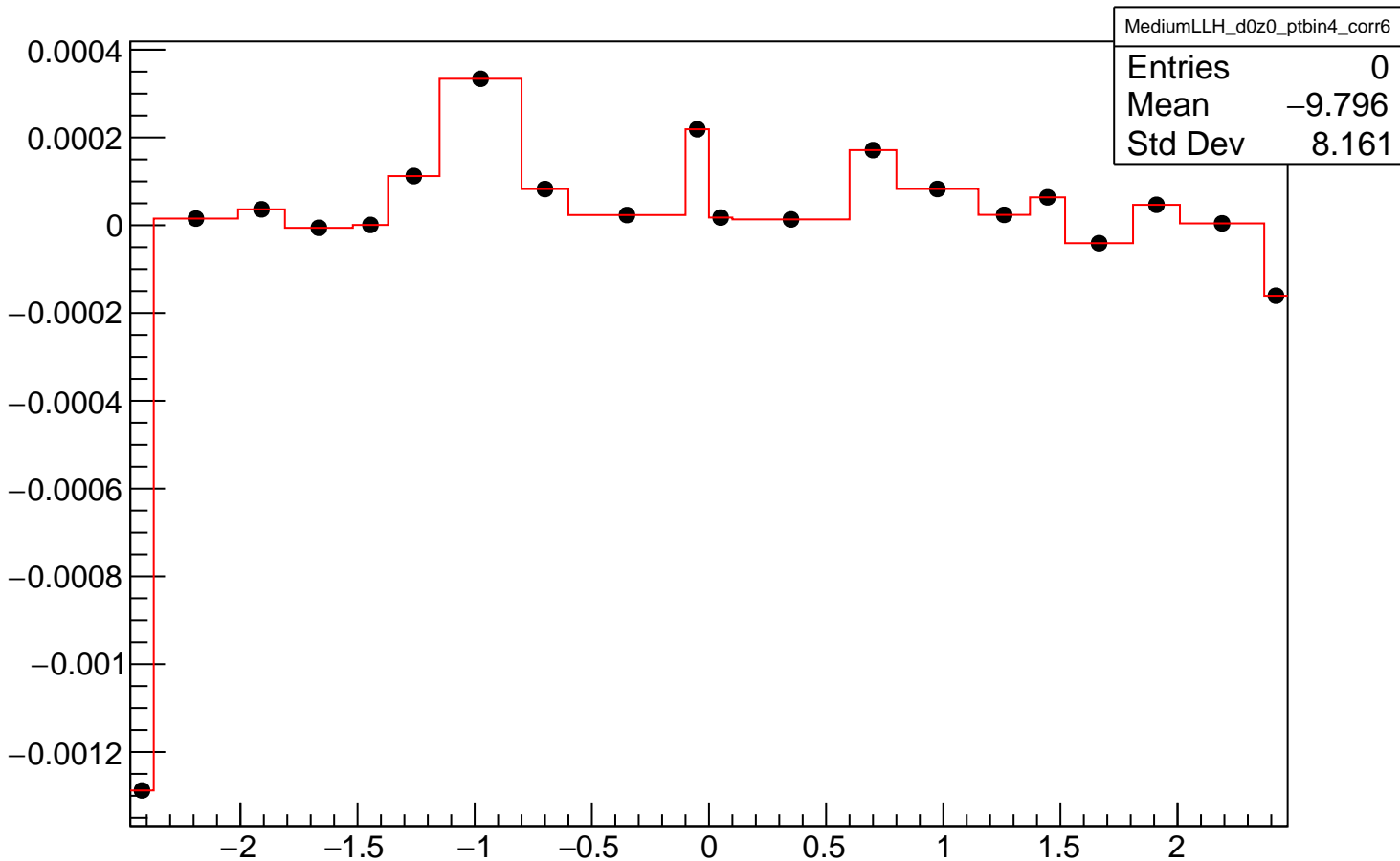
# FullSim\_MediumLLH\_d0z0\_corr6

MediumLLH\_d0z0\_ptbin3\_corr6

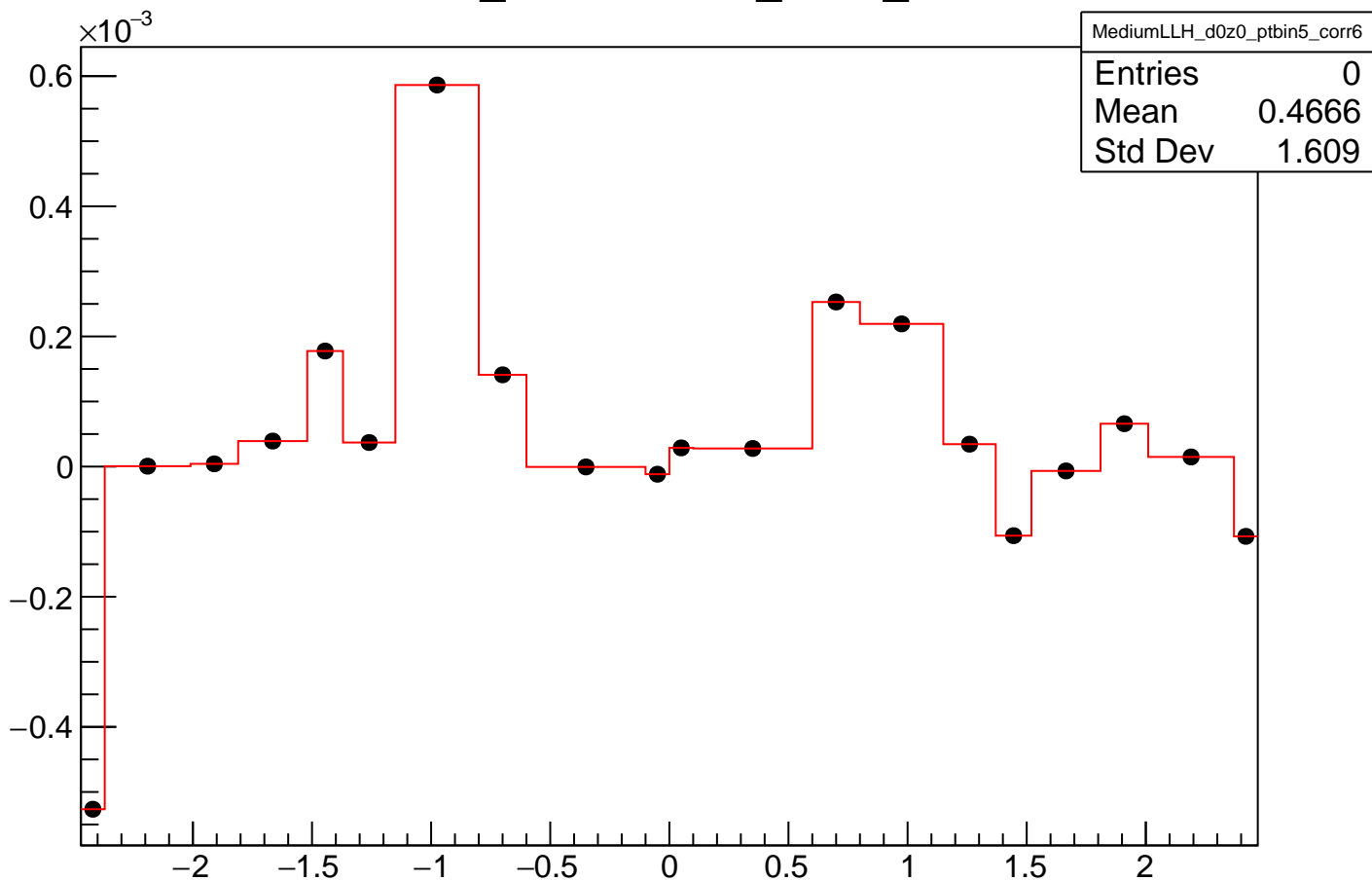
Entries	0
Mean	-19.6
Std Dev	20.18



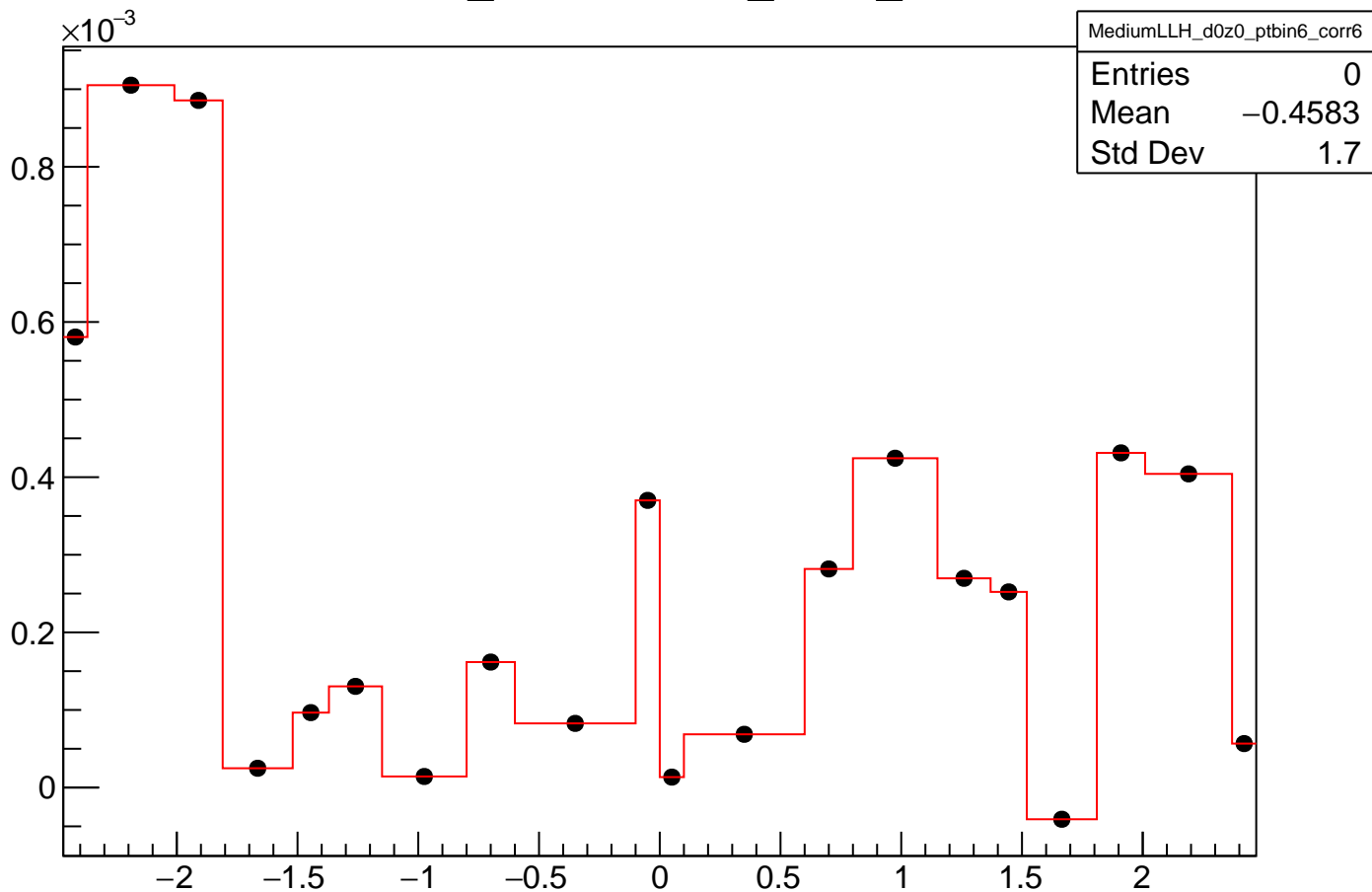
# FullSim\_MediumLLH\_d0z0\_corr6



# FullSim\_MediumLLH\_d0z0\_corr6

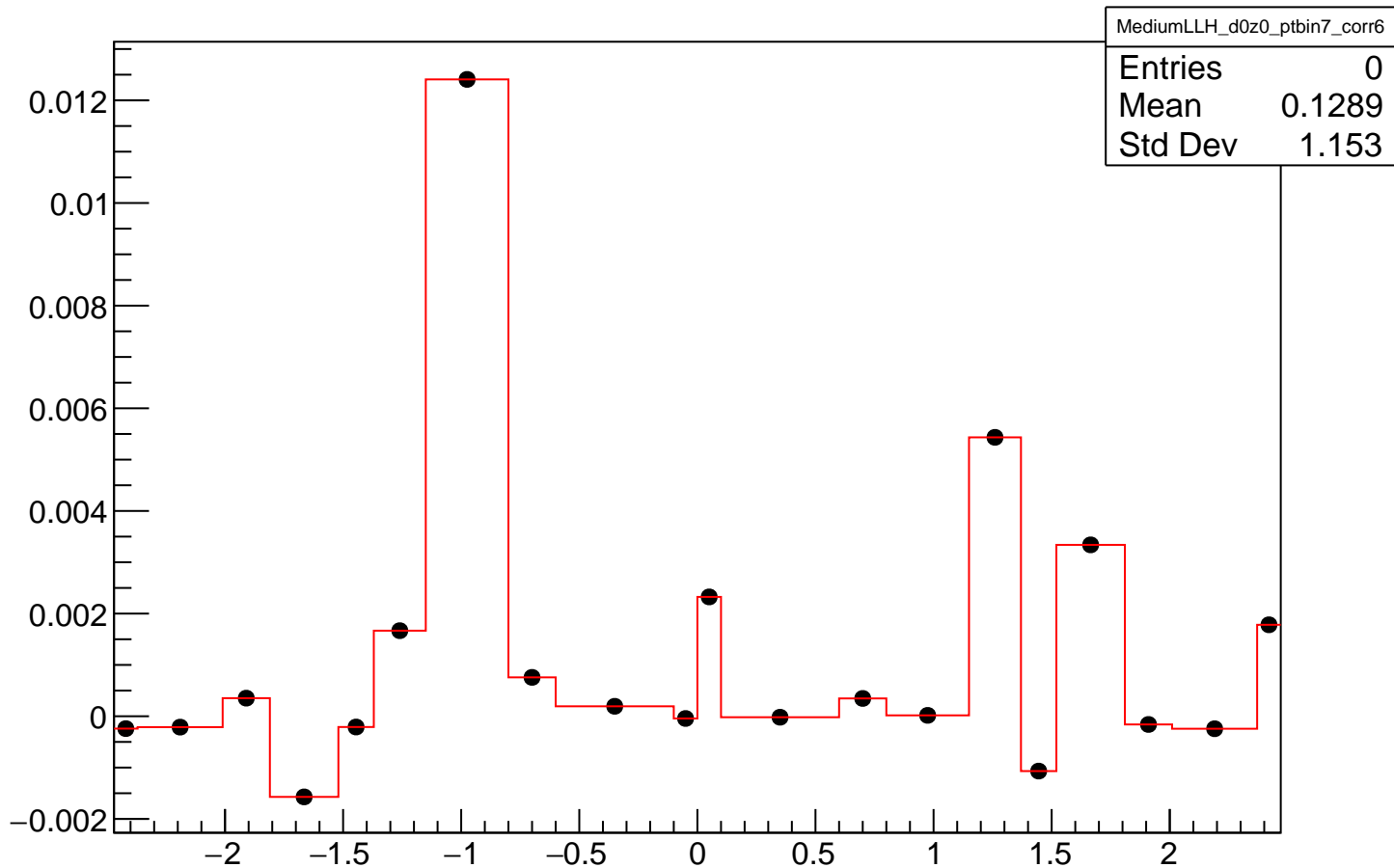


# FullSim\_MediumLLH\_d0z0\_corr6

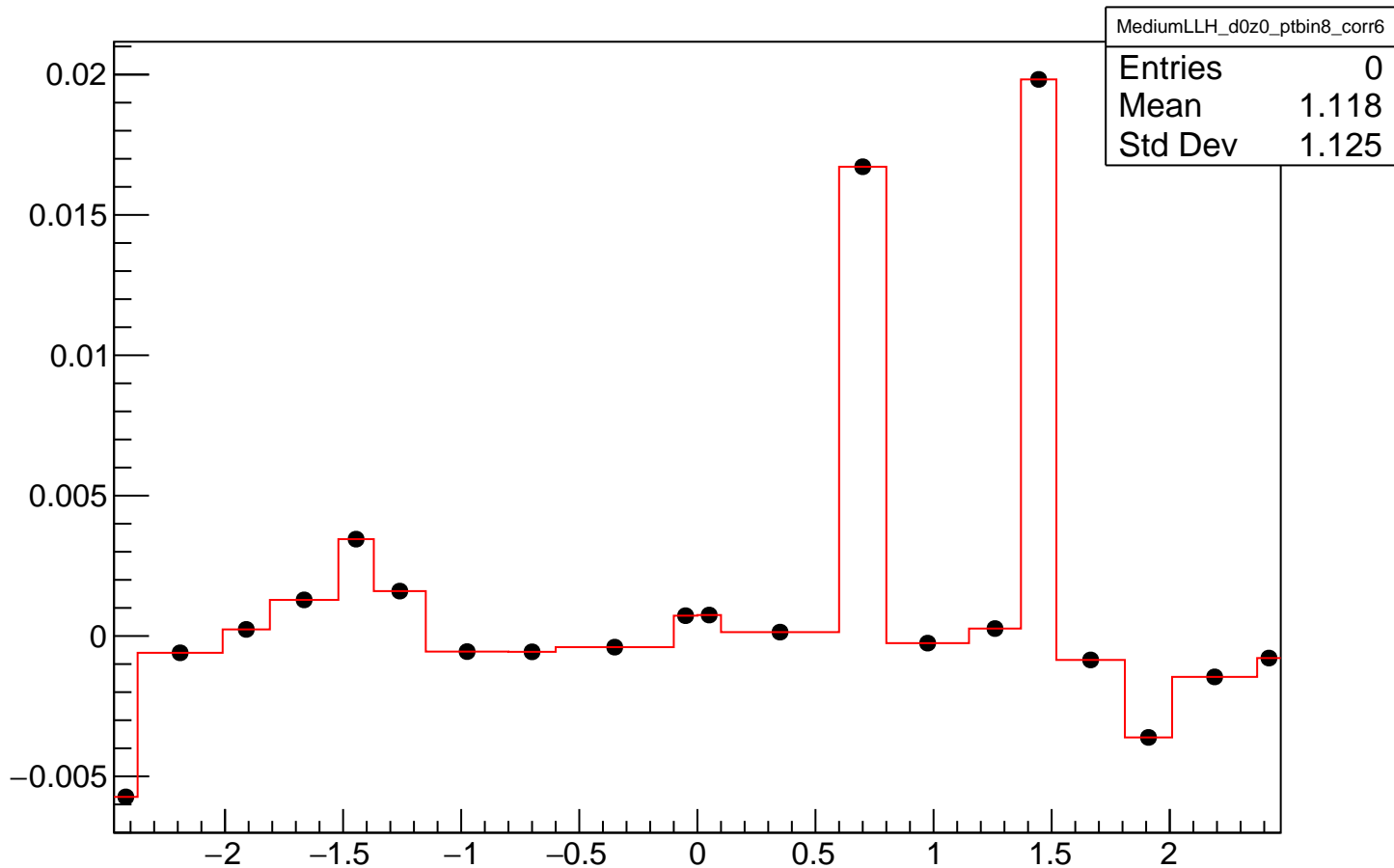




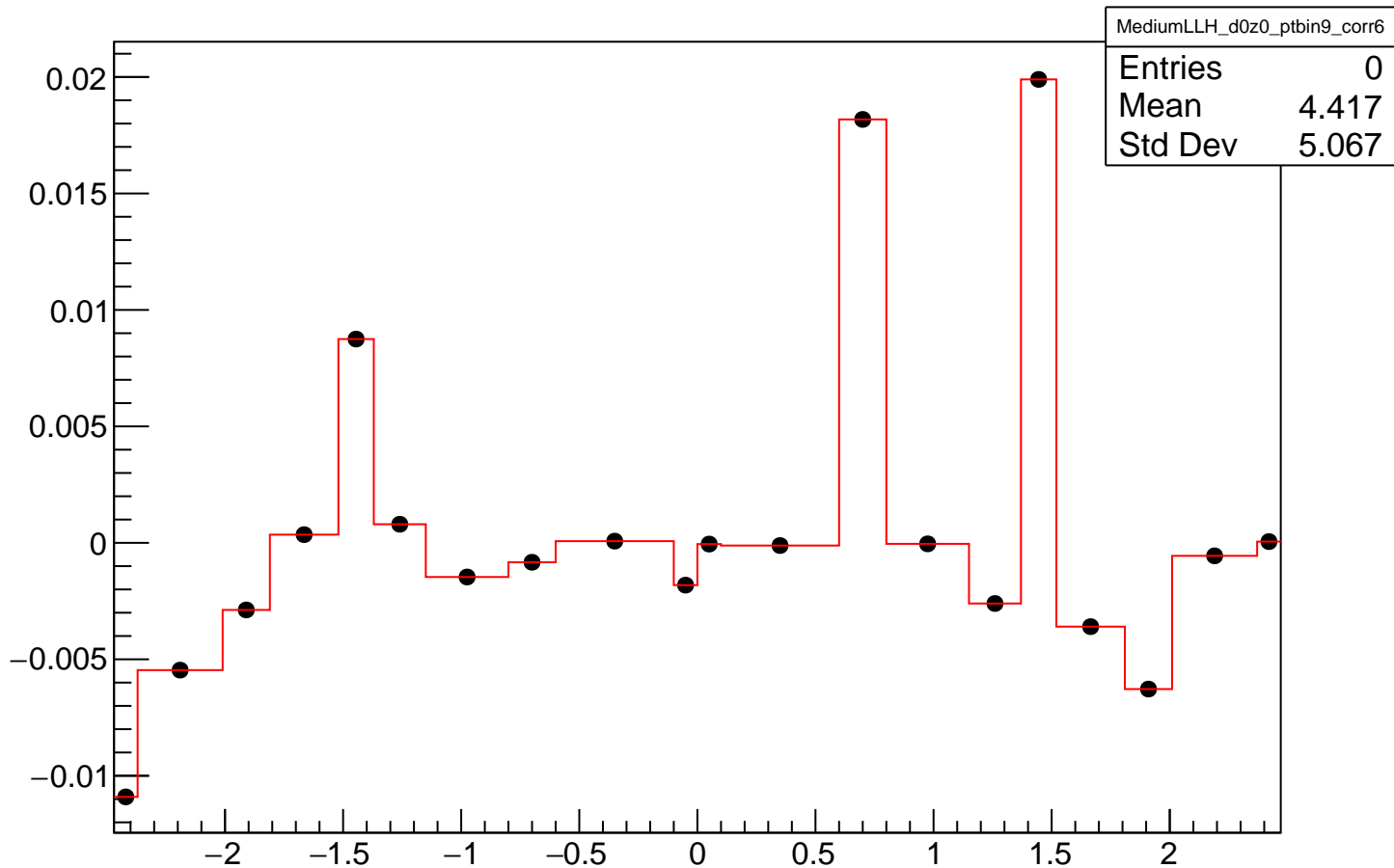
# FullSim\_MediumLLH\_d0z0\_corr6



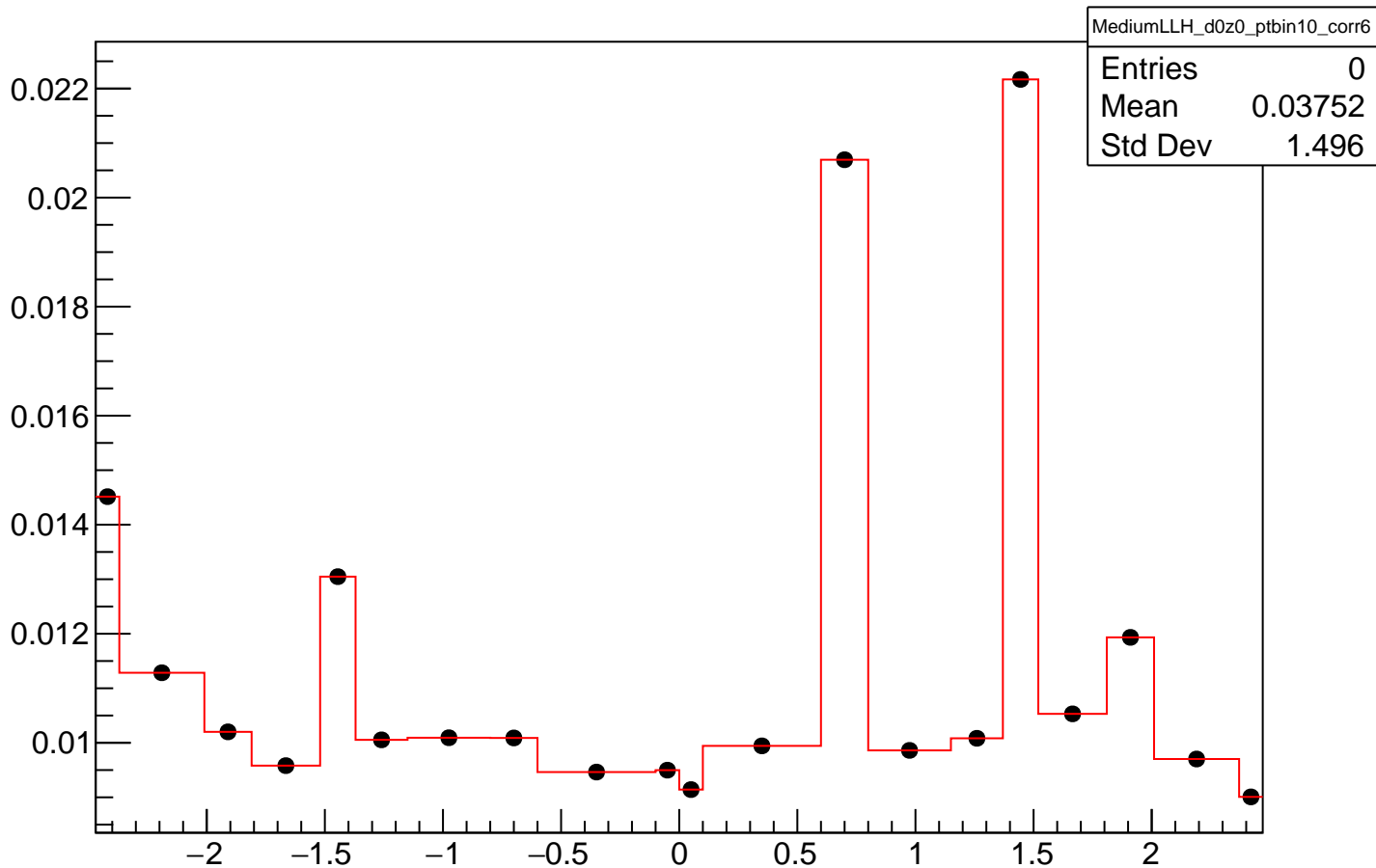
# FullSim\_MediumLLH\_d0z0\_corr6



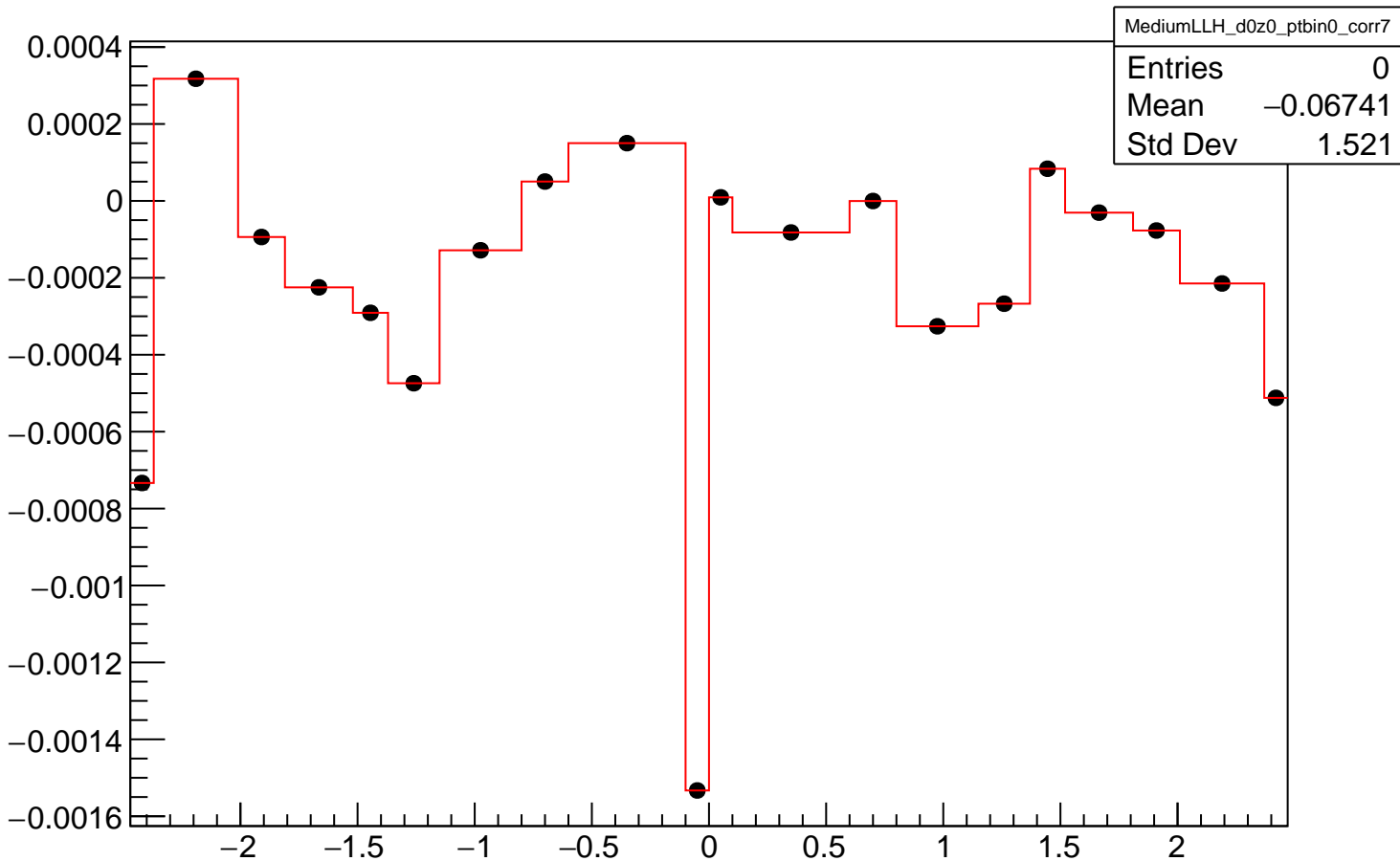
# FullSim\_MediumLLH\_d0z0\_corr6



# FullSim\_MediumLLH\_d0z0\_corr6

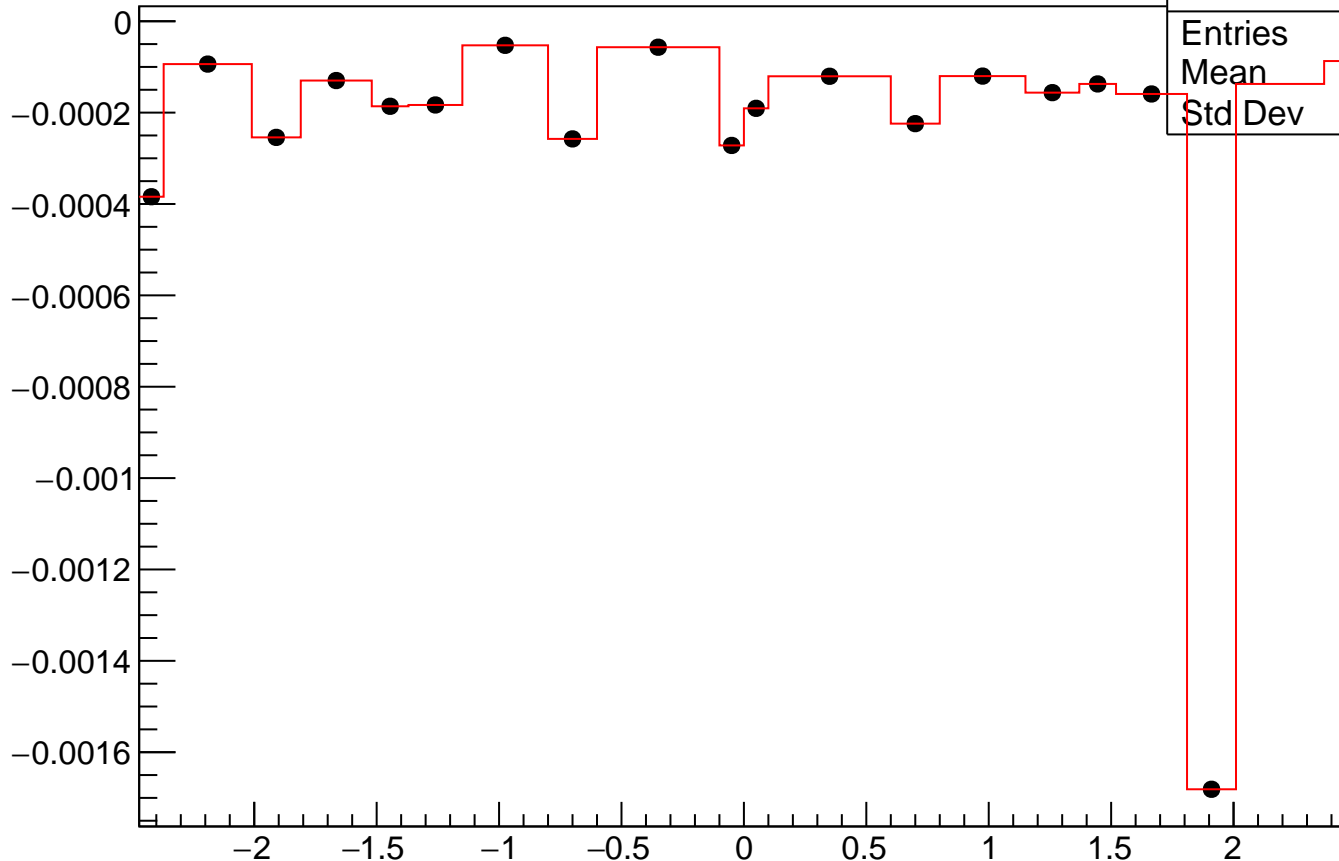


# FullSim\_MediumLLH\_d0z0\_corr7

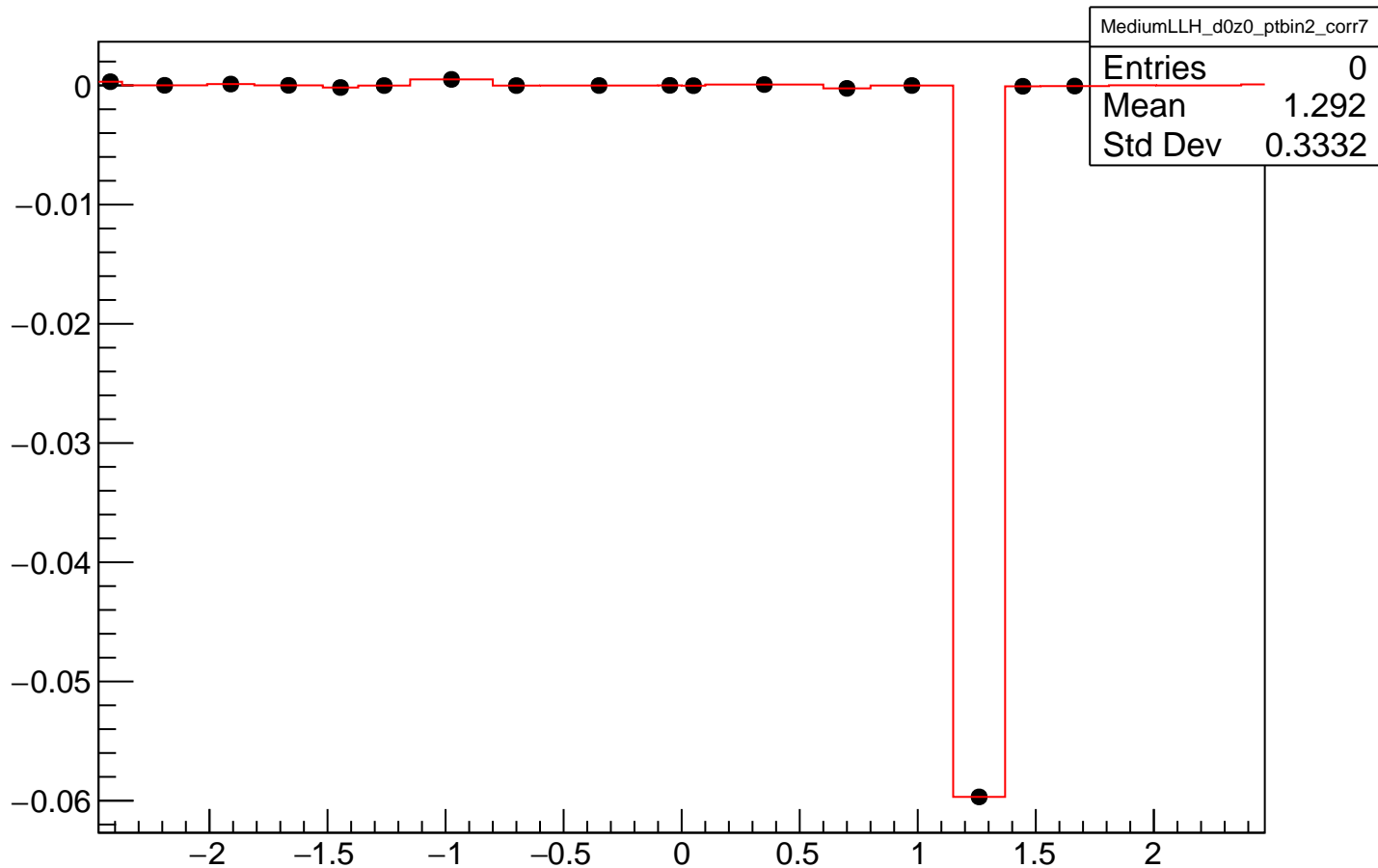


# FullSim\_MediumLLH\_d0z0\_corr7

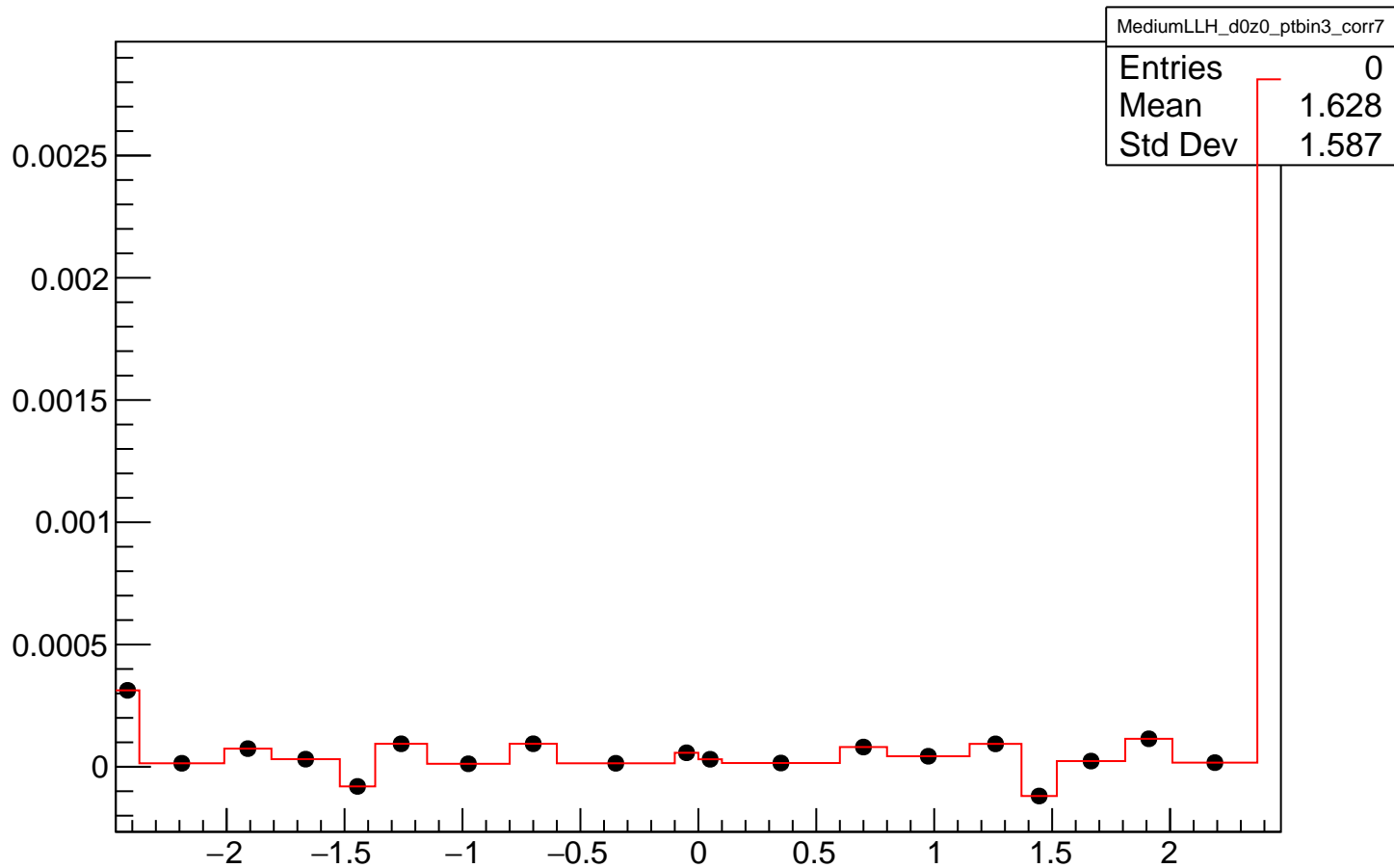
MediumLLH_d0z0_ptbin1_corr7	
Entries	0
Mean	0.4313
Std Dev	1.597



# FullSim\_MediumLLH\_d0z0\_corr7

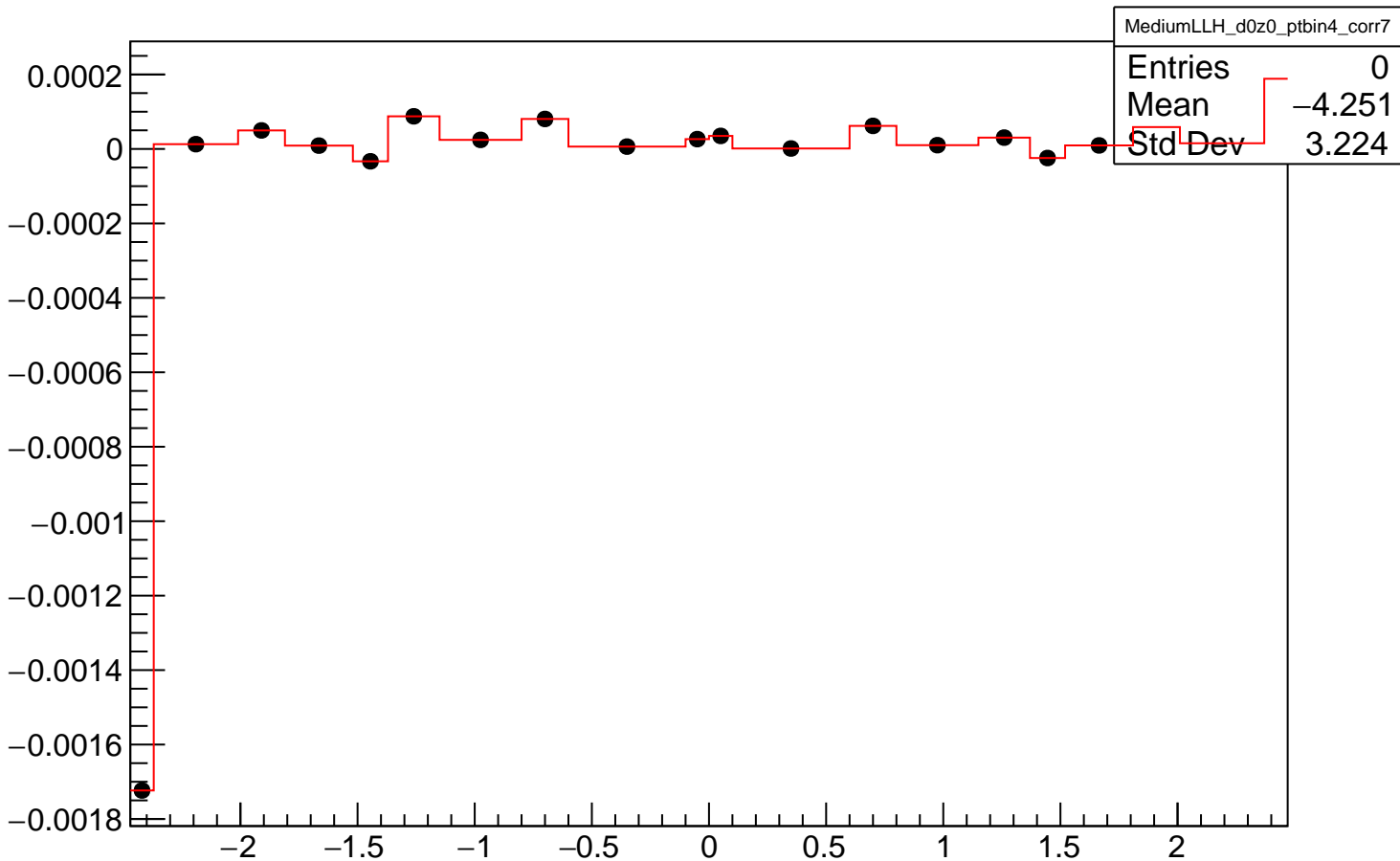


# FullSim\_MediumLLH\_d0z0\_corr7

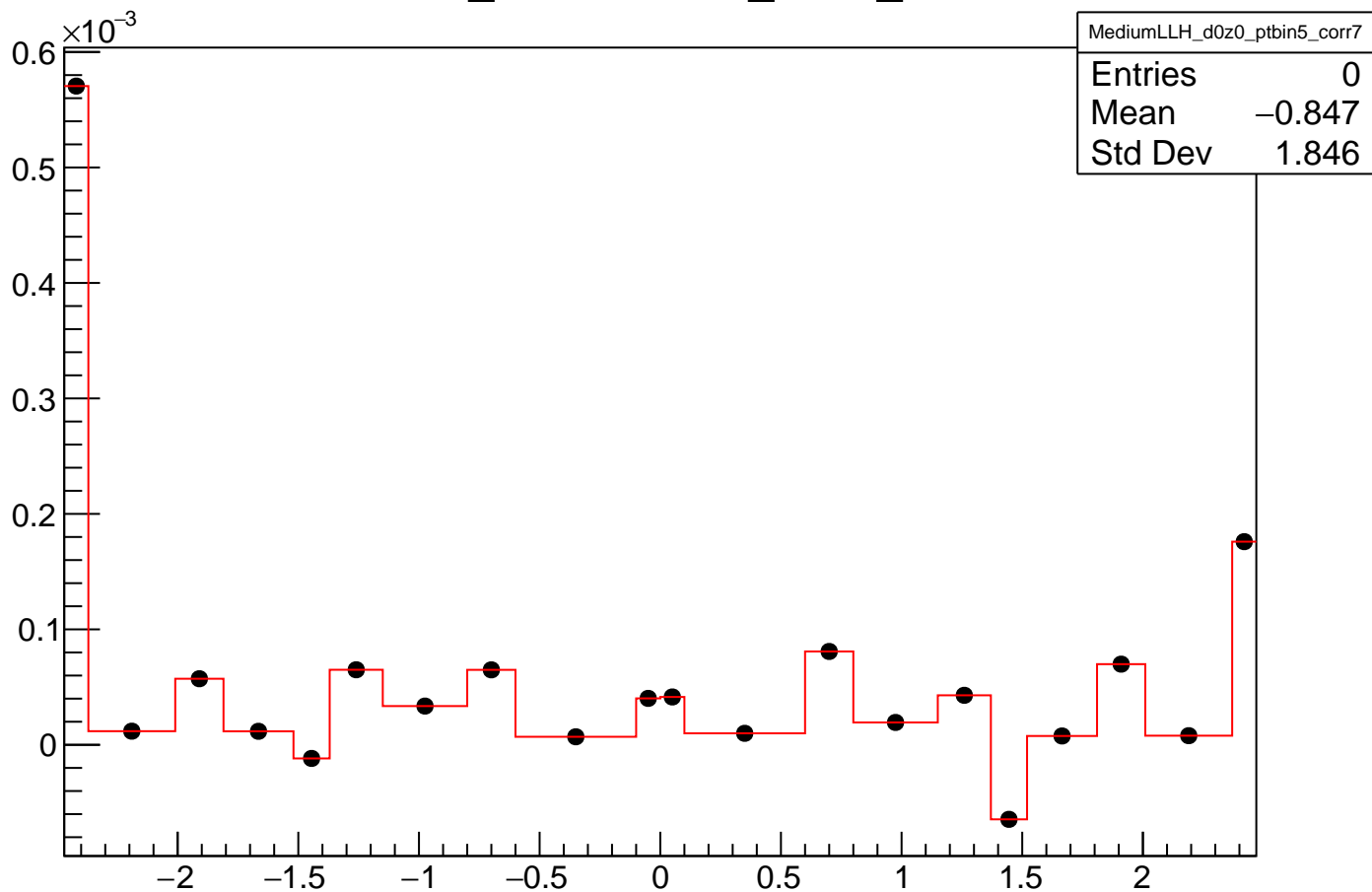




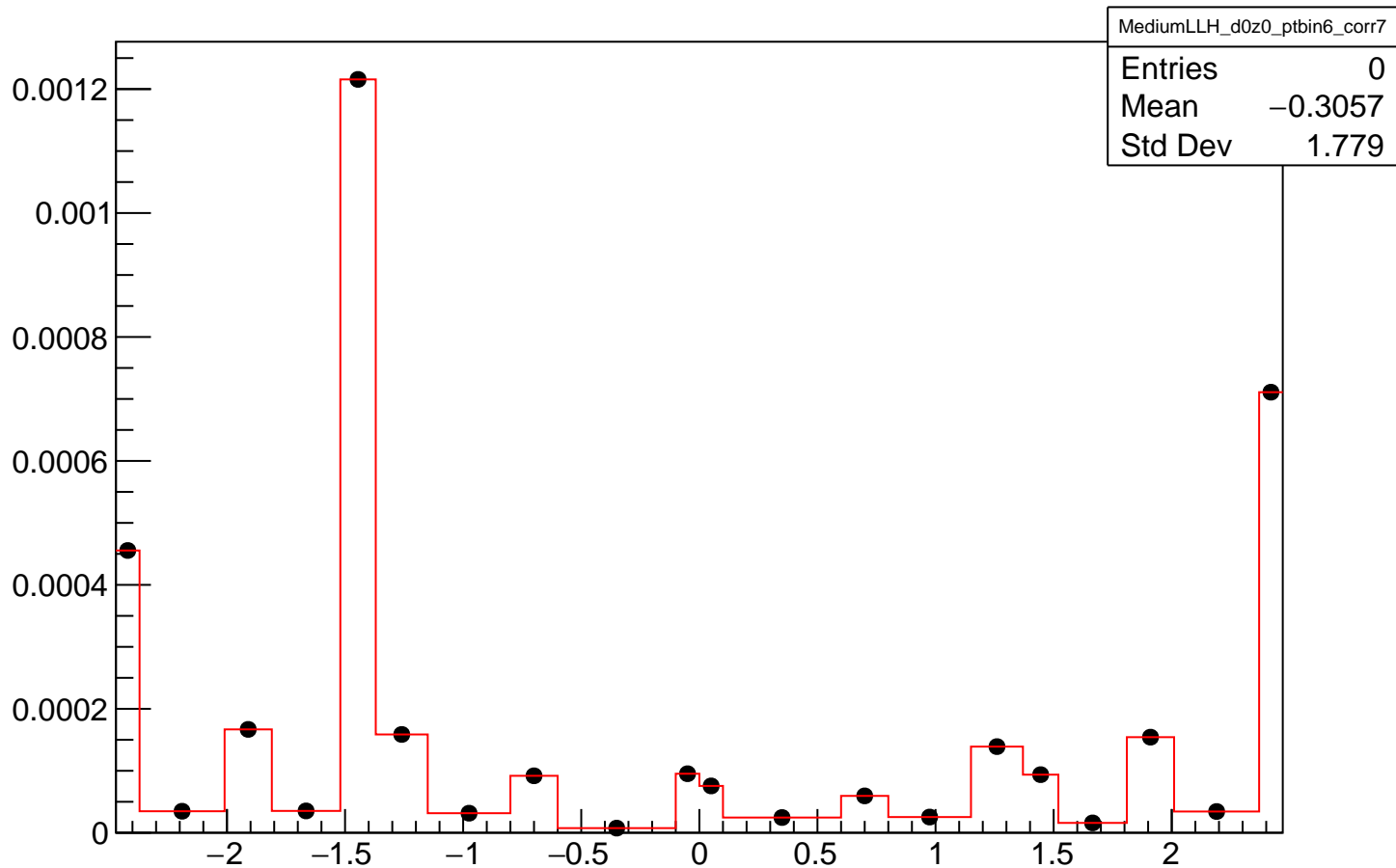
# FullSim\_MediumLLH\_d0z0\_corr7



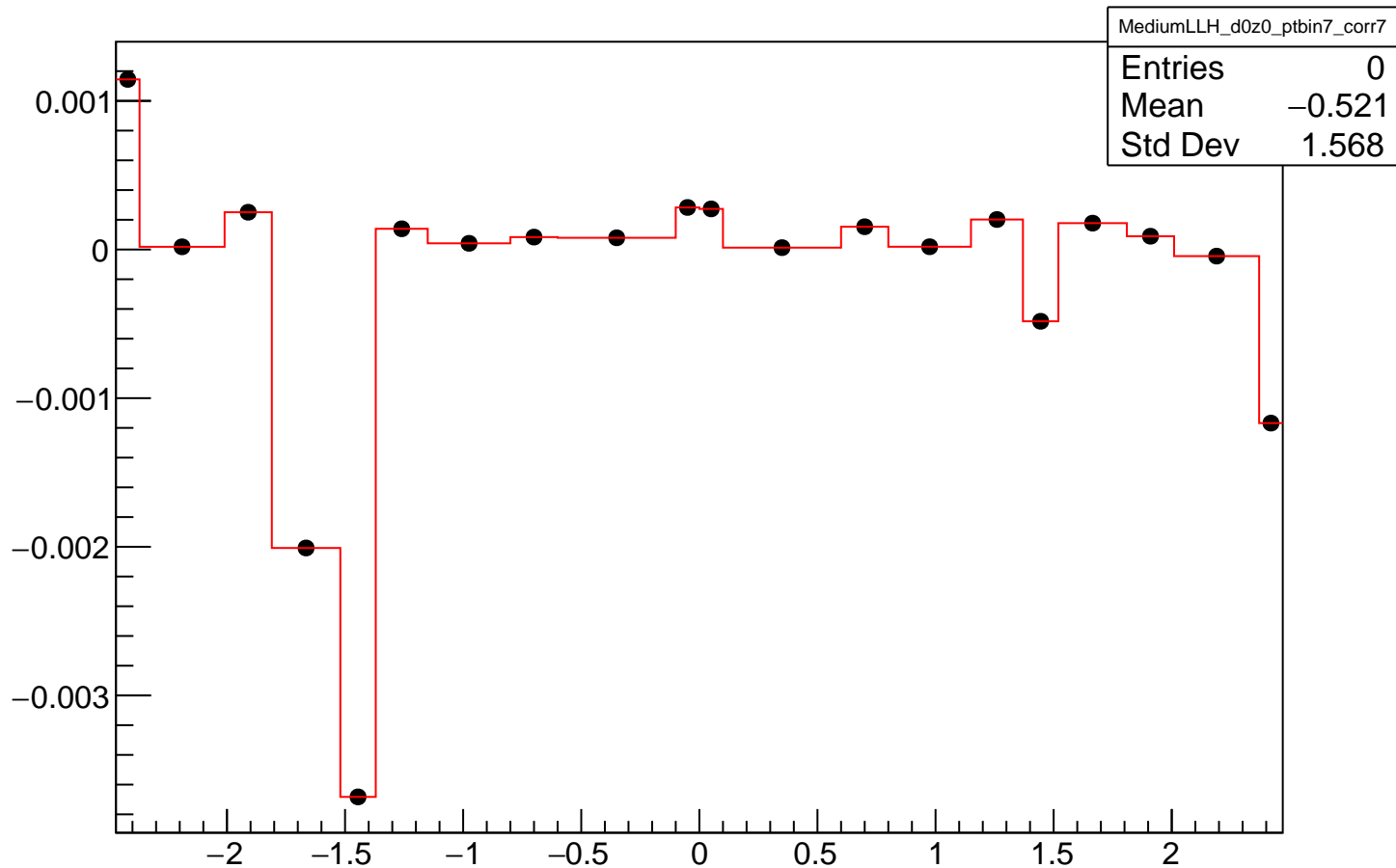
# FullSim\_MediumLLH\_d0z0\_corr7



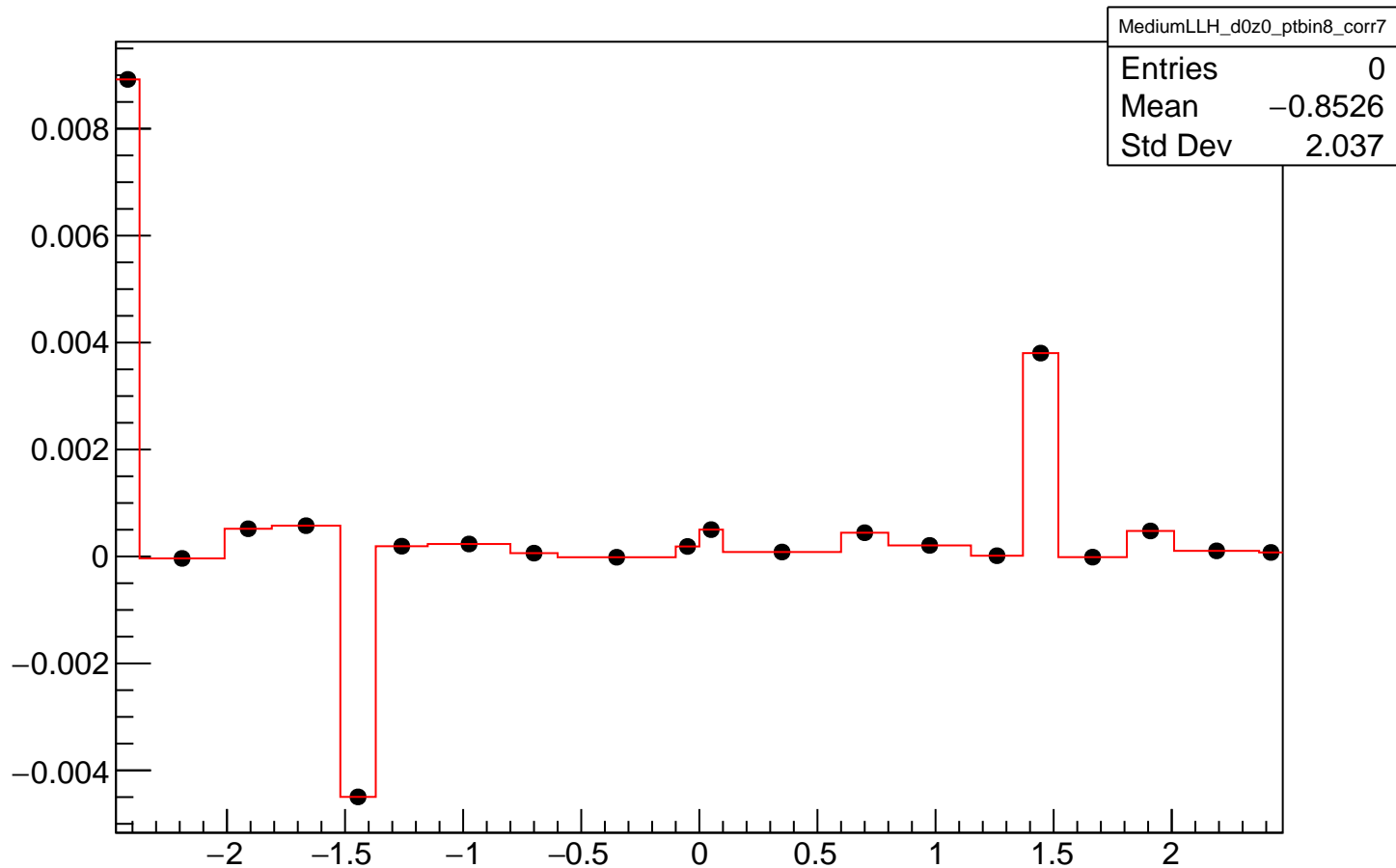
# FullSim\_MediumLLH\_d0z0\_corr7



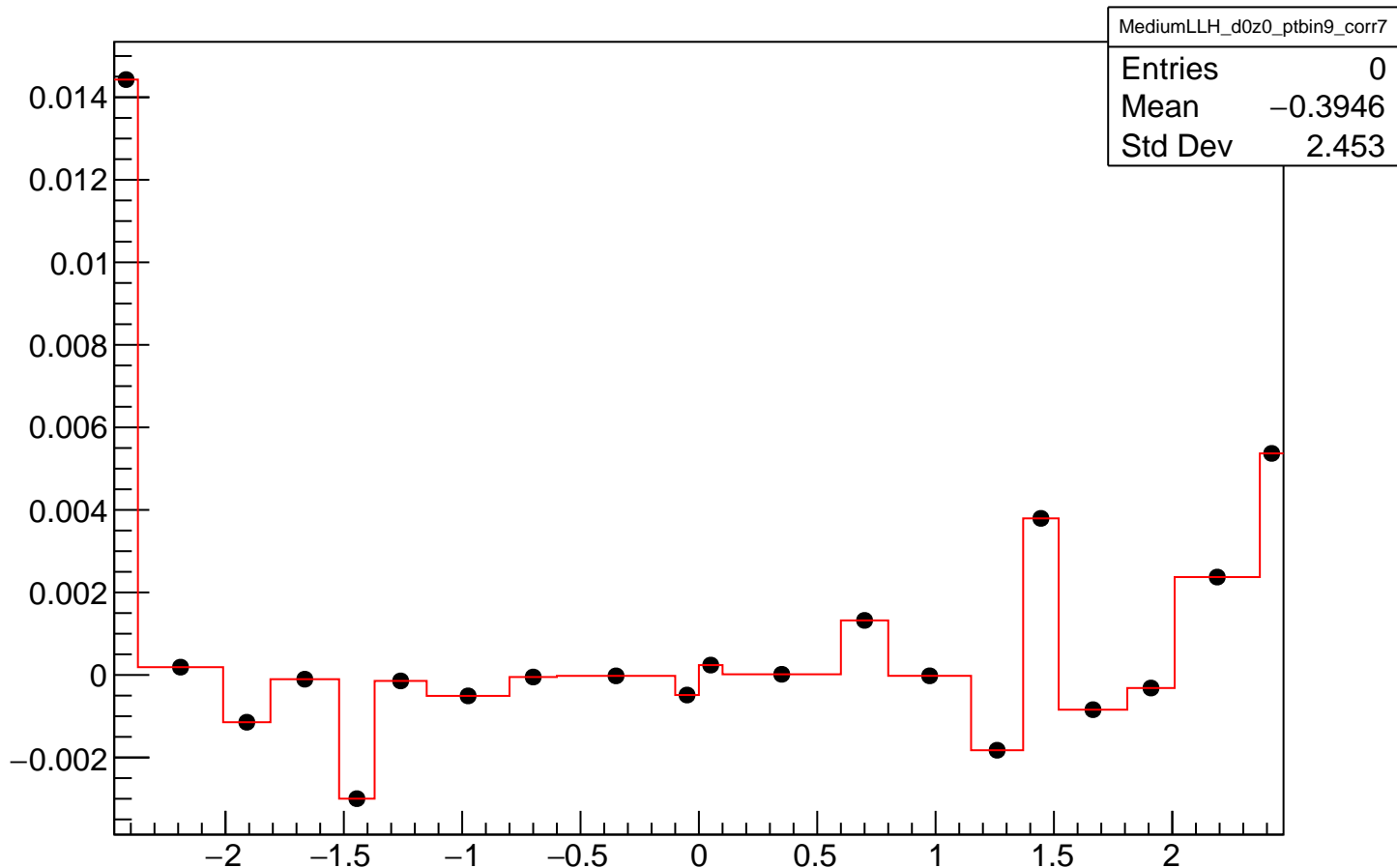
# FullSim\_MediumLLH\_d0z0\_corr7



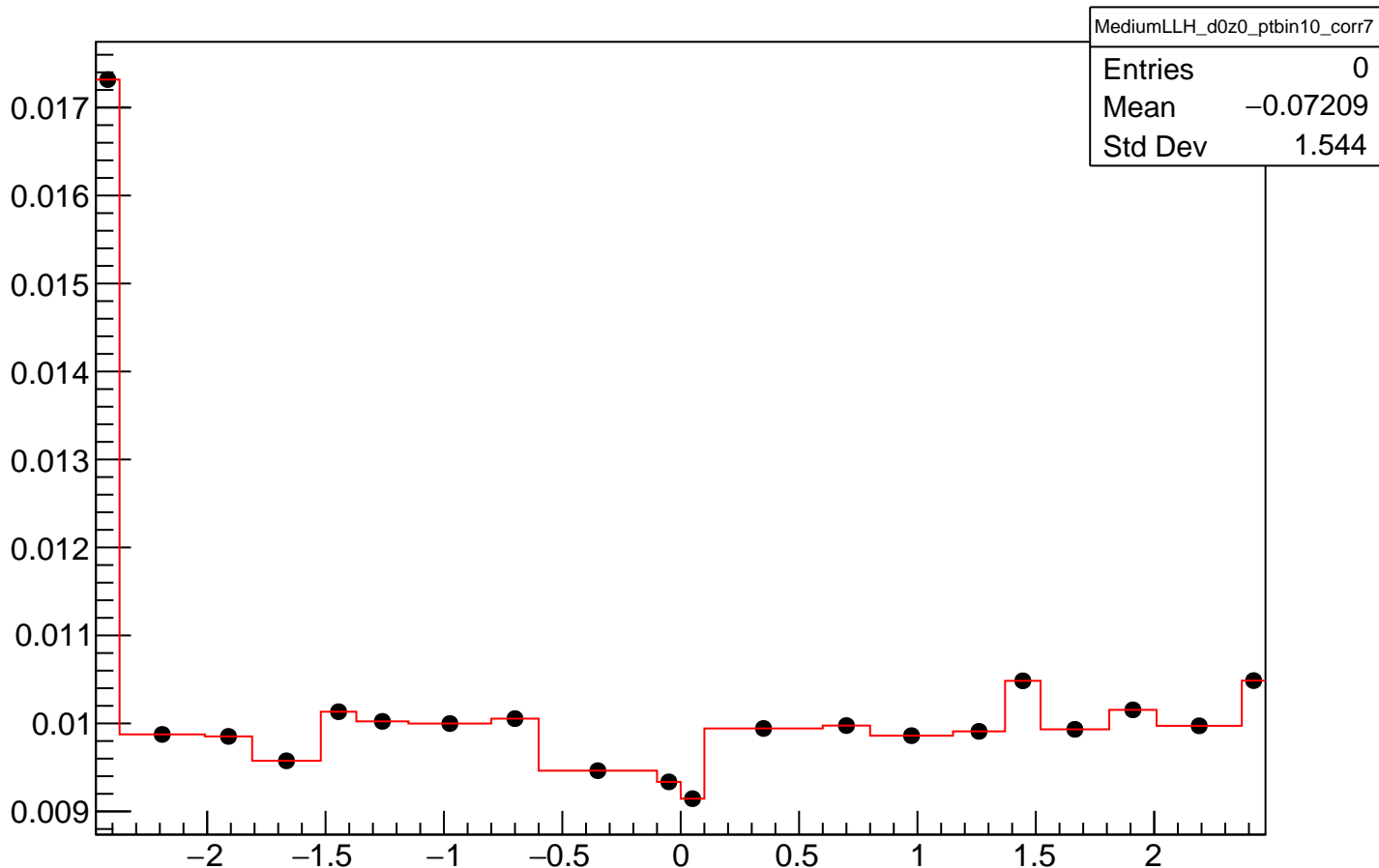
# FullSim\_MediumLLH\_d0z0\_corr7



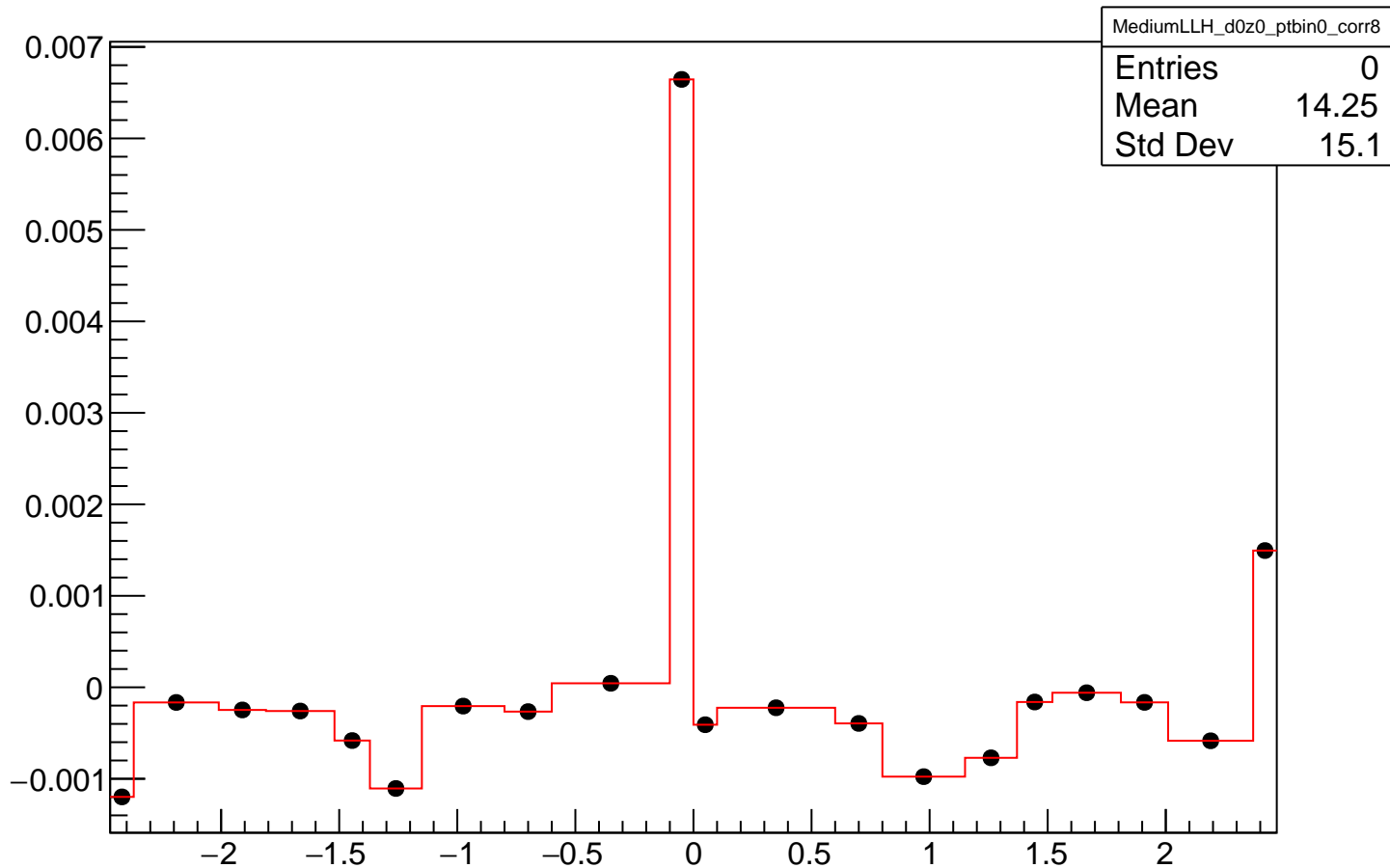
# FullSim\_MediumLLH\_d0z0\_corr7



# FullSim\_MediumLLH\_d0z0\_corr7

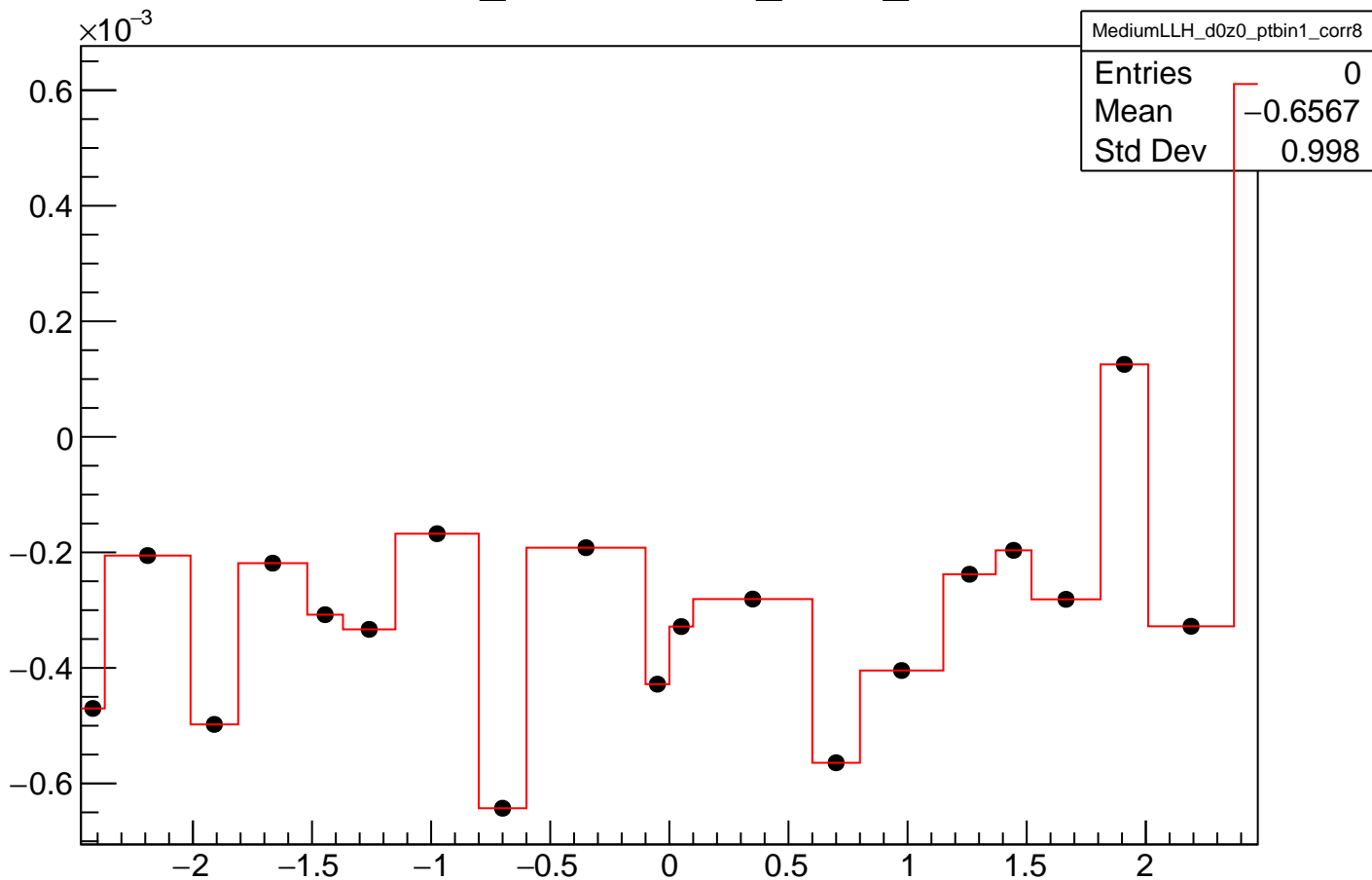


# FullSim\_MediumLLH\_d0z0\_corr8

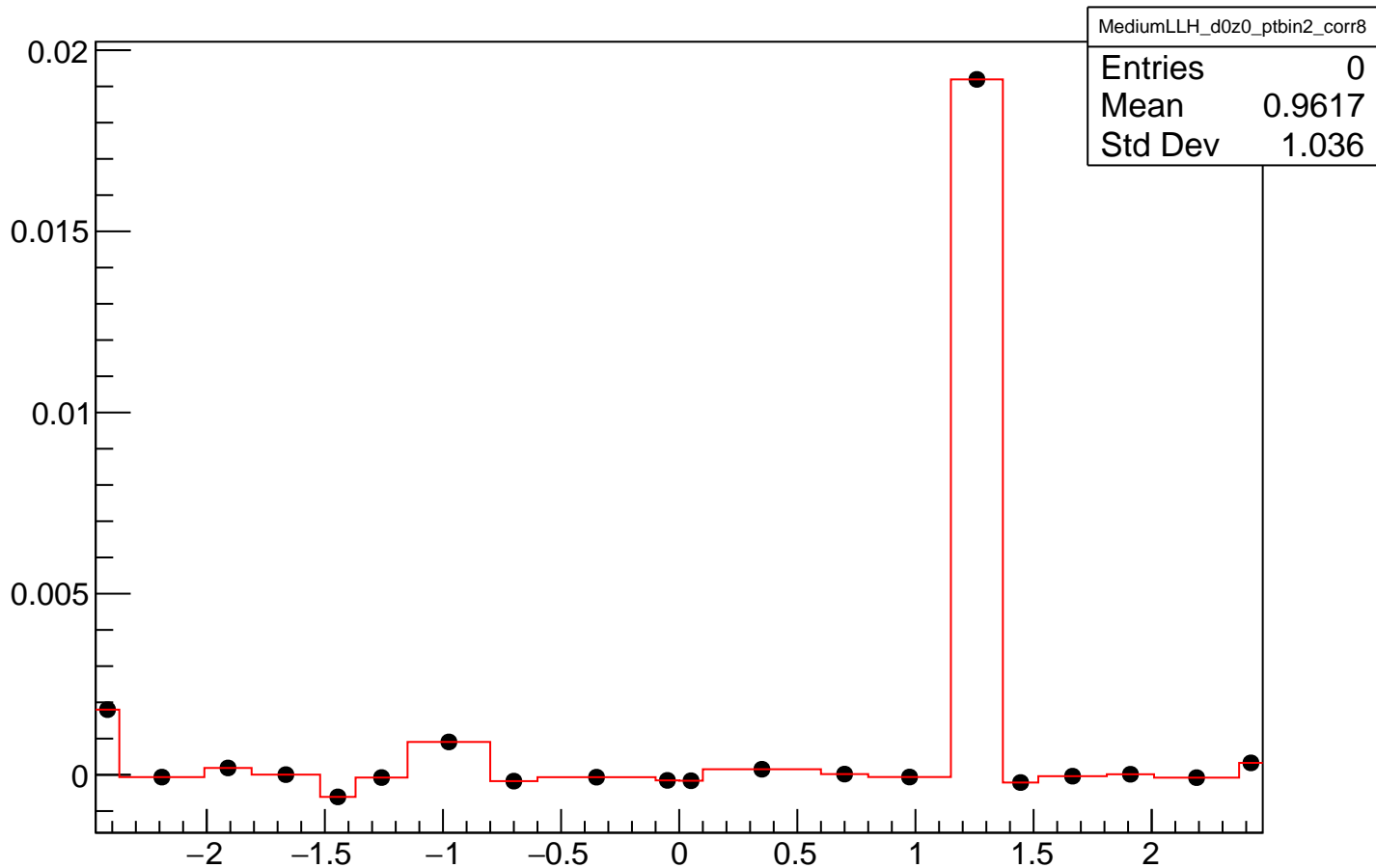




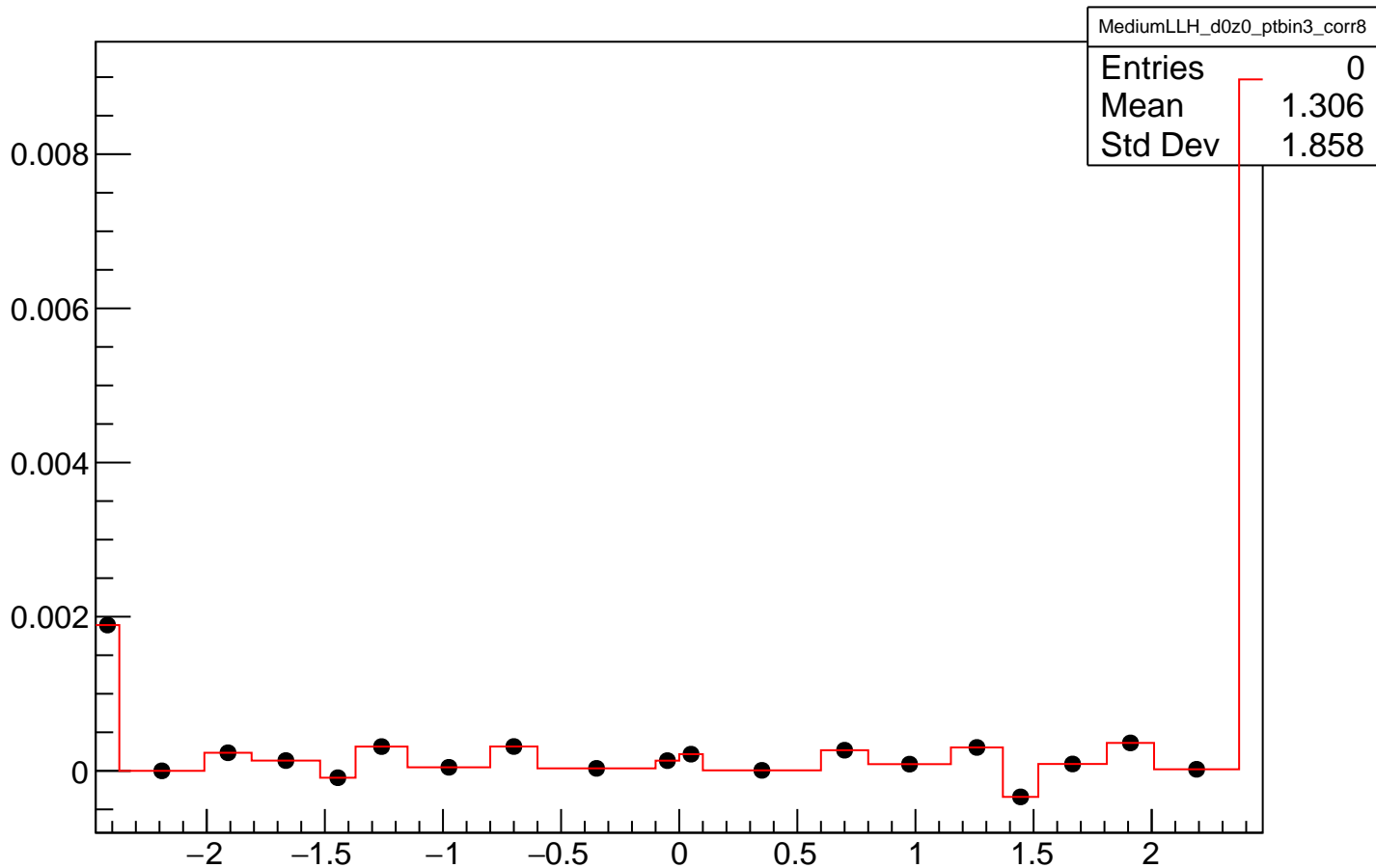
# FullSim\_MediumLLH\_d0z0\_corr8



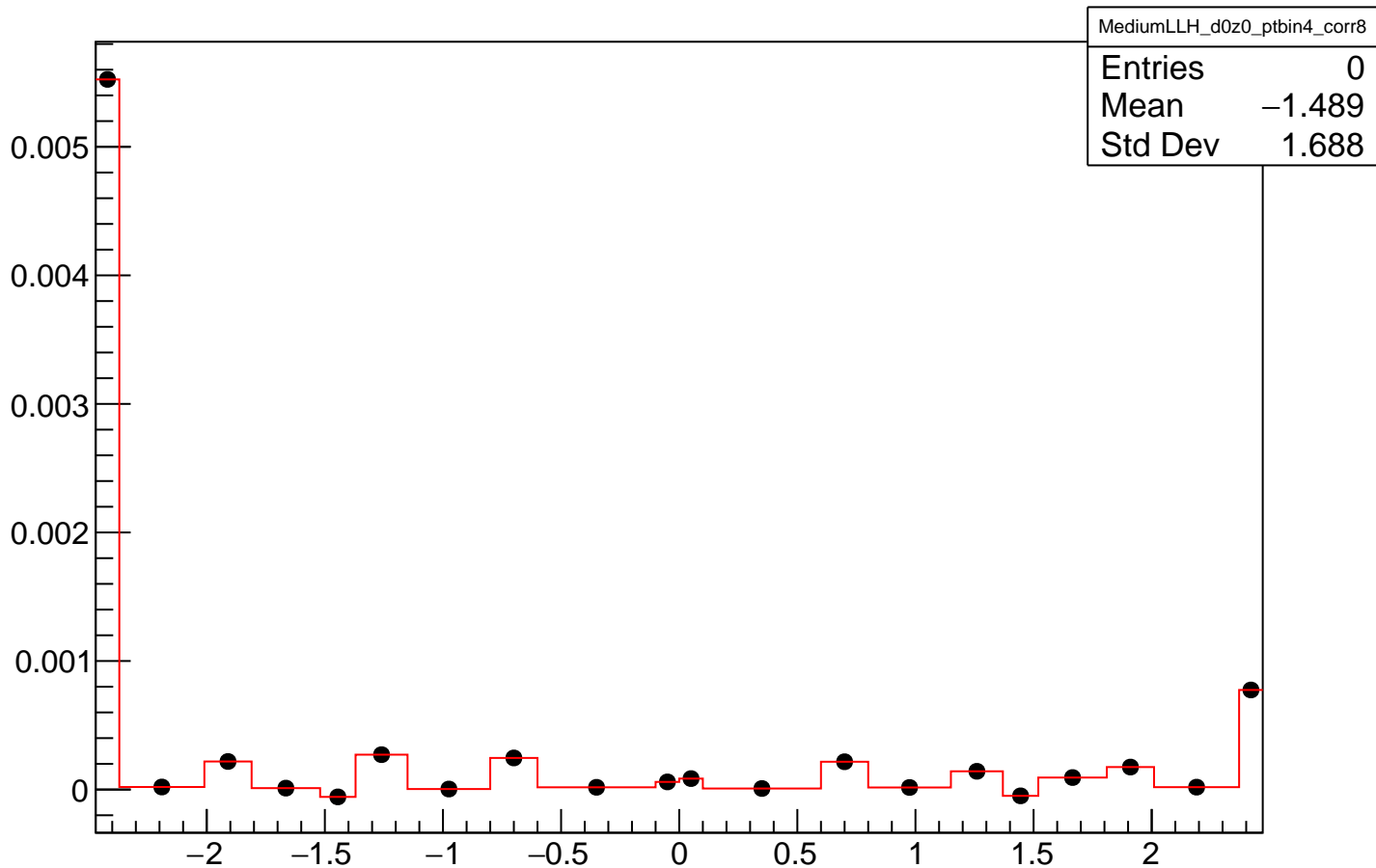
# FullSim\_MediumLLH\_d0z0\_corr8



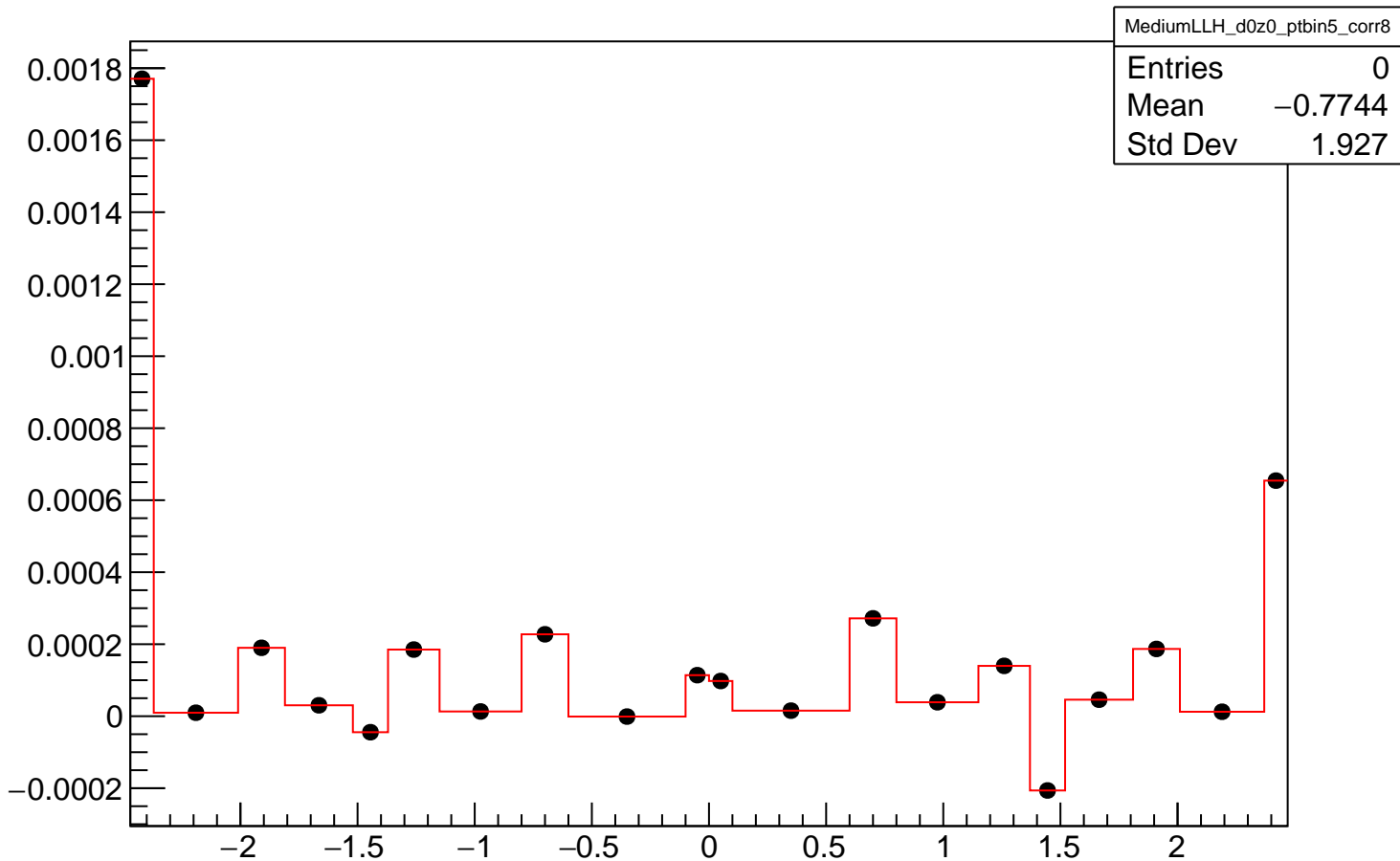
# FullSim\_MediumLLH\_d0z0\_corr8



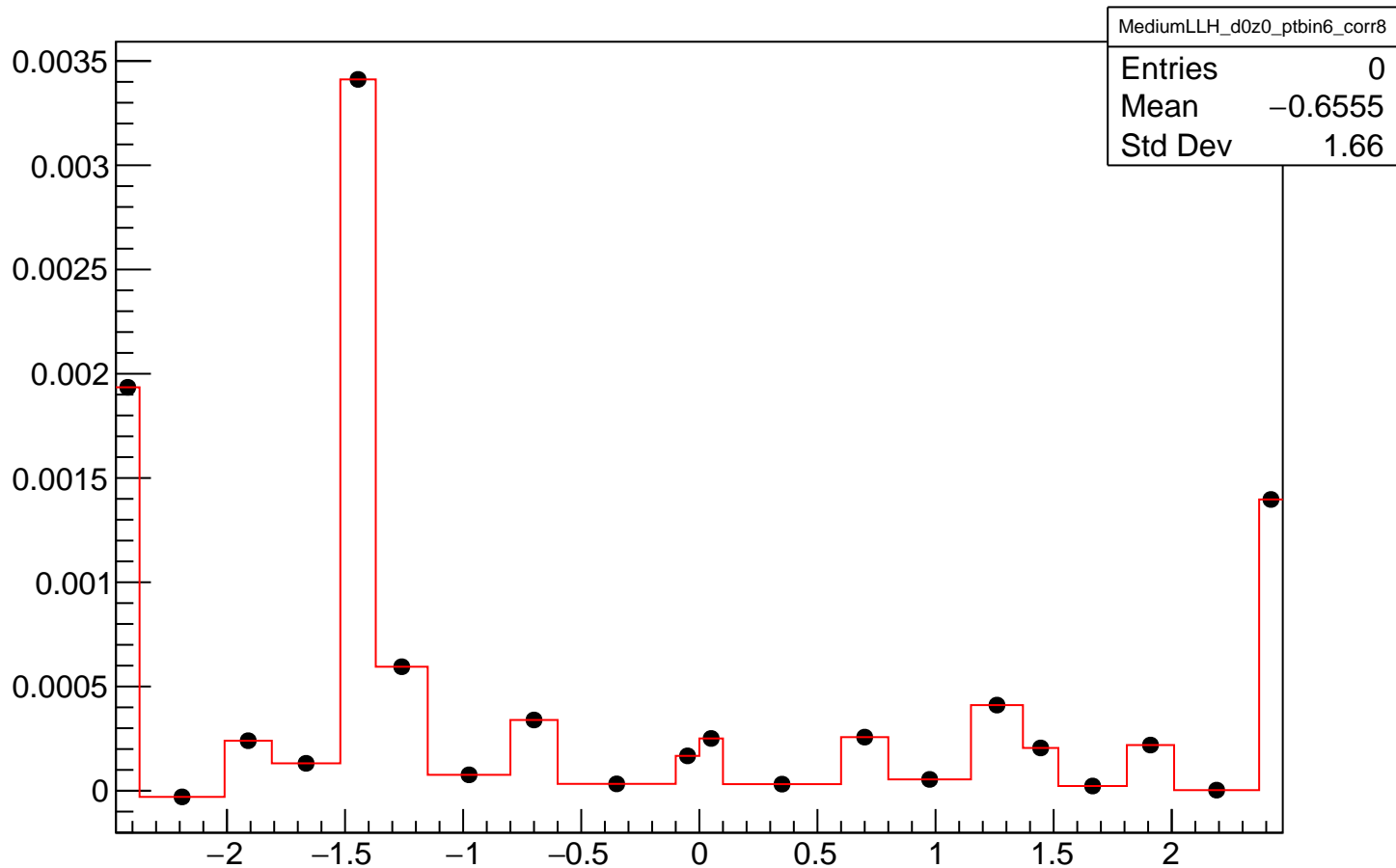
# FullSim\_MediumLLH\_d0z0\_corr8



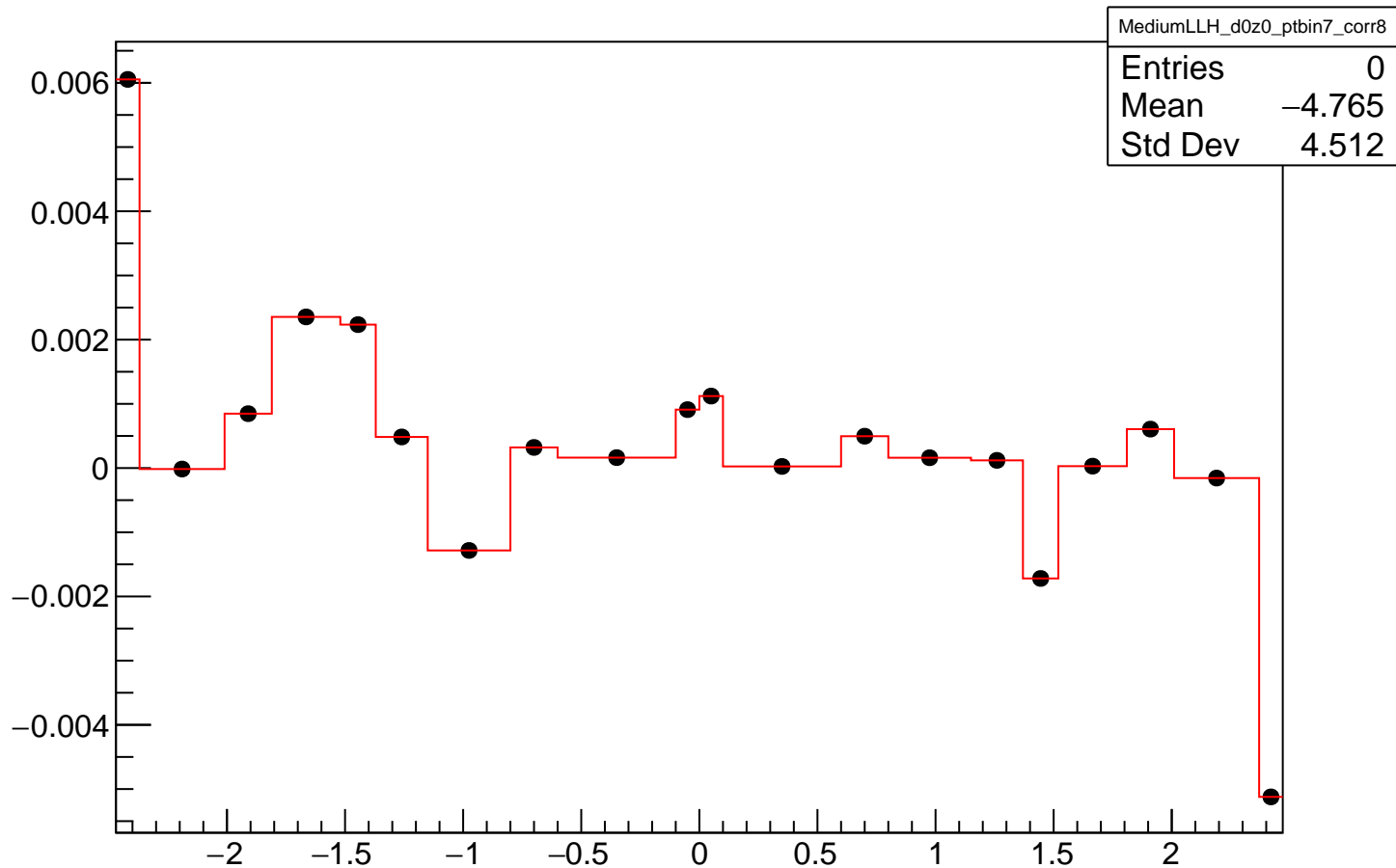
# FullSim\_MediumLLH\_d0z0\_corr8



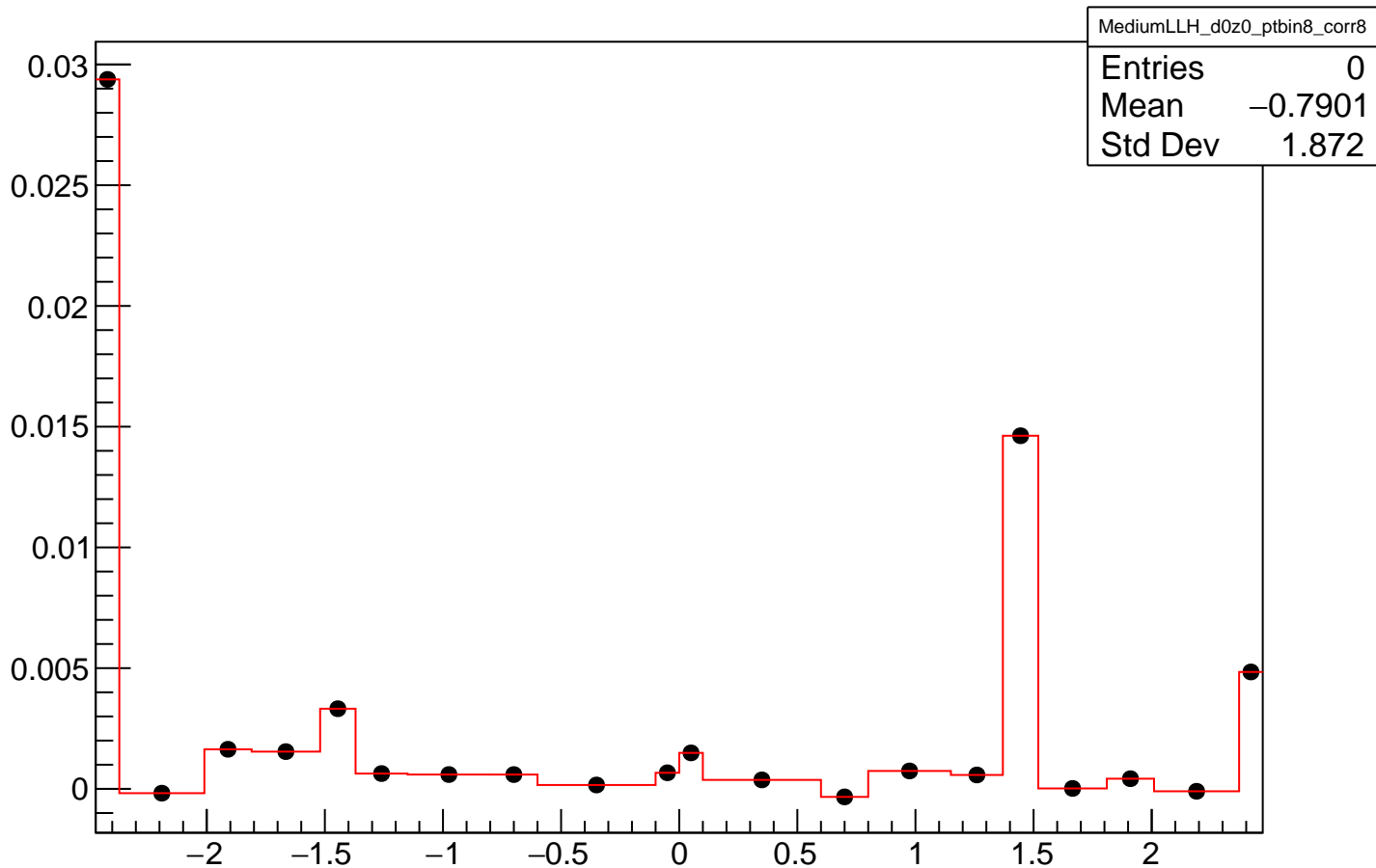
# FullSim\_MediumLLH\_d0z0\_corr8



# FullSim\_MediumLLH\_d0z0\_corr8

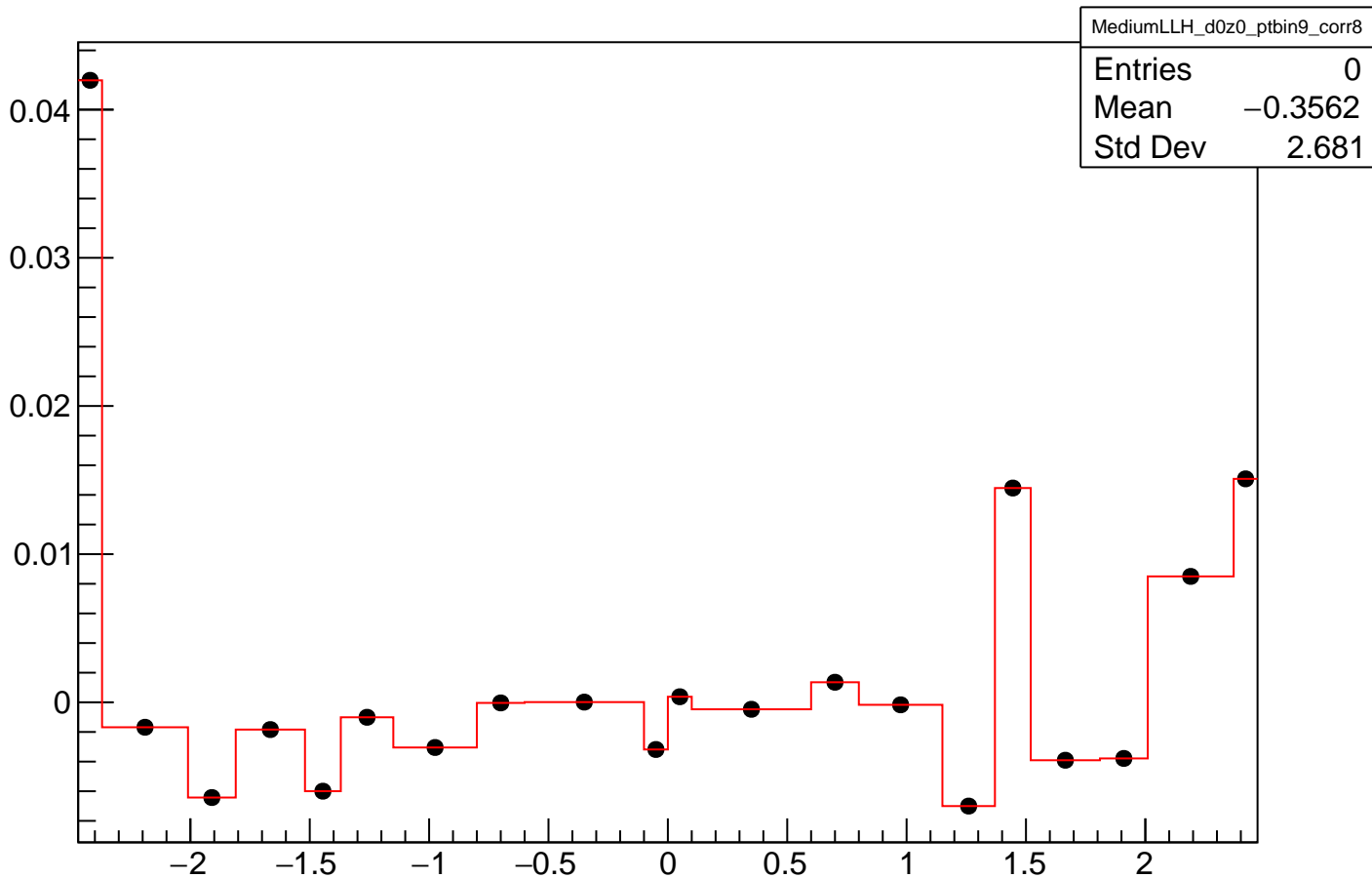


# FullSim\_MediumLLH\_d0z0\_corr8

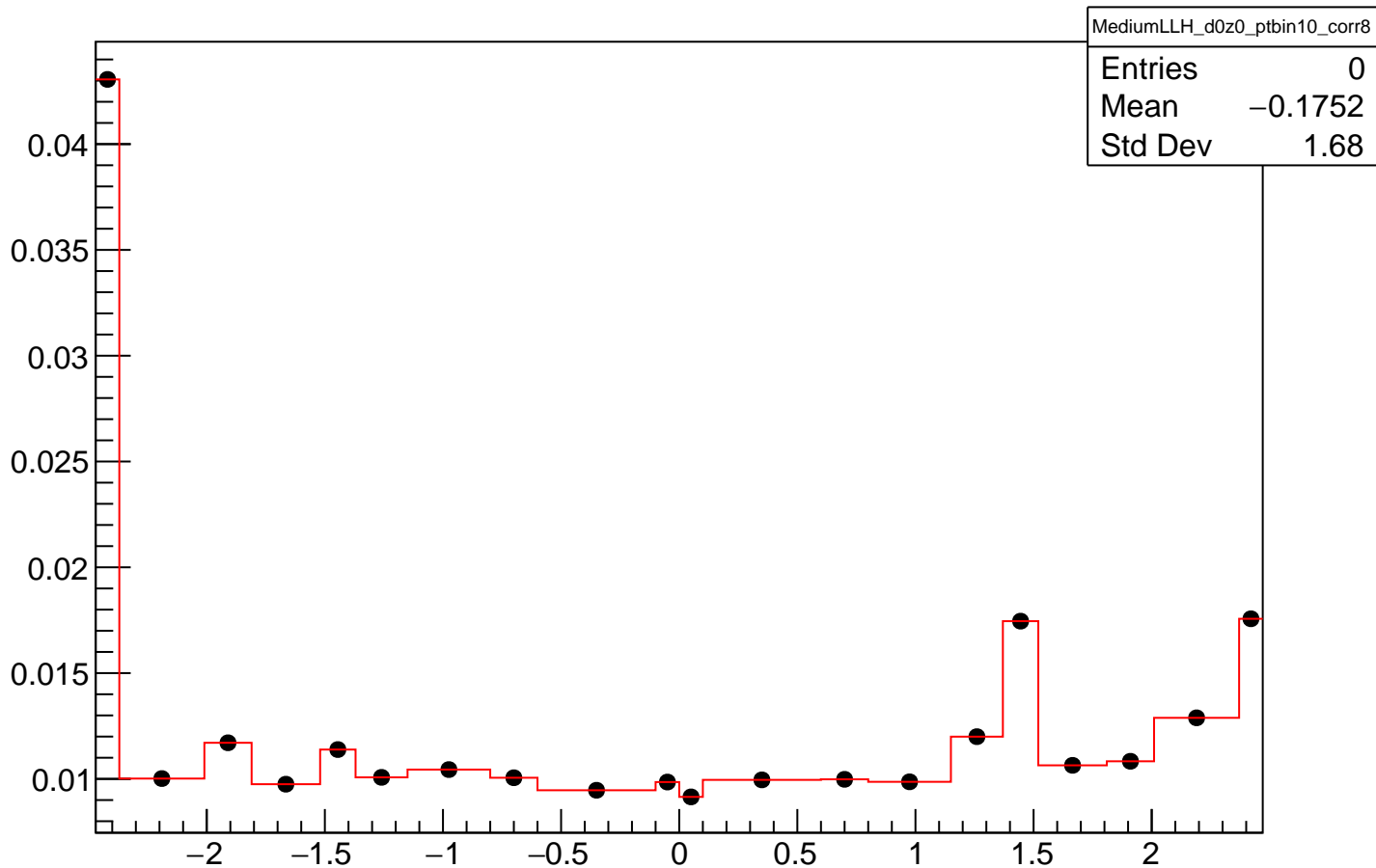




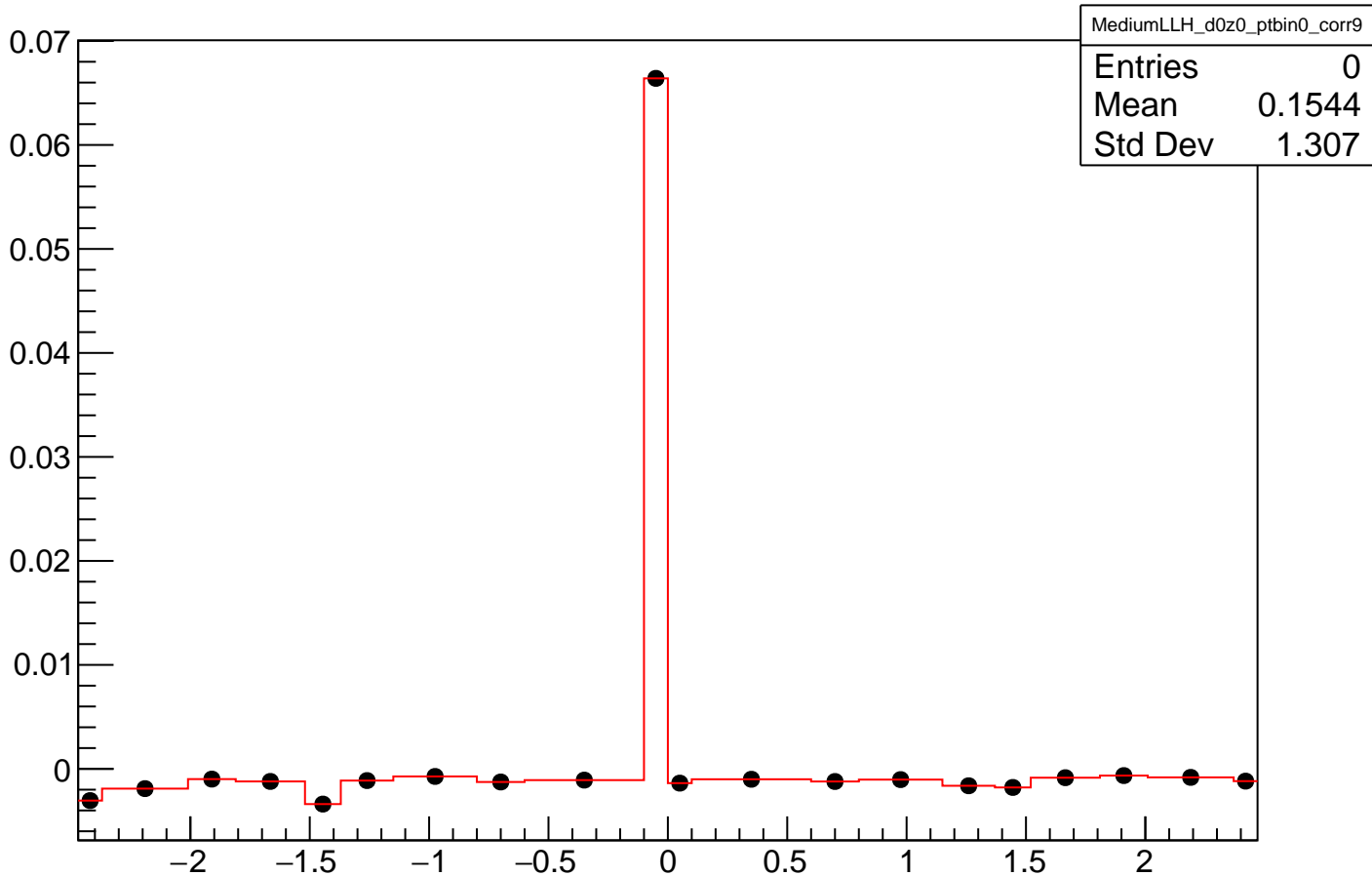
# FullSim\_MediumLLH\_d0z0\_corr8



# FullSim\_MediumLLH\_d0z0\_corr8



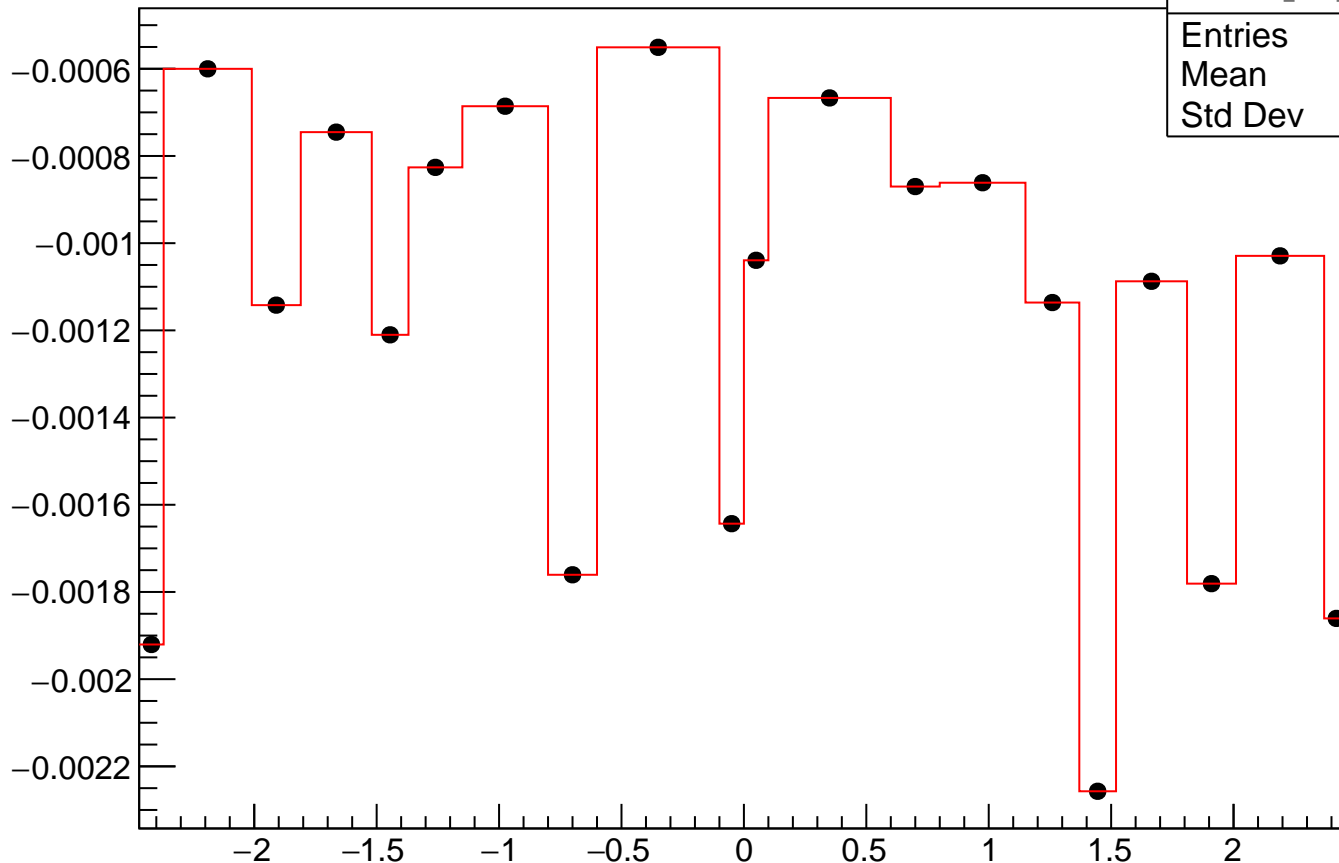
# FullSim\_MediumLLH\_d0z0\_corr9



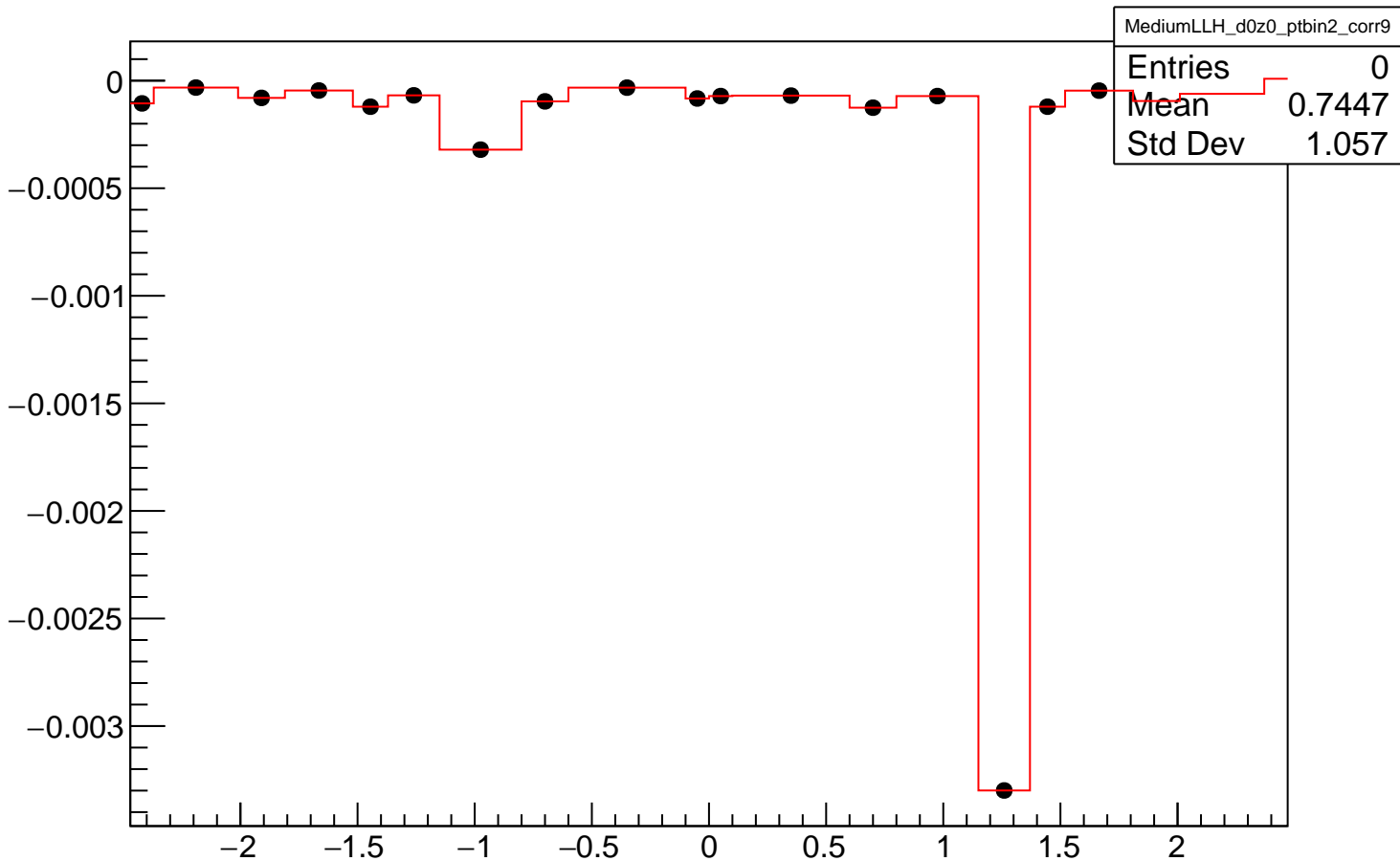
# FullSim\_MediumLLH\_d0z0\_corr9

MediumLLH\_d0z0\_ptbin1\_corr9

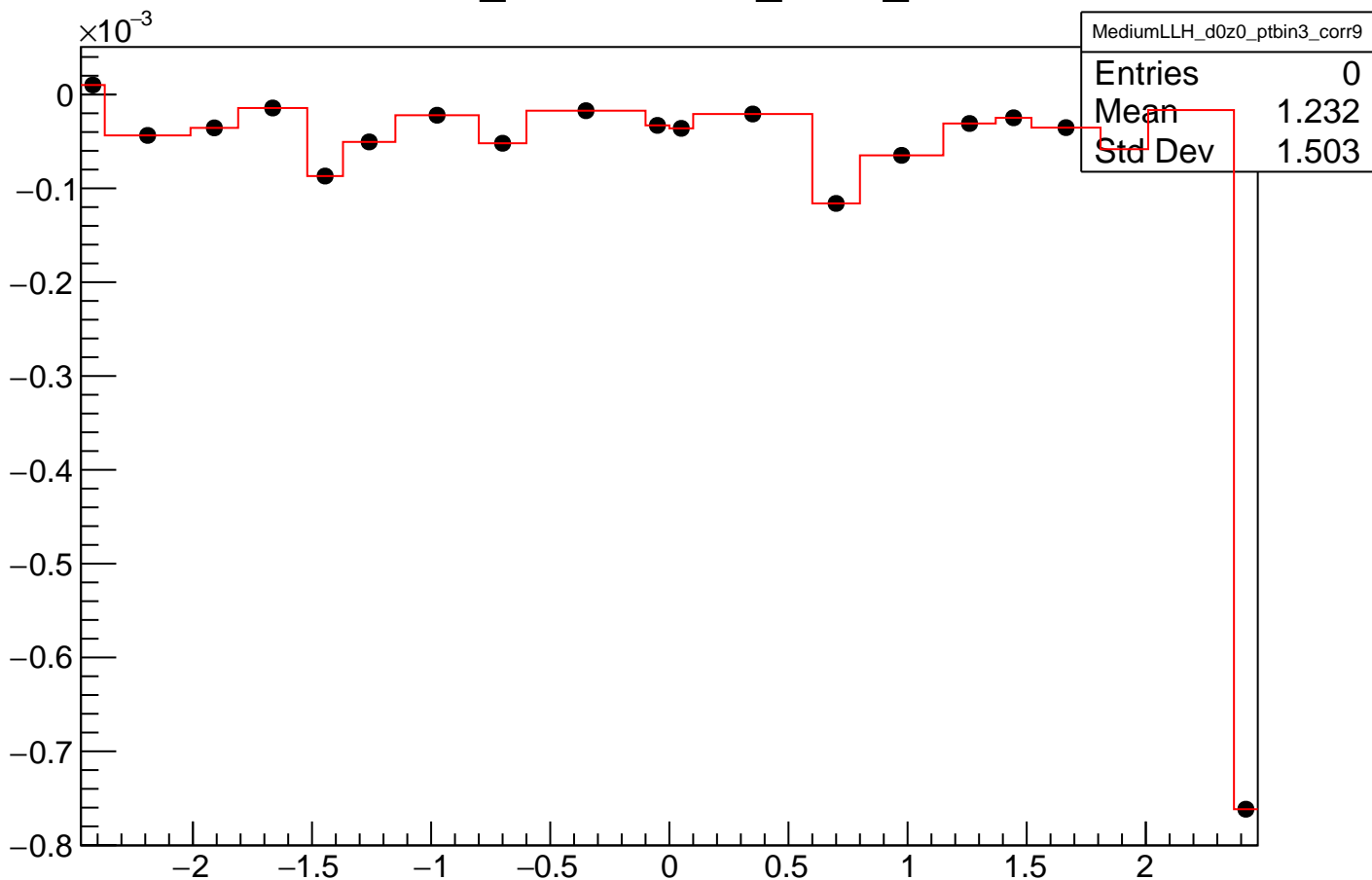
Entries	0
Mean	0.1709
Std Dev	1.569



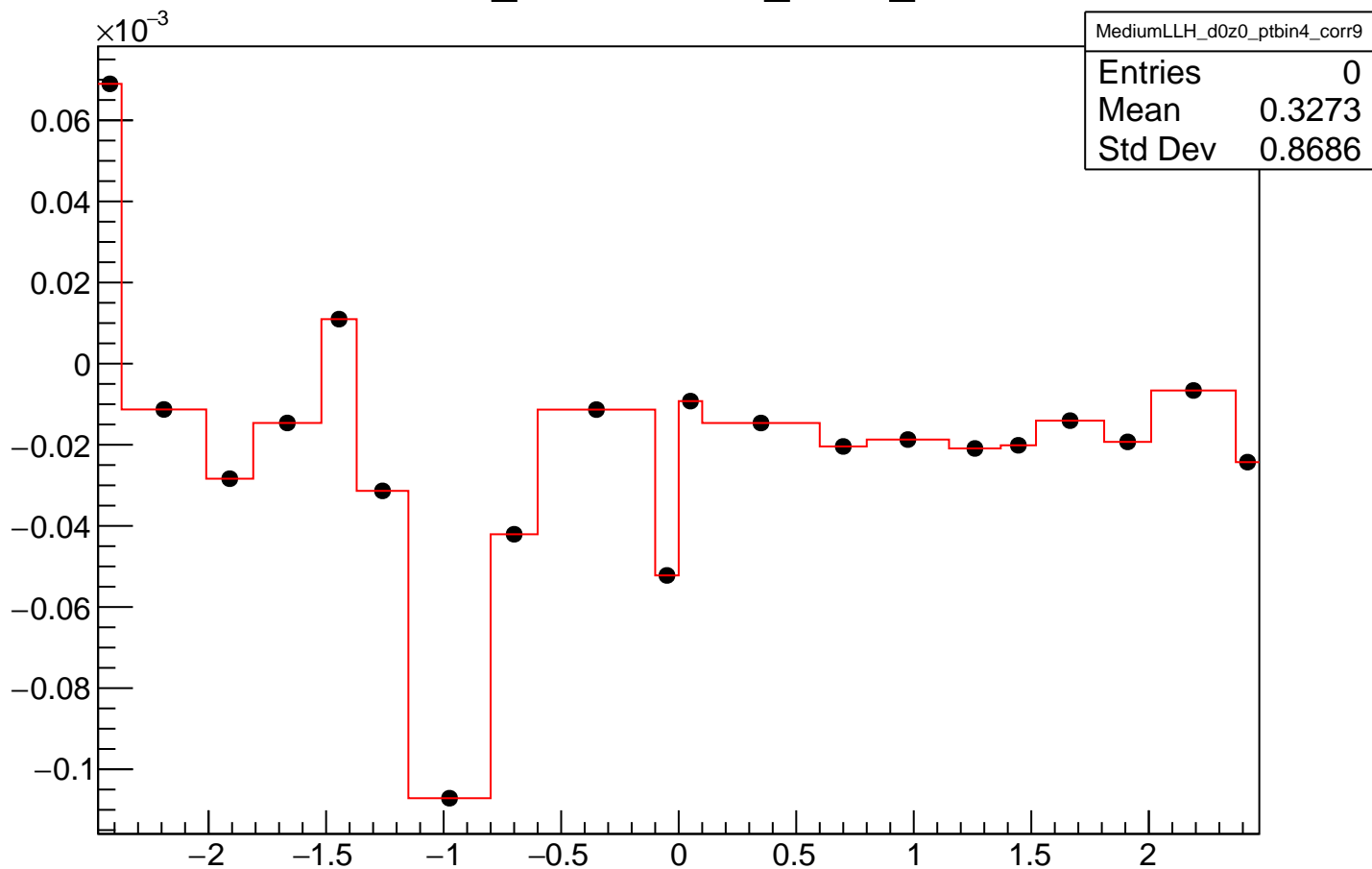
# FullSim\_MediumLLH\_d0z0\_corr9



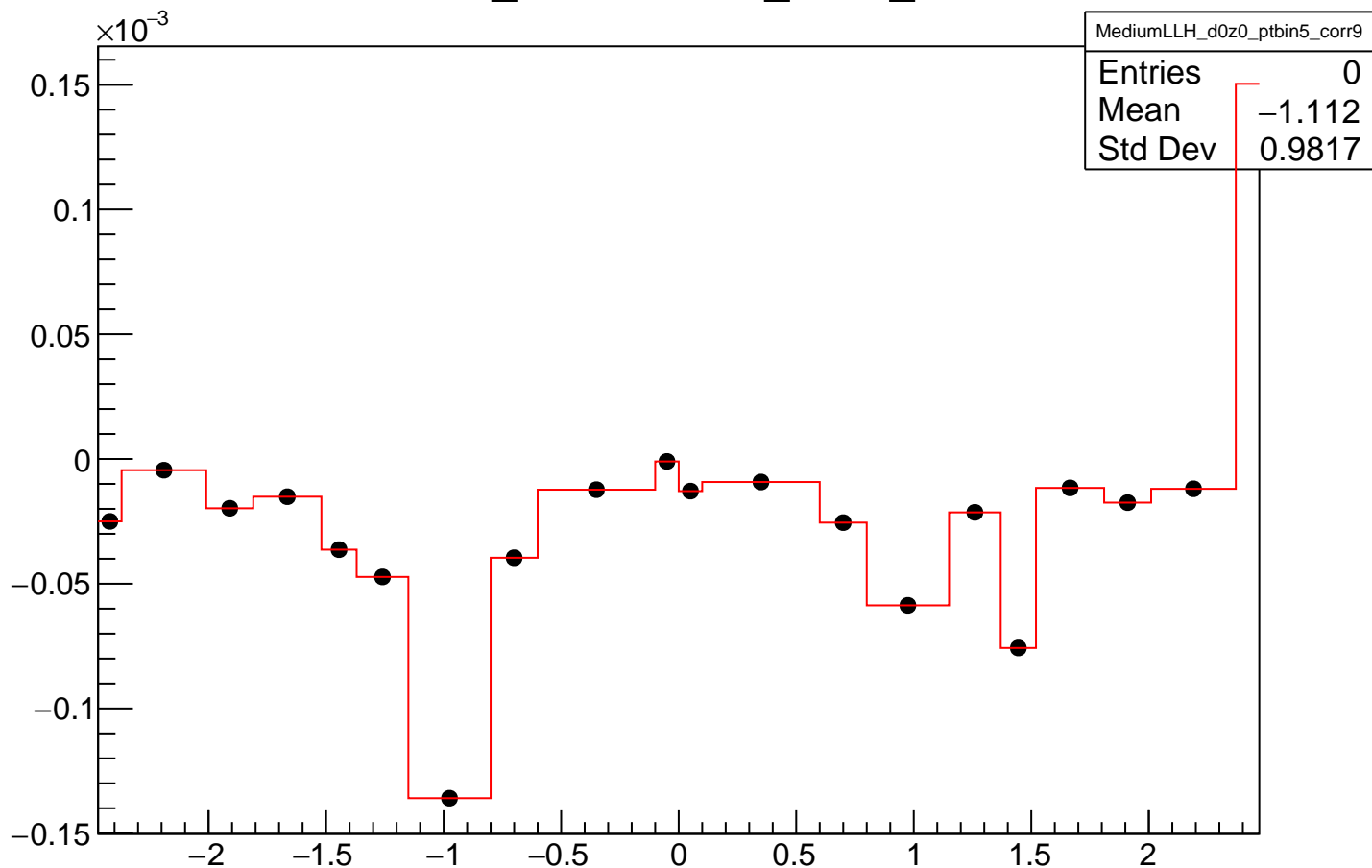
# FullSim\_MediumLLH\_d0z0\_corr9



# FullSim\_MediumLLH\_d0z0\_corr9

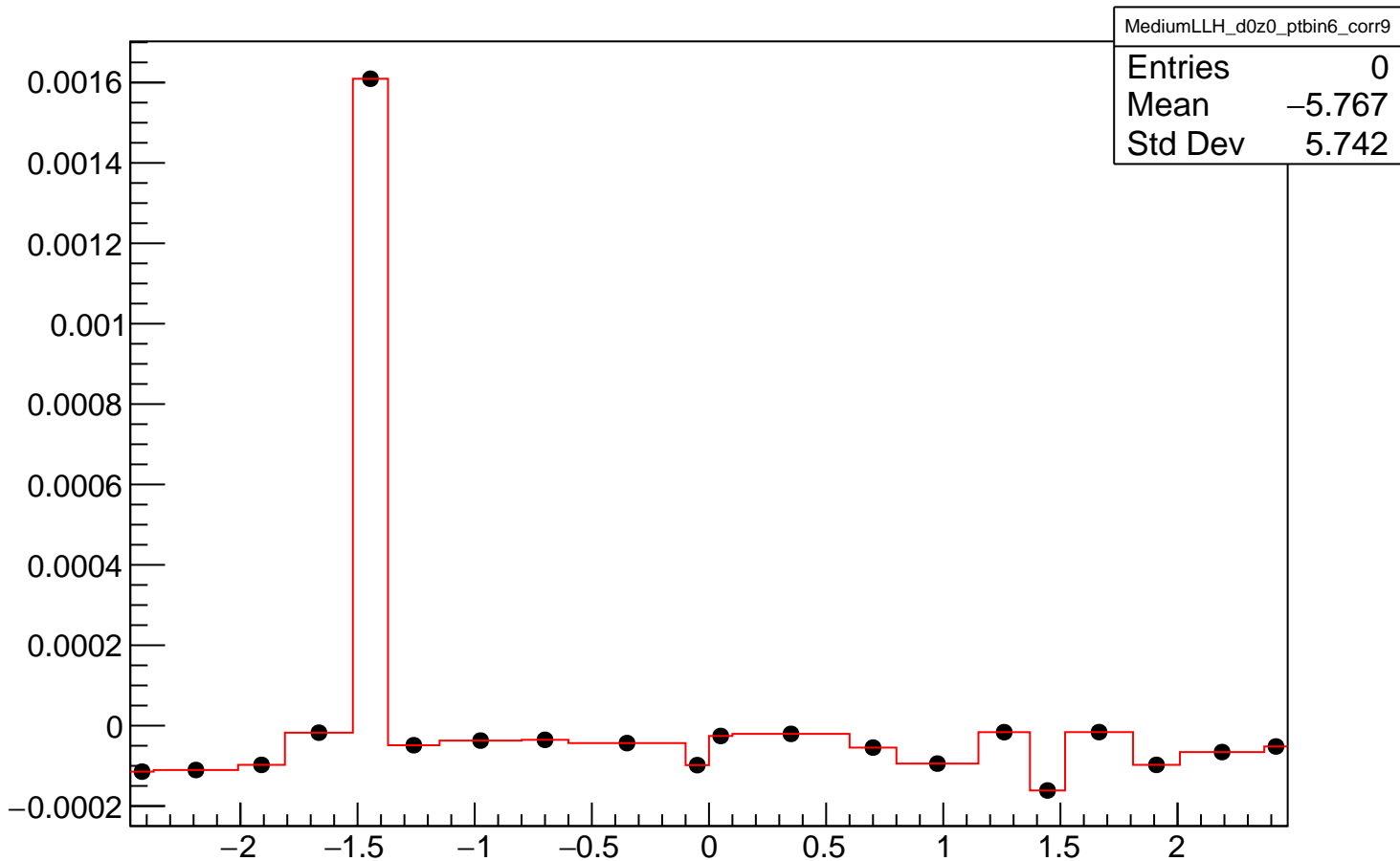


# FullSim\_MediumLLH\_d0z0\_corr9





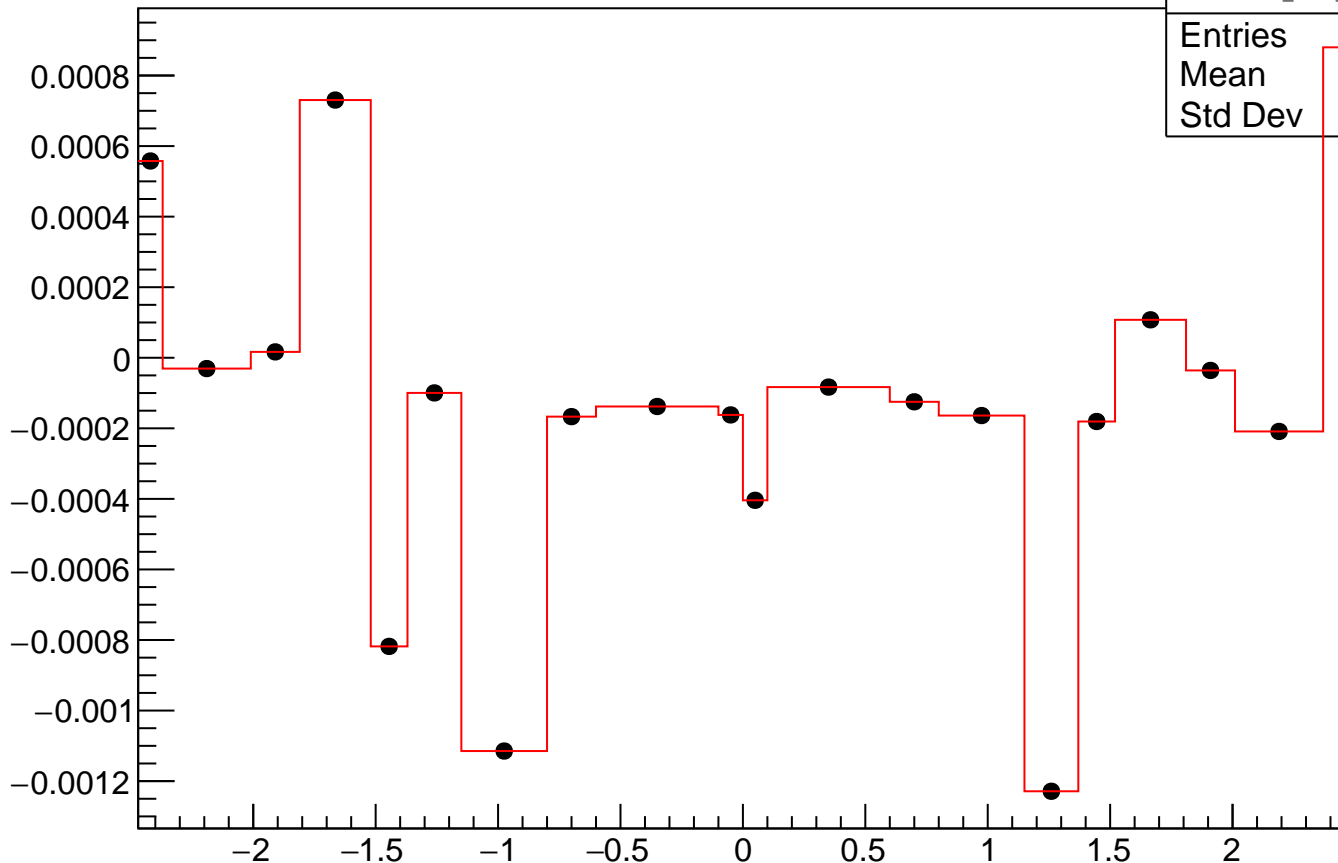
# FullSim\_MediumLLH\_d0z0\_corr9



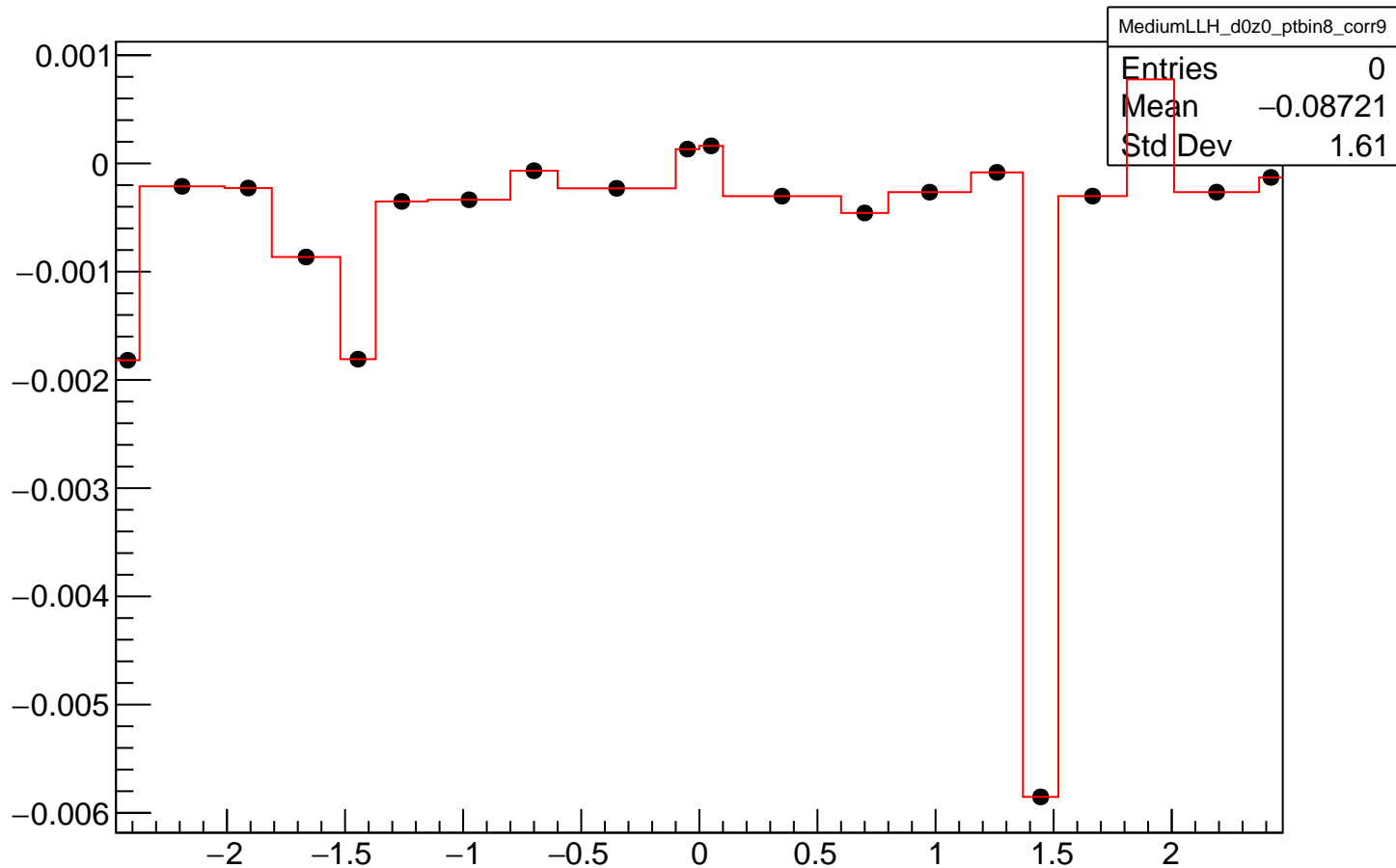
# FullSim\_MediumLLH\_d0z0\_corr9

MediumLLH\_d0z0\_ptbin7\_corr9

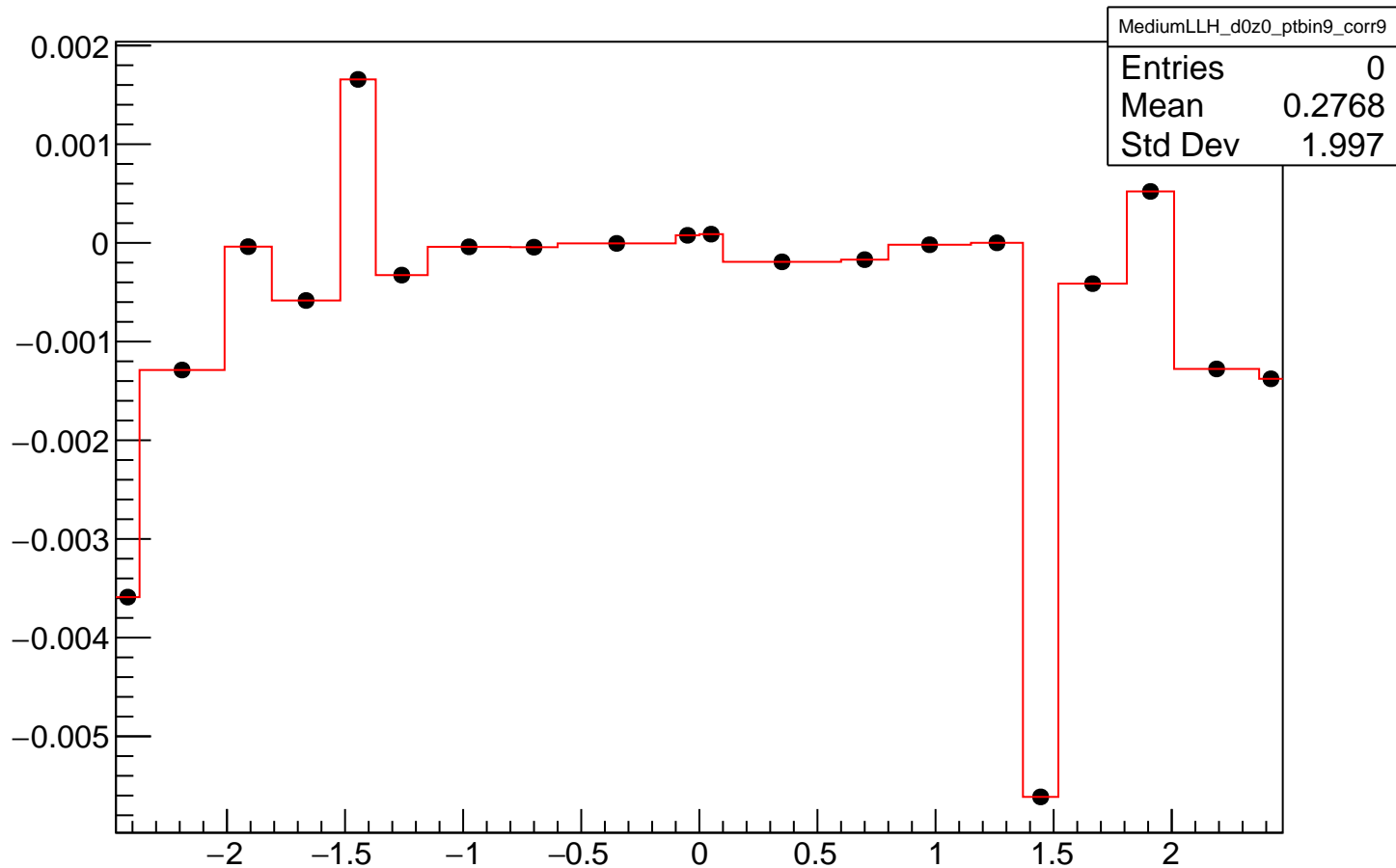
Entries	0
Mean	0.1072
Std Dev	1.22



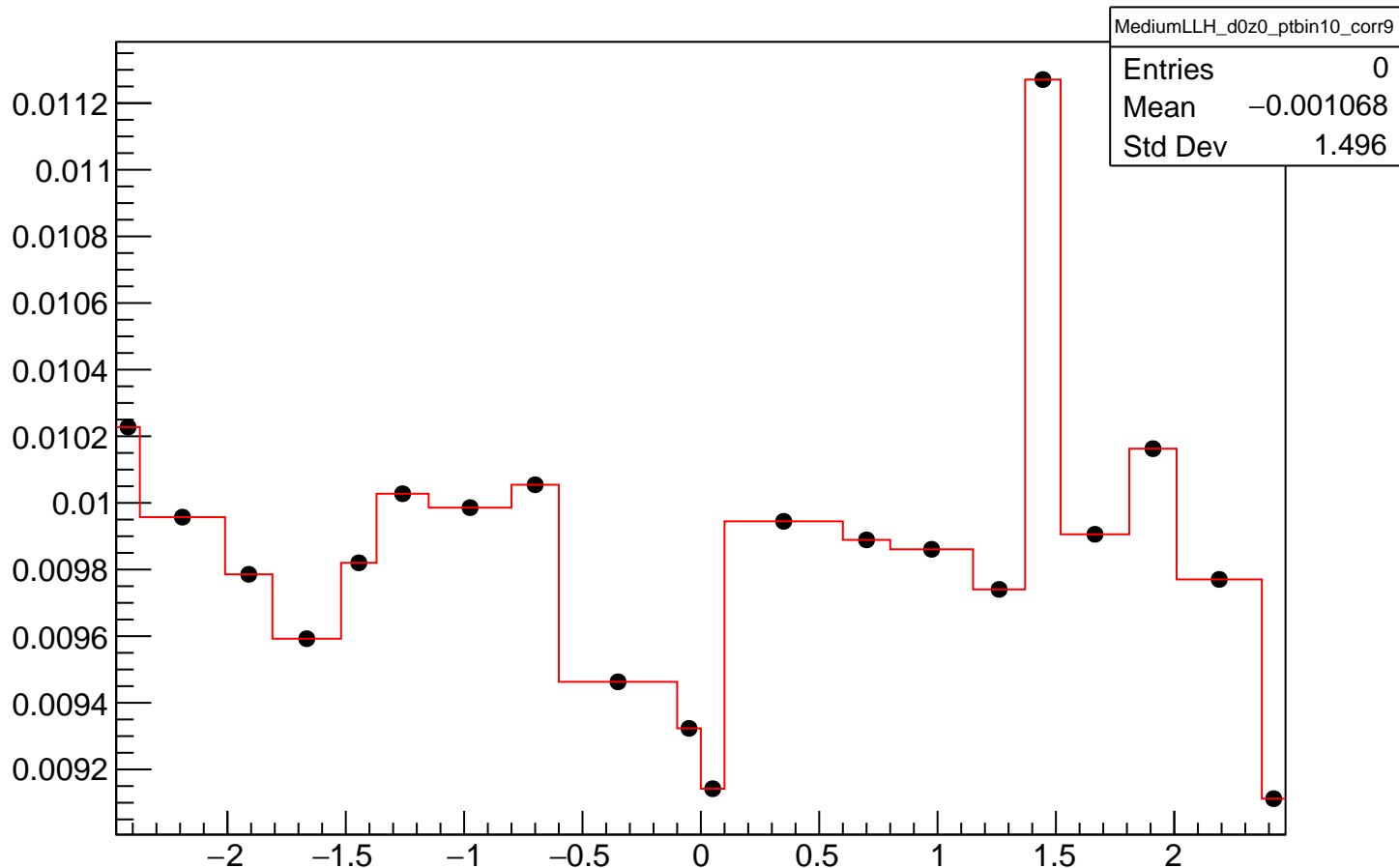
# FullSim\_MediumLLH\_d0z0\_corr9



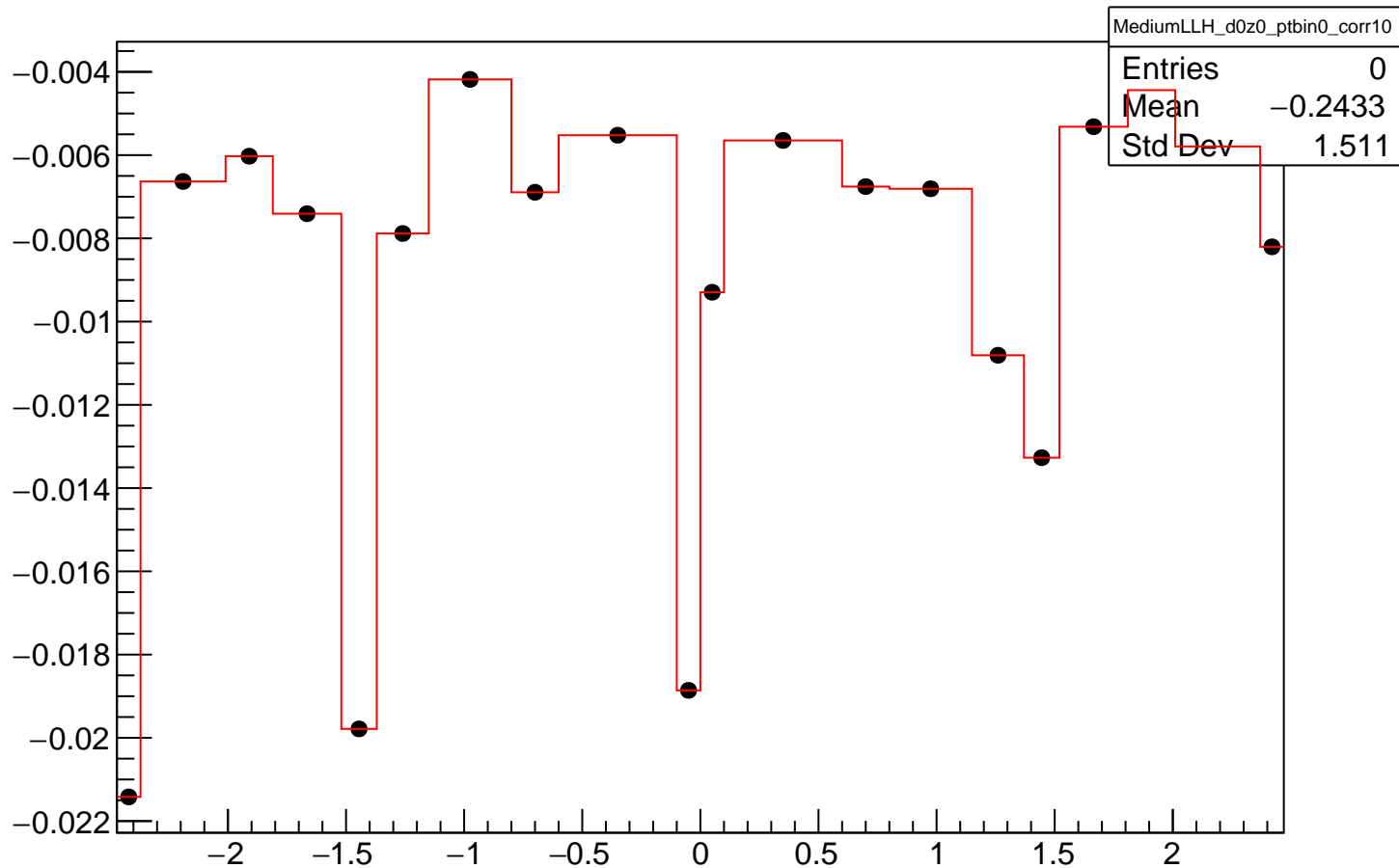
# FullSim\_MediumLLH\_d0z0\_corr9



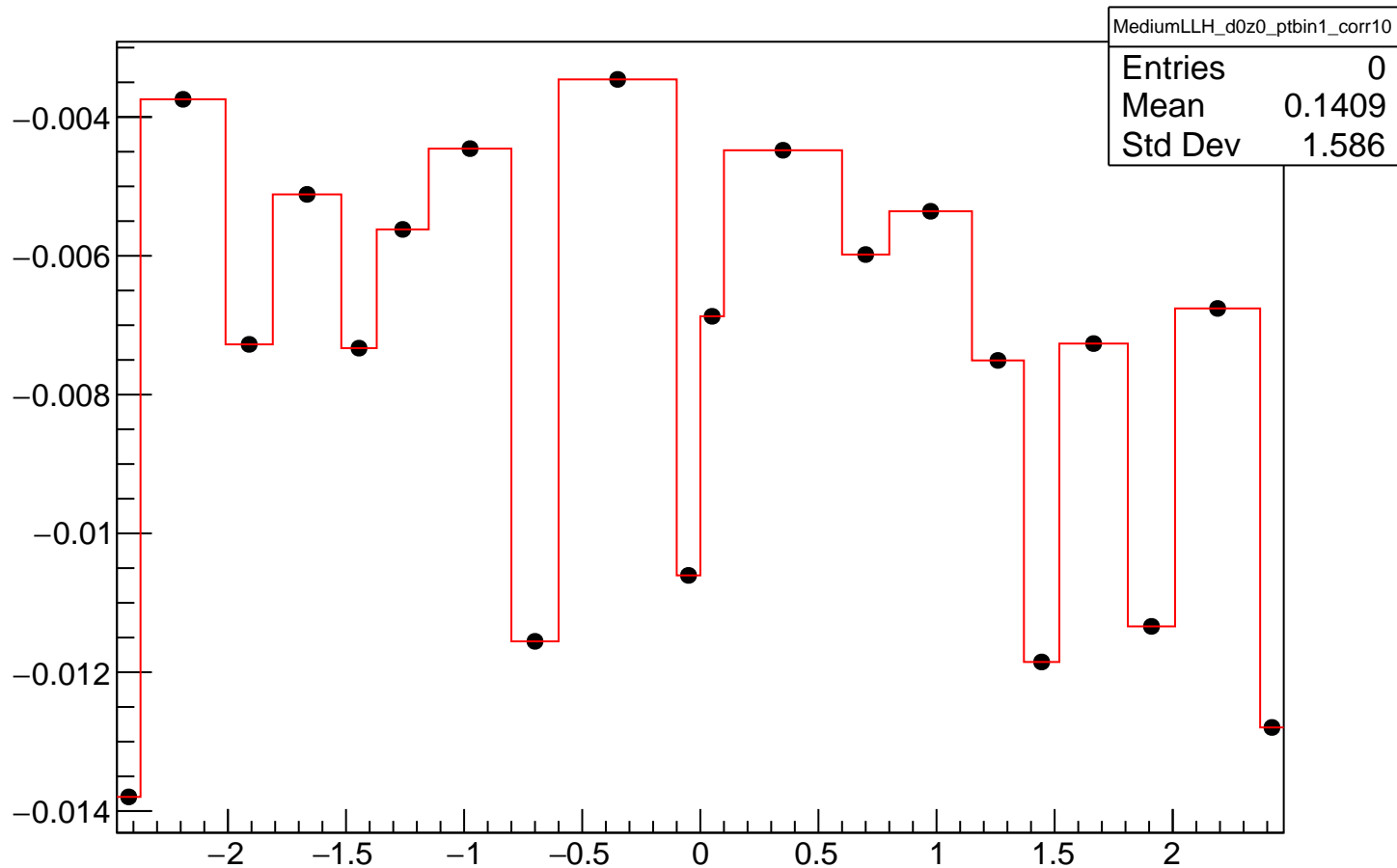
# FullSim\_MediumLLH\_d0z0\_corr9



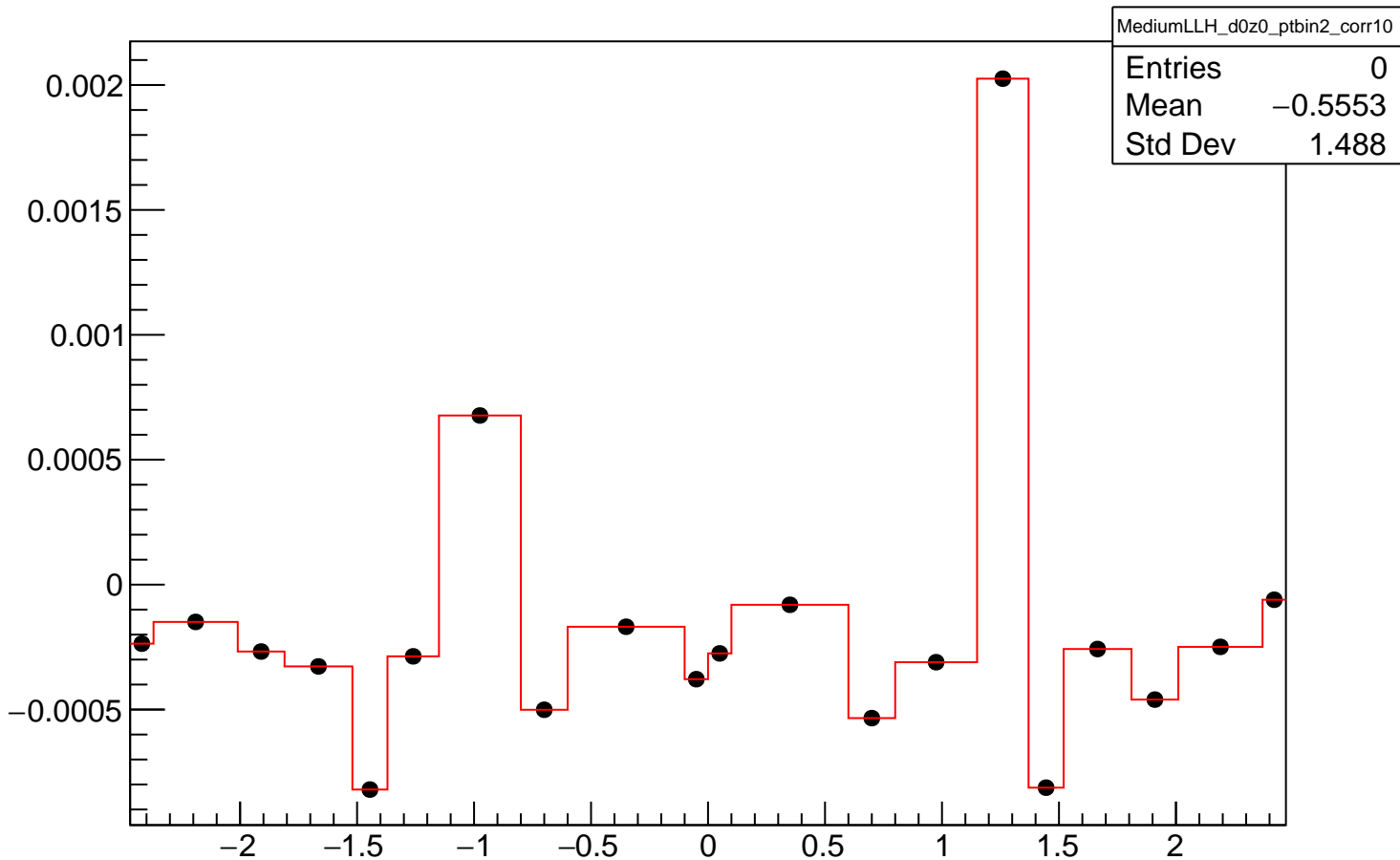
# FullSim\_MediumLLH\_d0z0\_corr10



# FullSim\_MediumLLH\_d0z0\_corr10

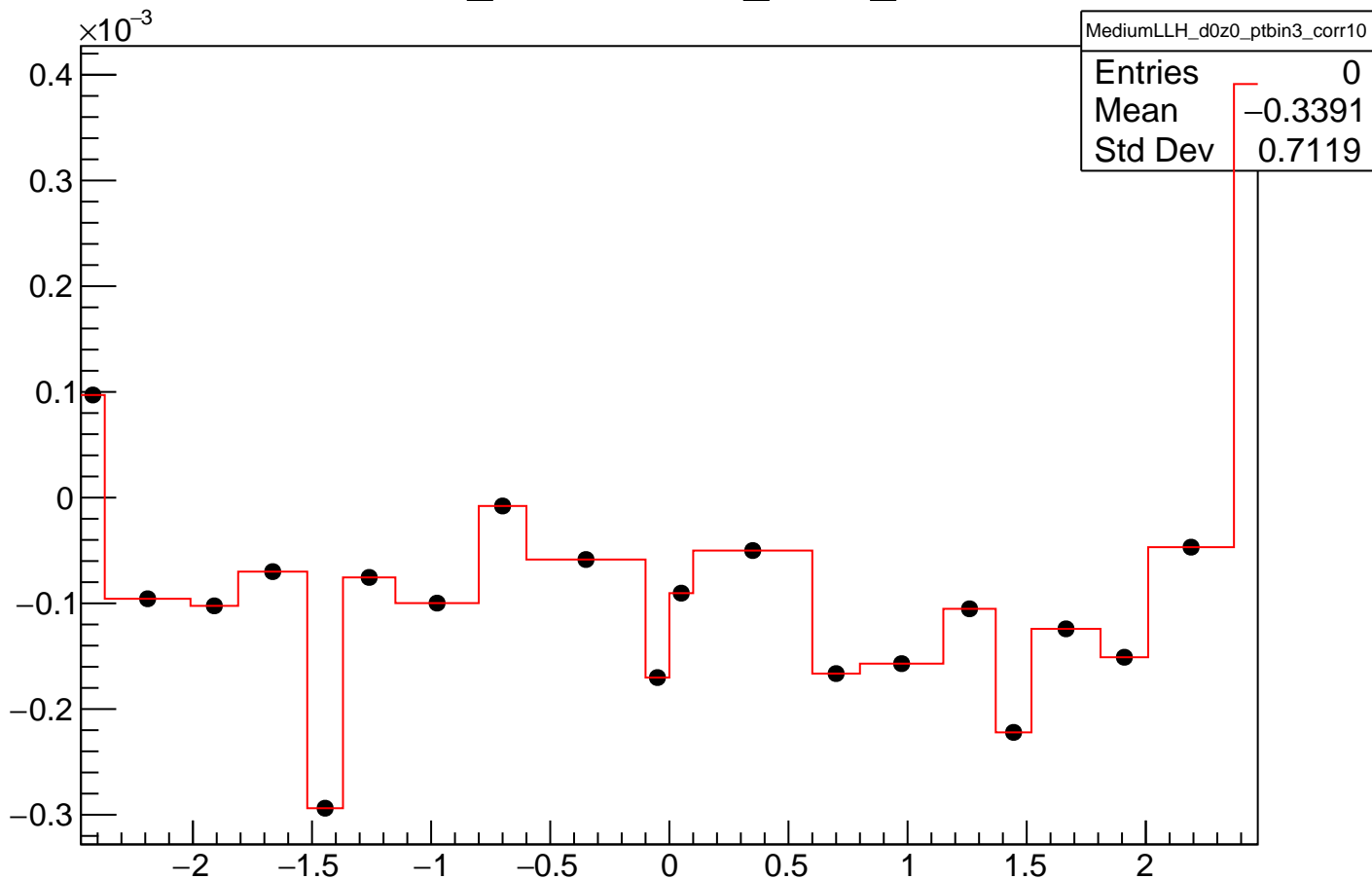


# FullSim\_MediumLLH\_d0z0\_corr10

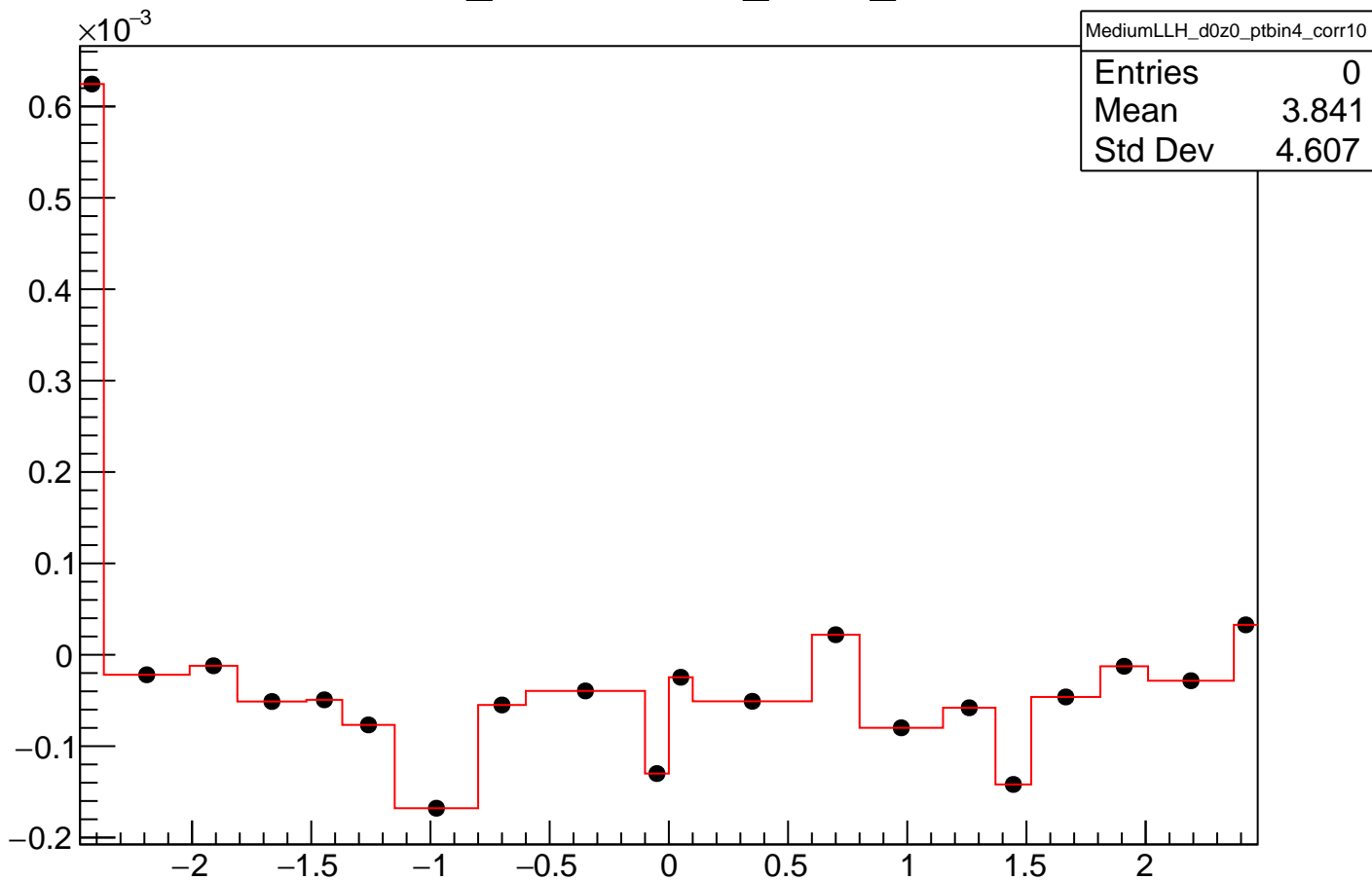




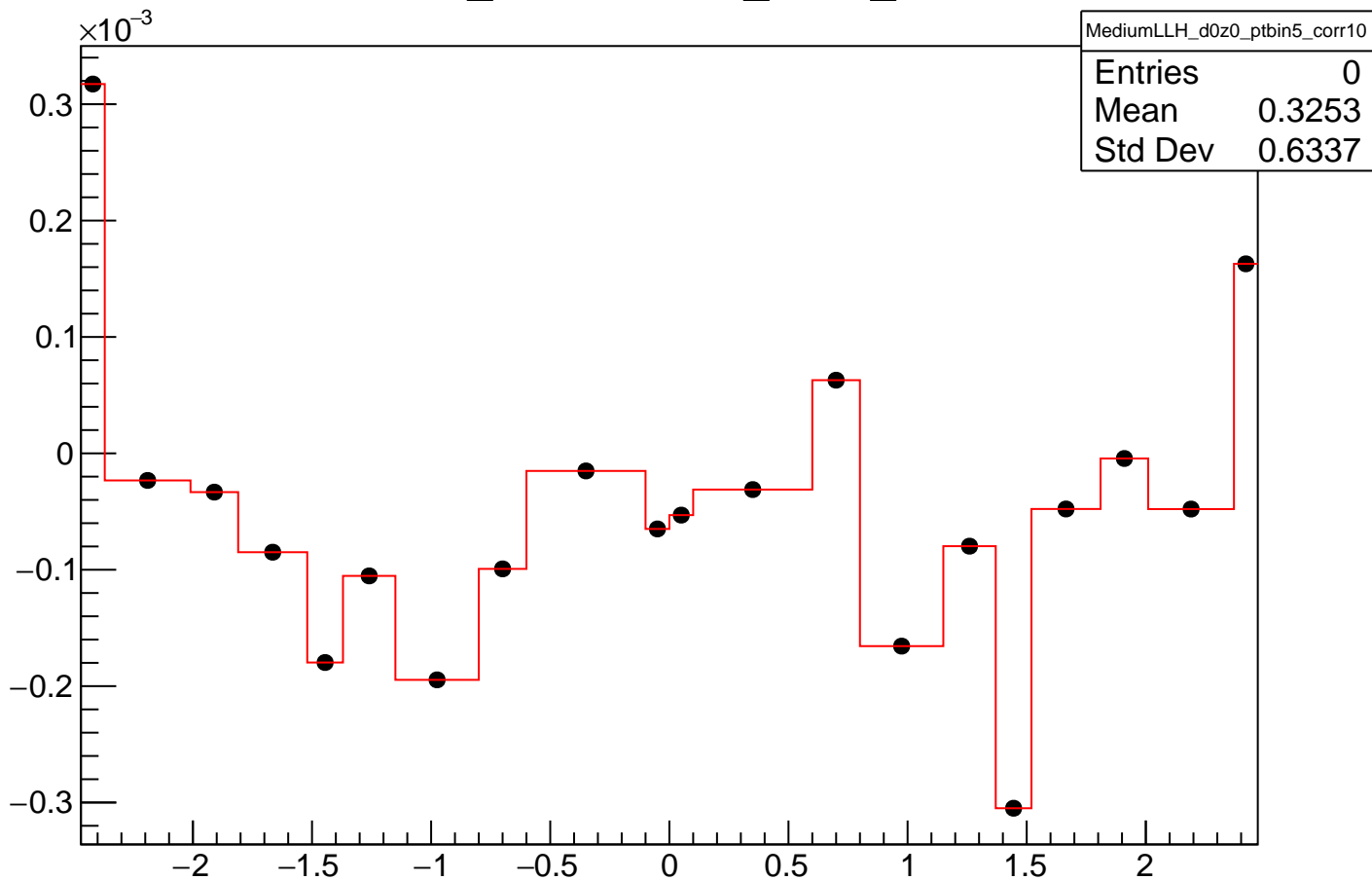
# FullSim\_MediumLLH\_d0z0\_corr10



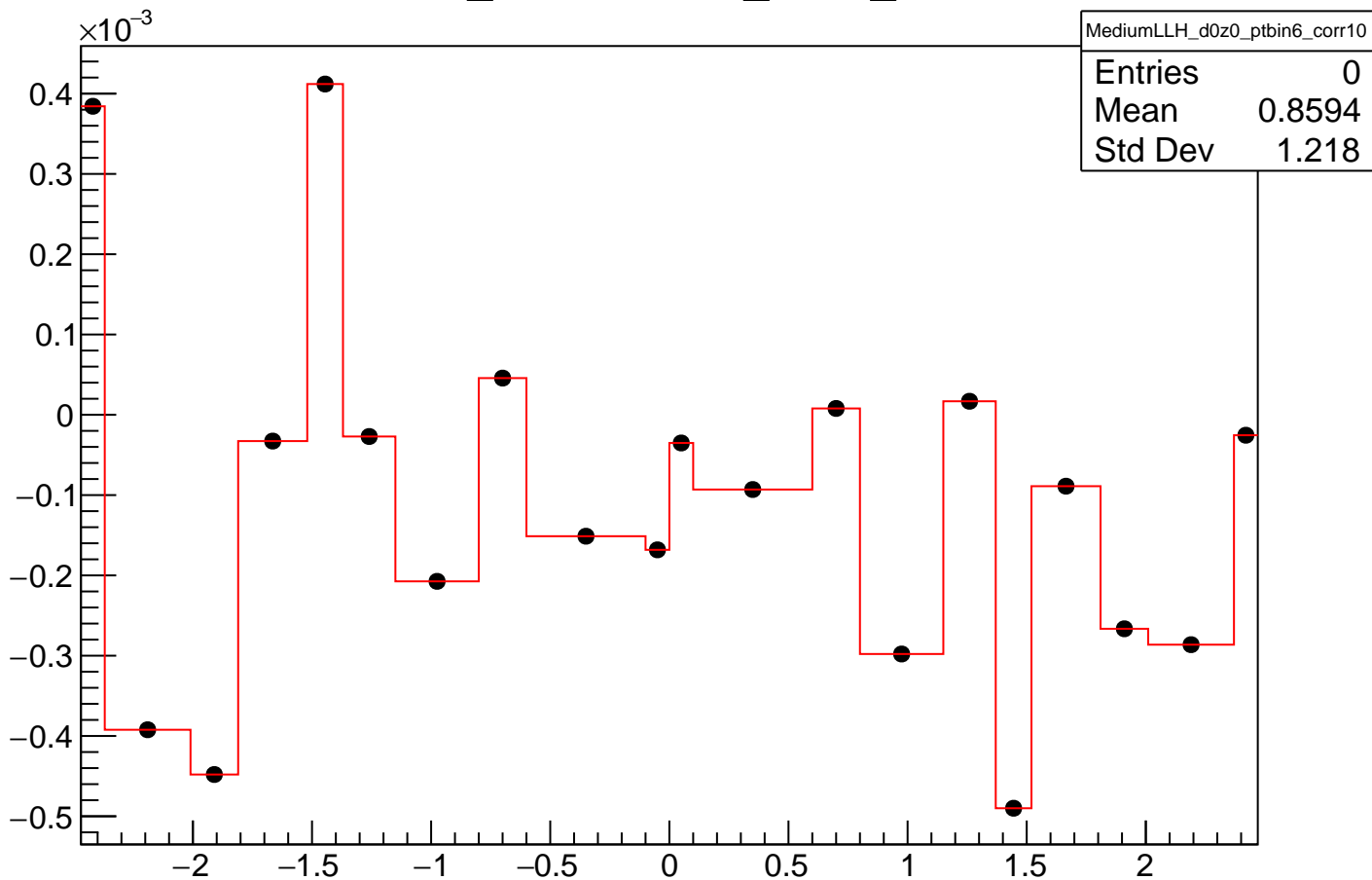
# FullSim\_MediumLLH\_d0z0\_corr10



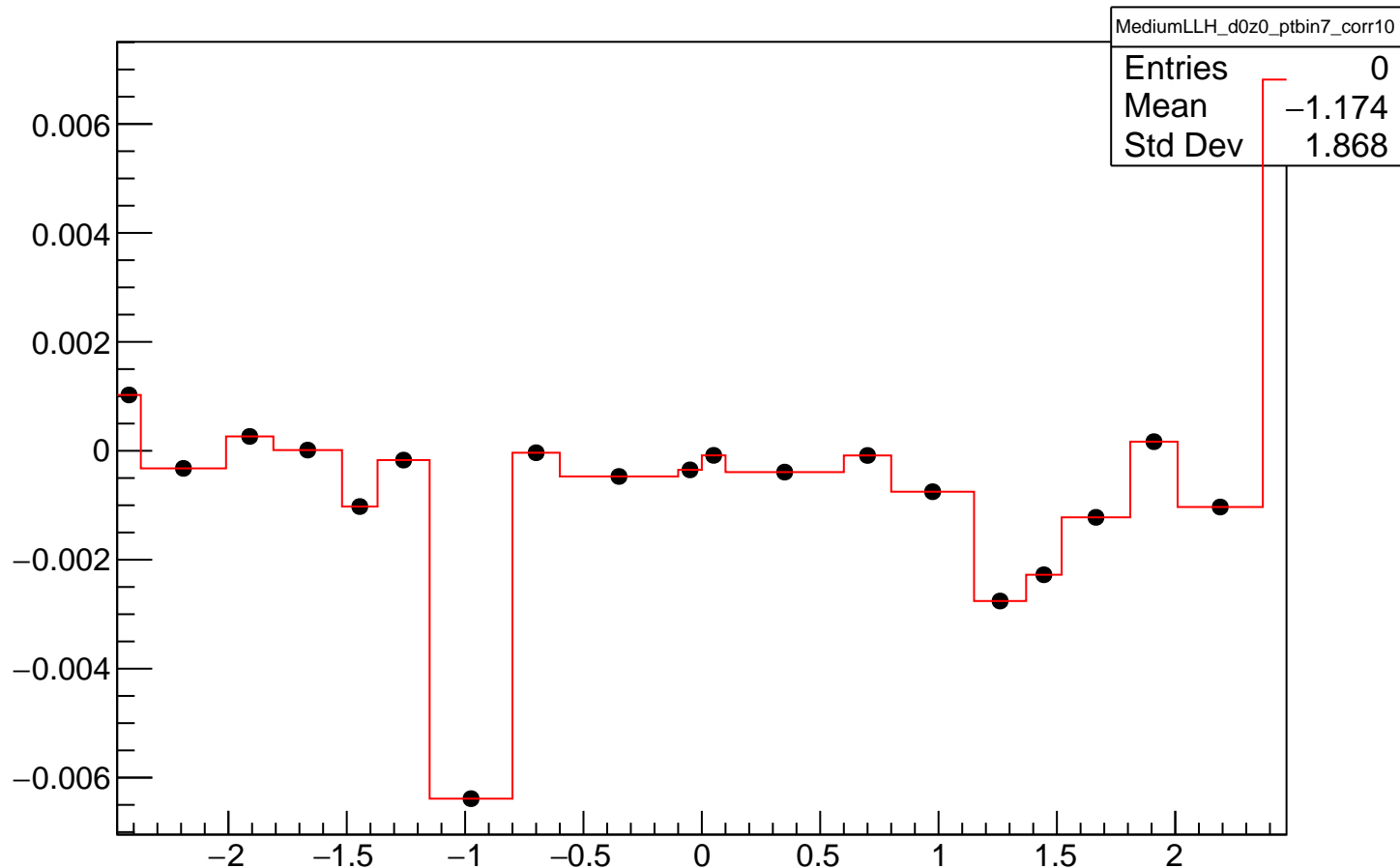
# FullSim\_MediumLLH\_d0z0\_corr10



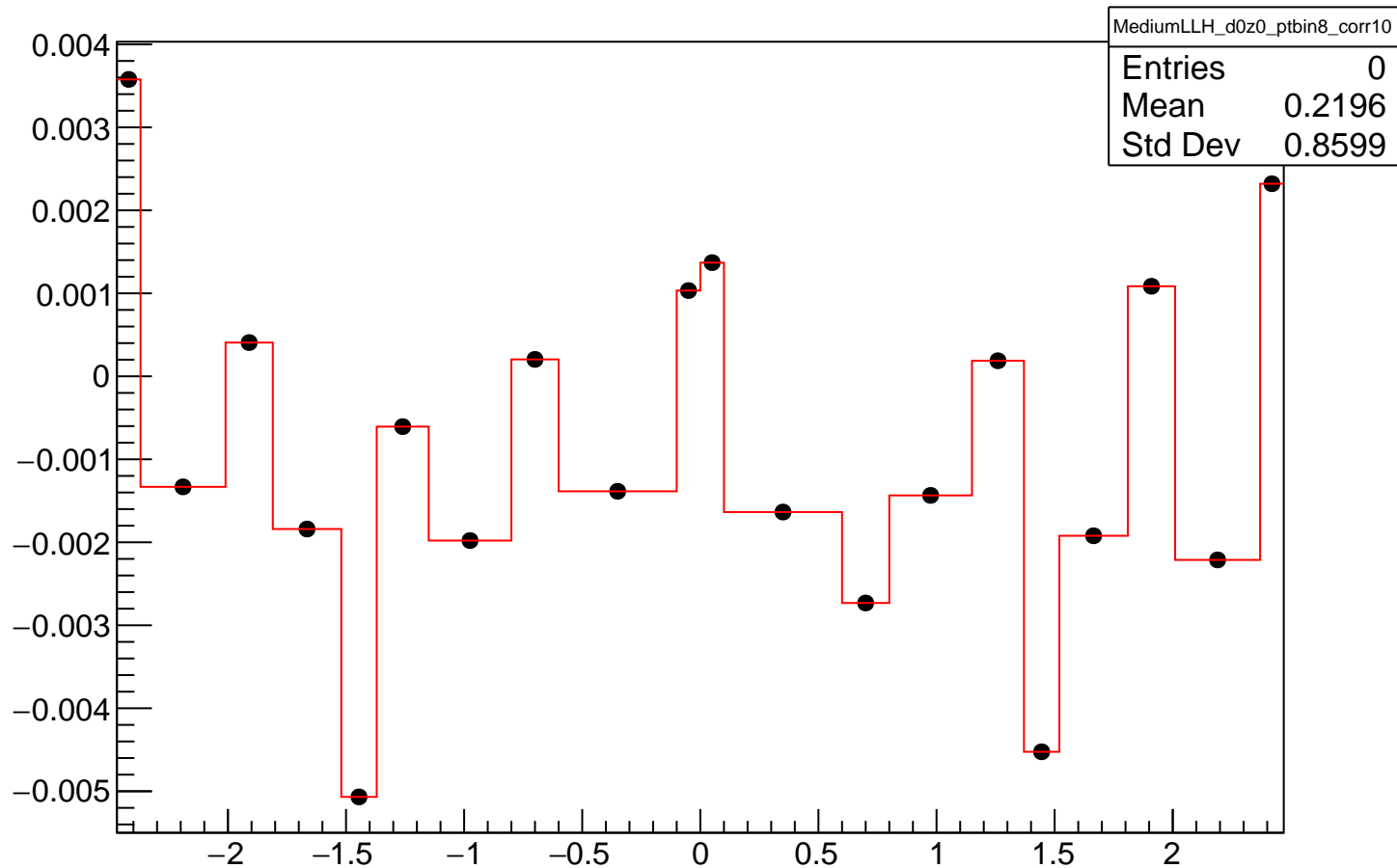
# FullSim\_MediumLLH\_d0z0\_corr10



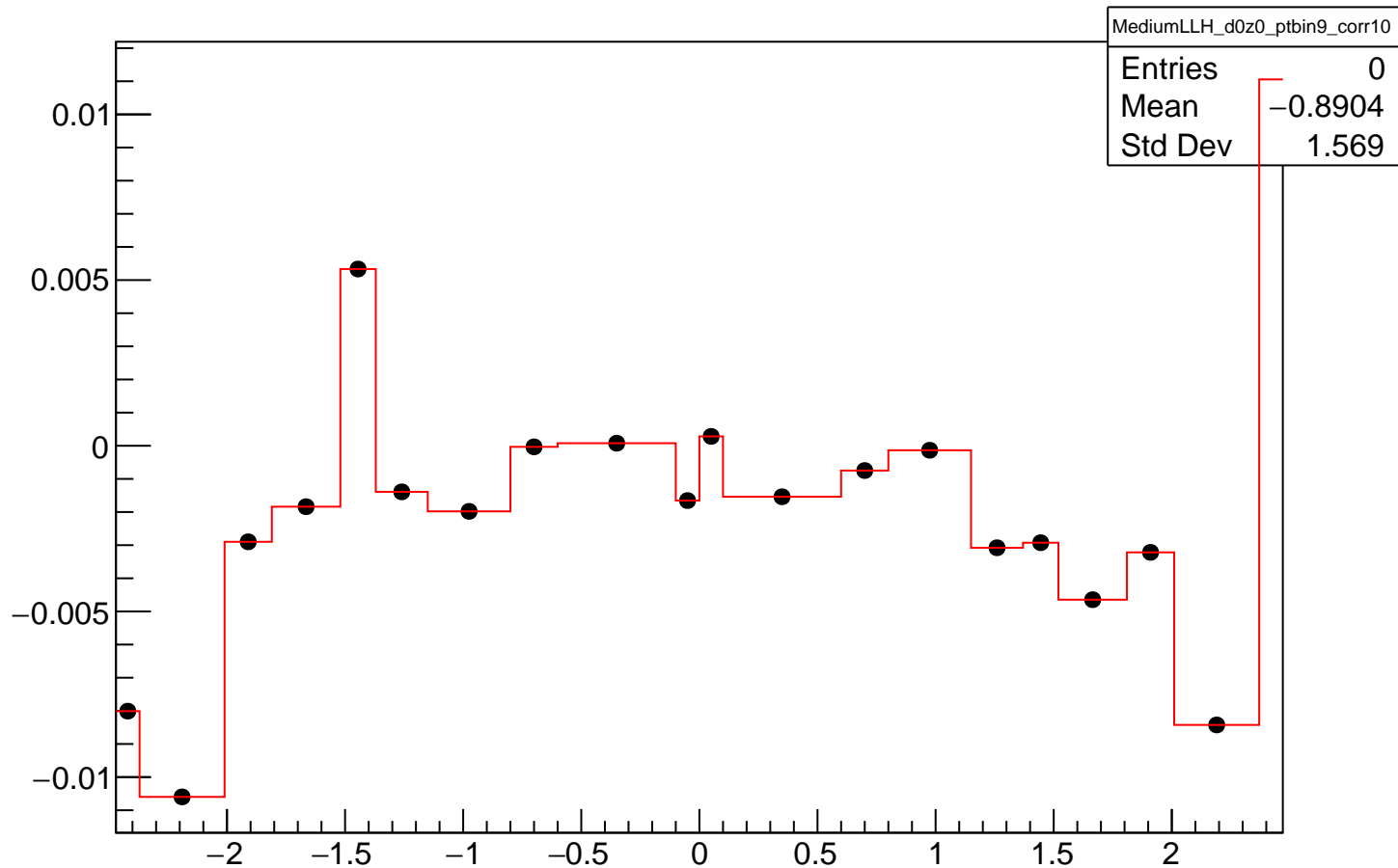
# FullSim\_MediumLLH\_d0z0\_corr10



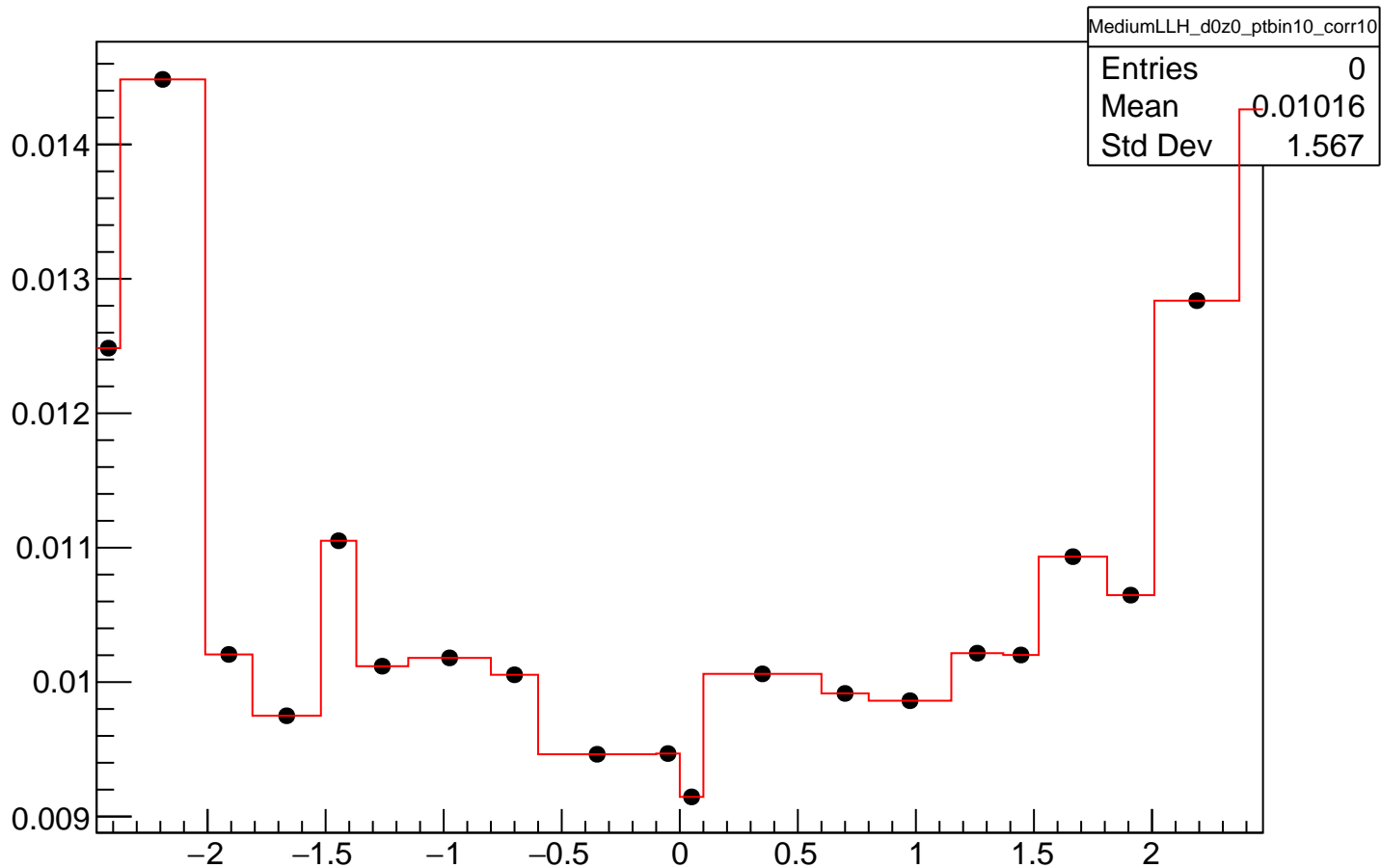
# FullSim\_MediumLLH\_d0z0\_corr10



# FullSim\_MediumLLH\_d0z0\_corr10

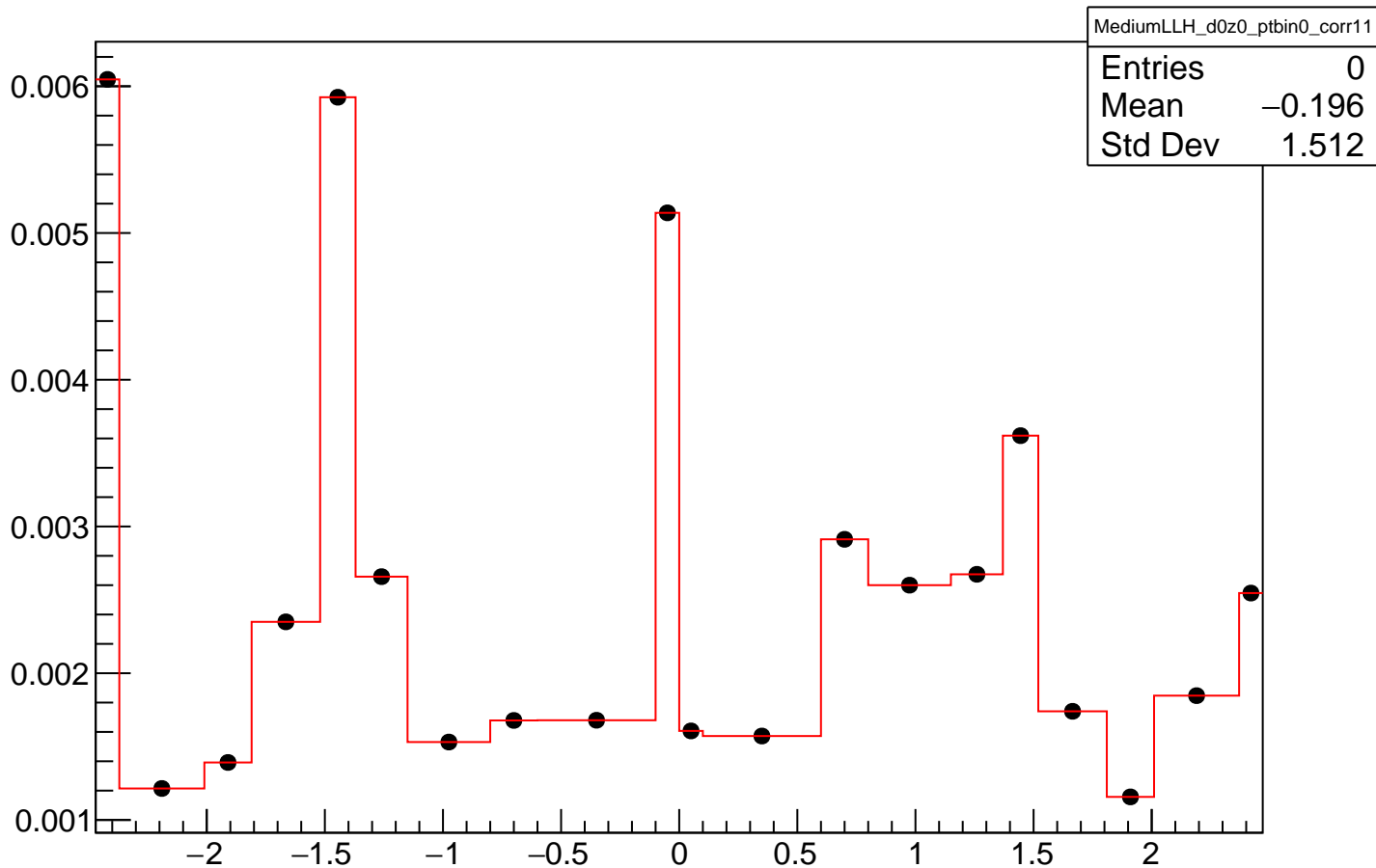


# FullSim\_MediumLLH\_d0z0\_corr10

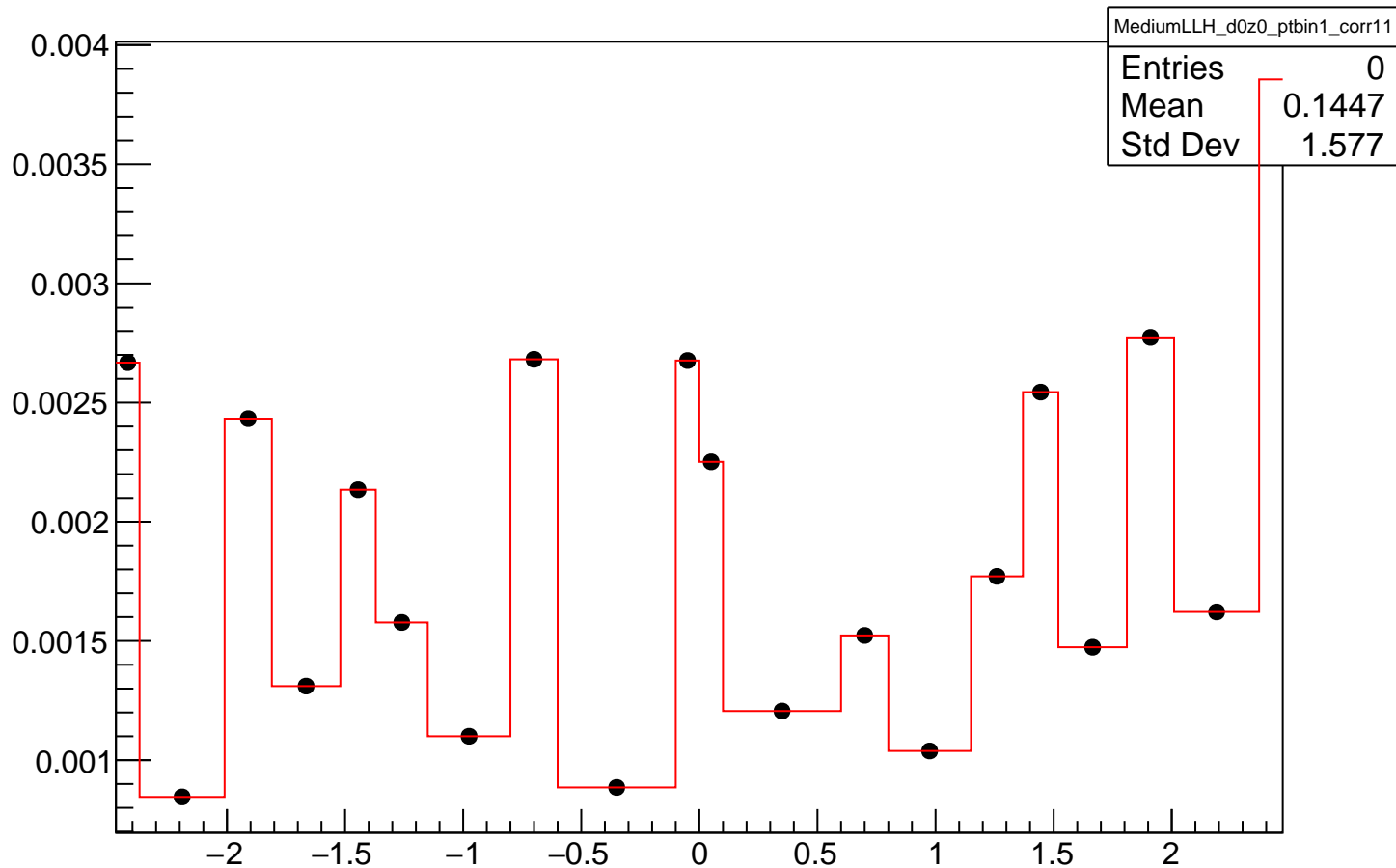




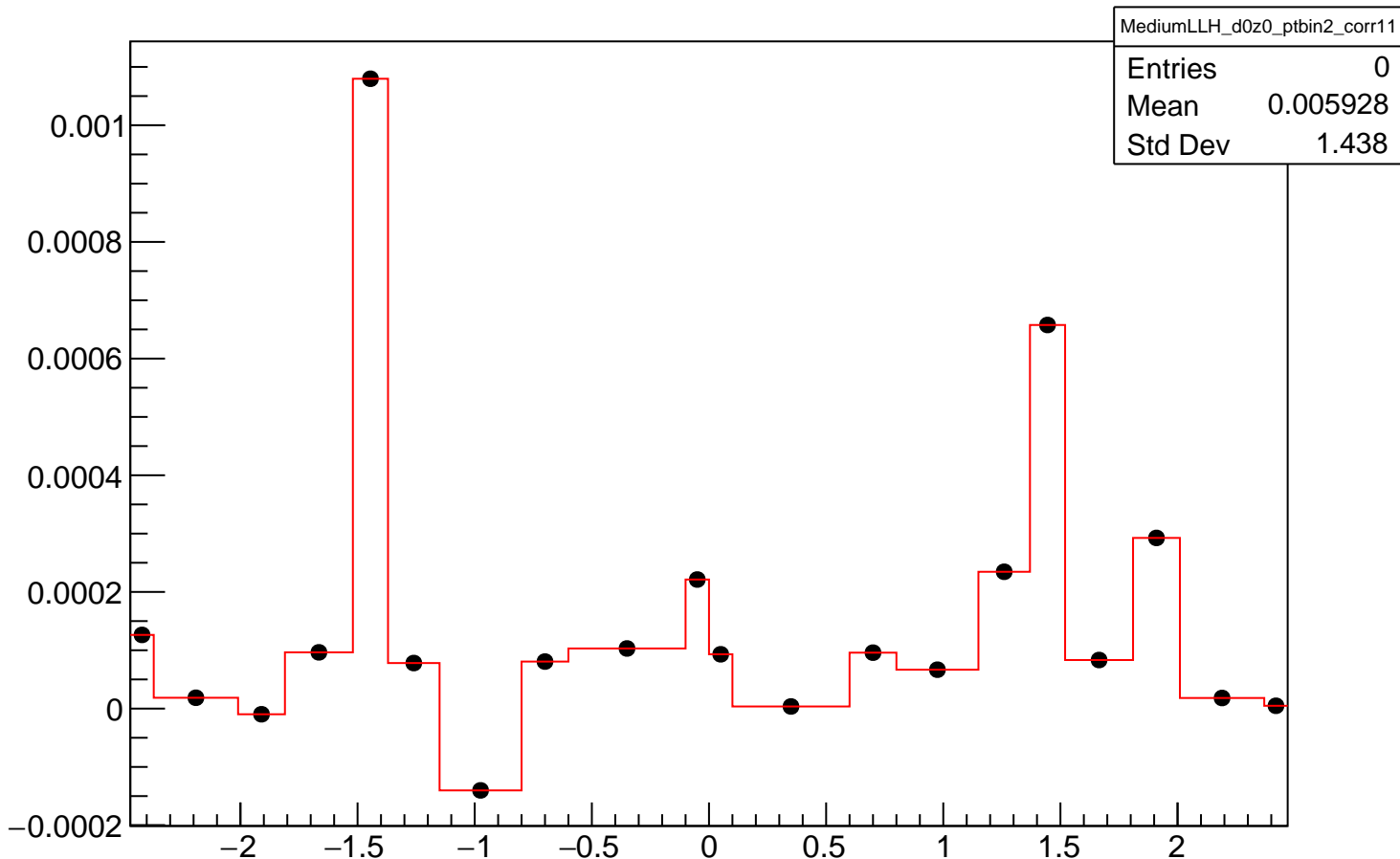
# FullSim\_MediumLLH\_d0z0\_corr11



# FullSim\_MediumLLH\_d0z0\_corr11

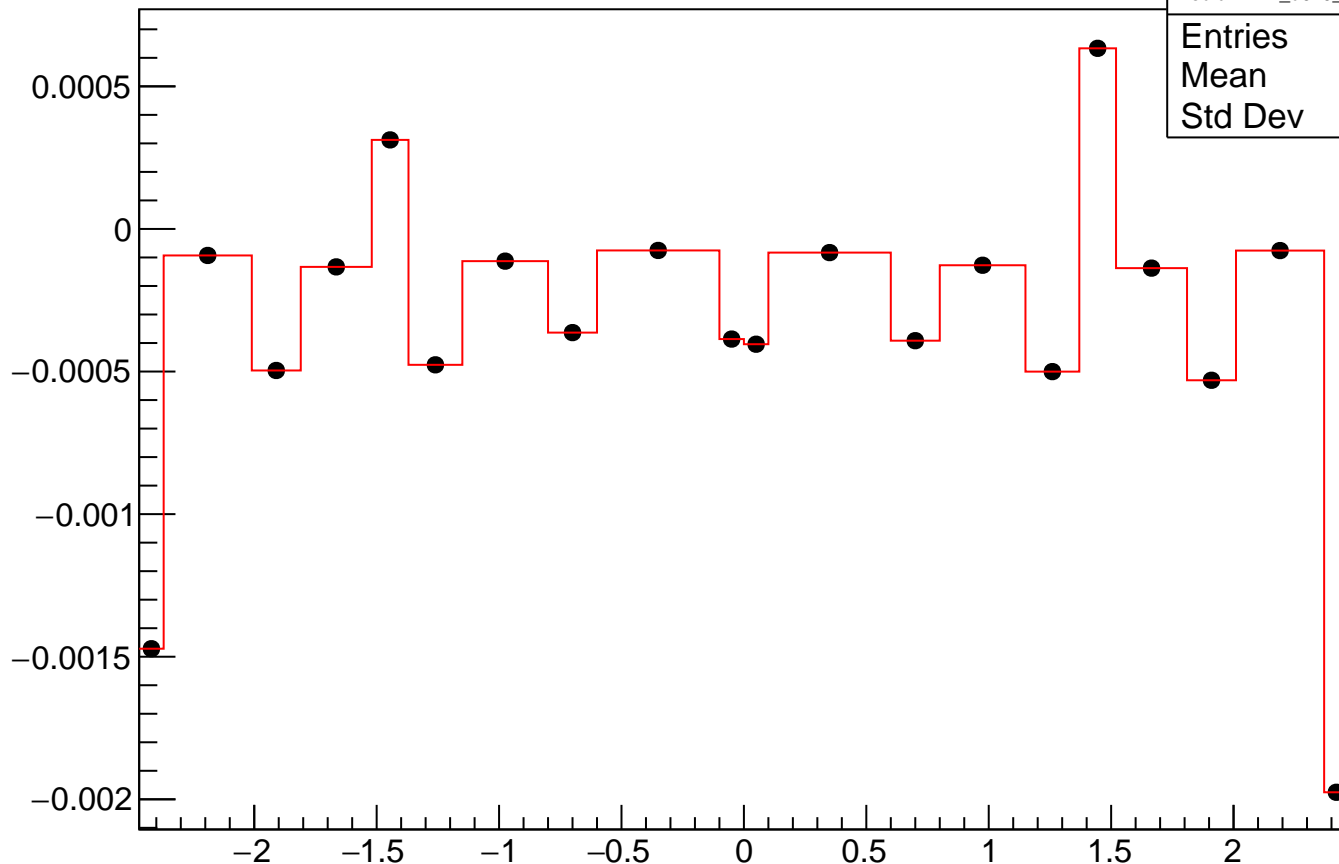


# FullSim\_MediumLLH\_d0z0\_corr11

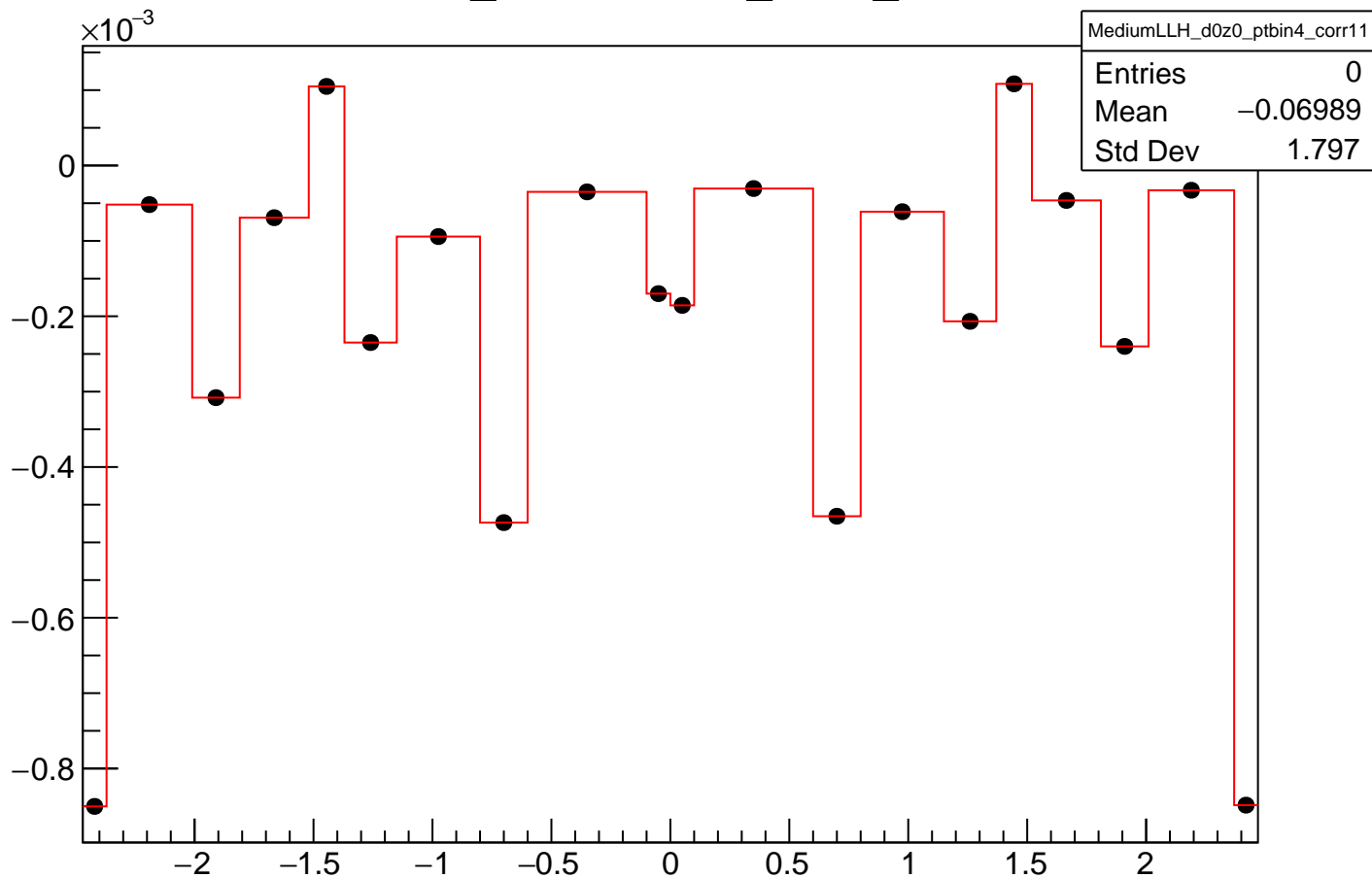


# FullSim\_MediumLLH\_d0z0\_corr11

MediumLLH_d0z0_ptbin3_corr11	
Entries	0
Mean	0.1244
Std Dev	1.927

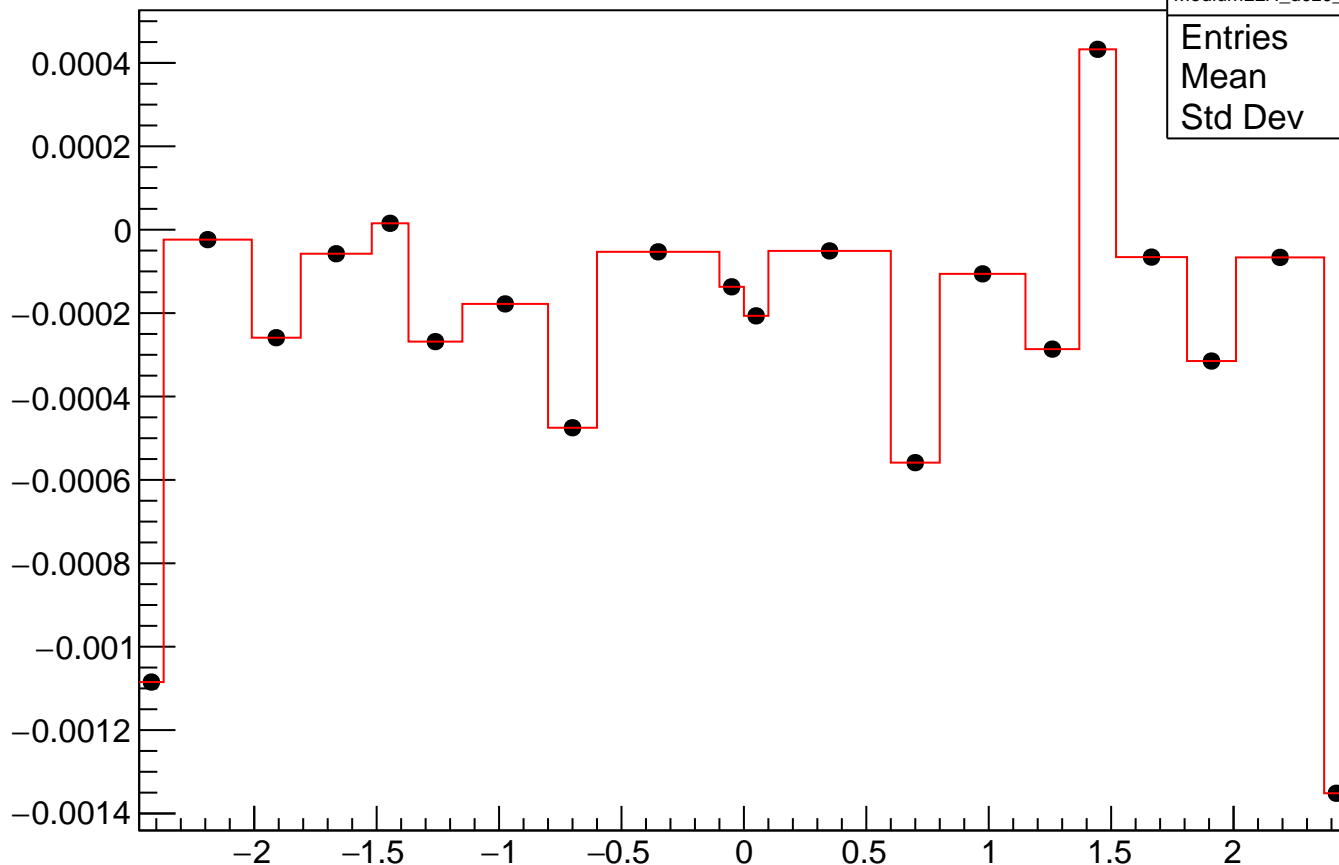


# FullSim\_MediumLLH\_d0z0\_corr11

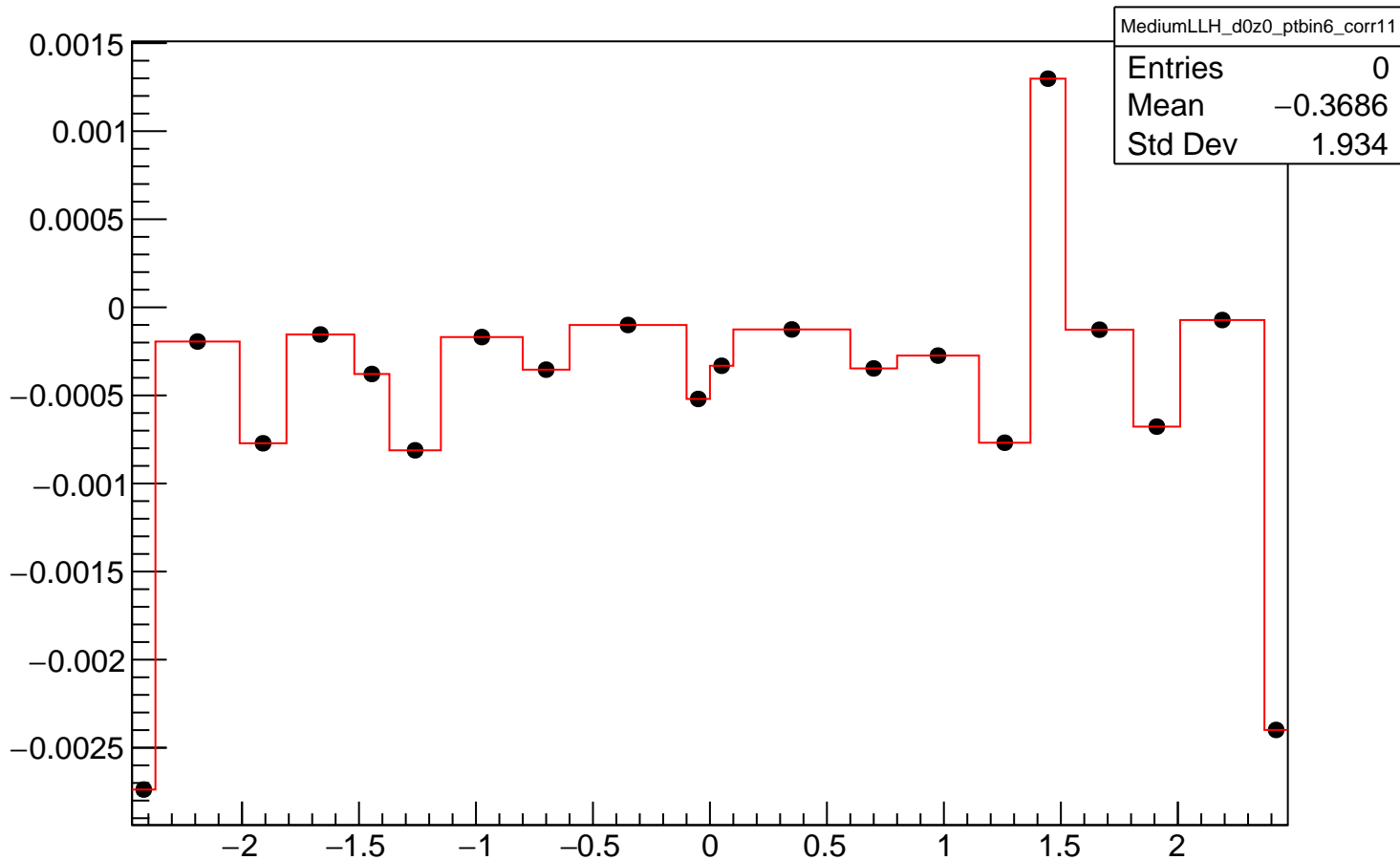


# FullSim\_MediumLLH\_d0z0\_corr11

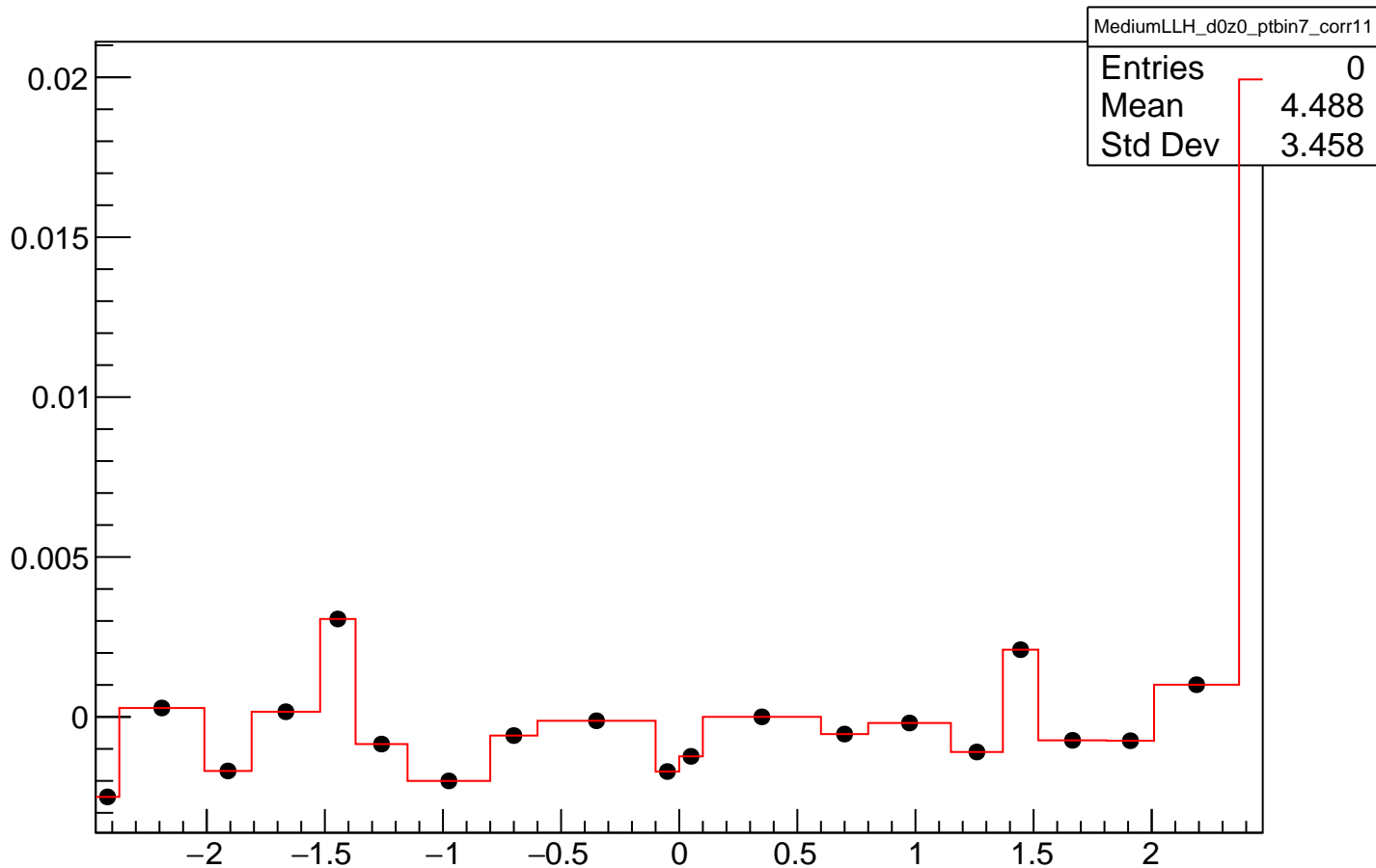
MediumLLH_d0z0_ptbin5_corr11	
Entries	0
Mean	0.053
Std Dev	1.872



# FullSim\_MediumLLH\_d0z0\_corr11

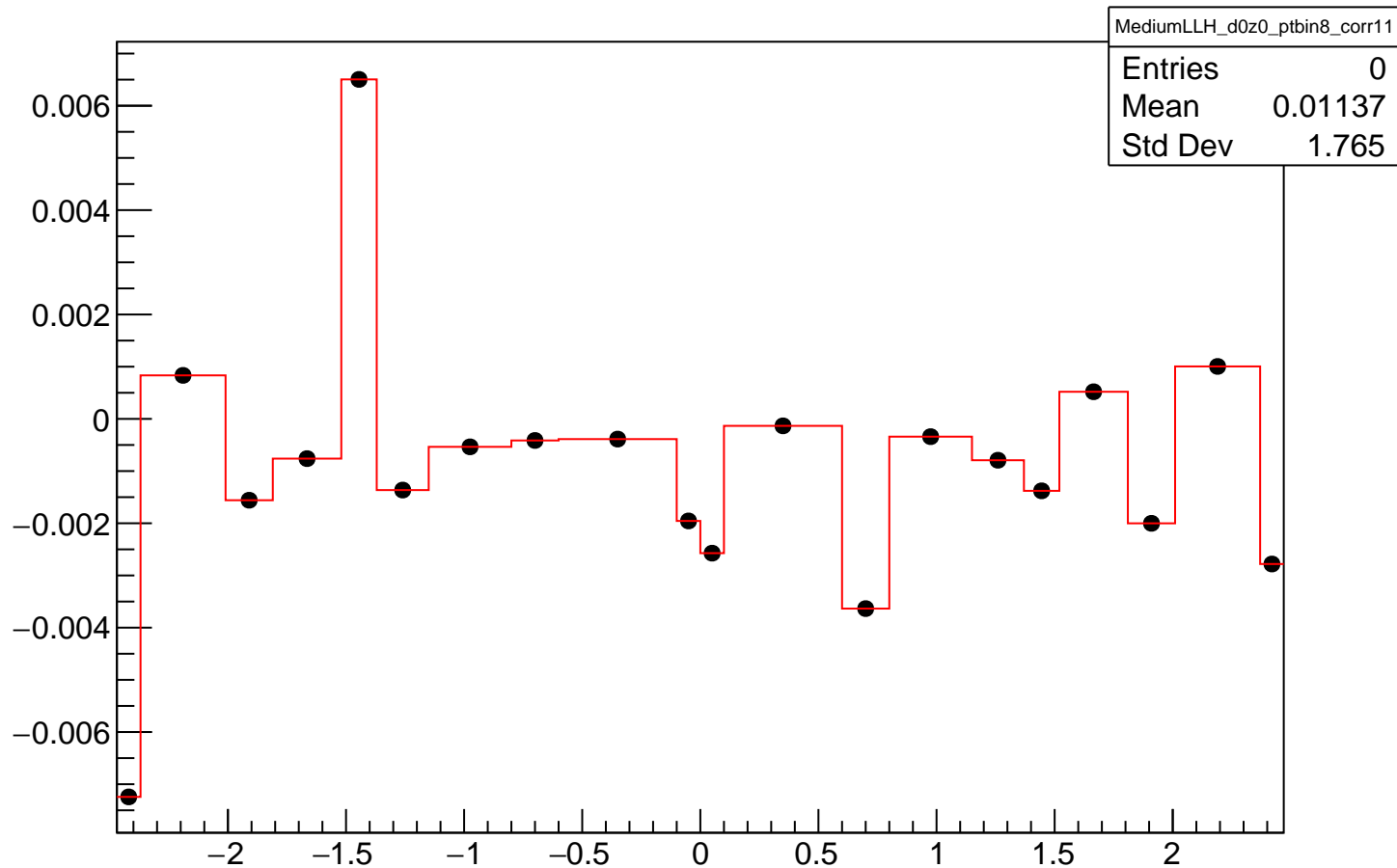


# FullSim\_MediumLLH\_d0z0\_corr11

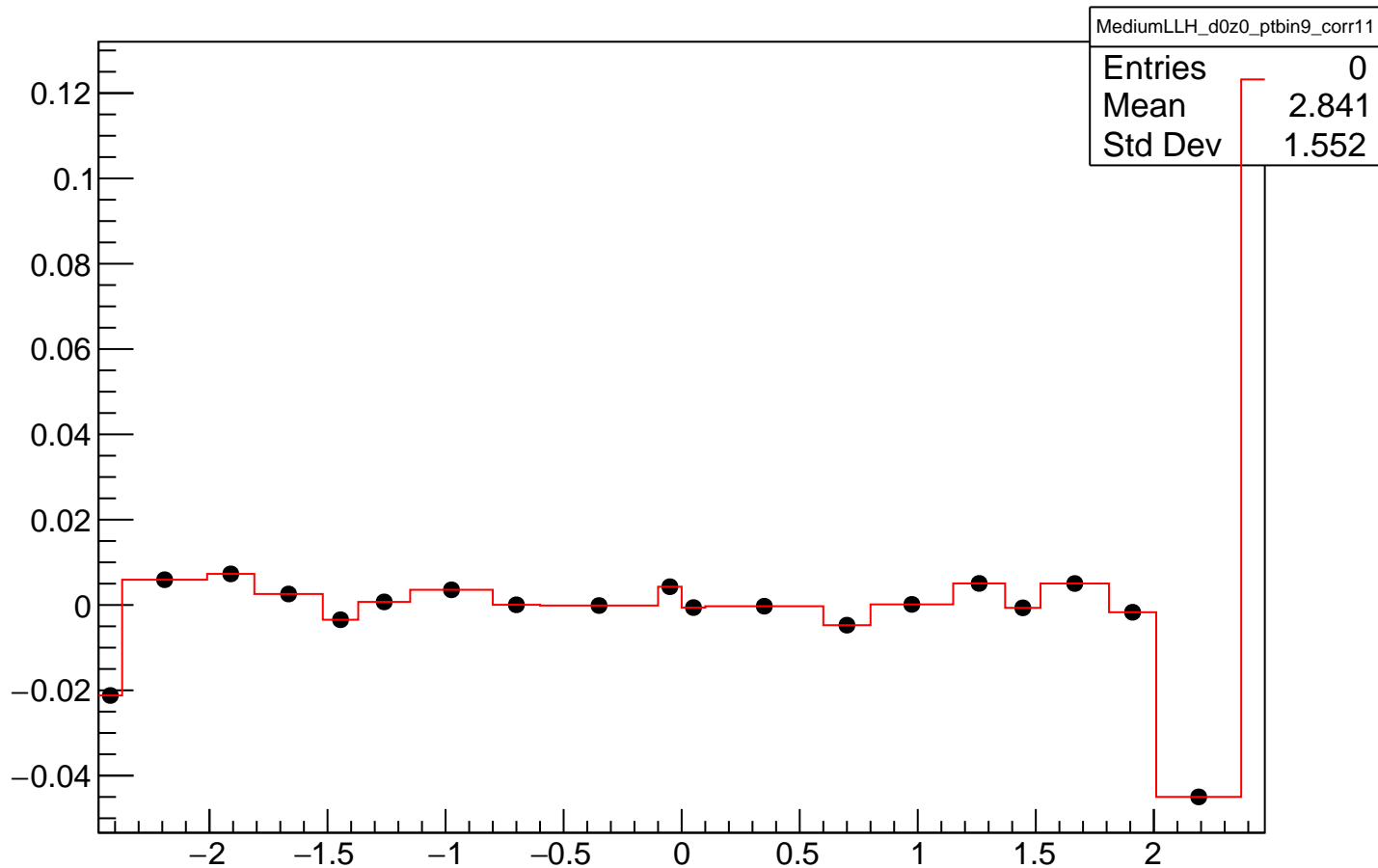




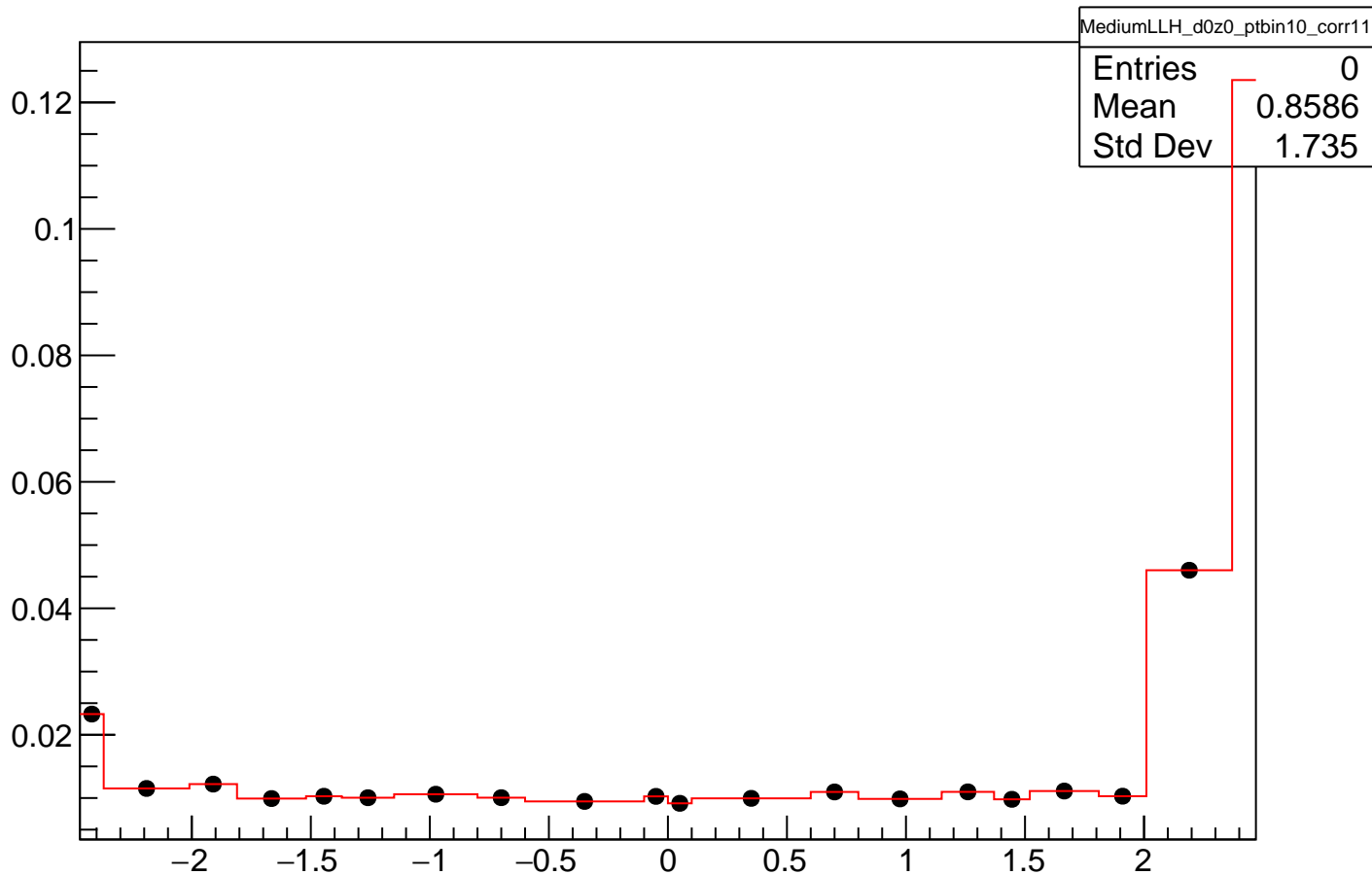
# FullSim\_MediumLLH\_d0z0\_corr11



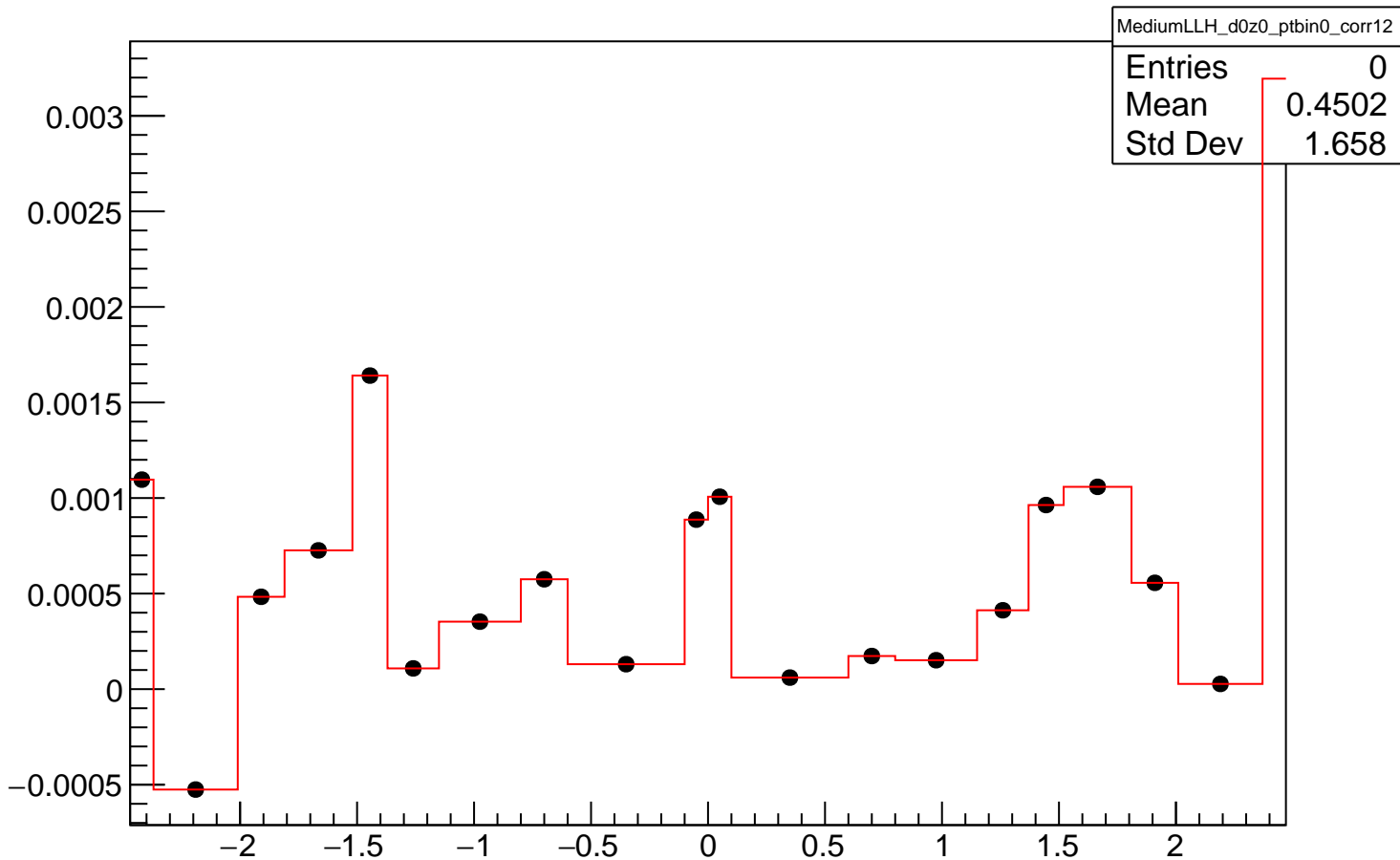
# FullSim\_MediumLLH\_d0z0\_corr11



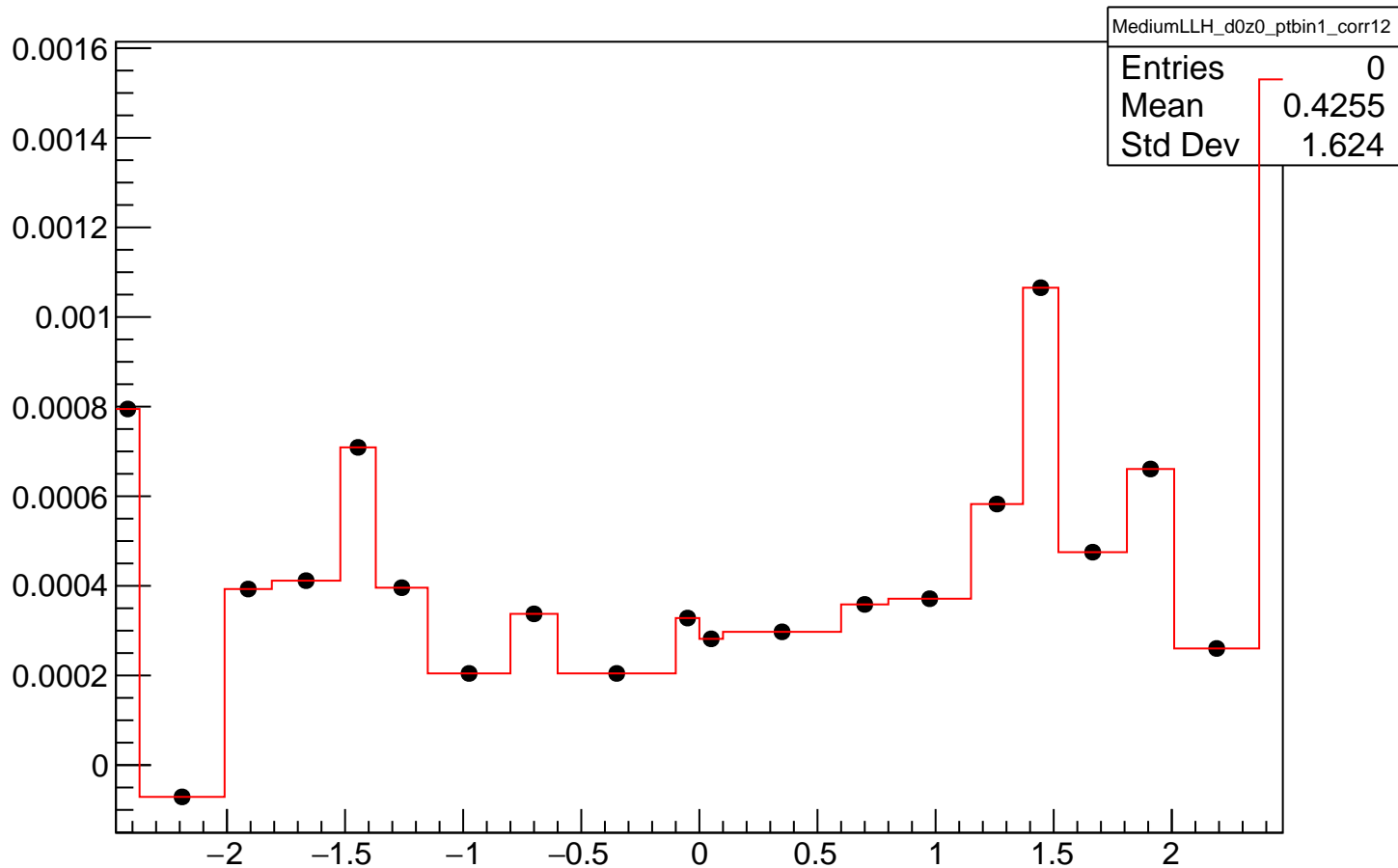
# FullSim\_MediumLLH\_d0z0\_corr11



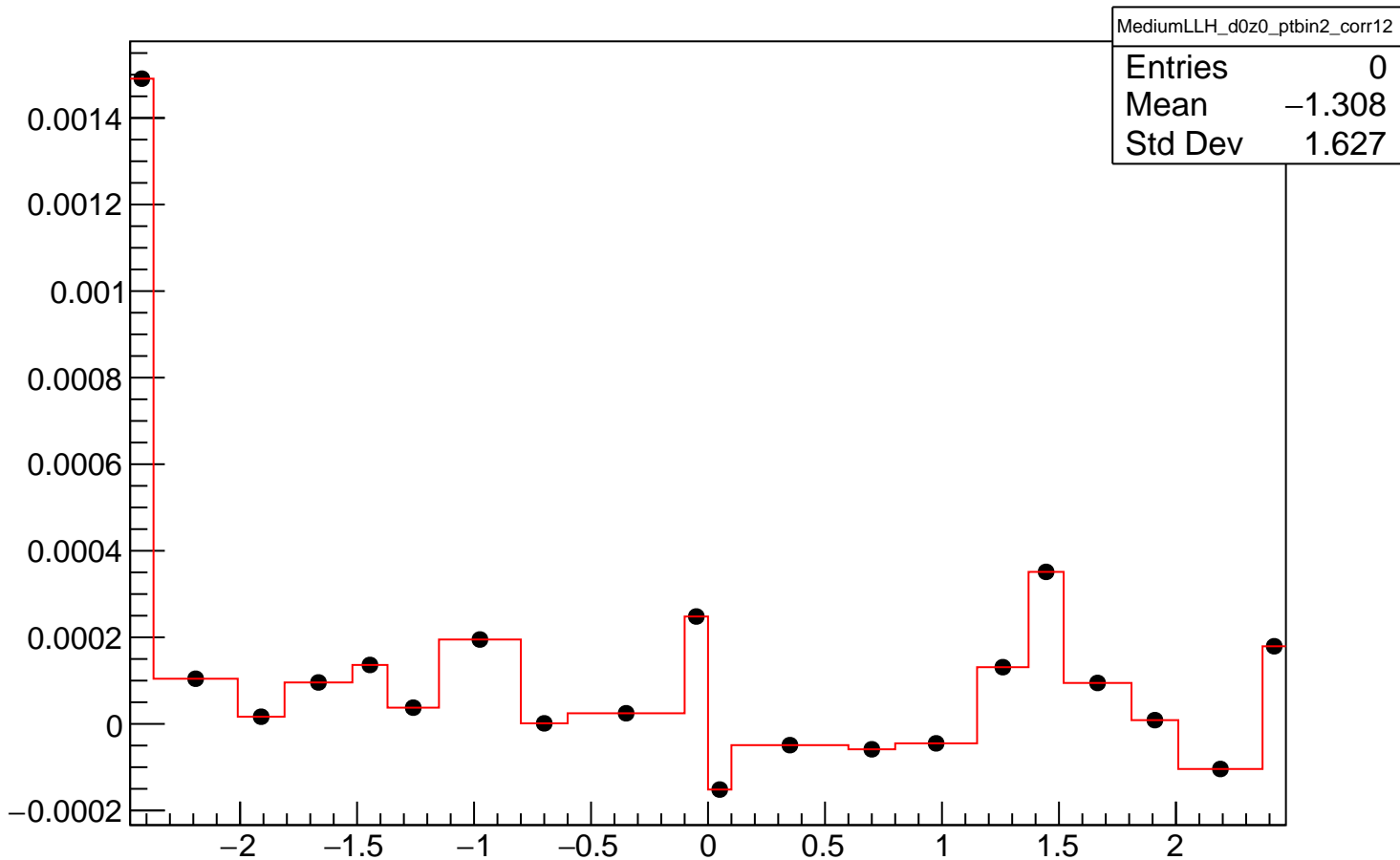
# FullSim\_MediumLLH\_d0z0\_corr12



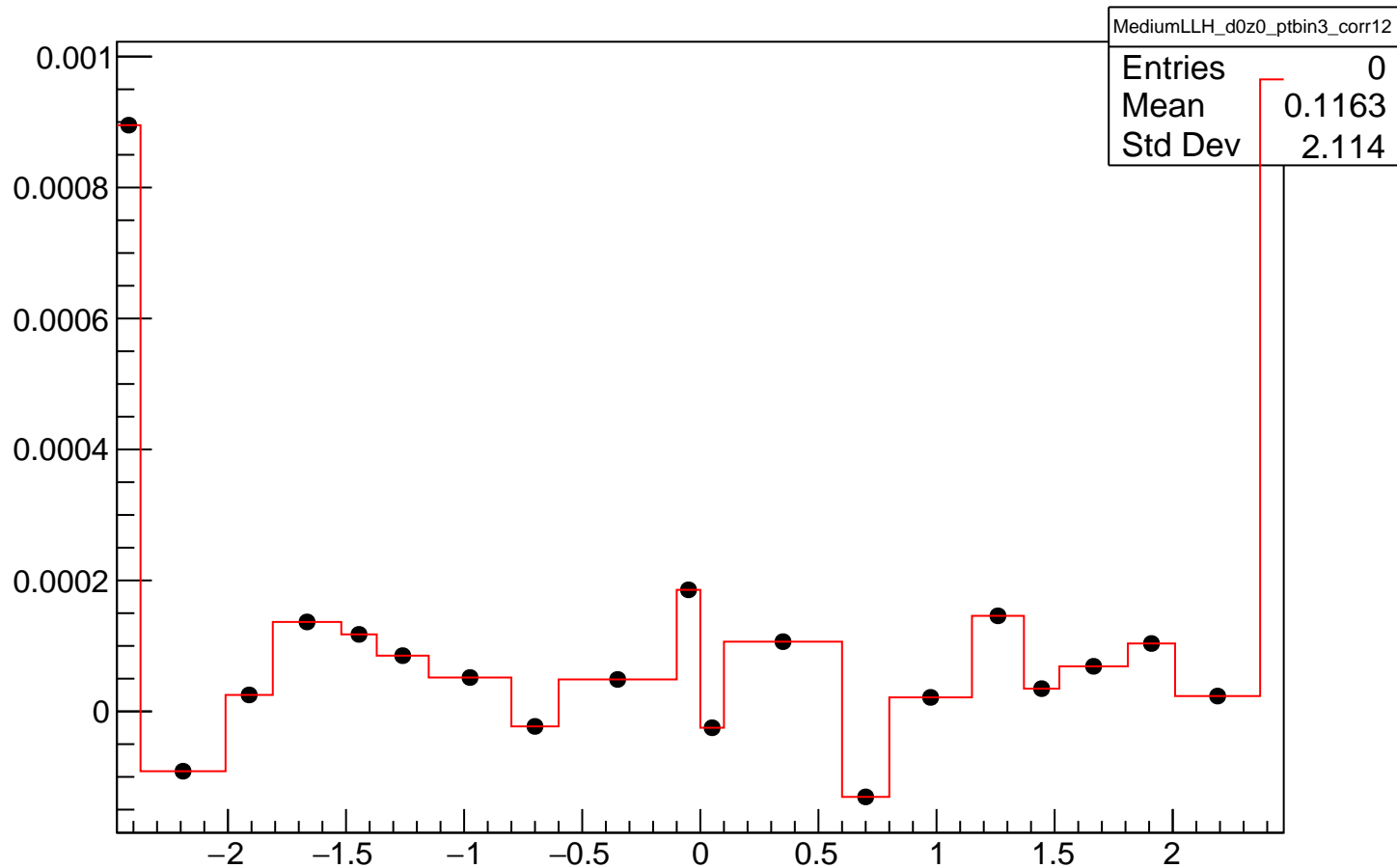
# FullSim\_MediumLLH\_d0z0\_corr12



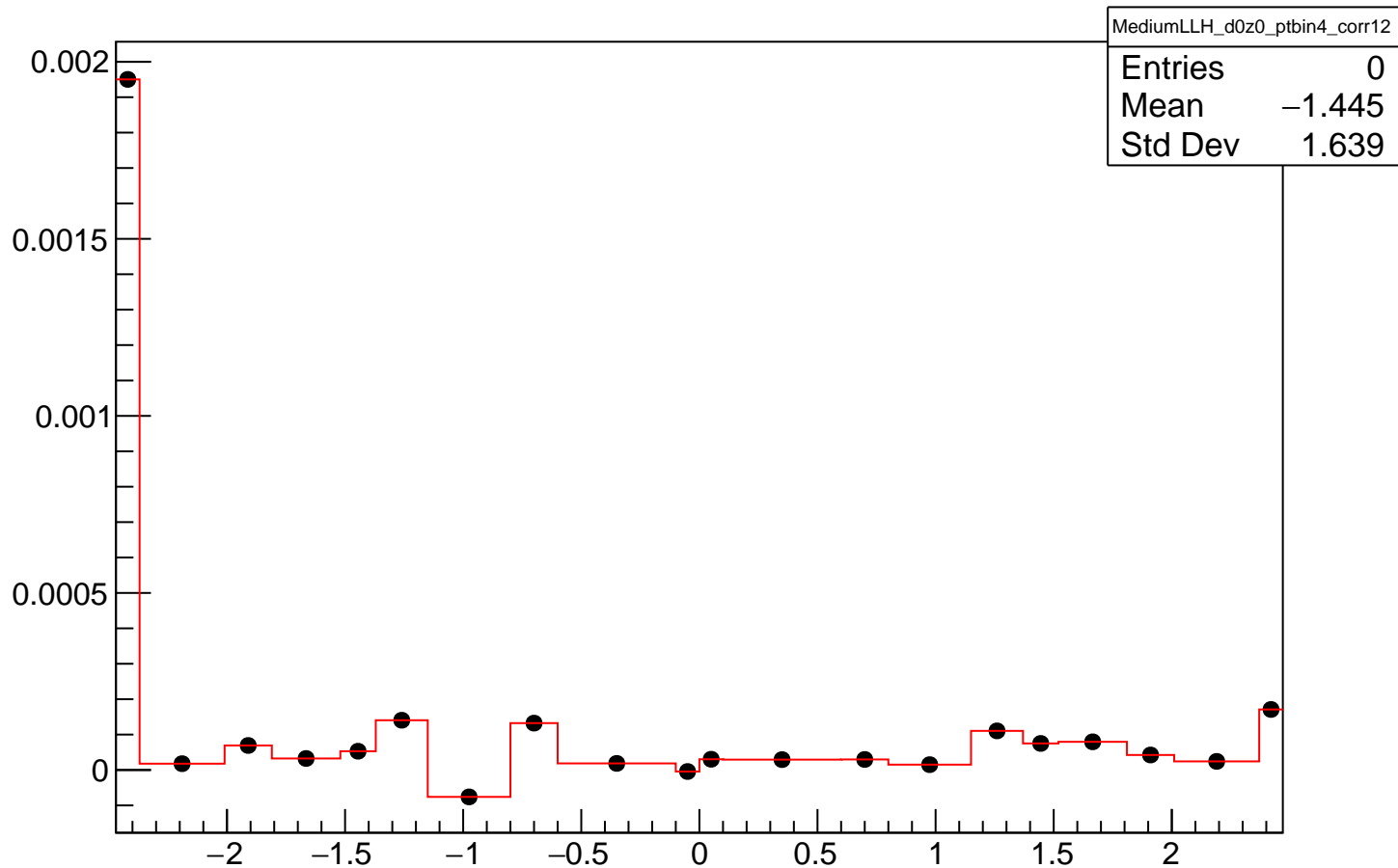
# FullSim\_MediumLLH\_d0z0\_corr12



# FullSim\_MediumLLH\_d0z0\_corr12

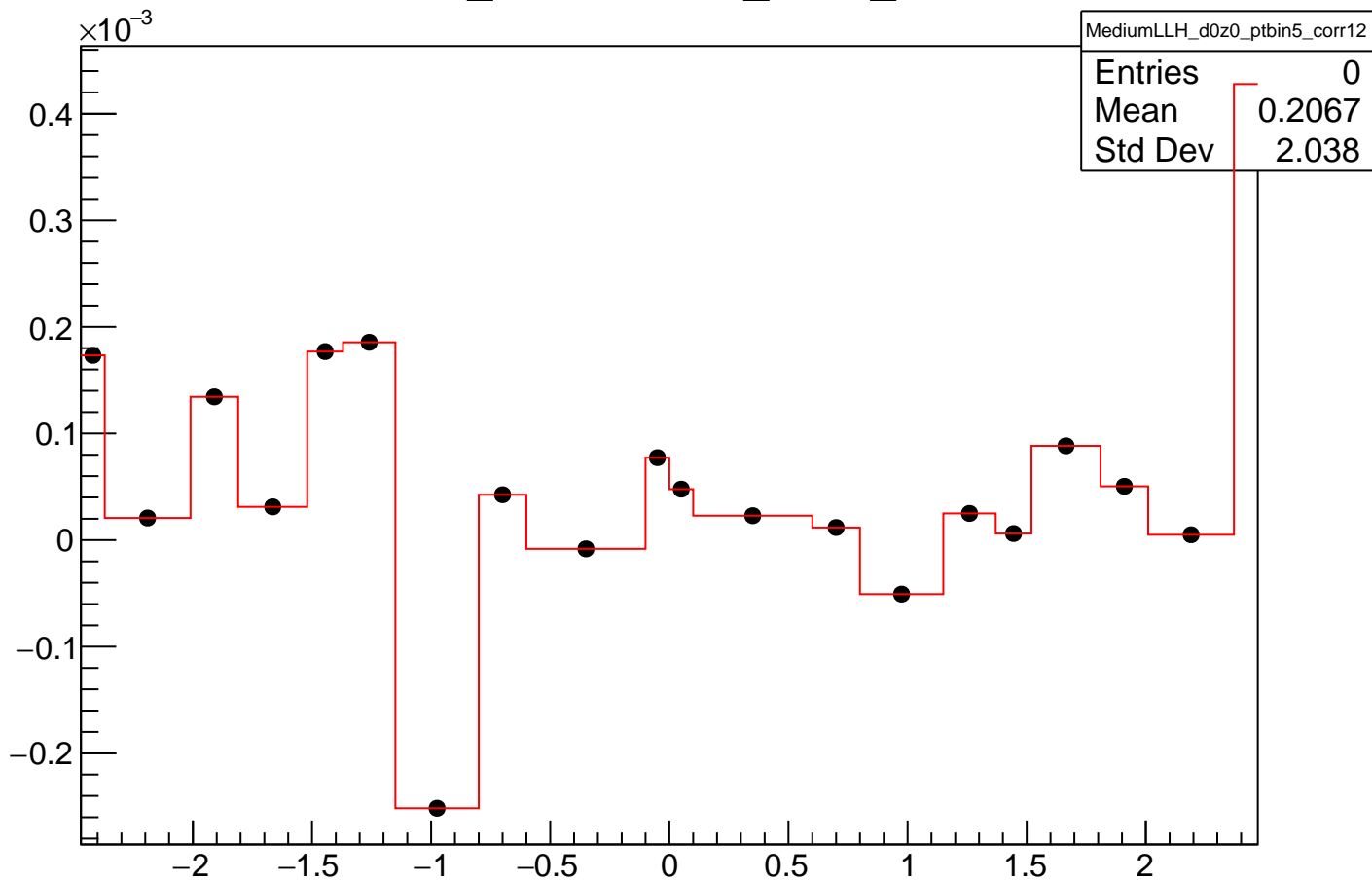


# FullSim\_MediumLLH\_d0z0\_corr12

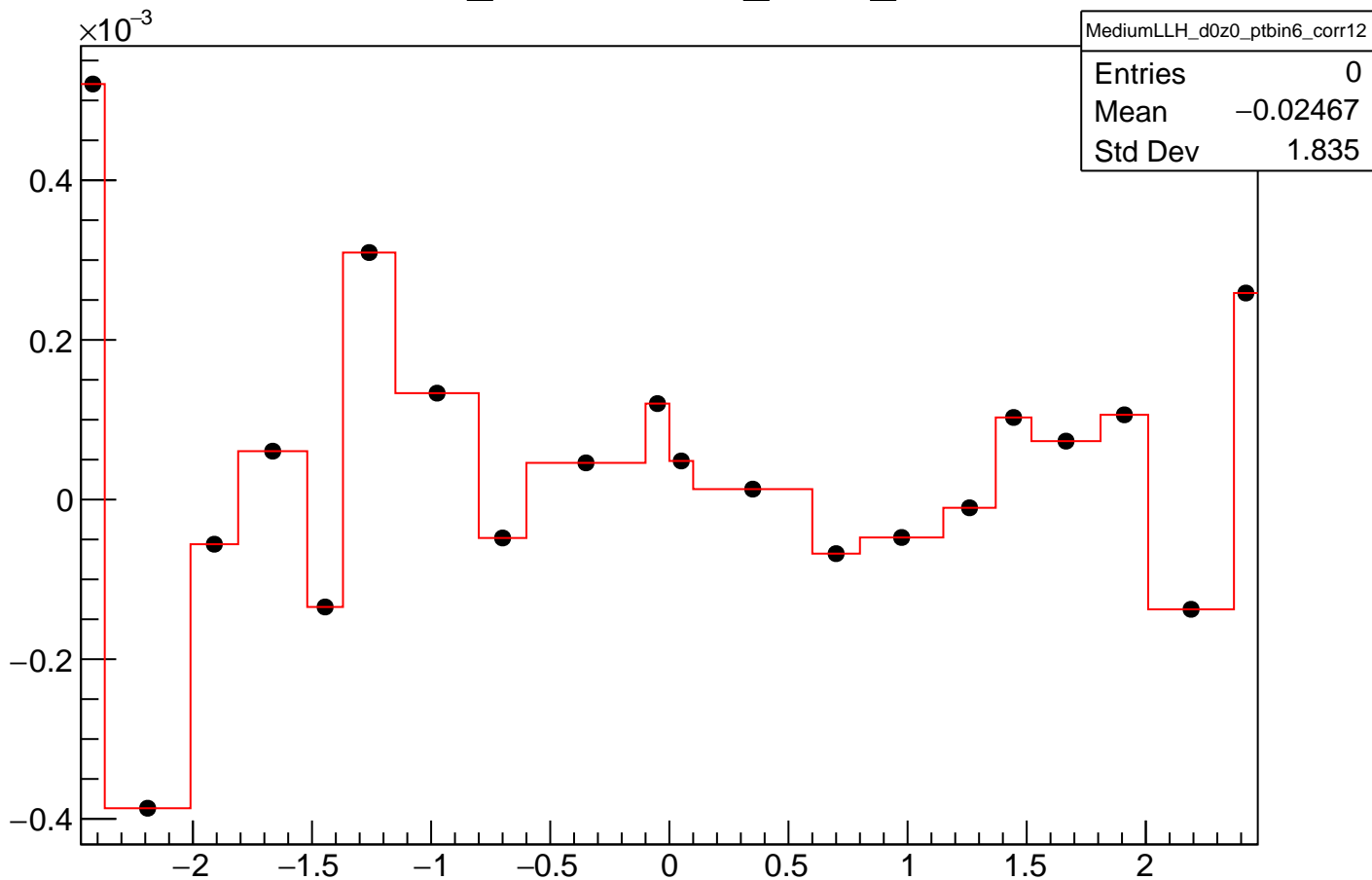




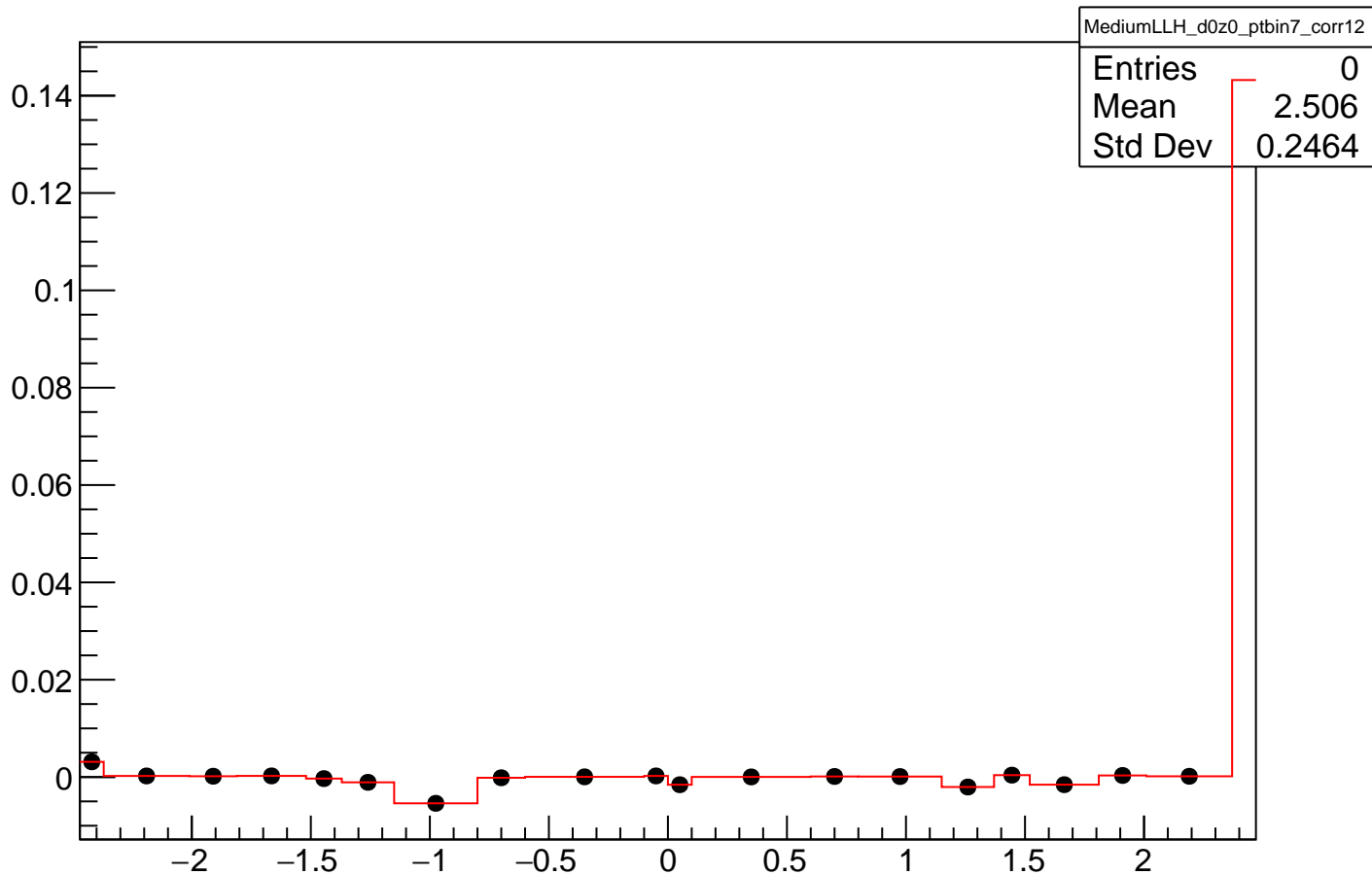
# FullSim\_MediumLLH\_d0z0\_corr12



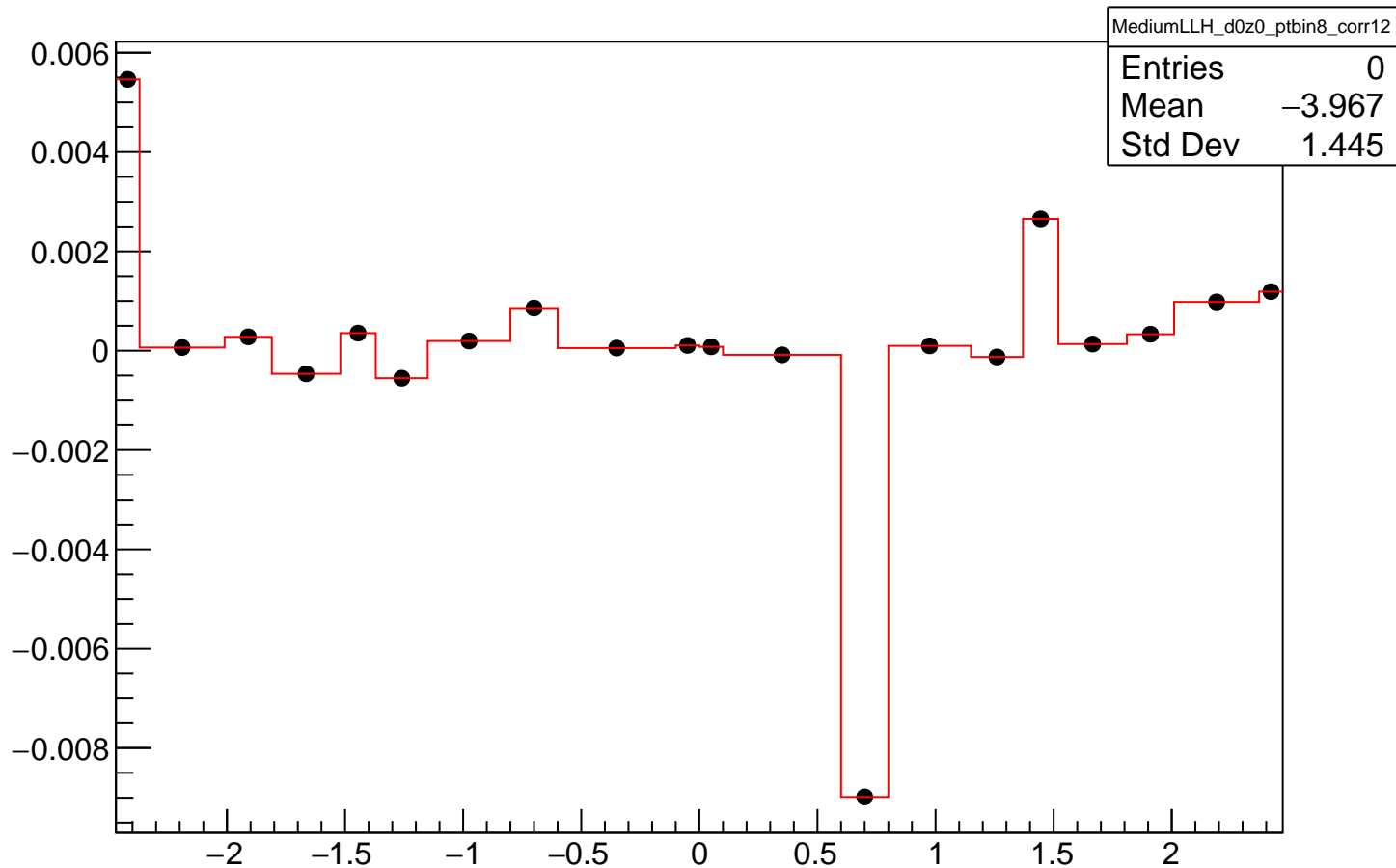
# FullSim\_MediumLLH\_d0z0\_corr12



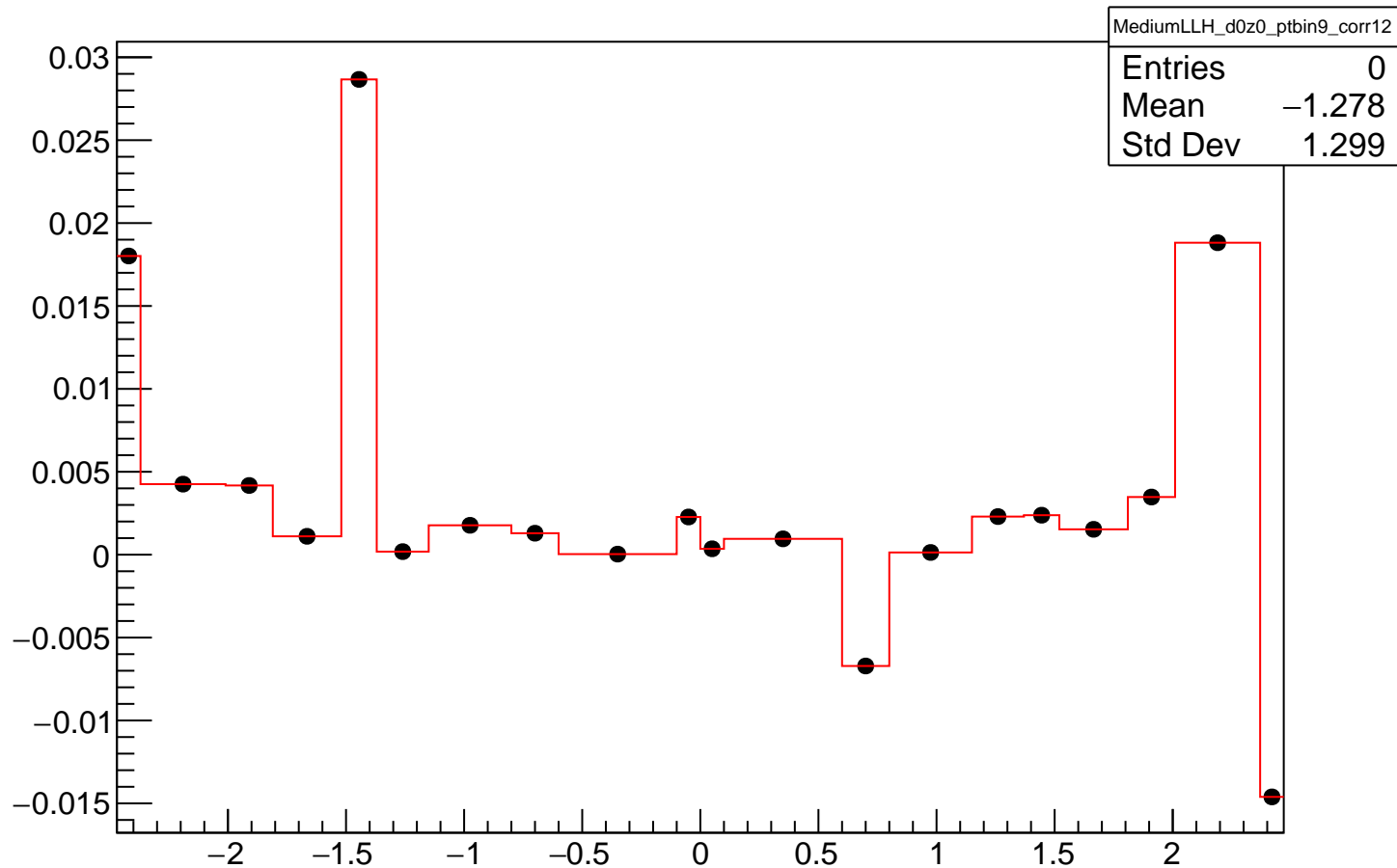
# FullSim\_MediumLLH\_d0z0\_corr12



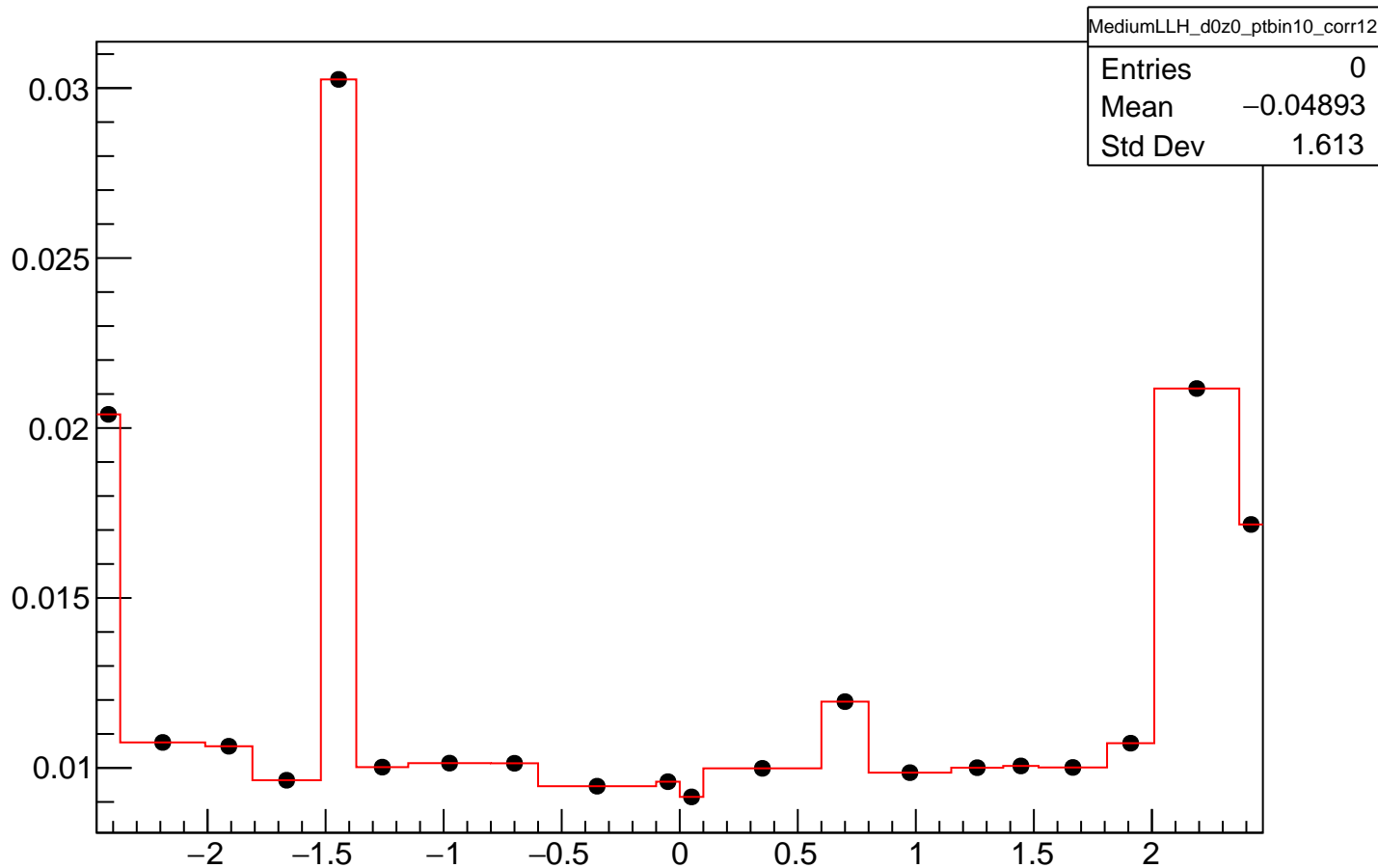
# FullSim\_MediumLLH\_d0z0\_corr12



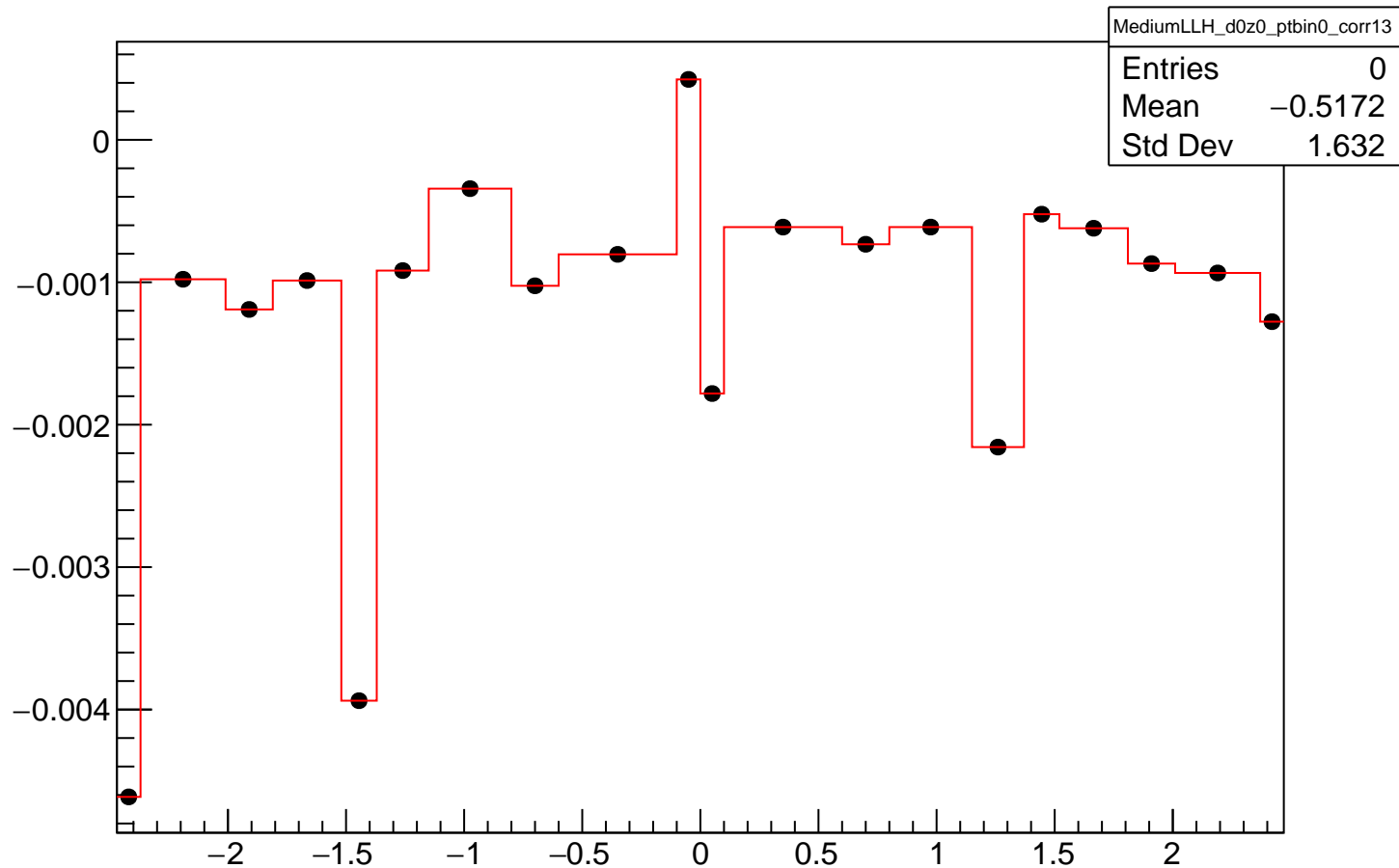
# FullSim\_MediumLLH\_d0z0\_corr12



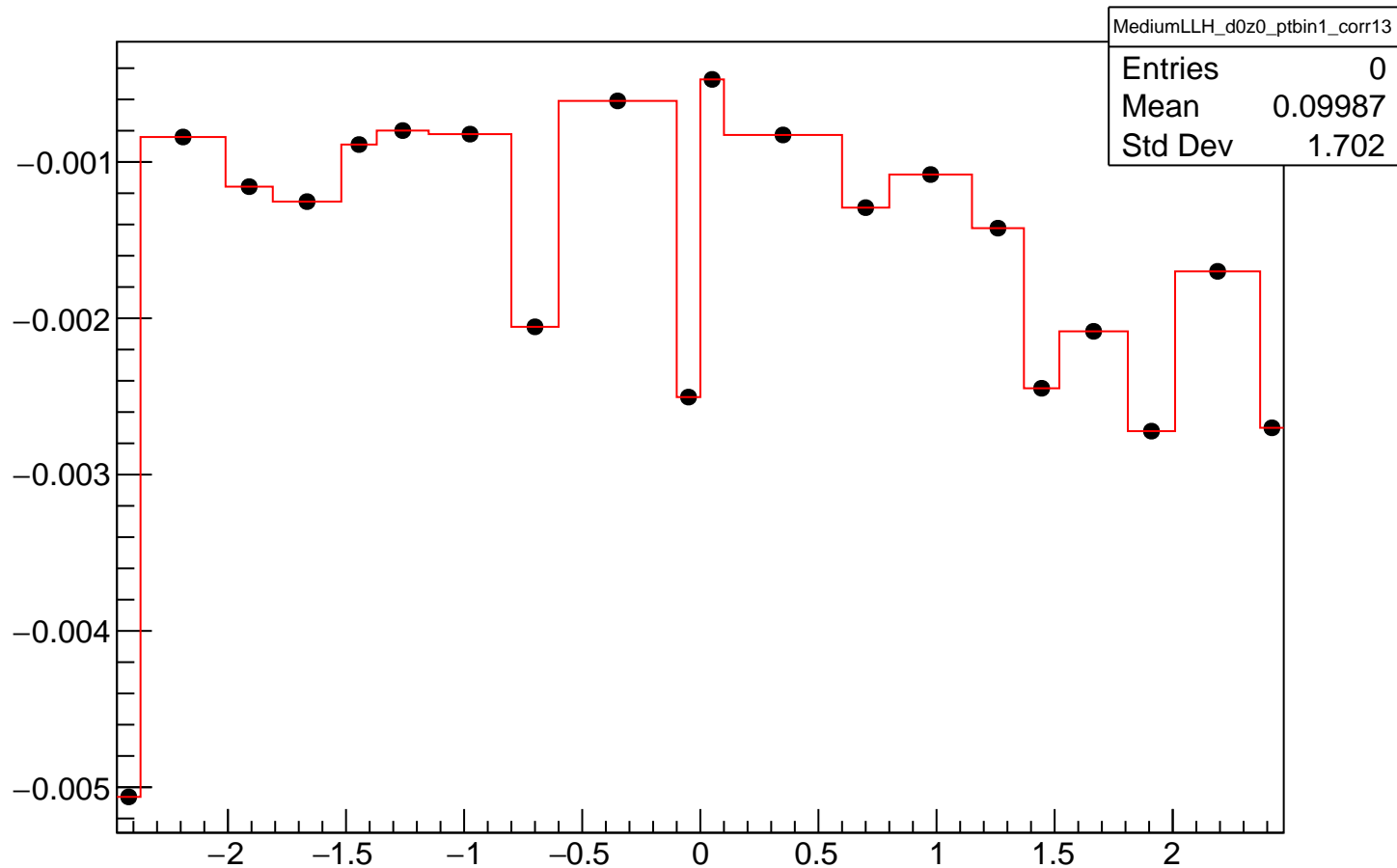
# FullSim\_MediumLLH\_d0z0\_corr12



# FullSim\_MediumLLH\_d0z0\_corr13

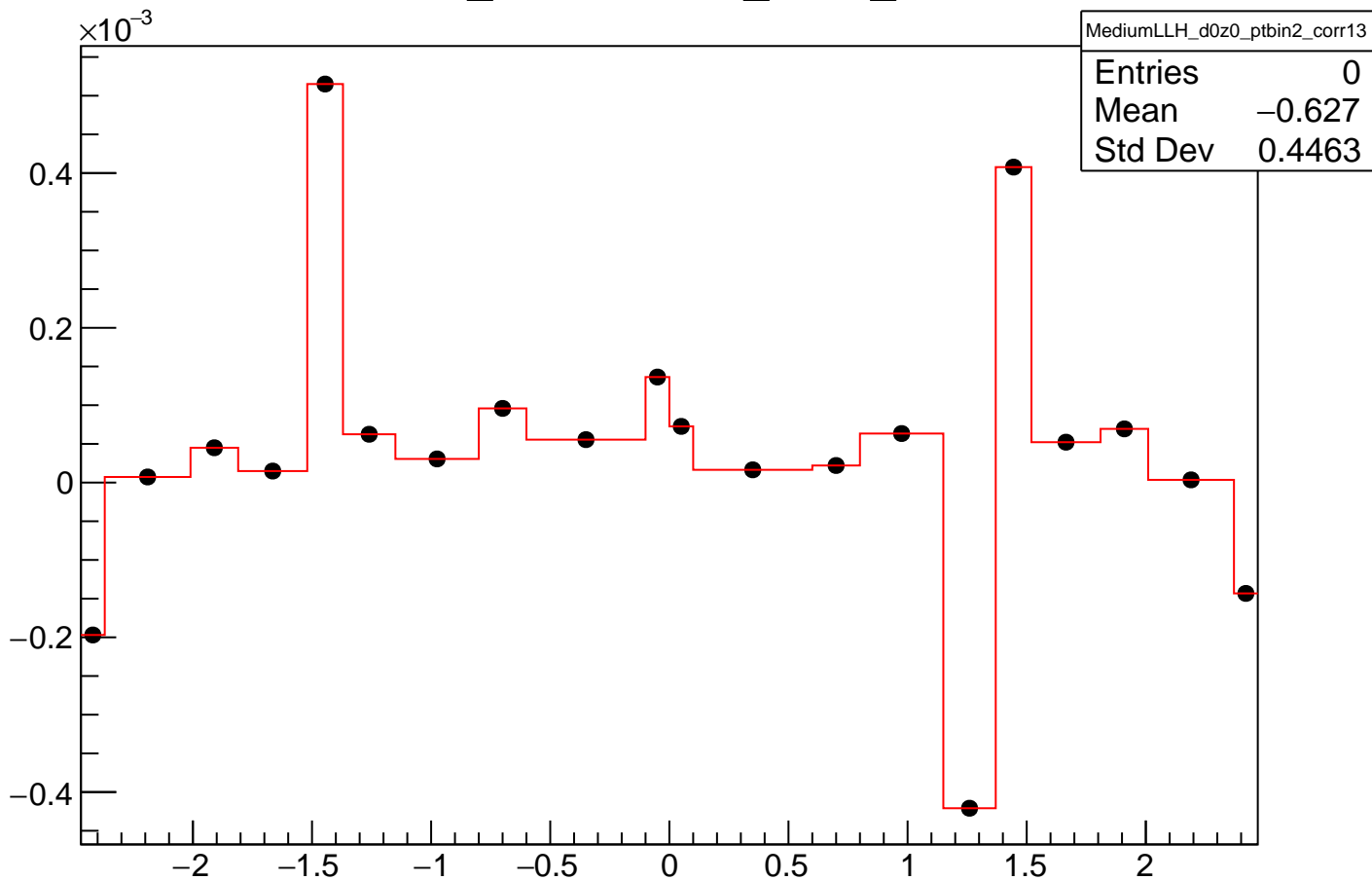


# FullSim\_MediumLLH\_d0z0\_corr13





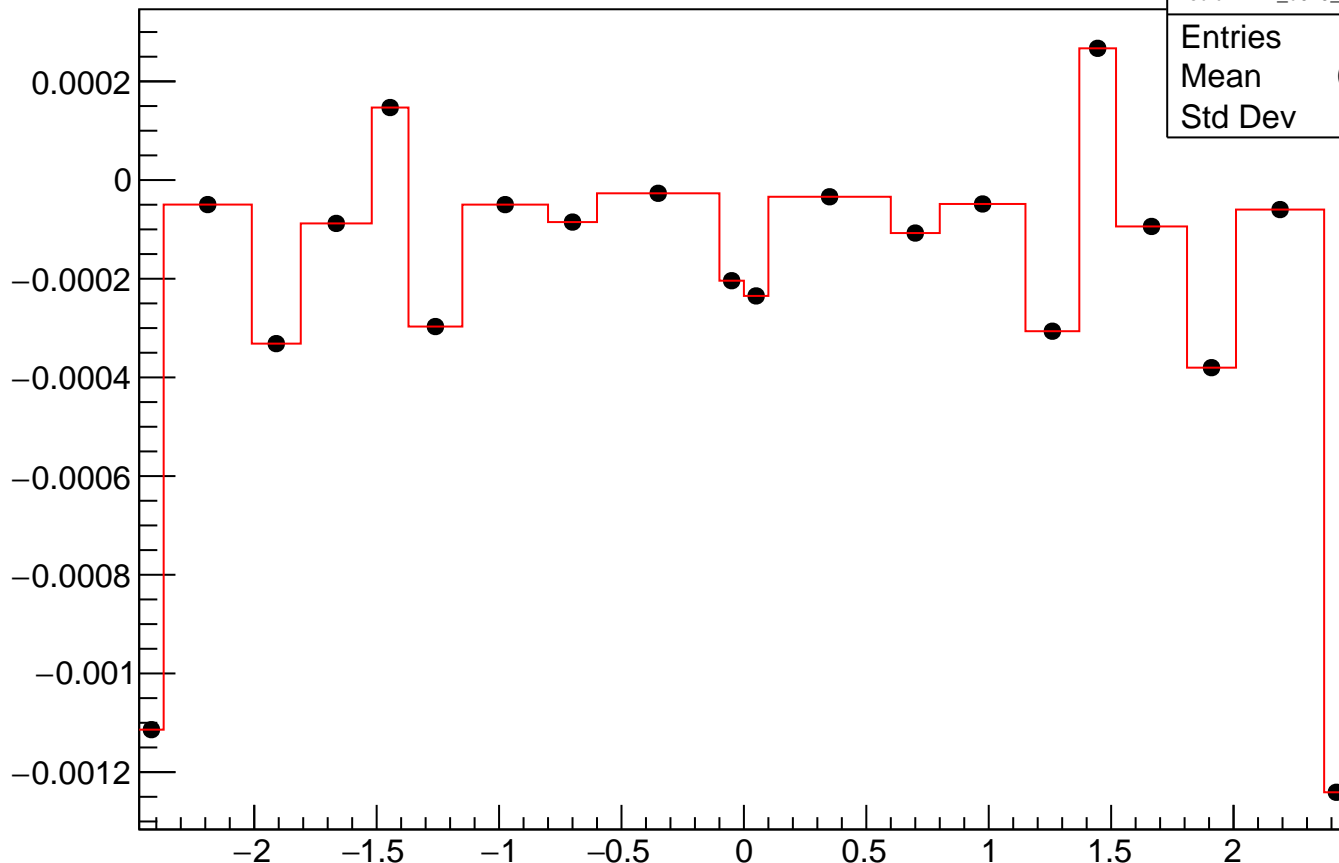
# FullSim\_MediumLLH\_d0z0\_corr13



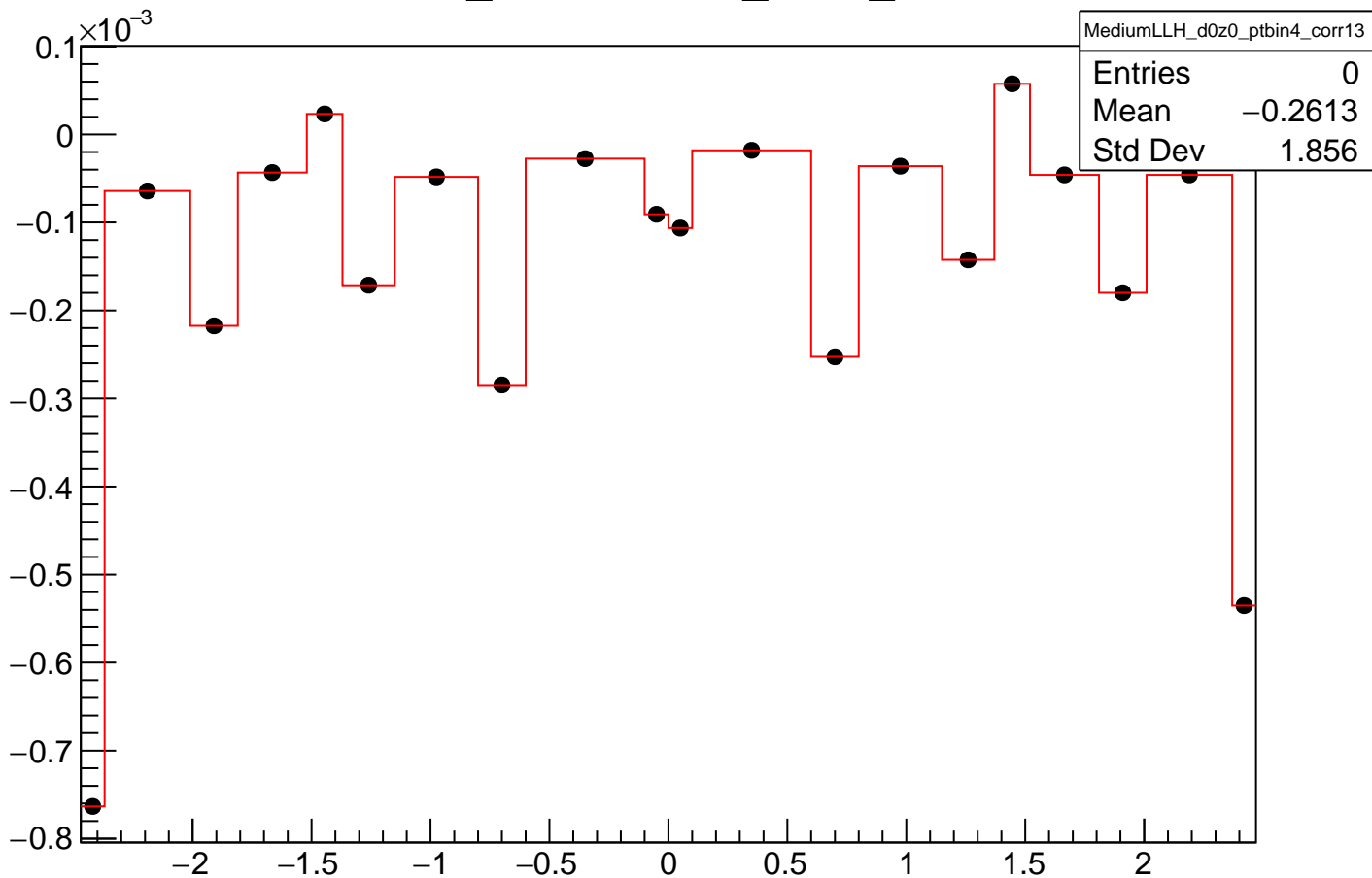
# FullSim\_MediumLLH\_d0z0\_corr13

MediumLLH\_d0z0\_ptbin3\_corr13

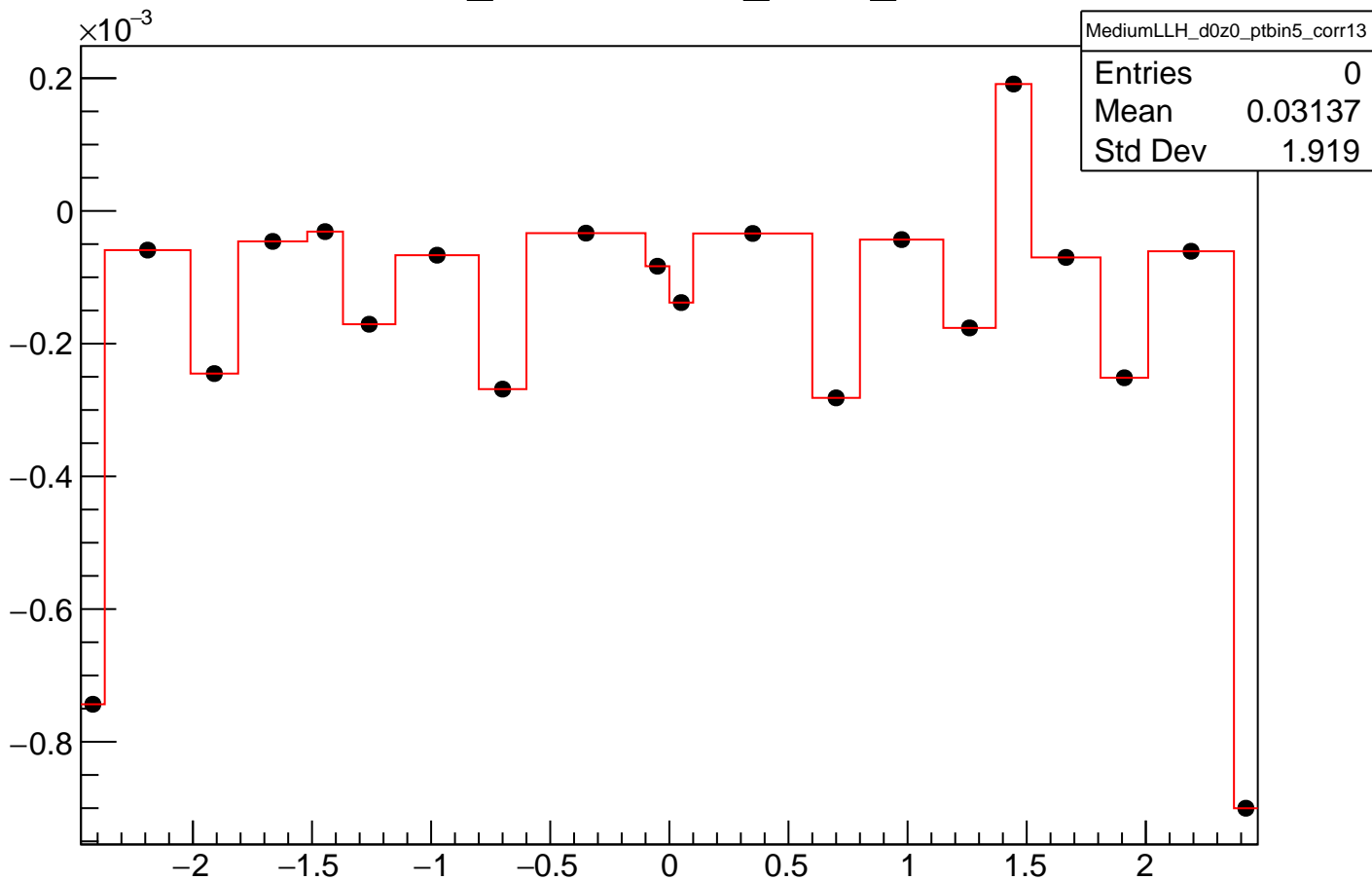
Entries	0
Mean	0.06669
Std Dev	2.019



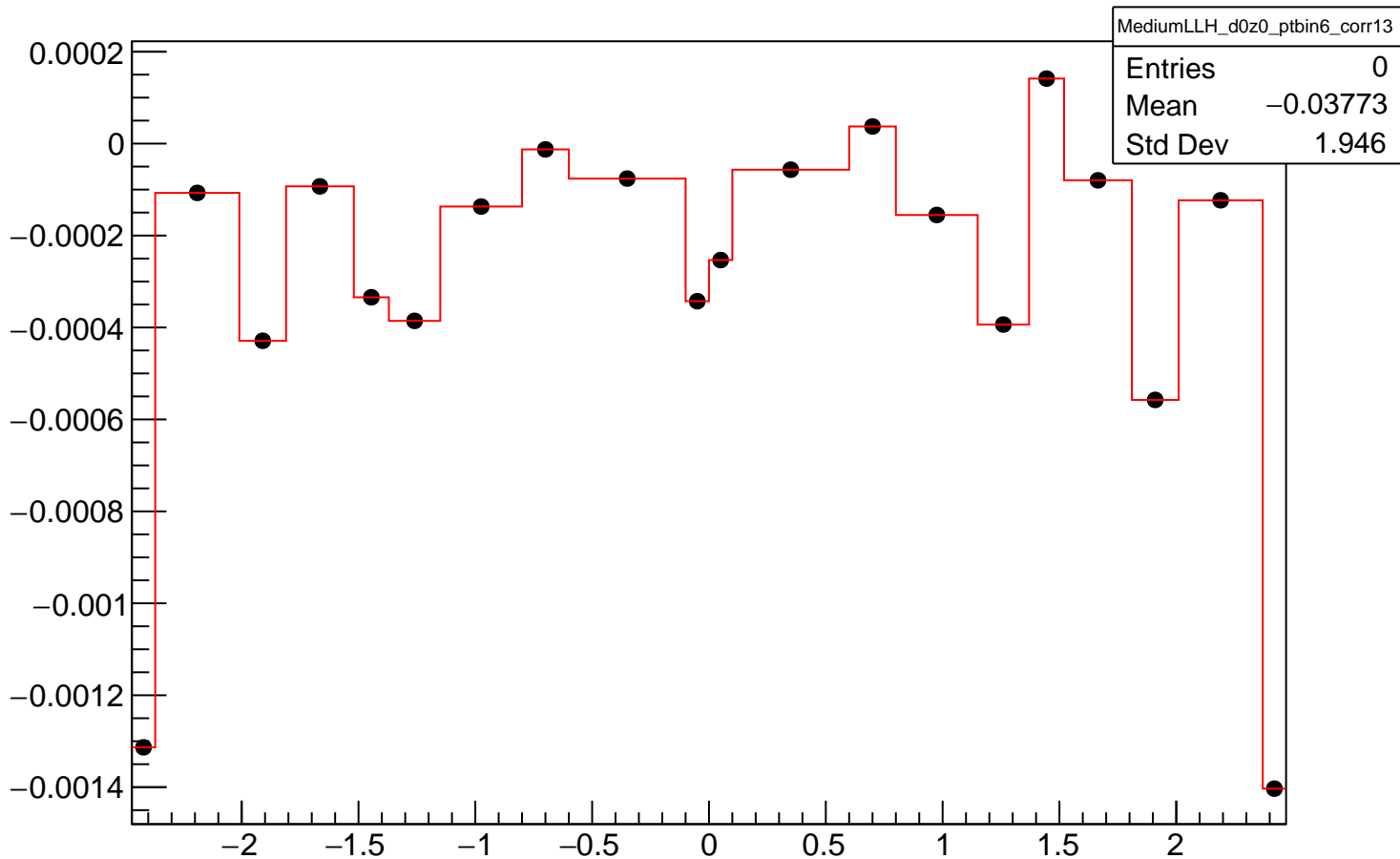
# FullSim\_MediumLLH\_d0z0\_corr13



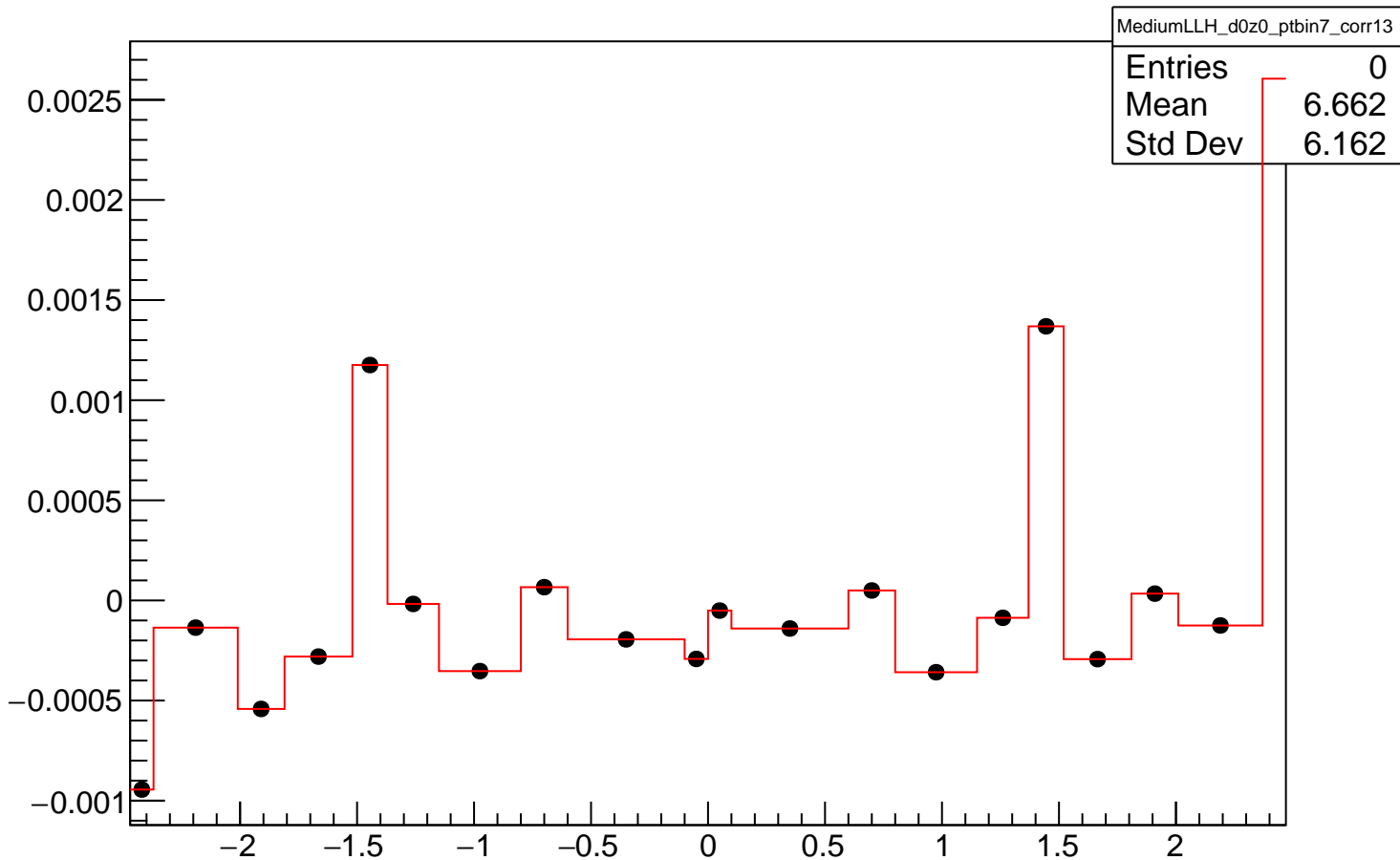
# FullSim\_MediumLLH\_d0z0\_corr13



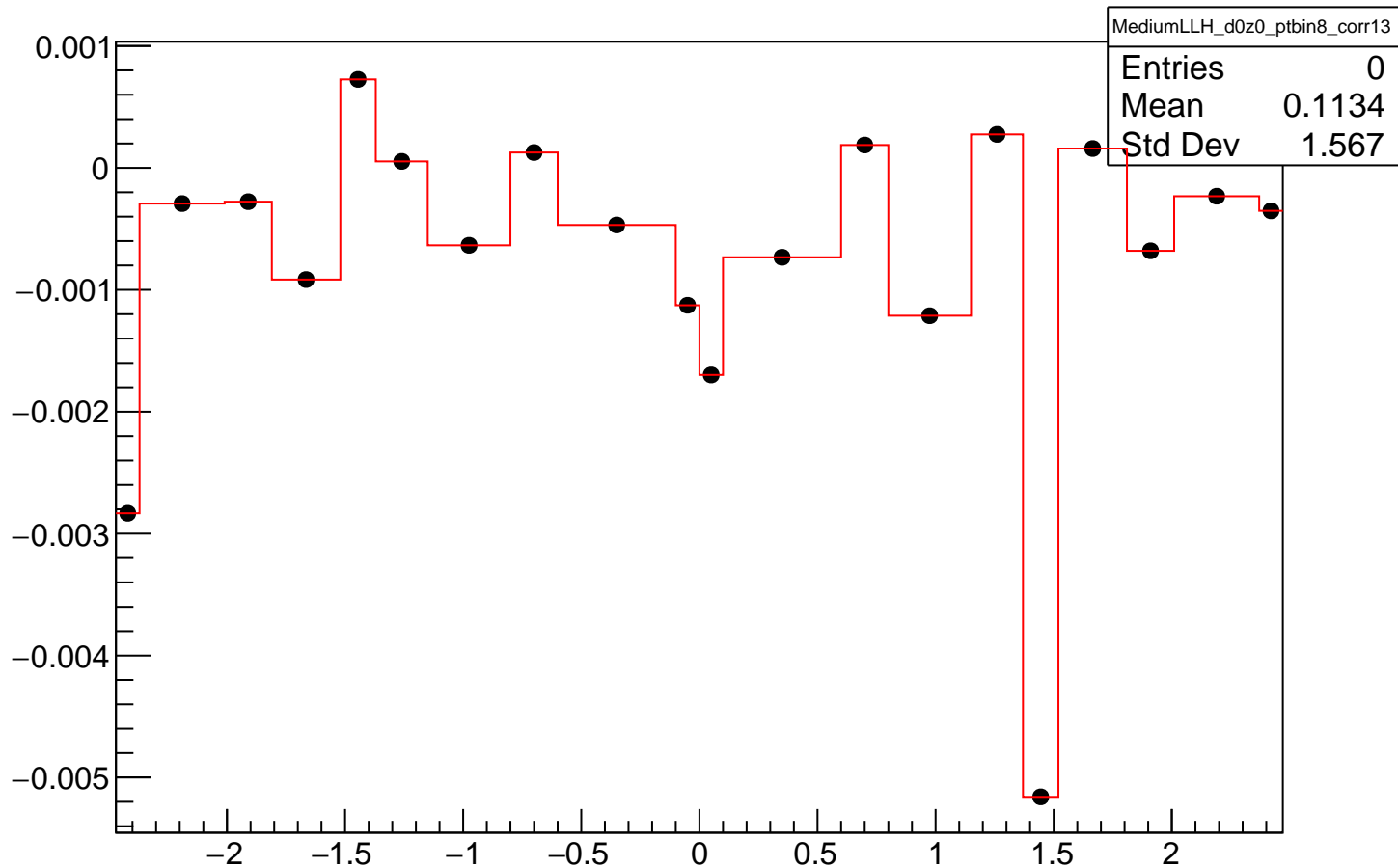
# FullSim\_MediumLLH\_d0z0\_corr13



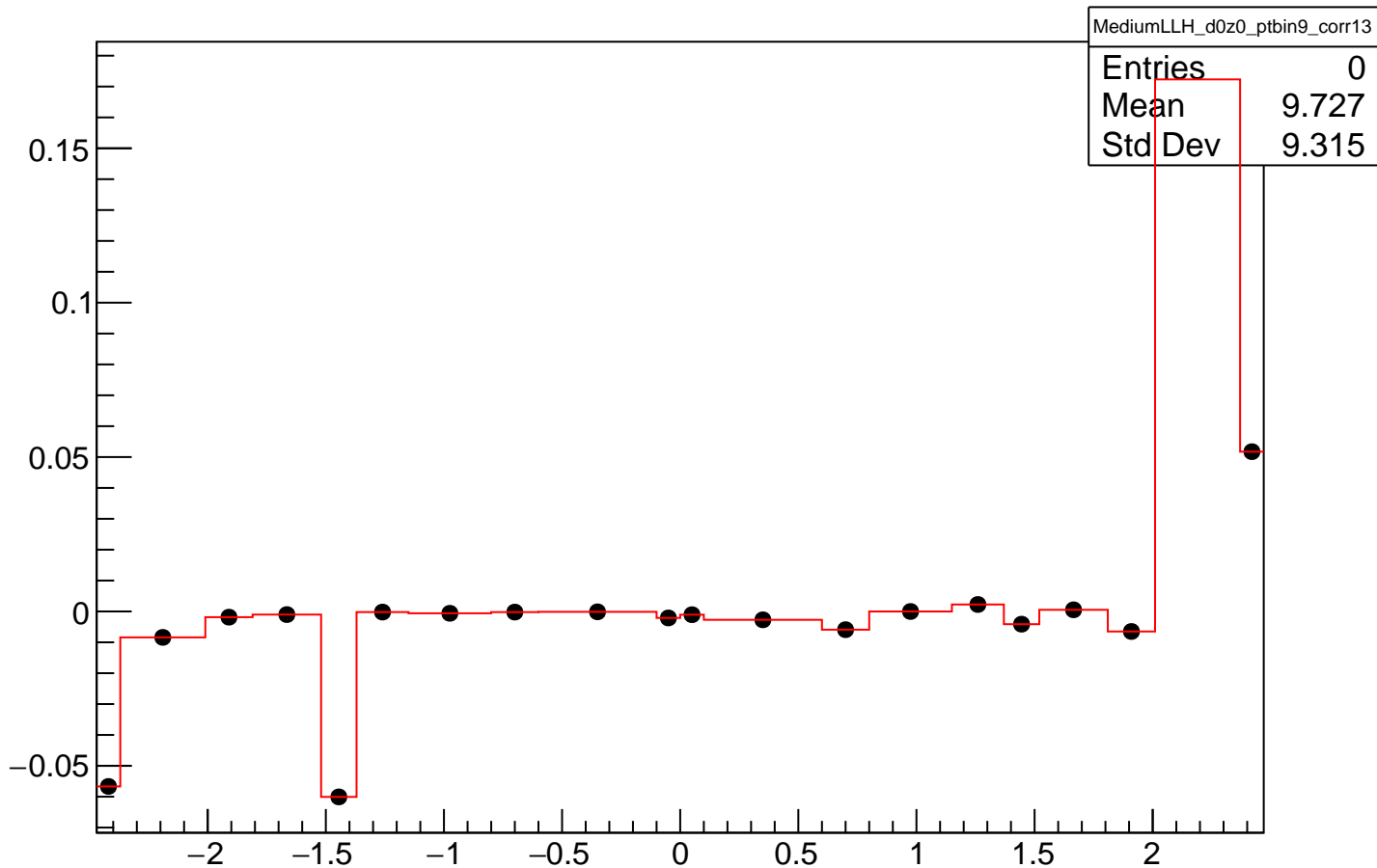
# FullSim\_MediumLLH\_d0z0\_corr13



# FullSim\_MediumLLH\_d0z0\_corr13

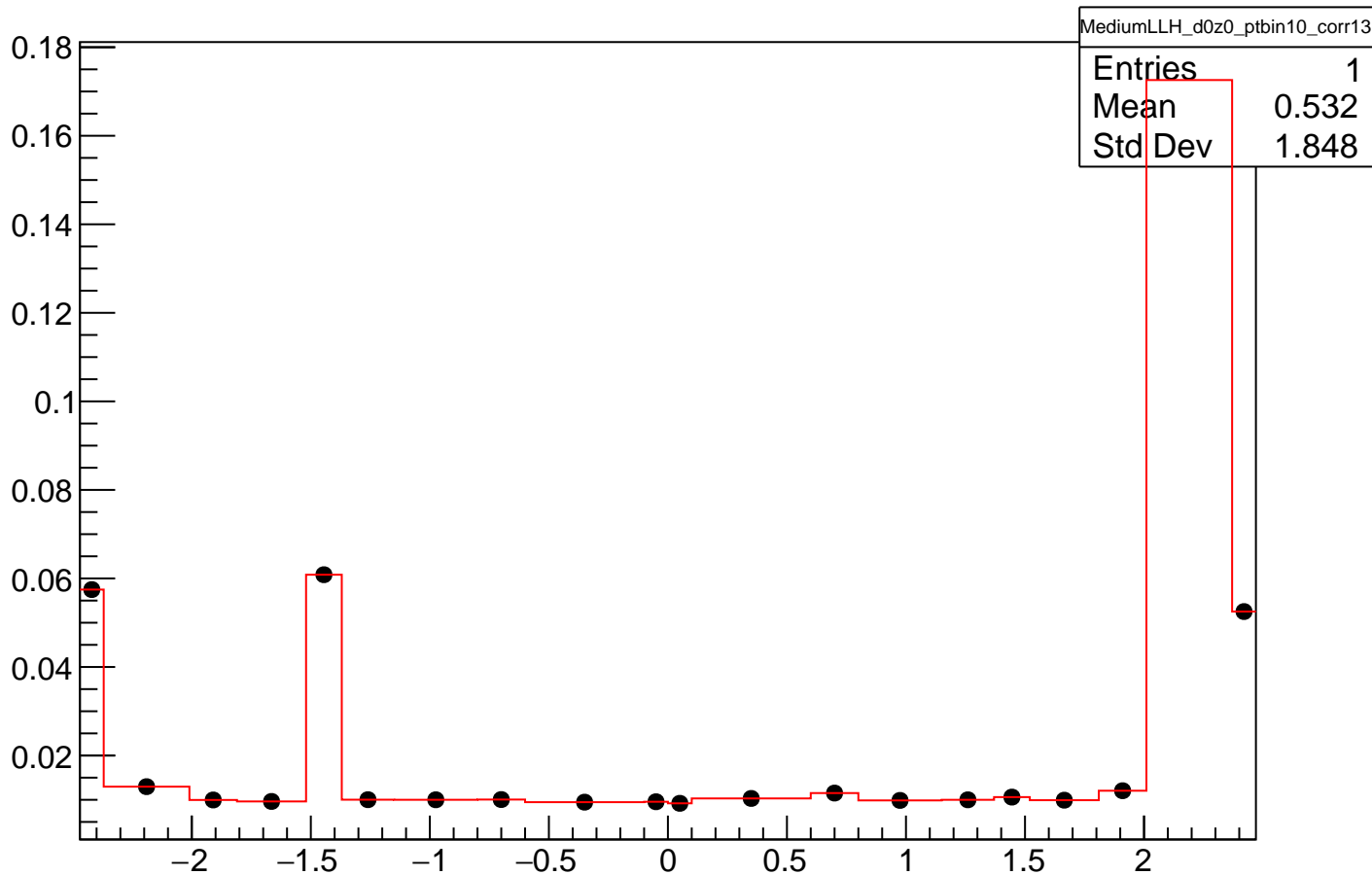


# FullSim\_MediumLLH\_d0z0\_corr13

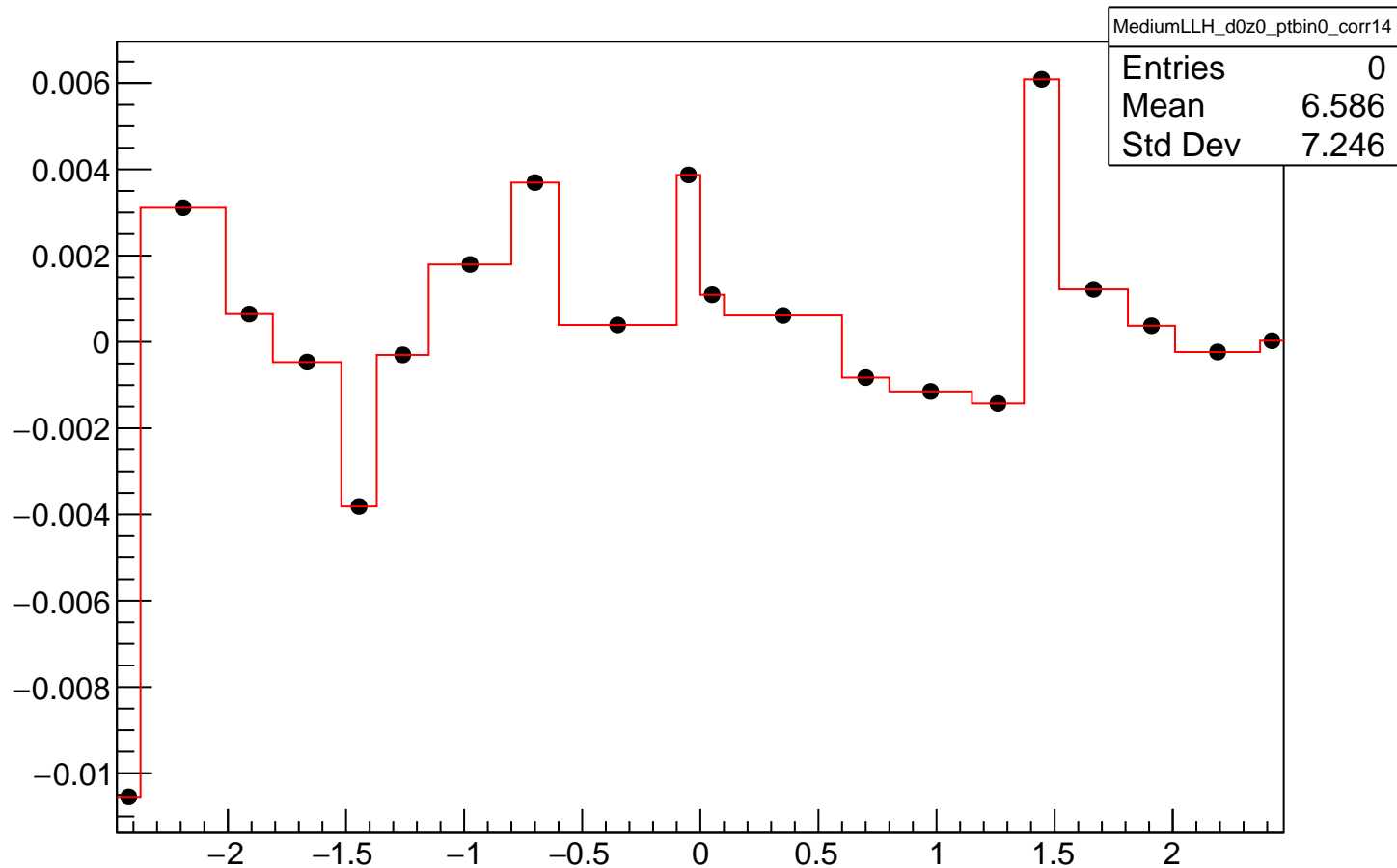




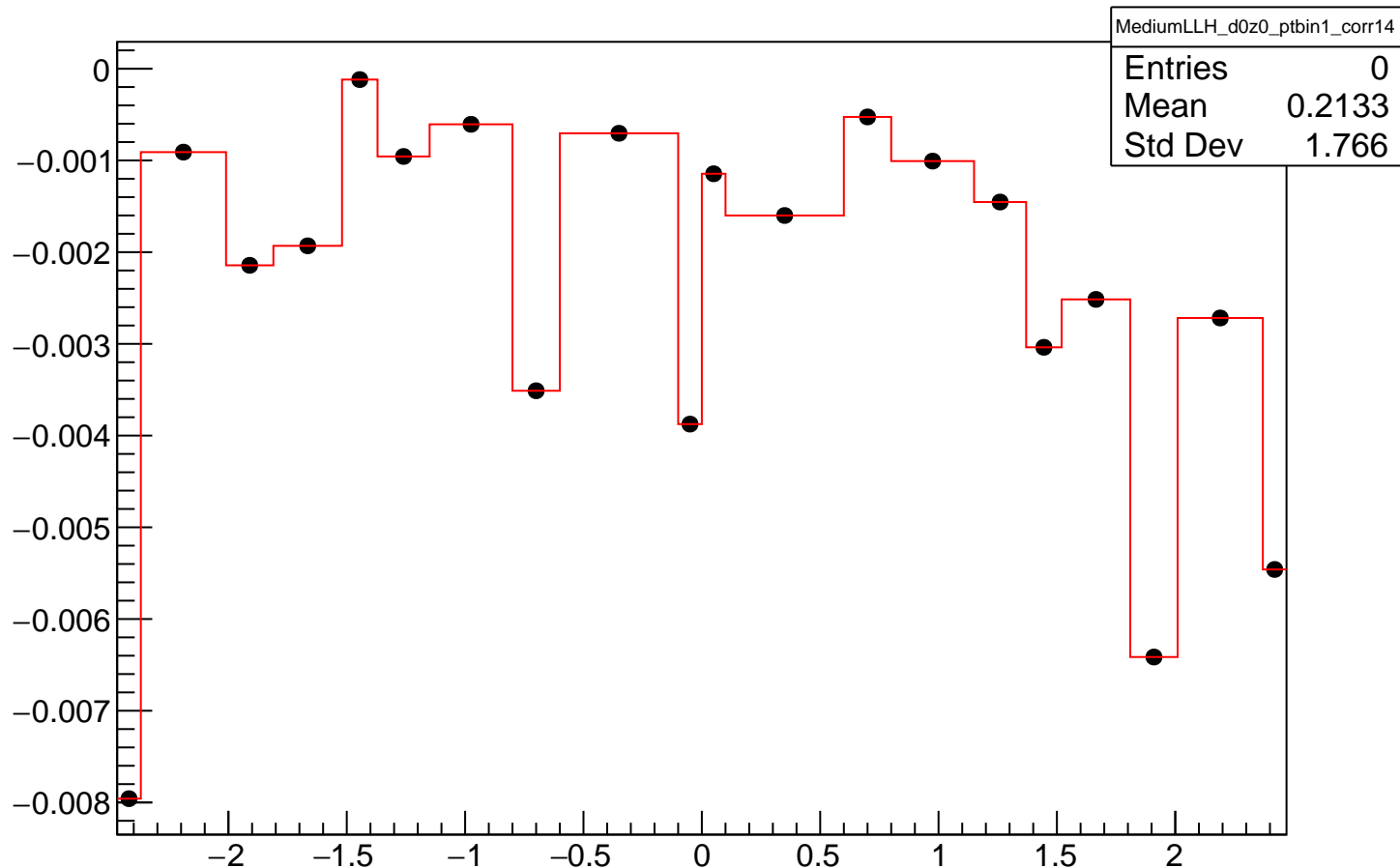
# FullSim\_MediumLLH\_d0z0\_corr13



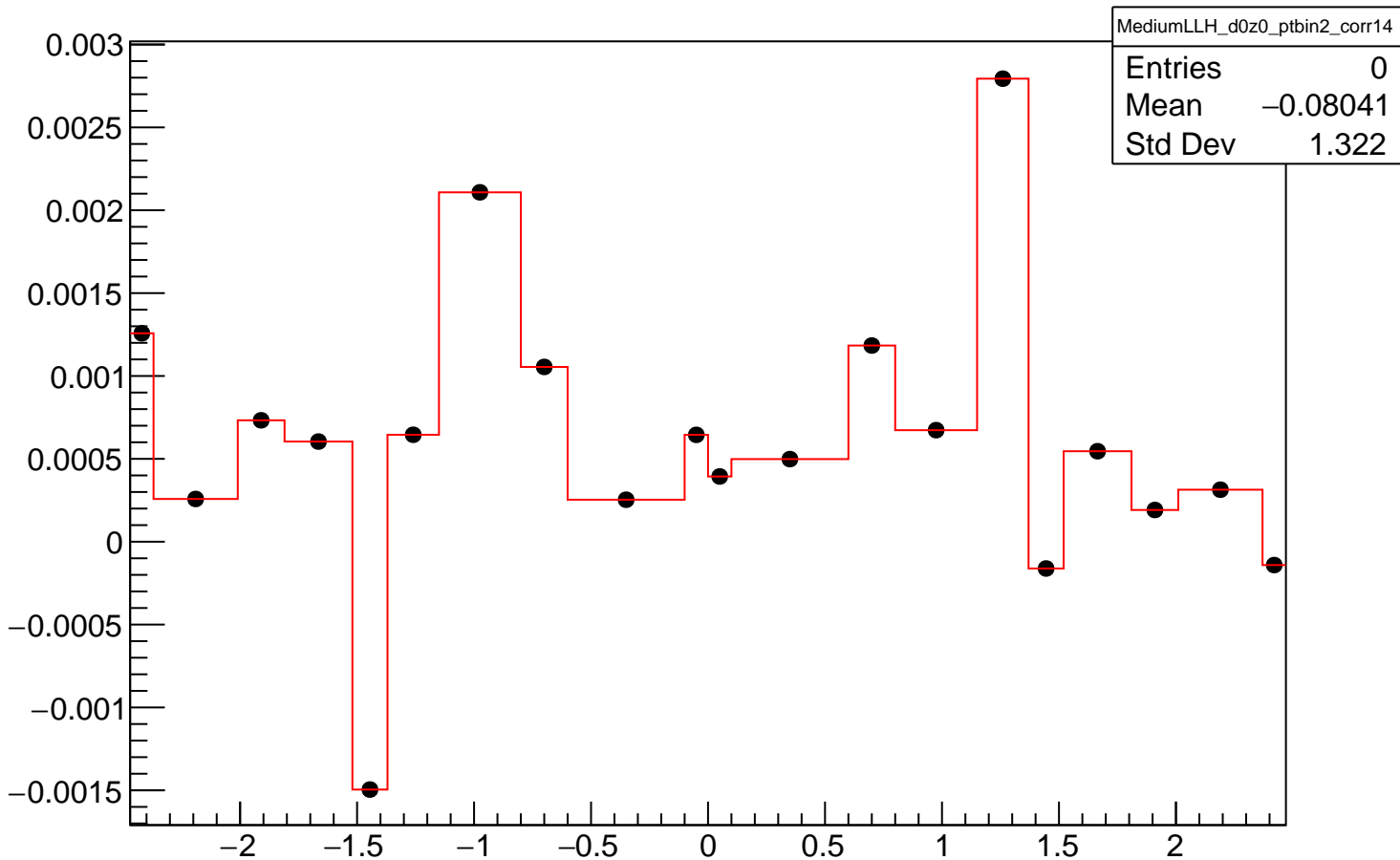
# FullSim\_MediumLLH\_d0z0\_corr14



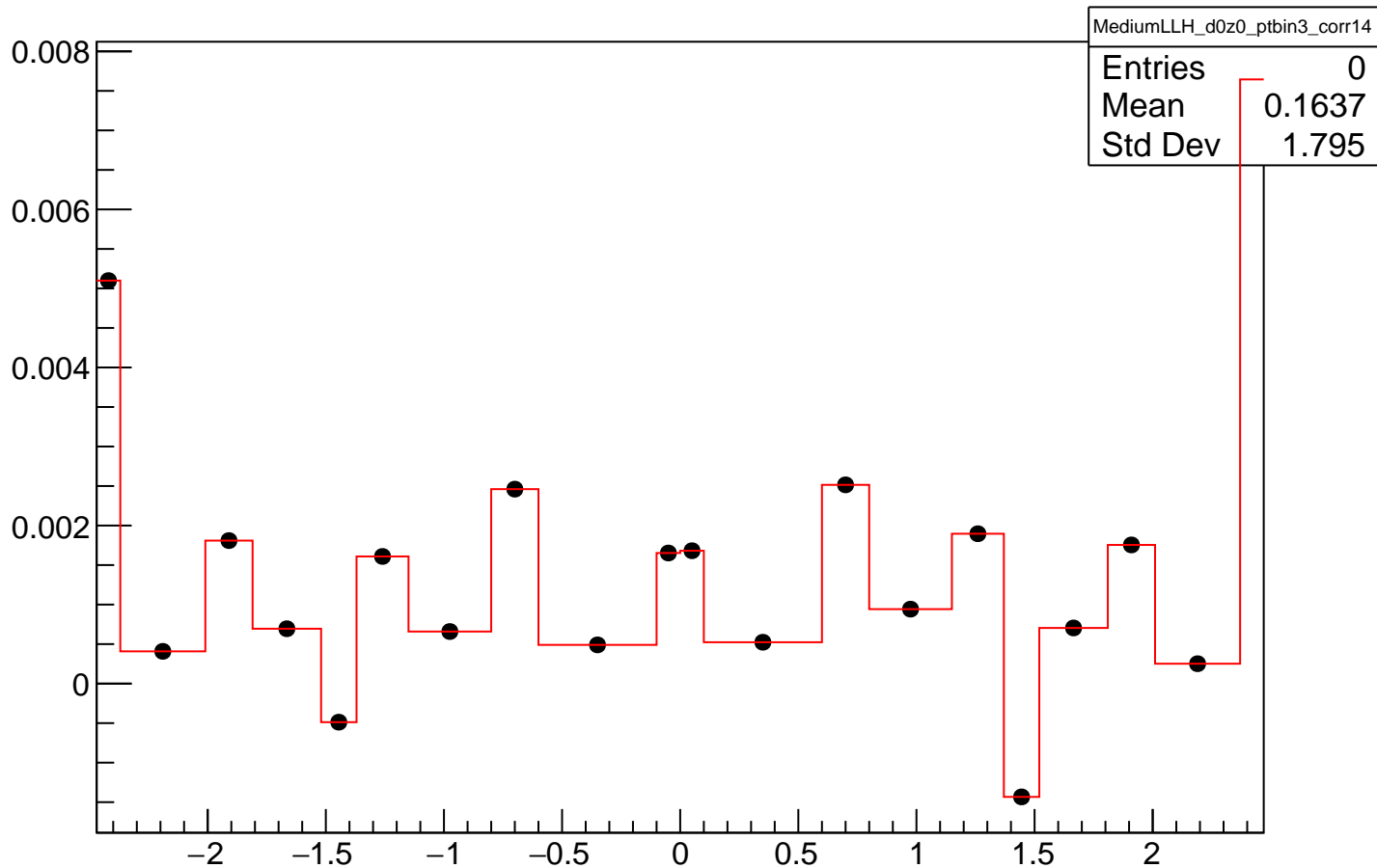
# FullSim\_MediumLLH\_d0z0\_corr14



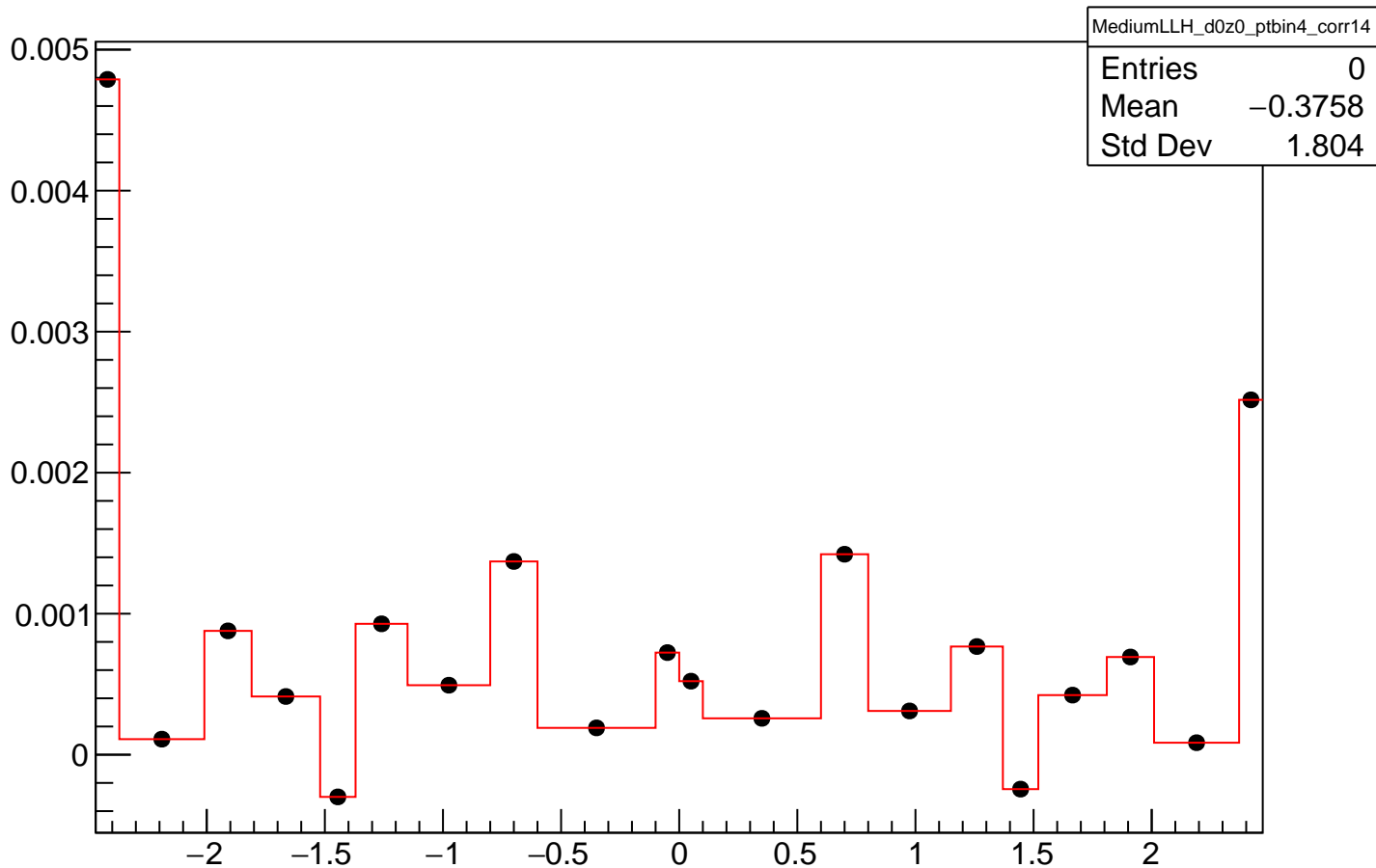
# FullSim\_MediumLLH\_d0z0\_corr14



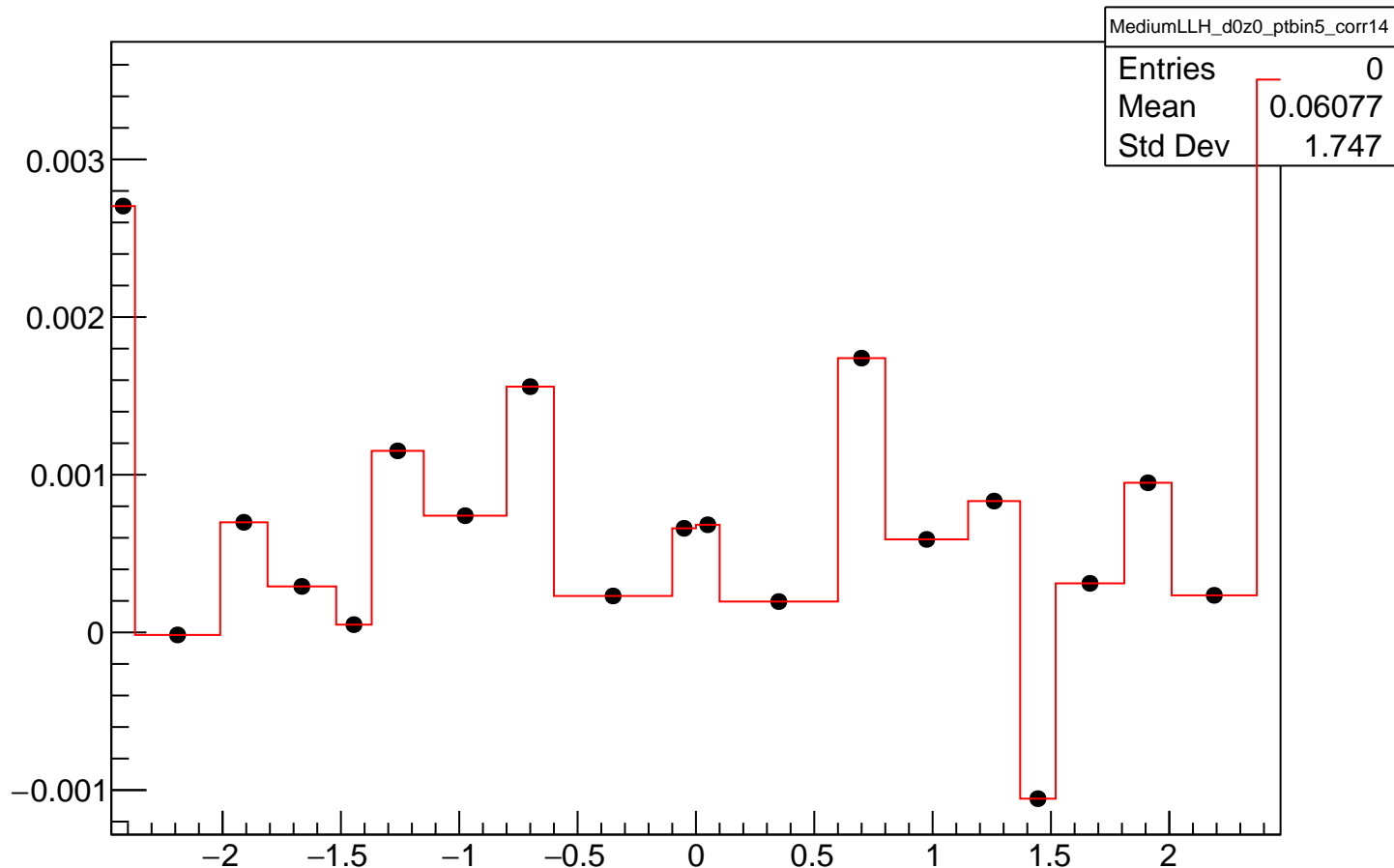
# FullSim\_MediumLLH\_d0z0\_corr14



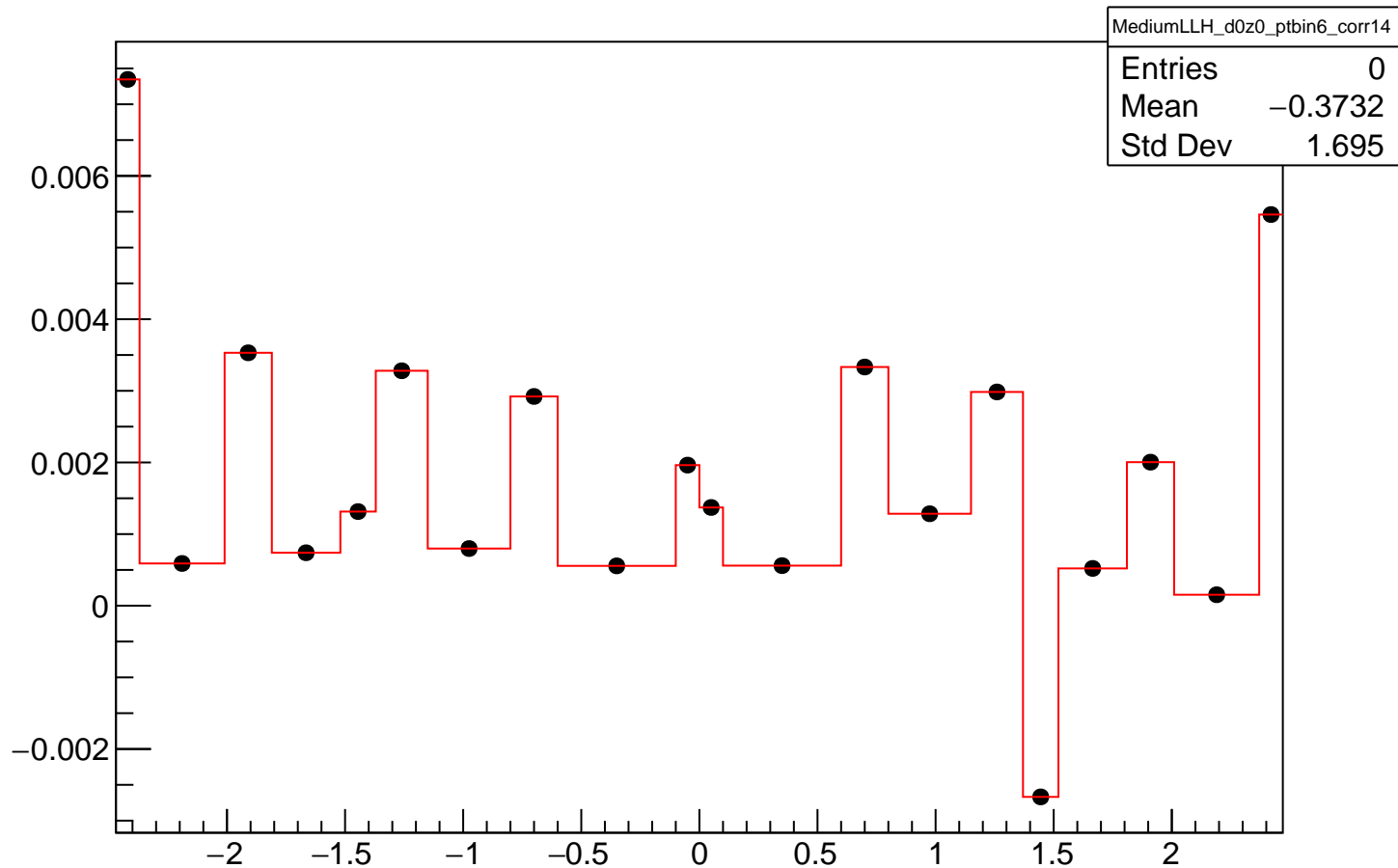
# FullSim\_MediumLLH\_d0z0\_corr14



# FullSim\_MediumLLH\_d0z0\_corr14

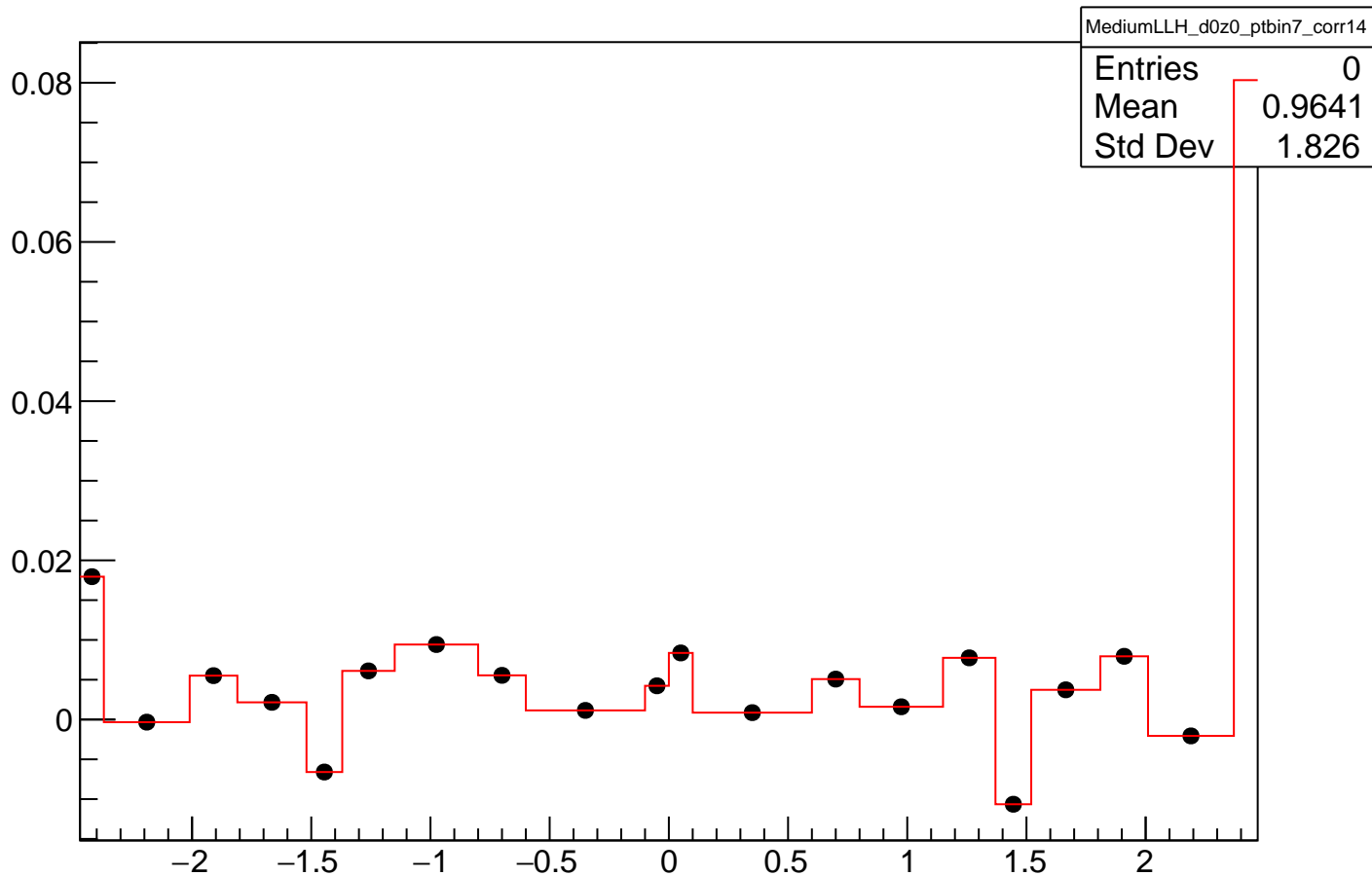


# FullSim\_MediumLLH\_d0z0\_corr14

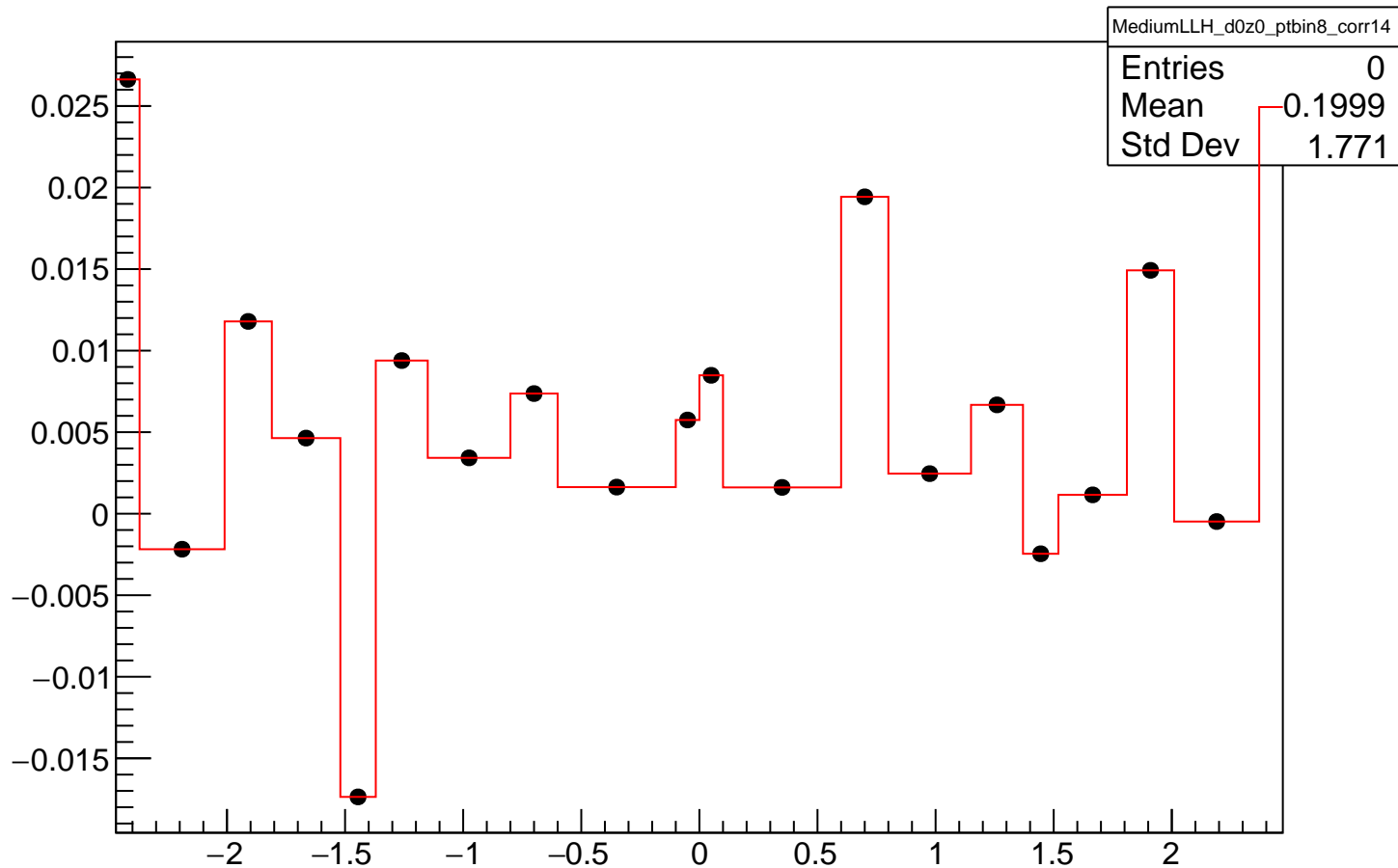




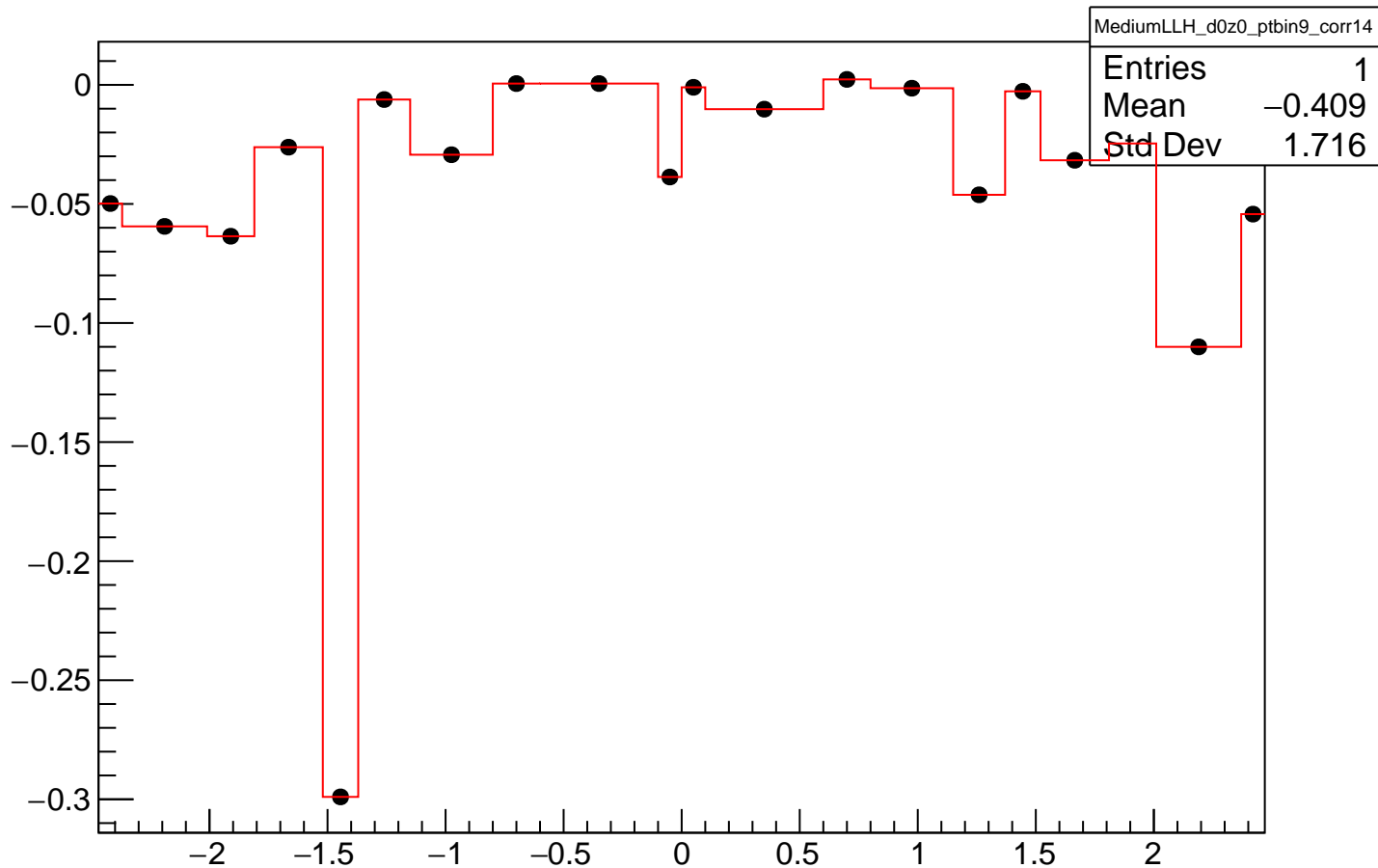
# FullSim\_MediumLLH\_d0z0\_corr14



# FullSim\_MediumLLH\_d0z0\_corr14



# FullSim\_MediumLLH\_d0z0\_corr14



# FullSim\_MediumLLH\_d0z0\_corr14

