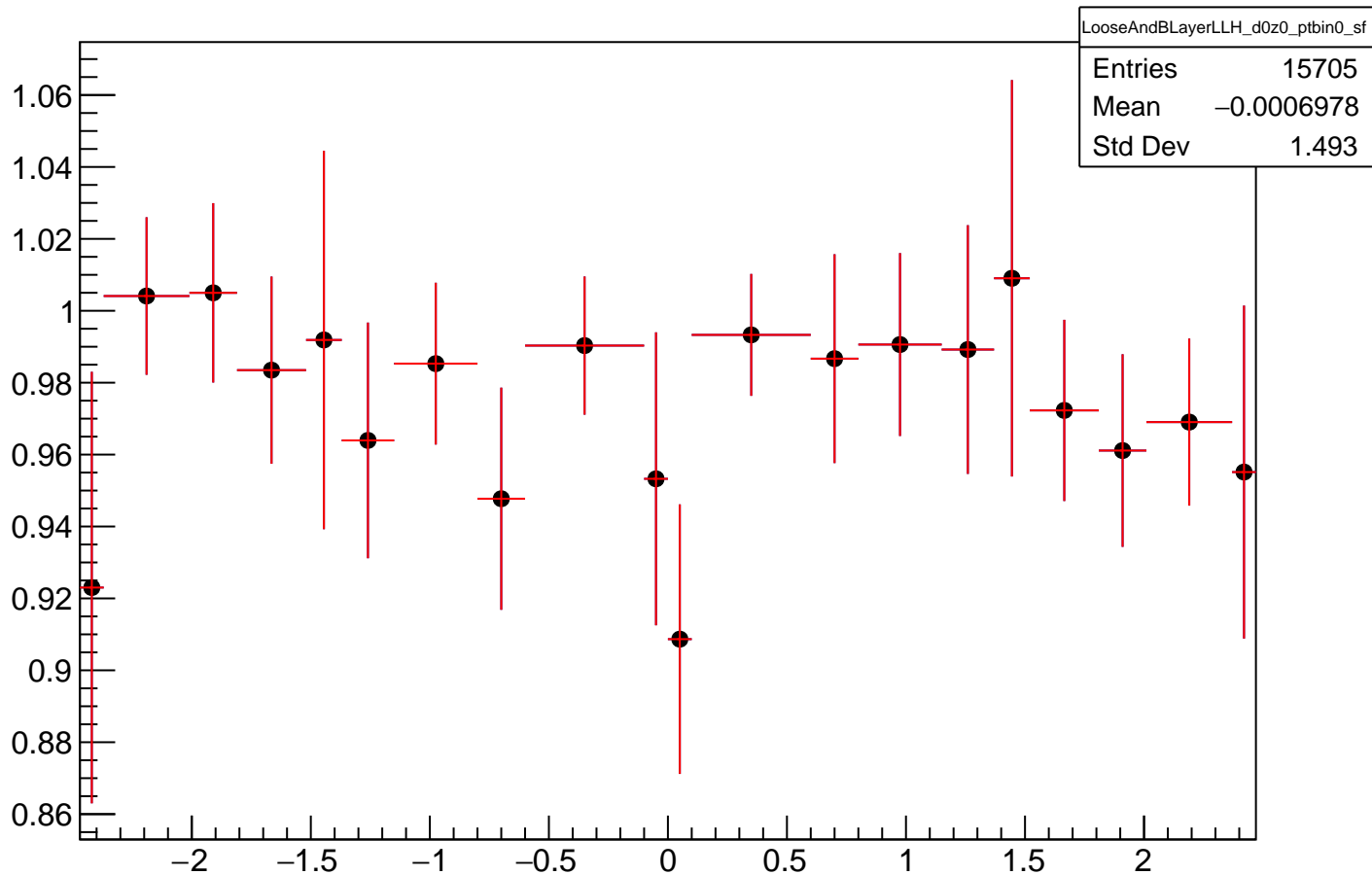
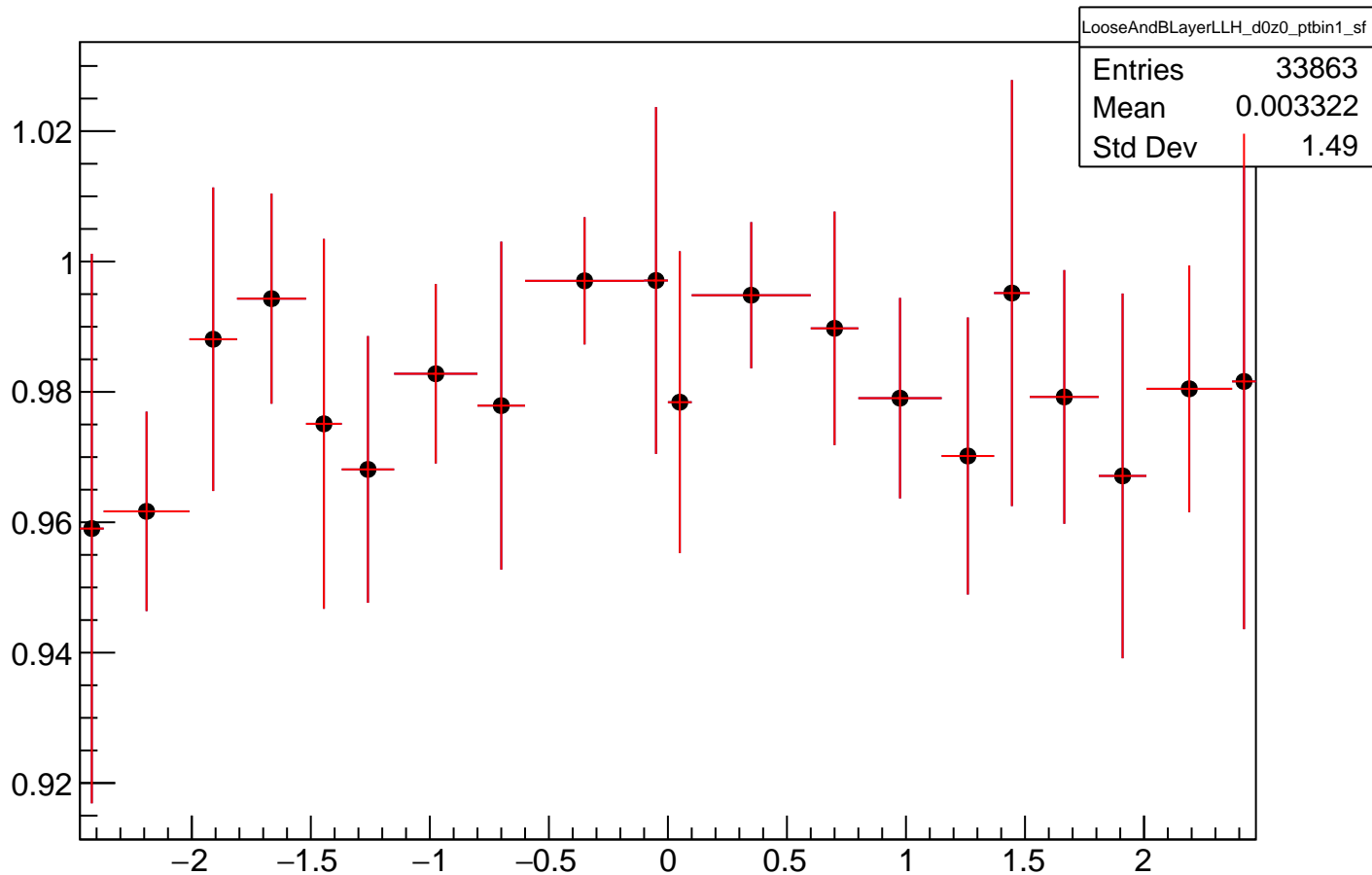


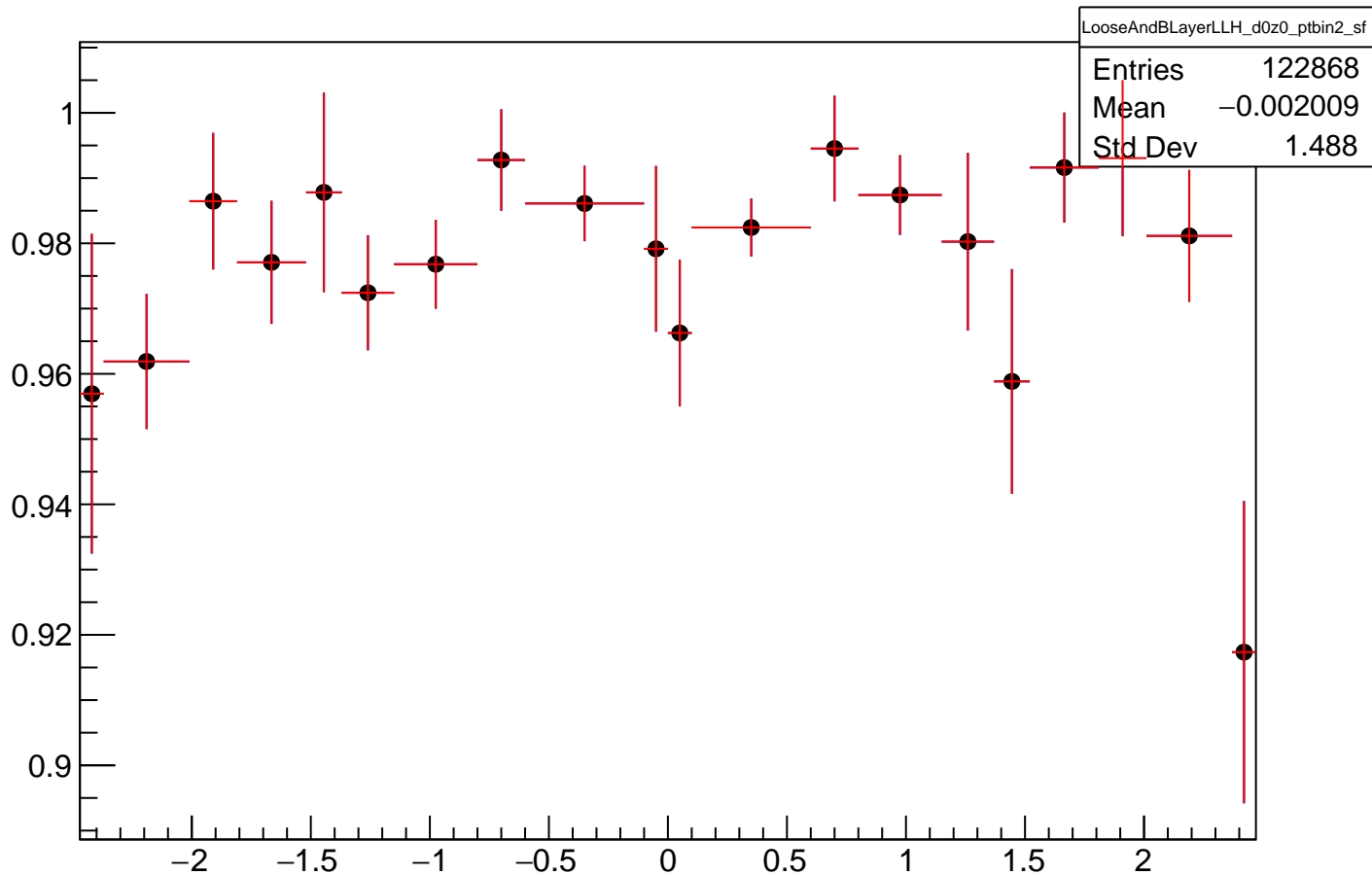
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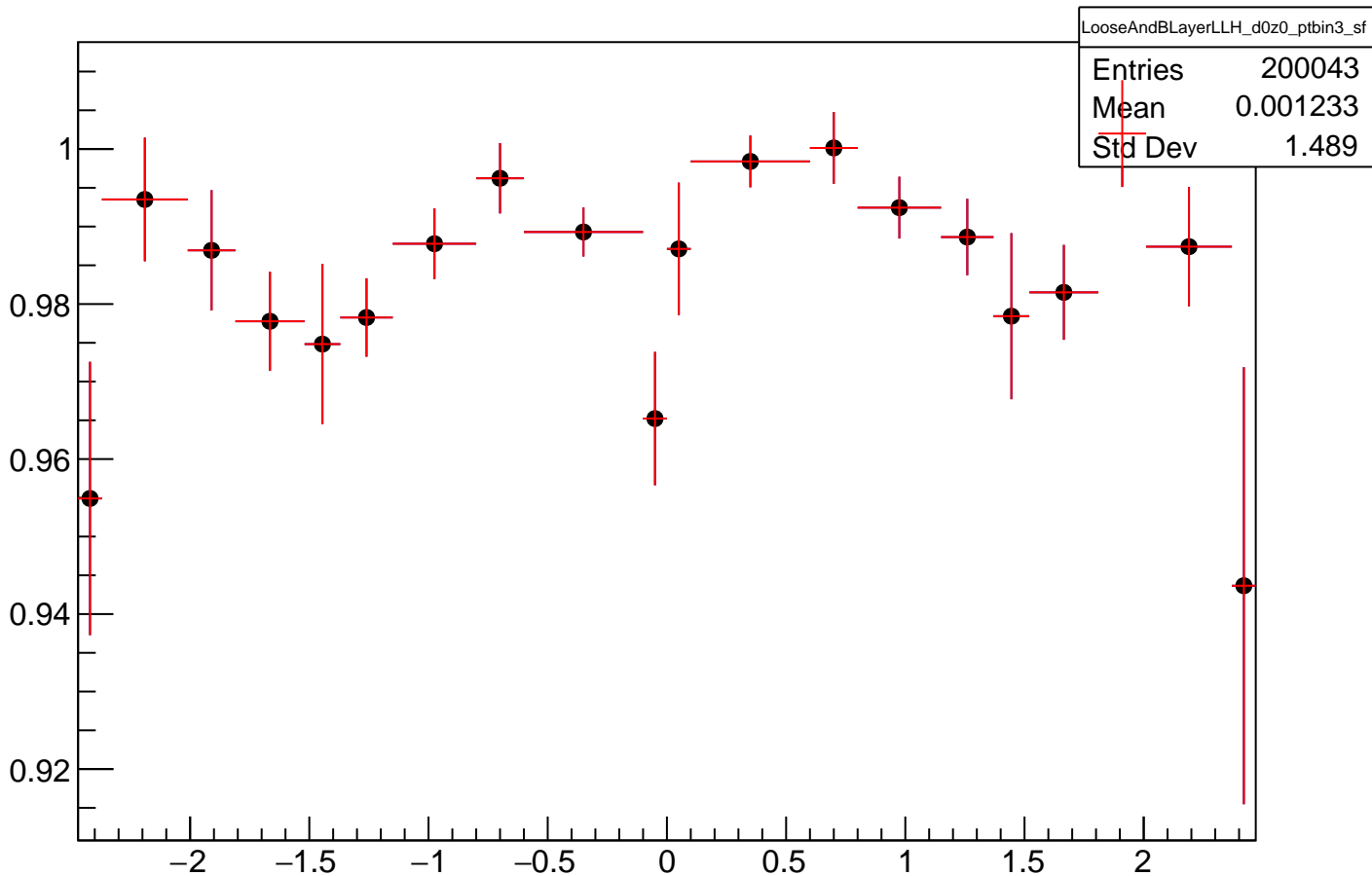
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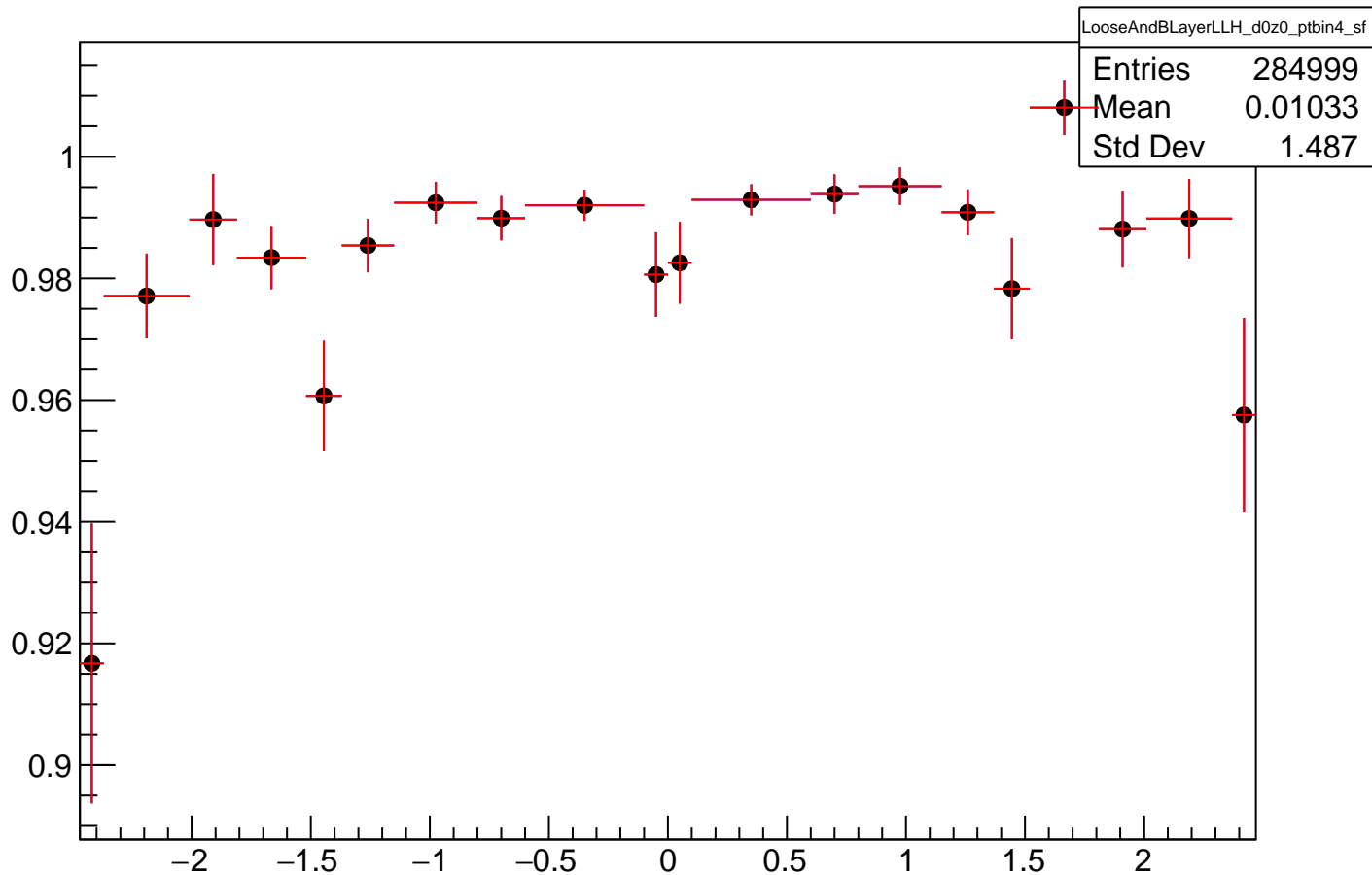
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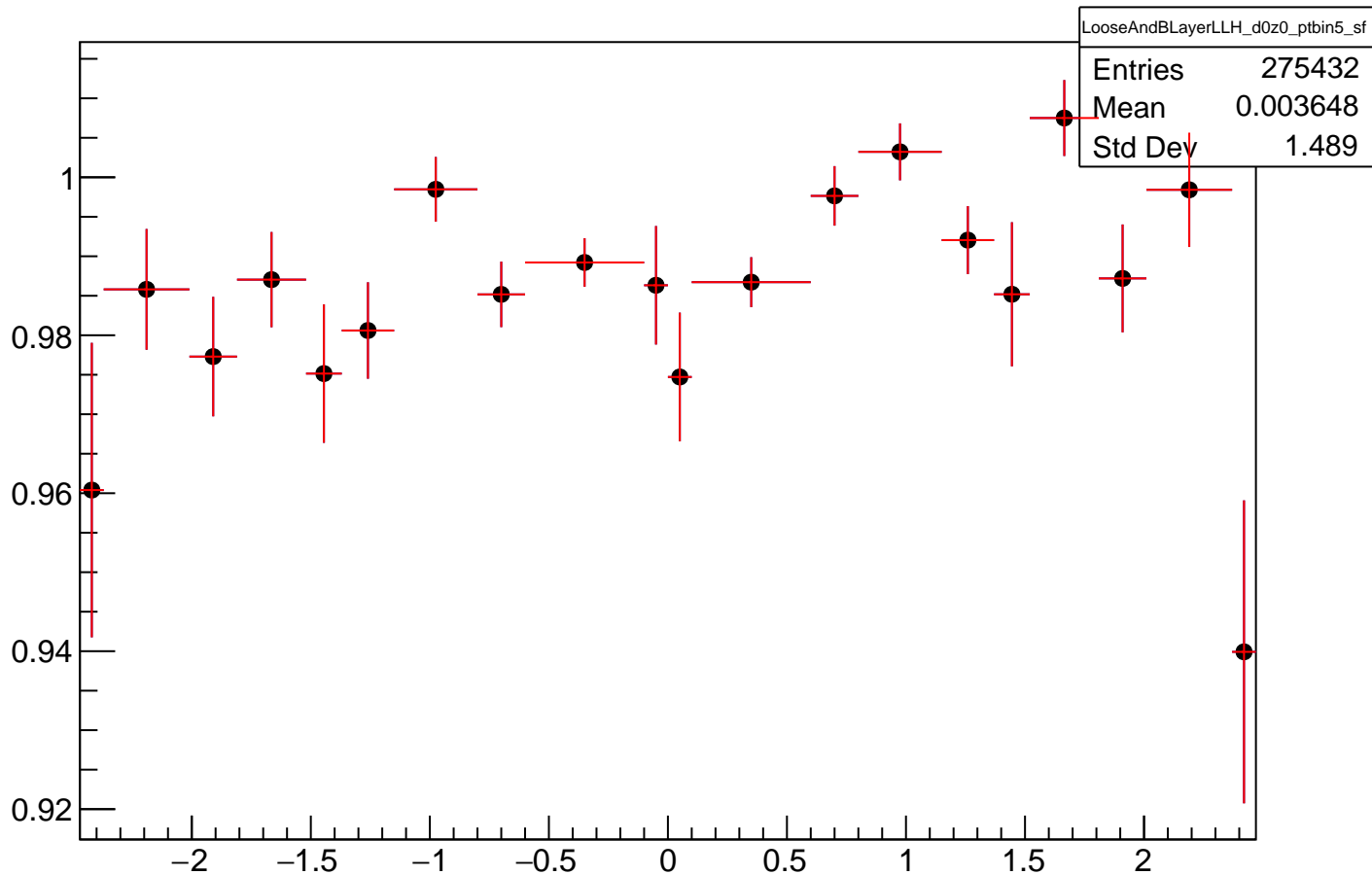
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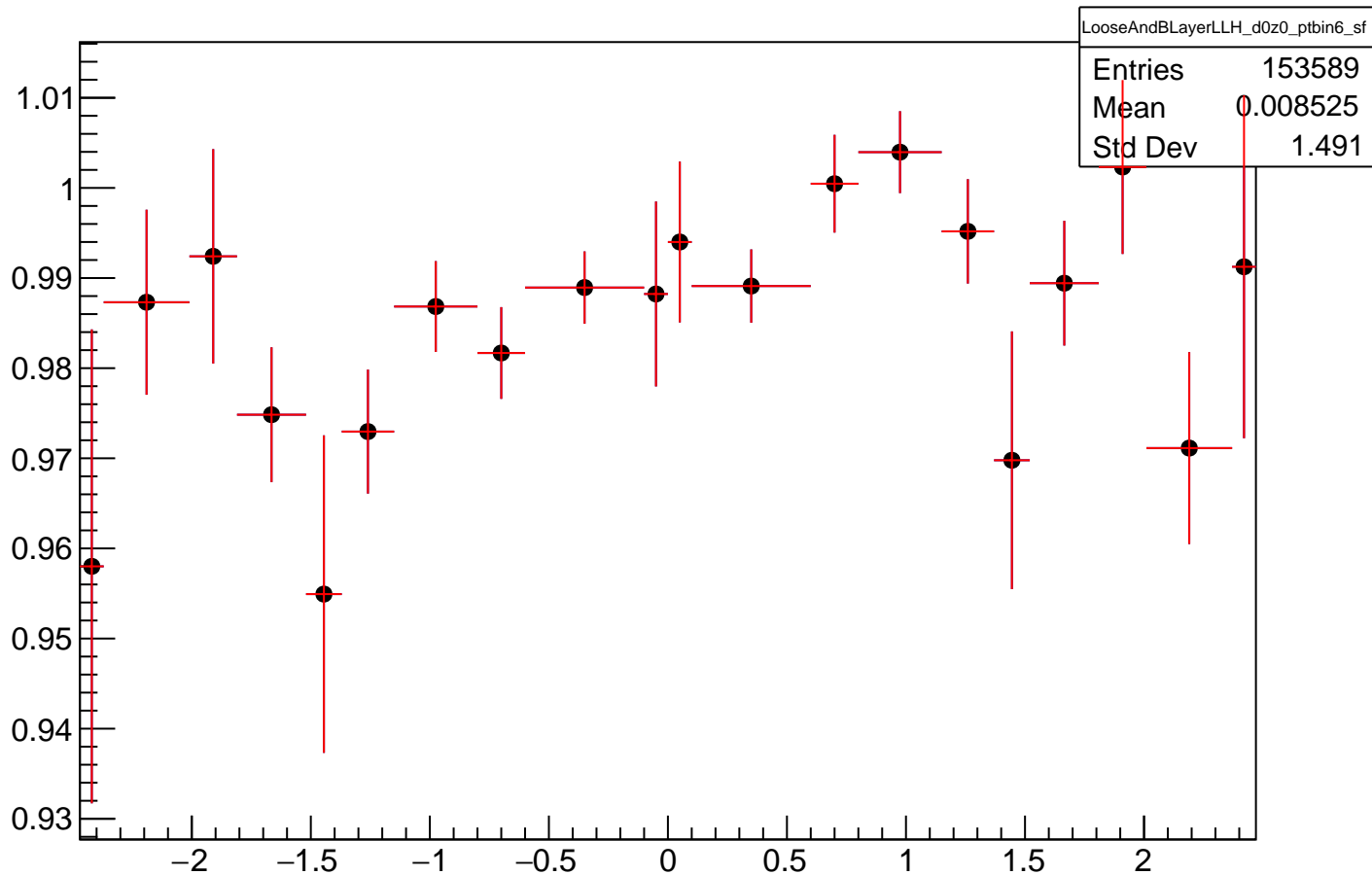
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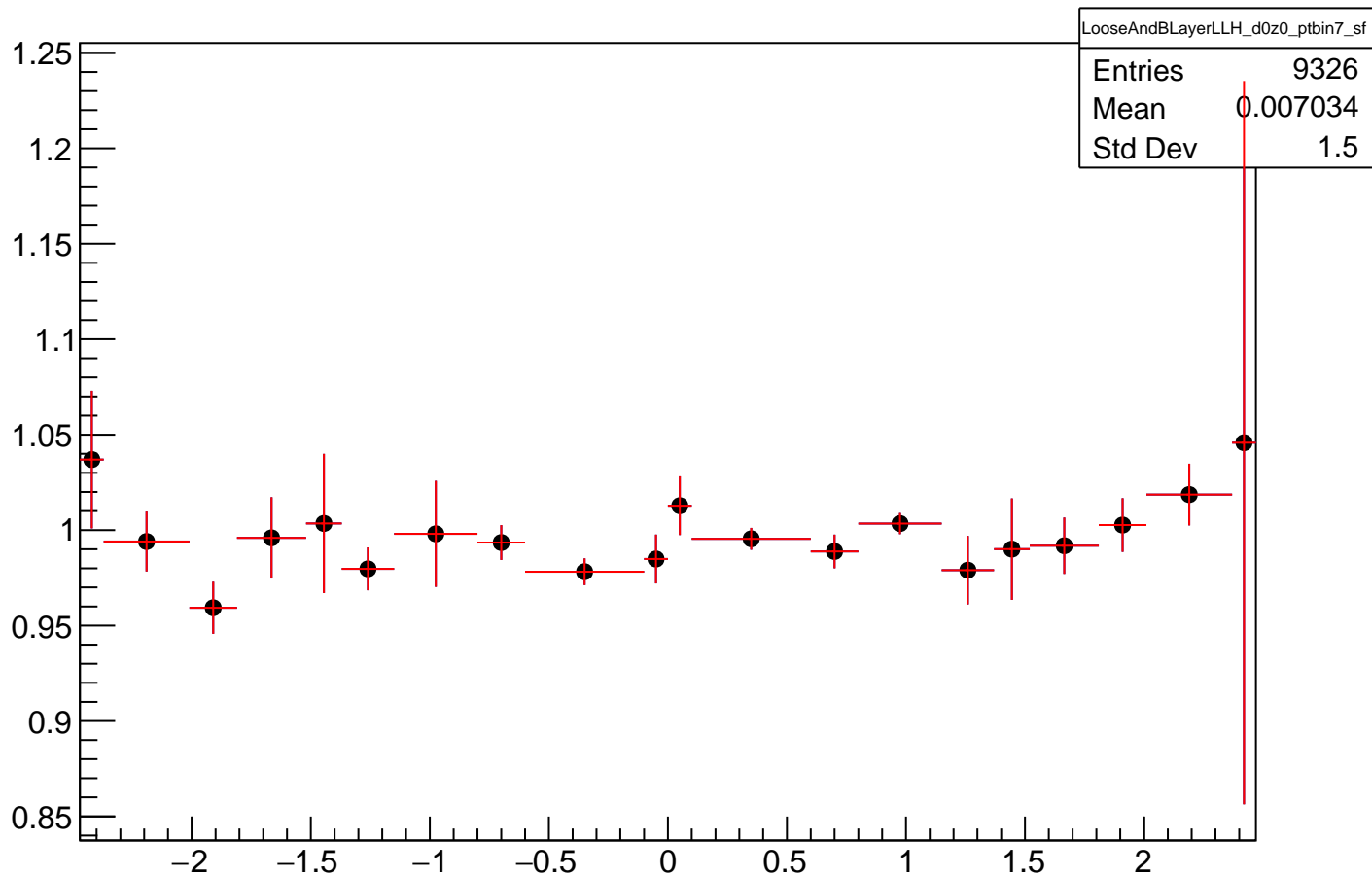
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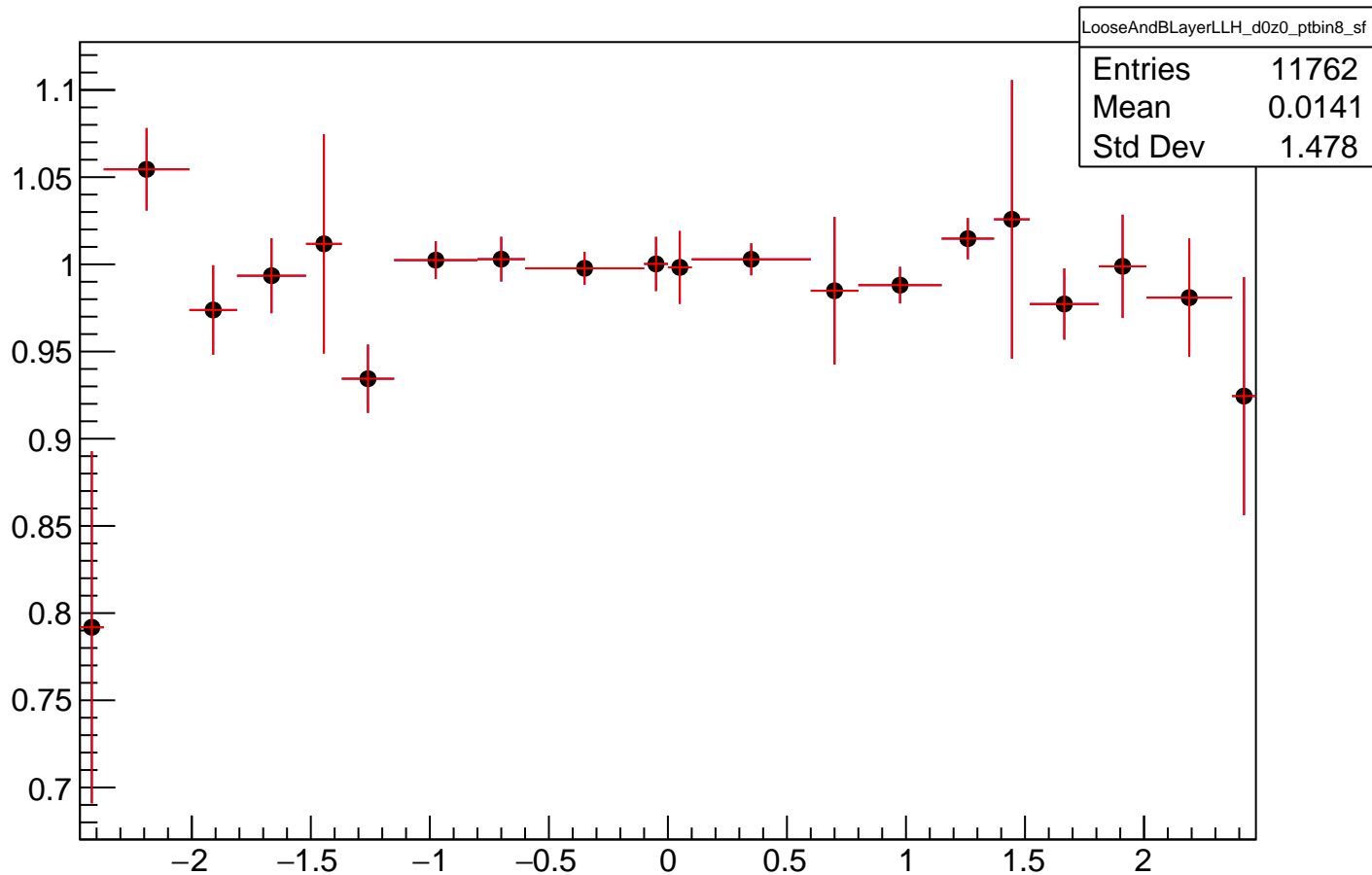
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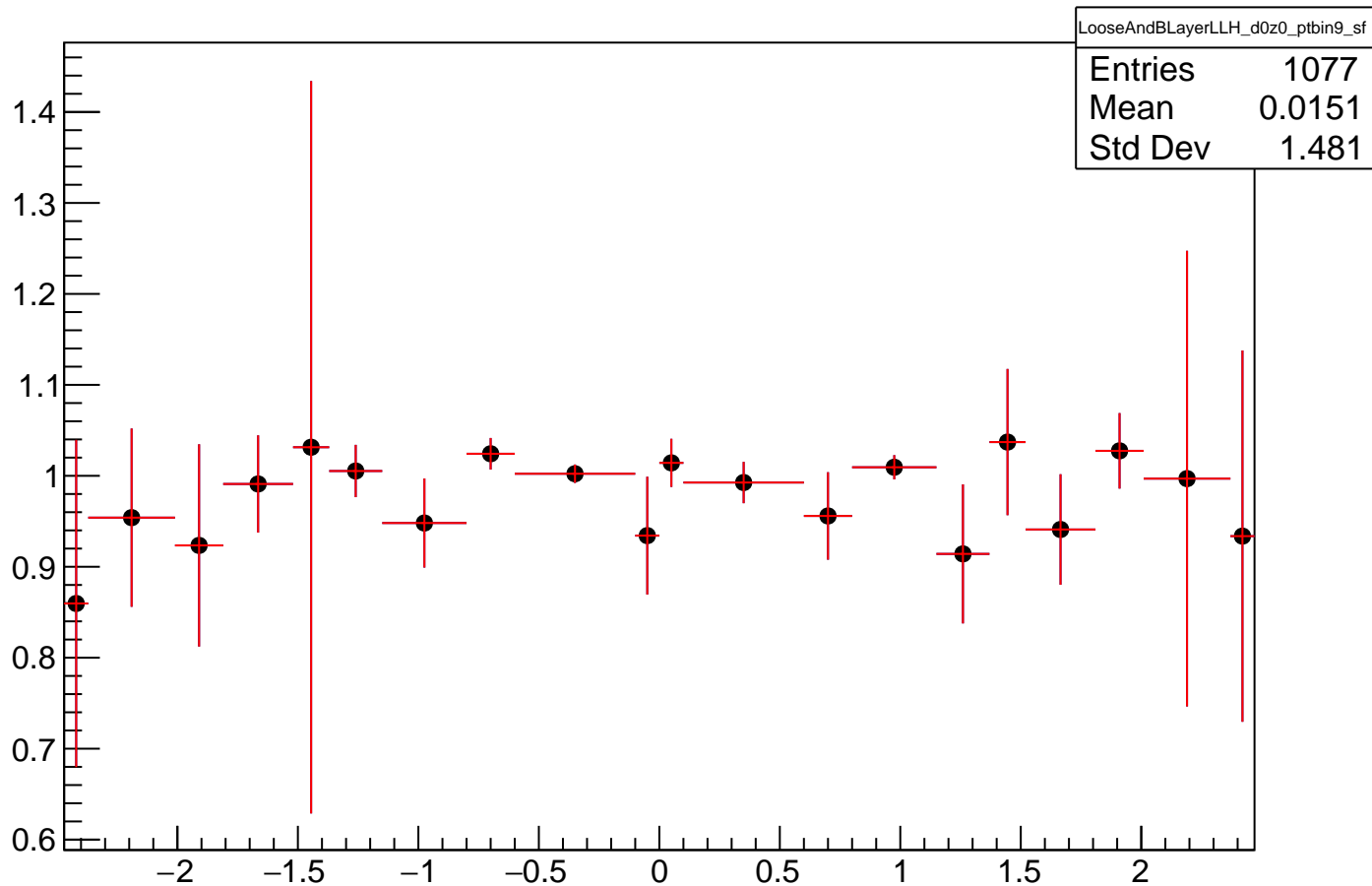
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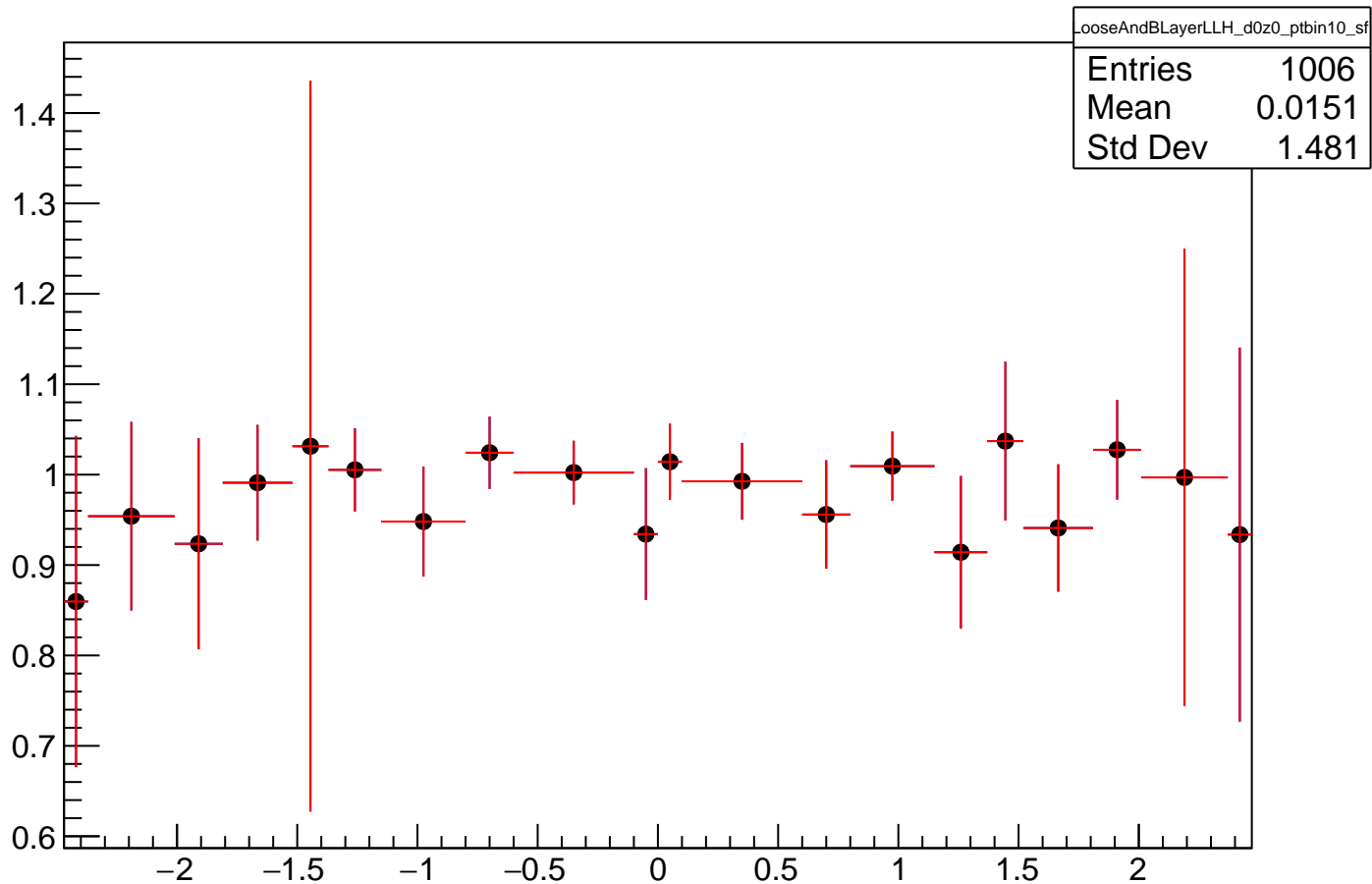
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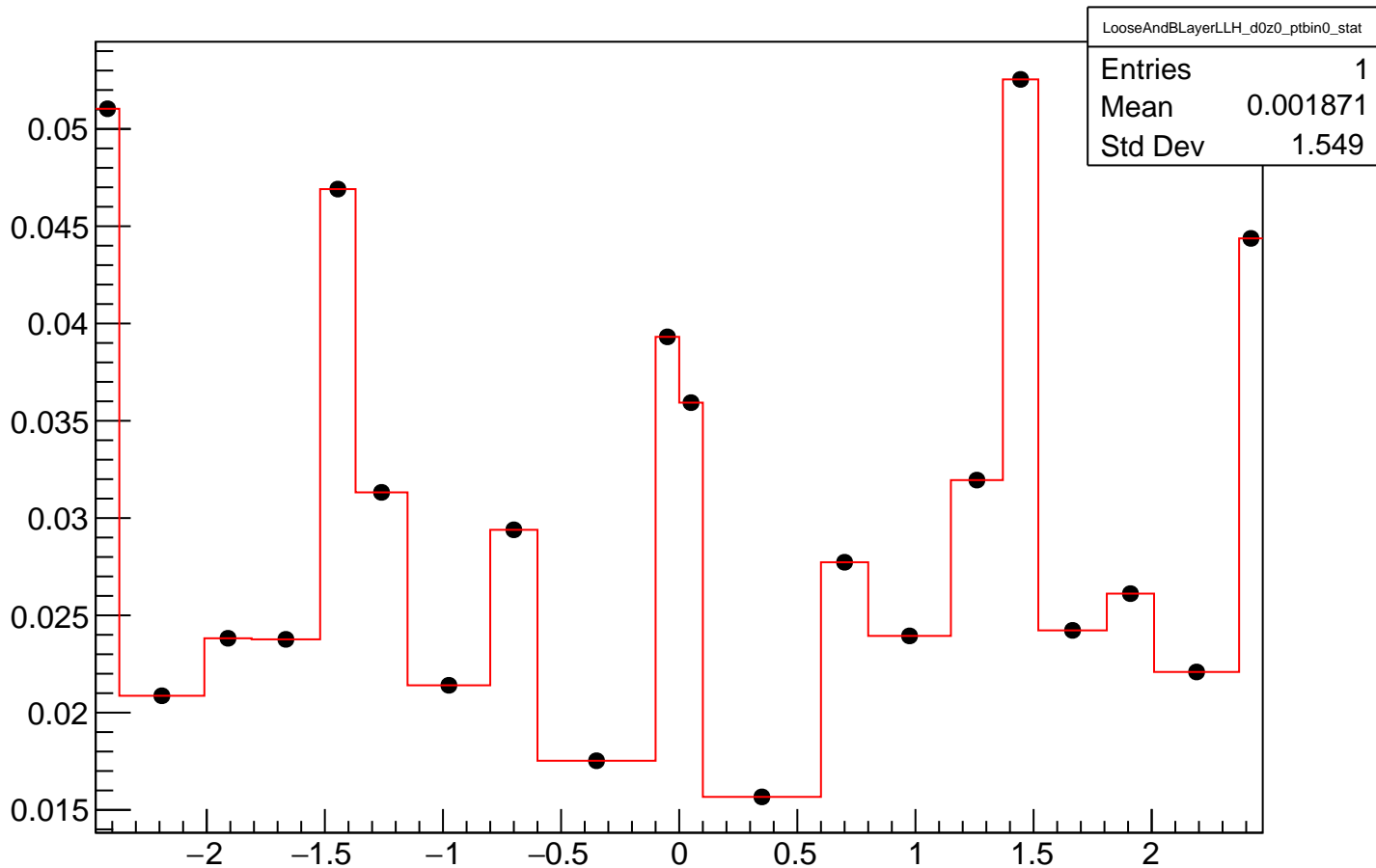
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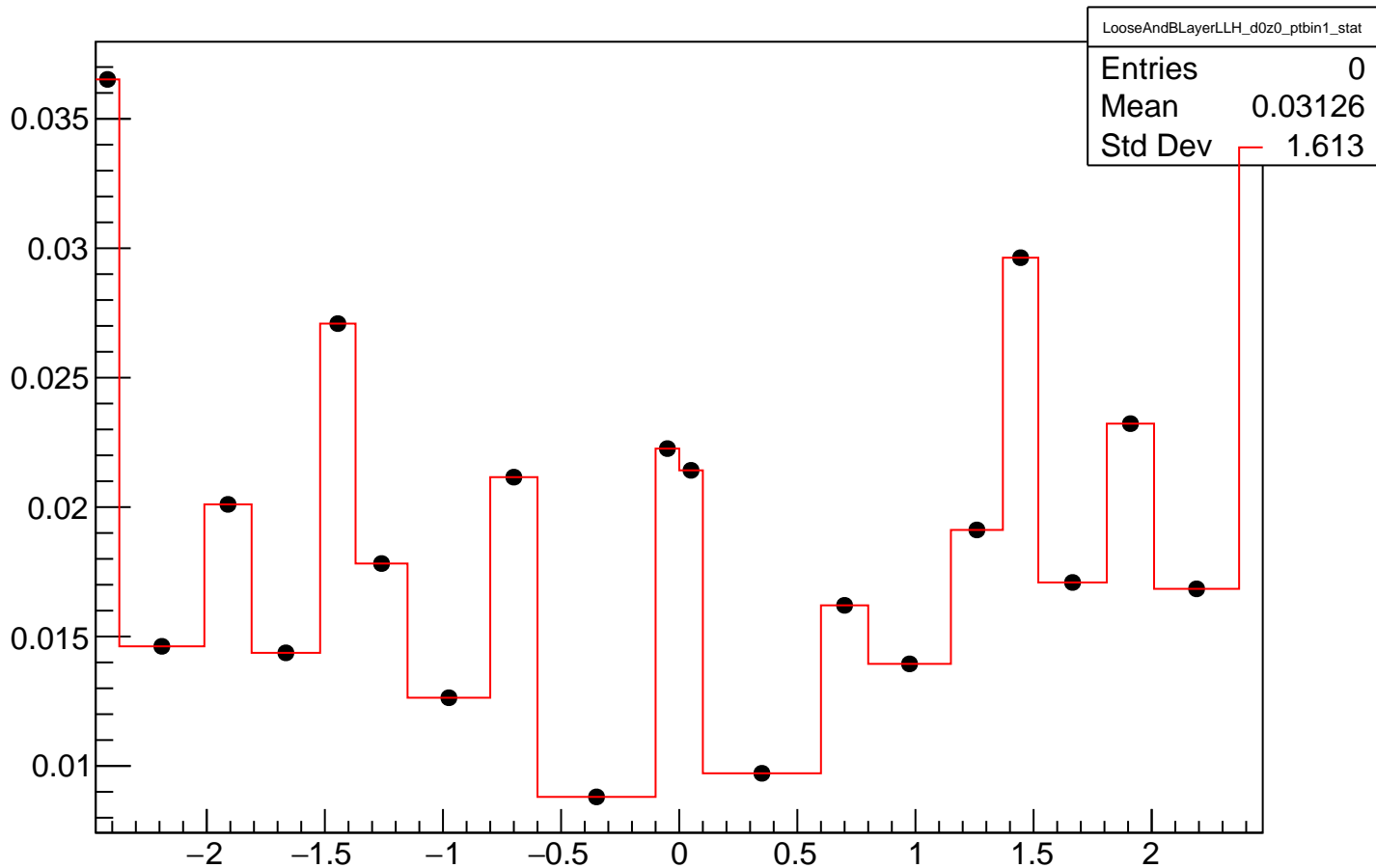
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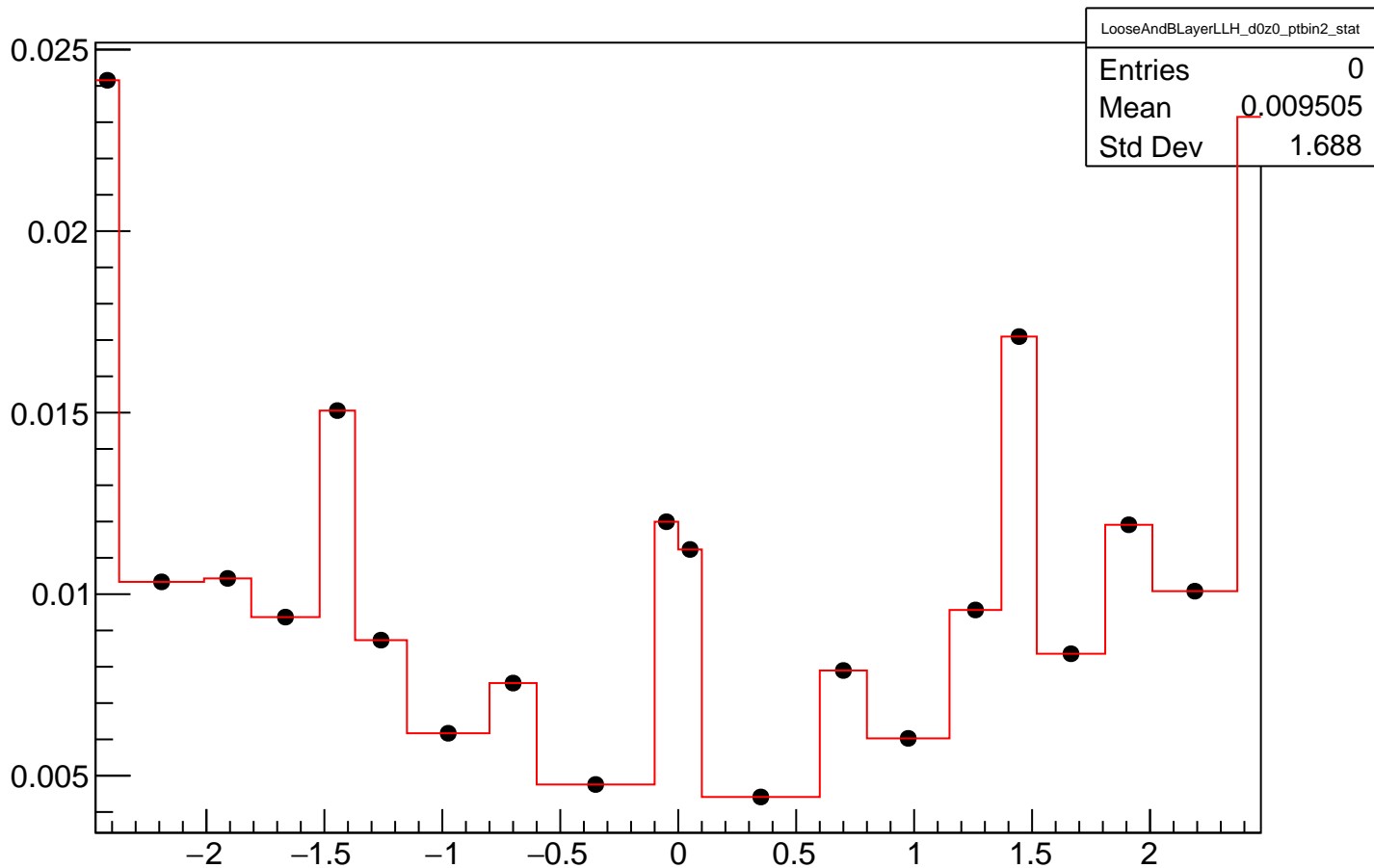
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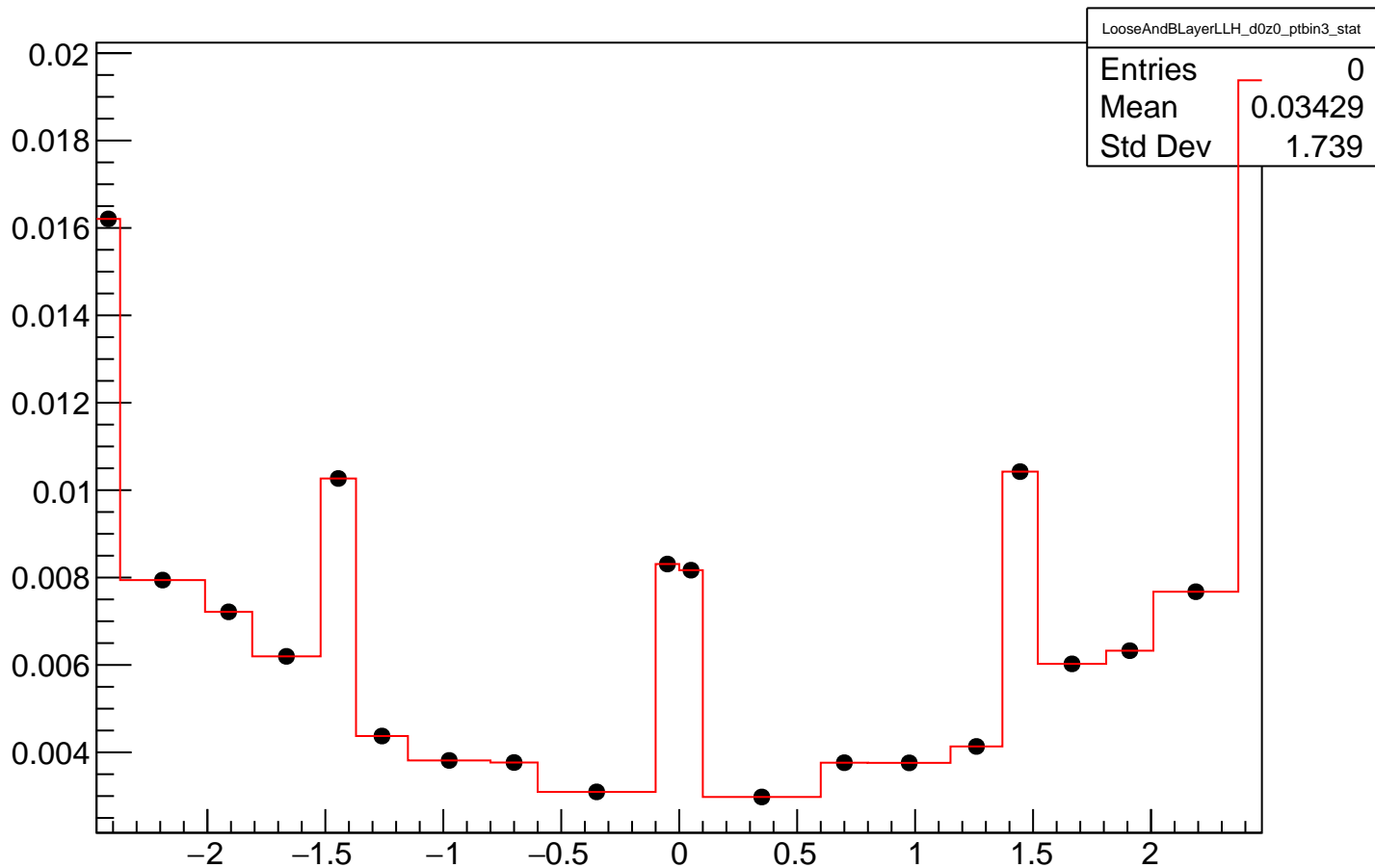
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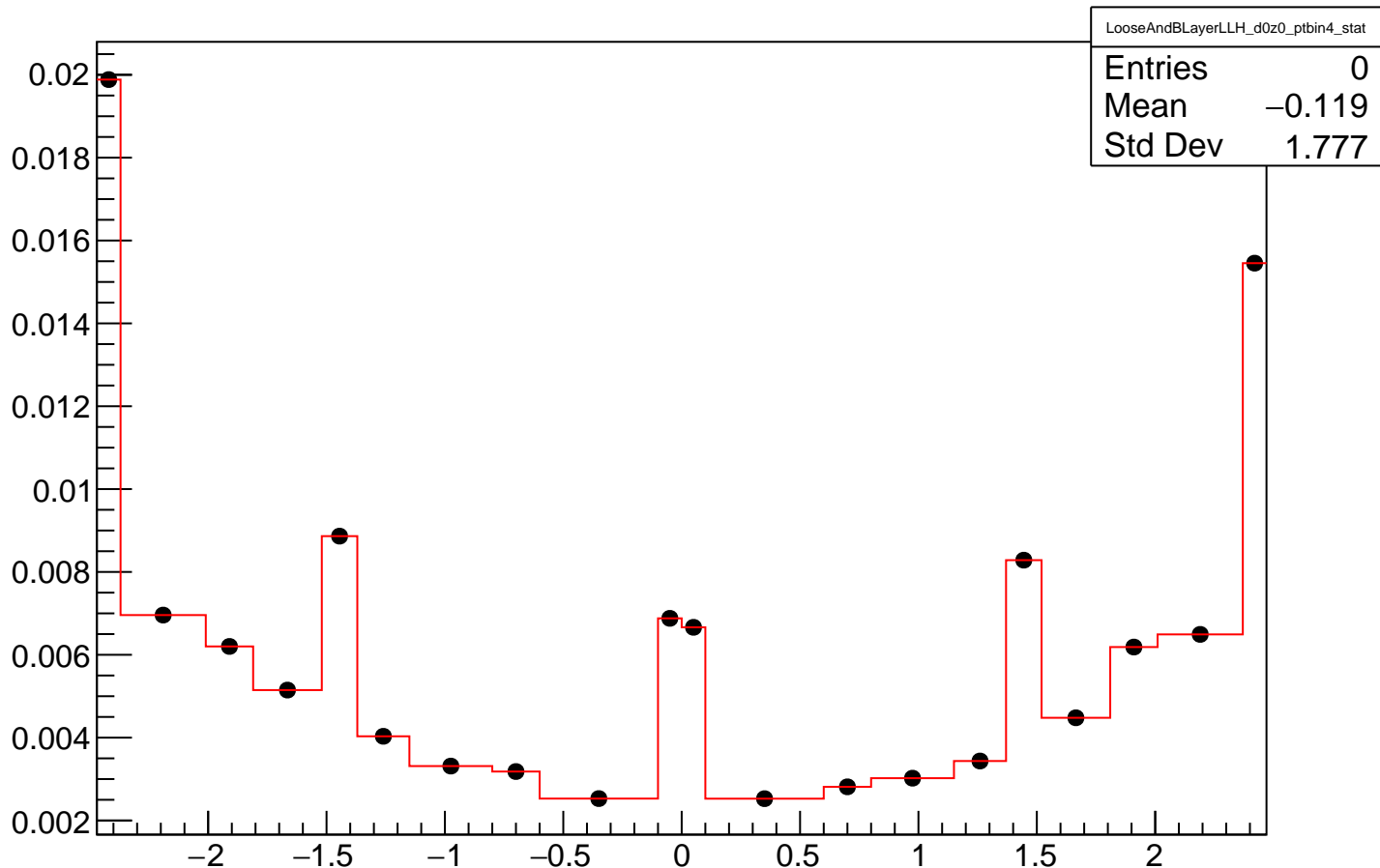
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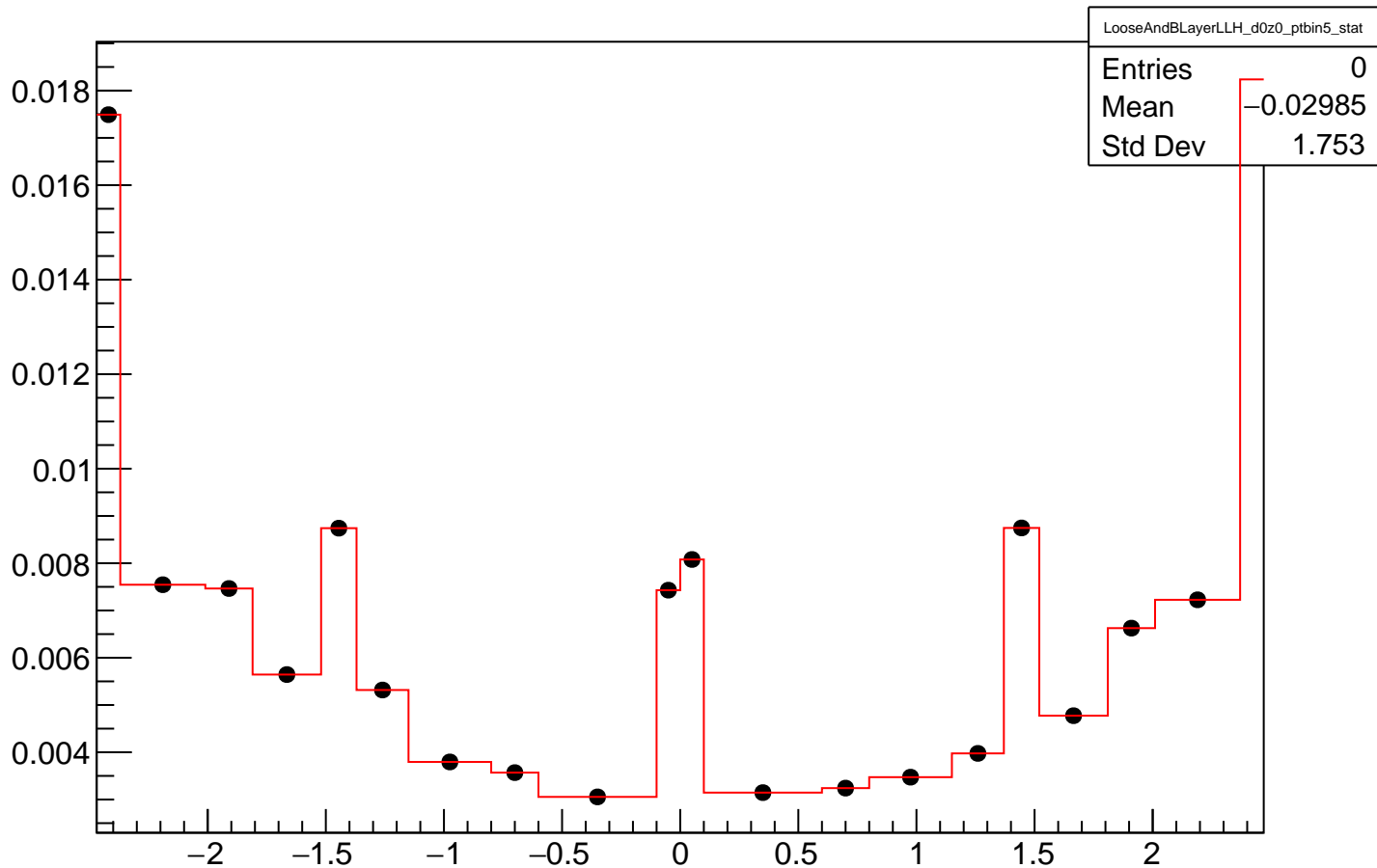
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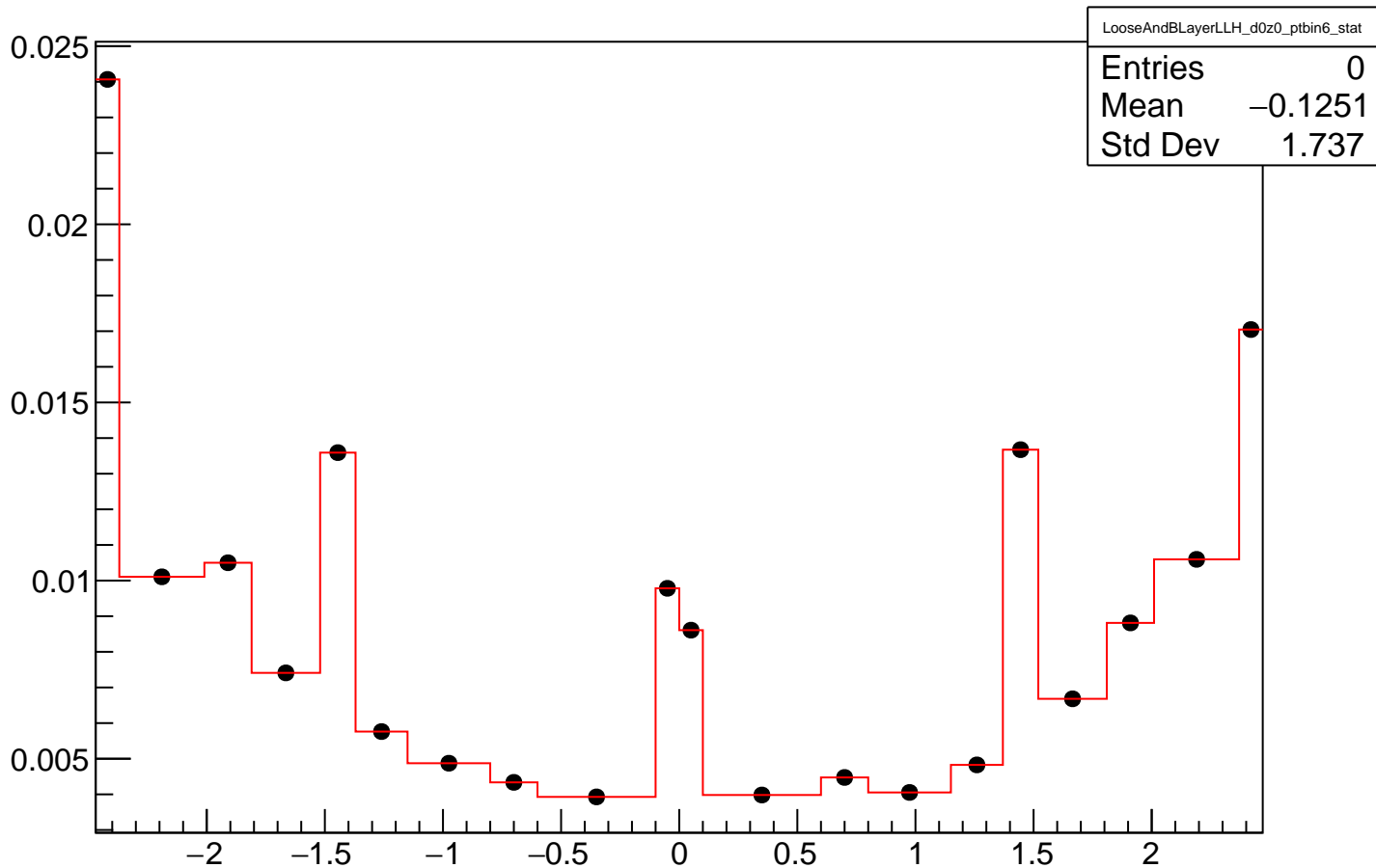
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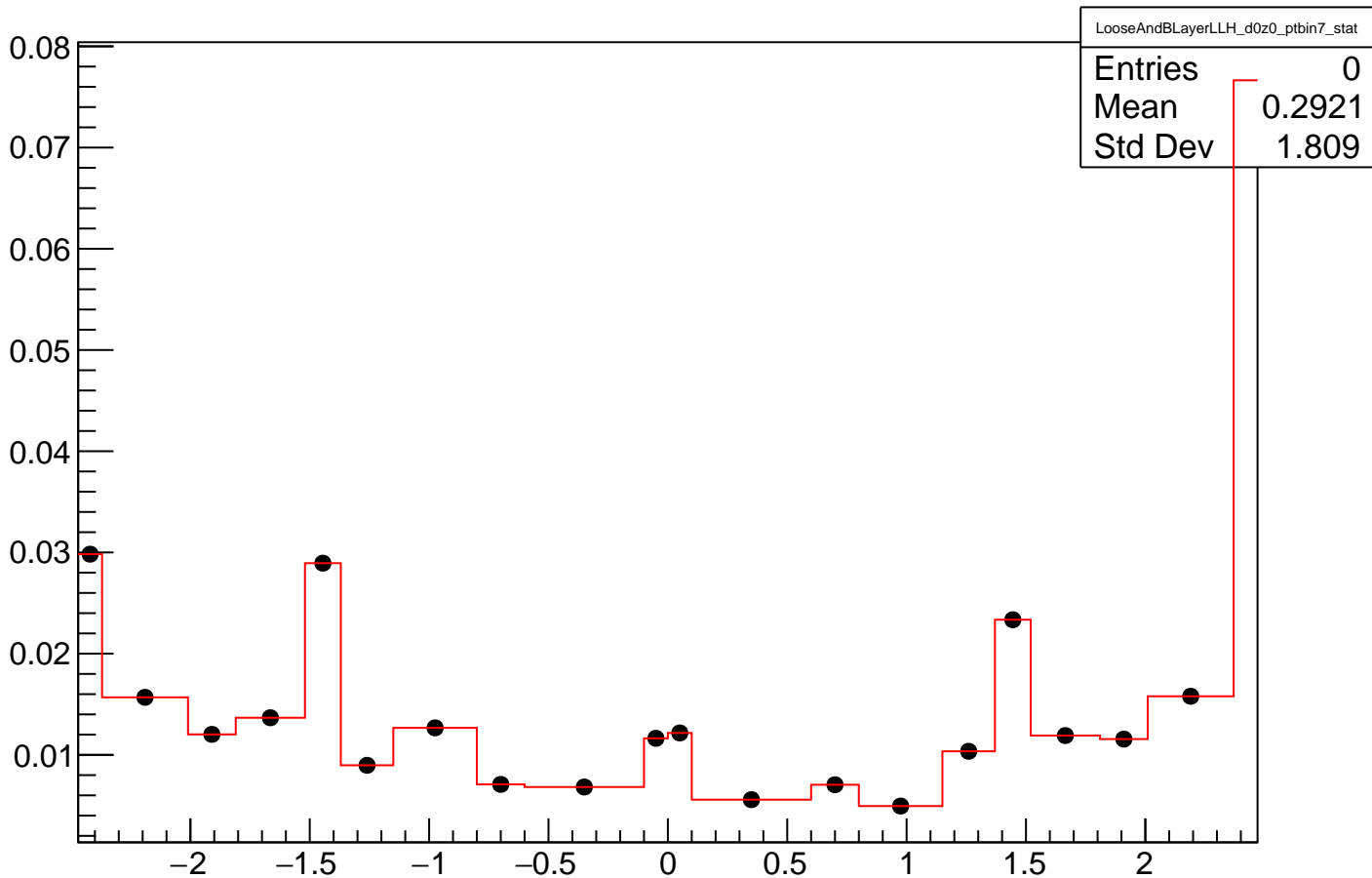
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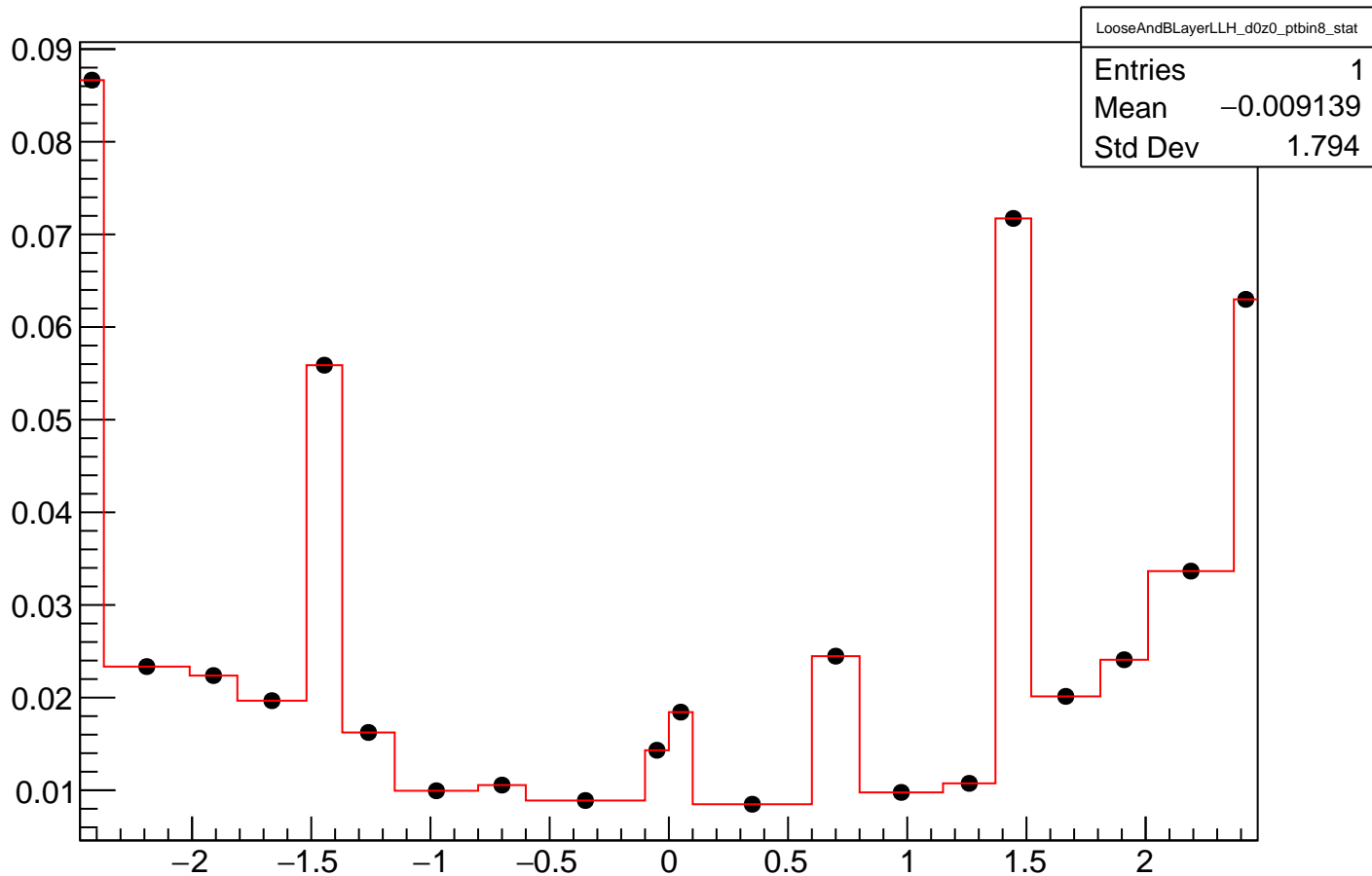
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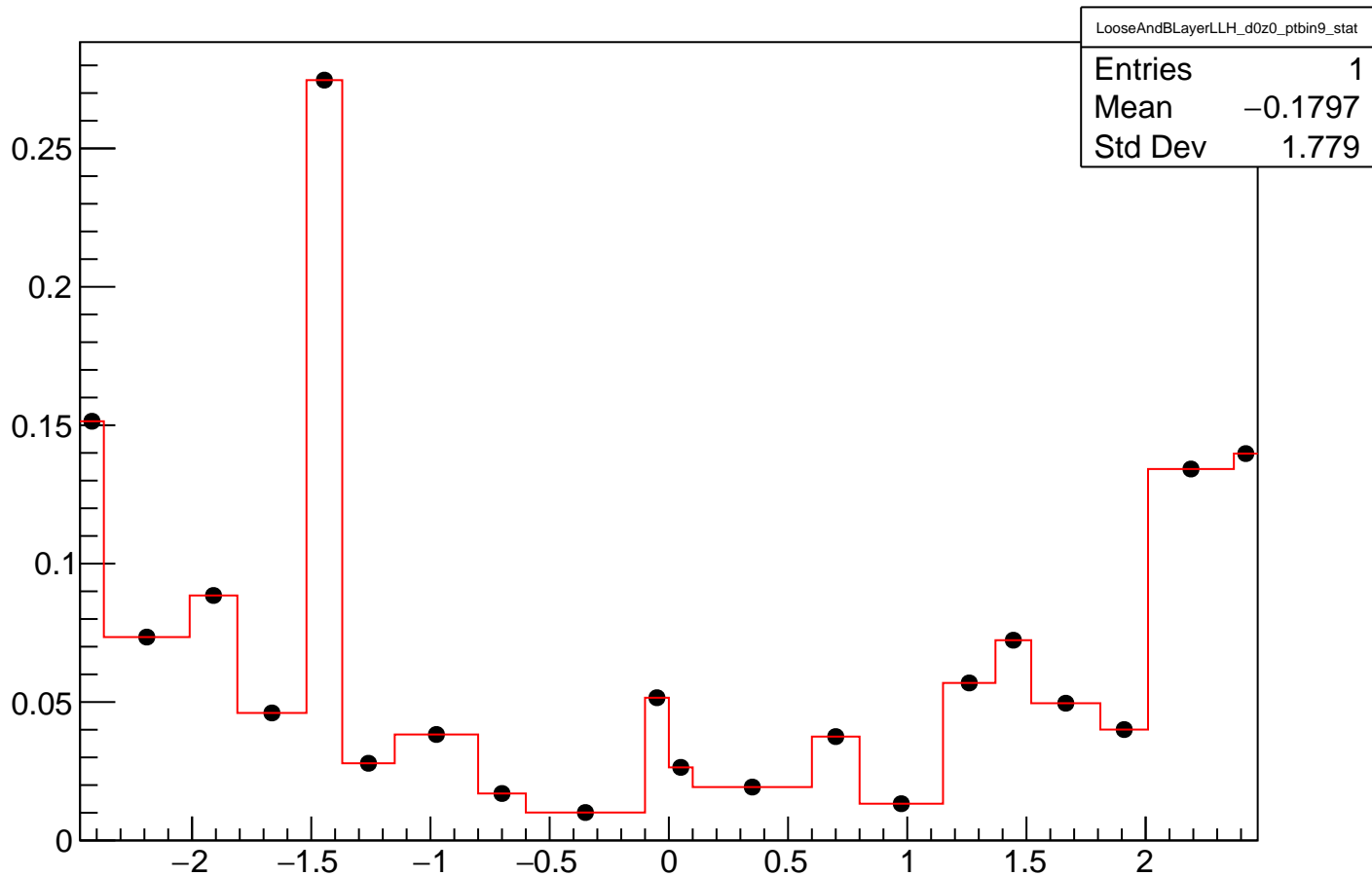
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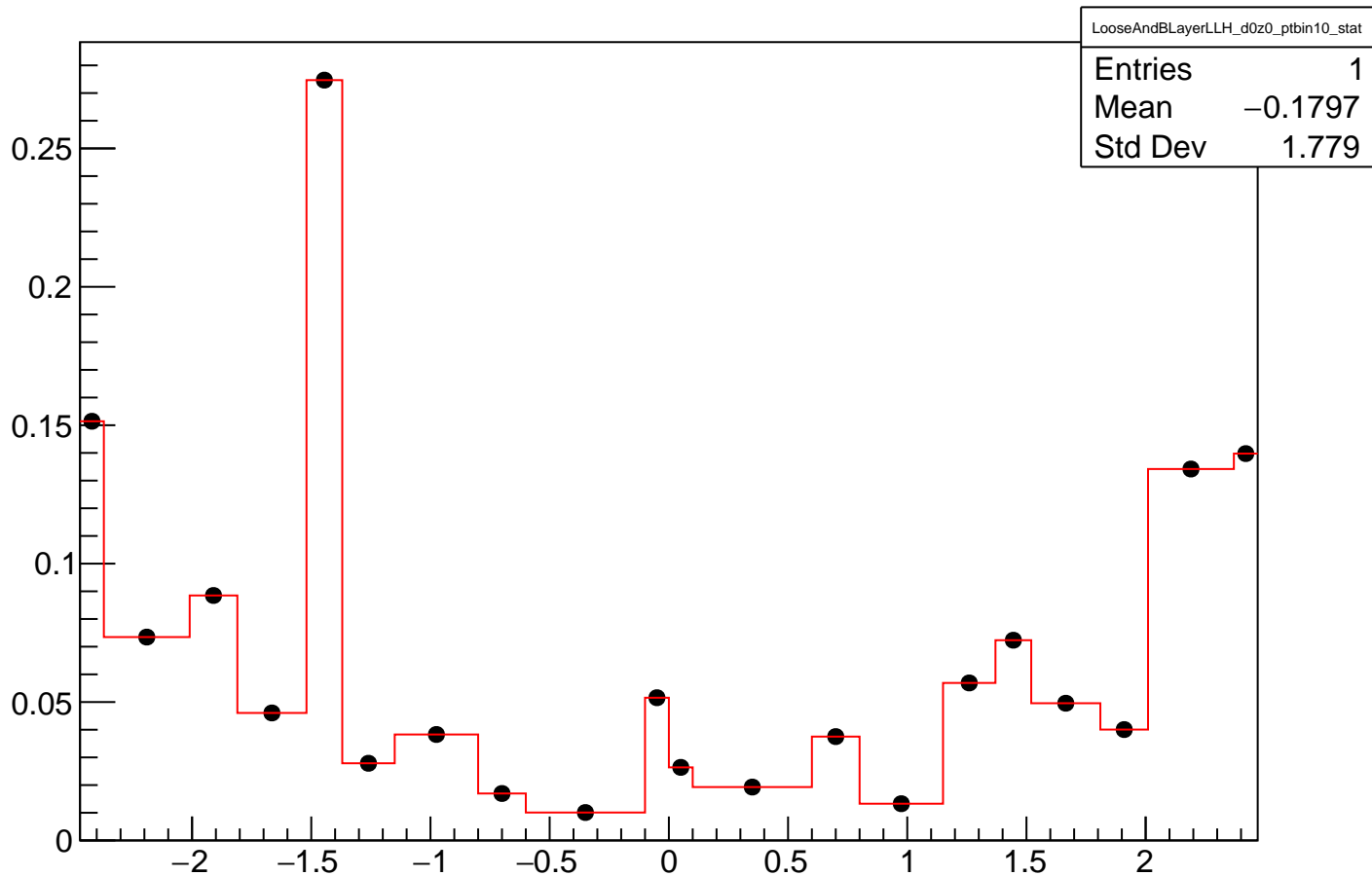
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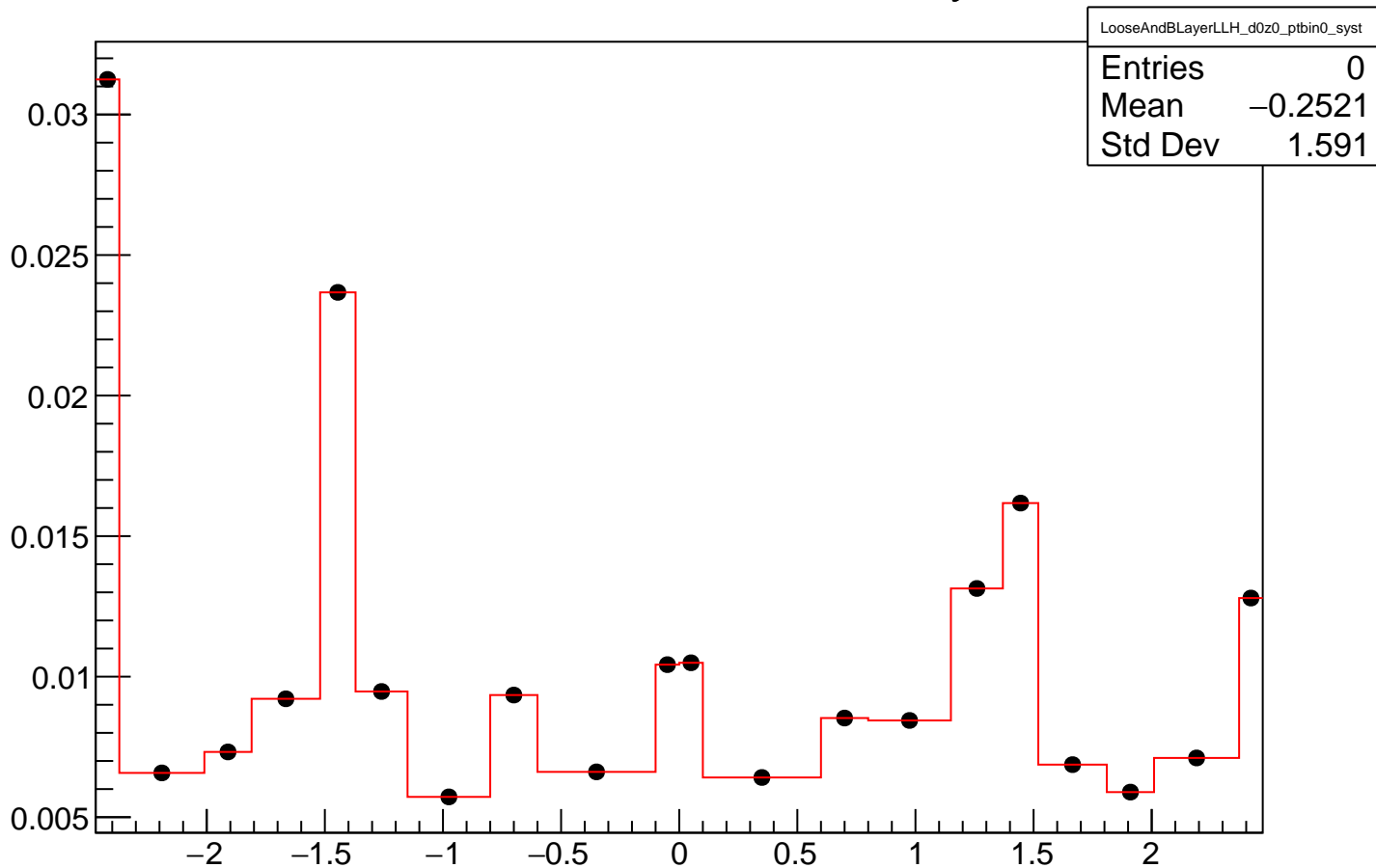
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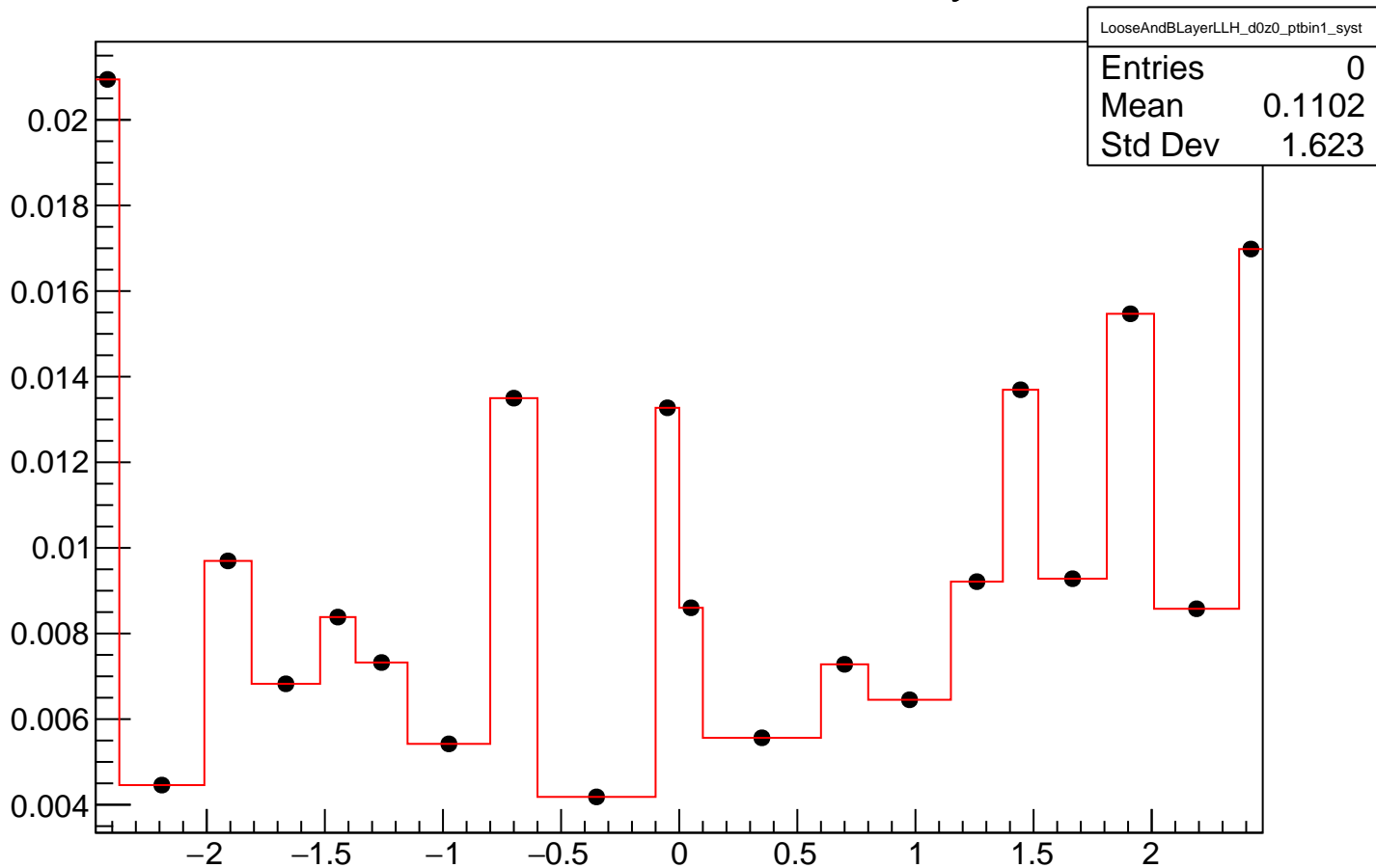
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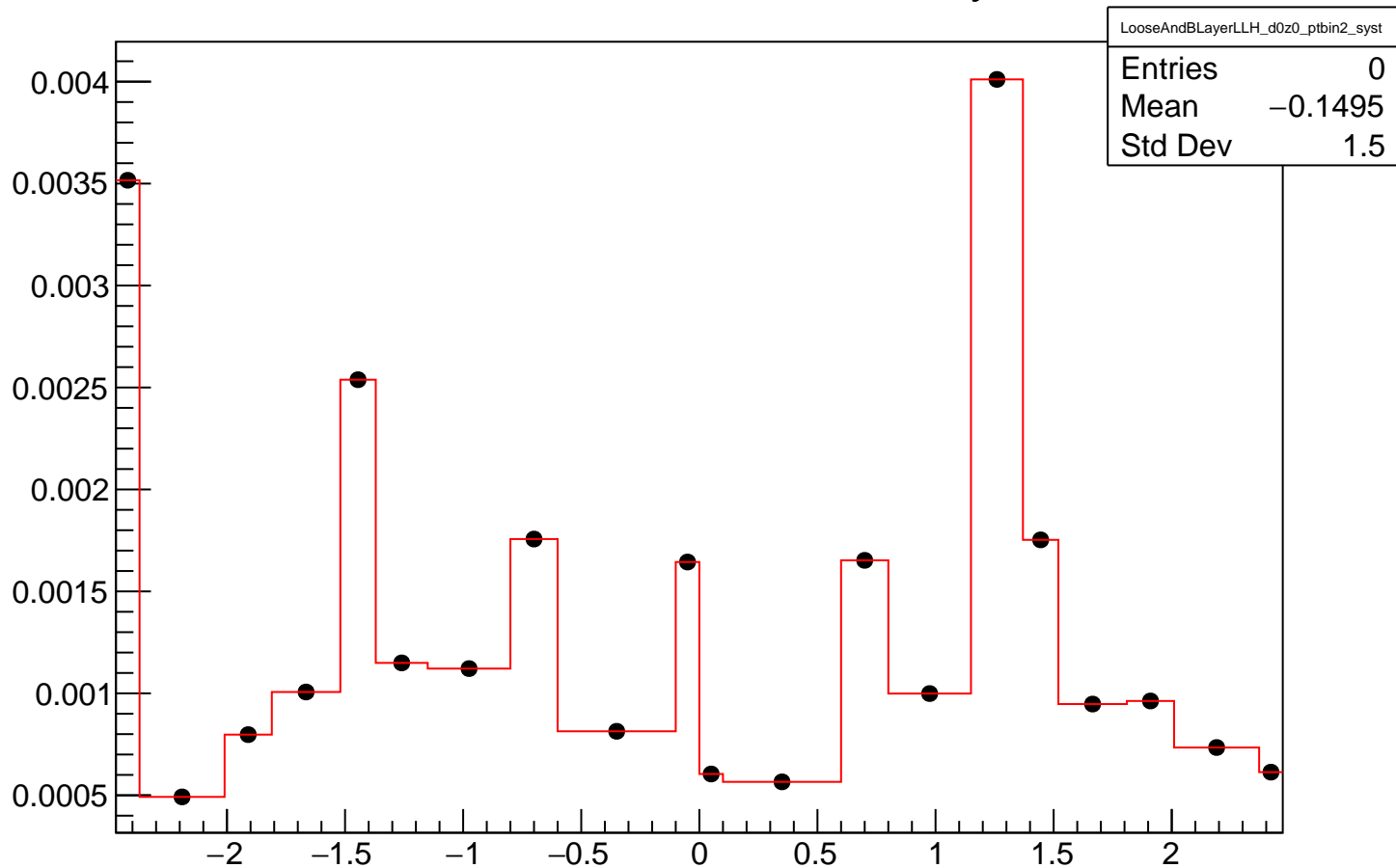
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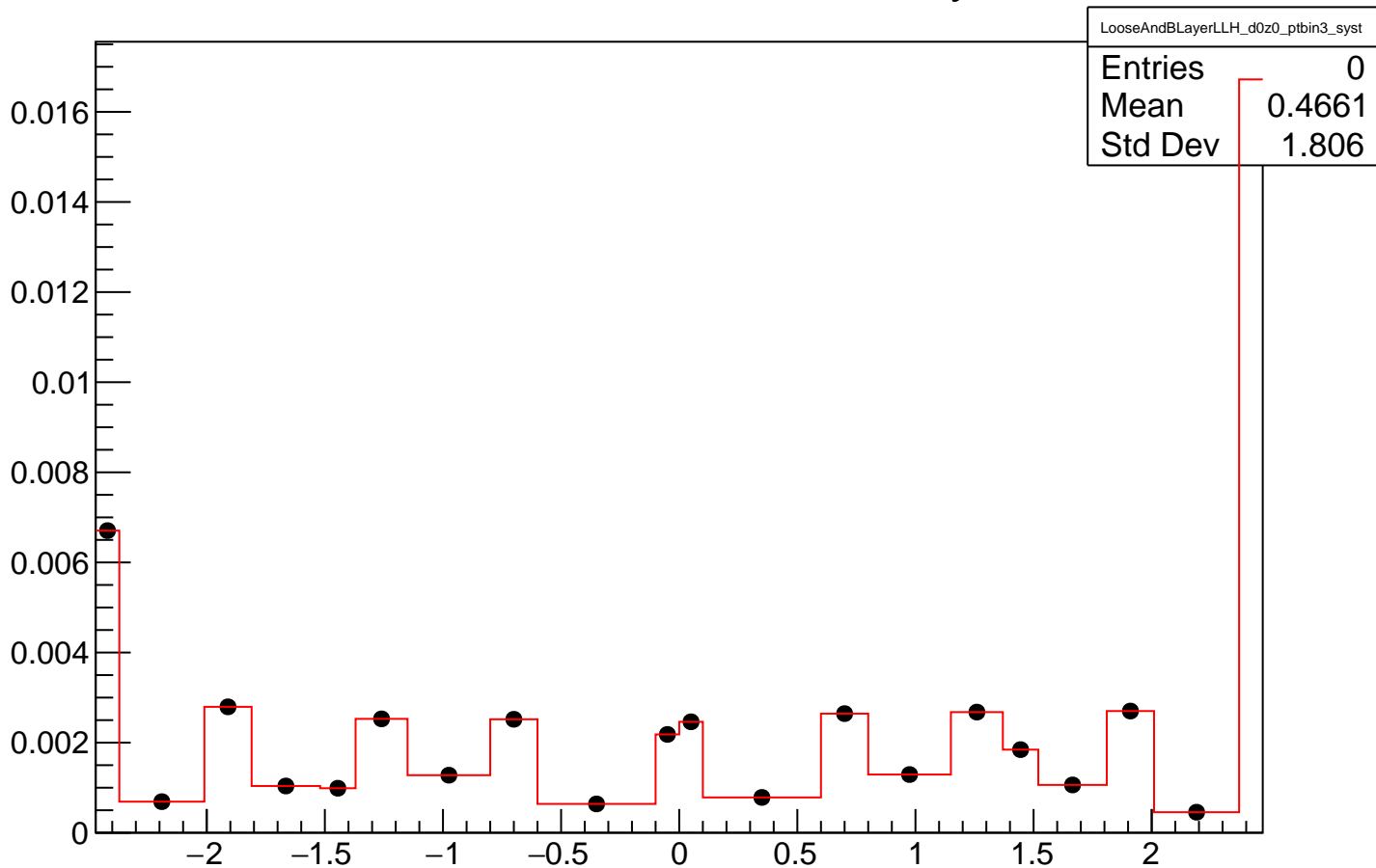
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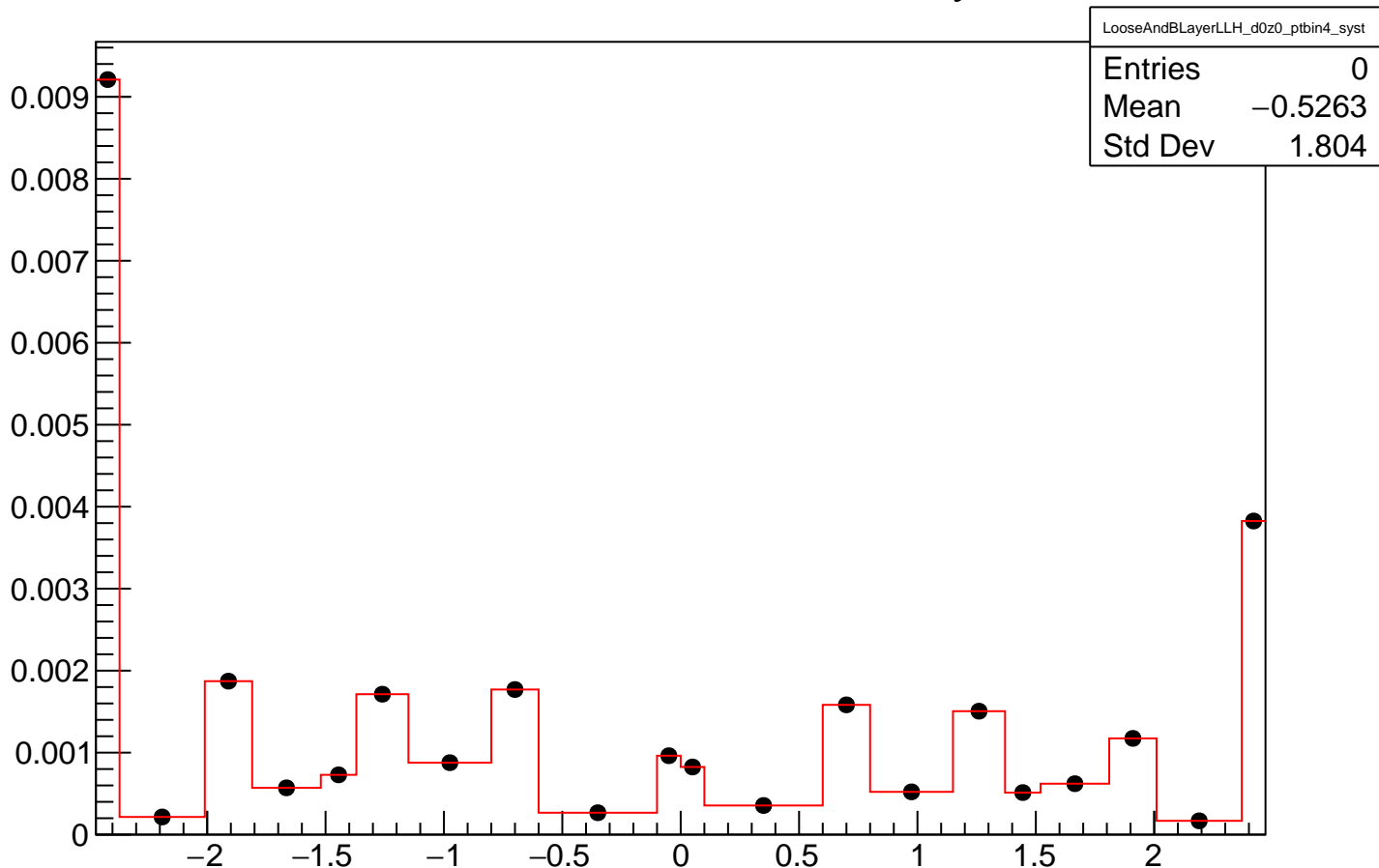
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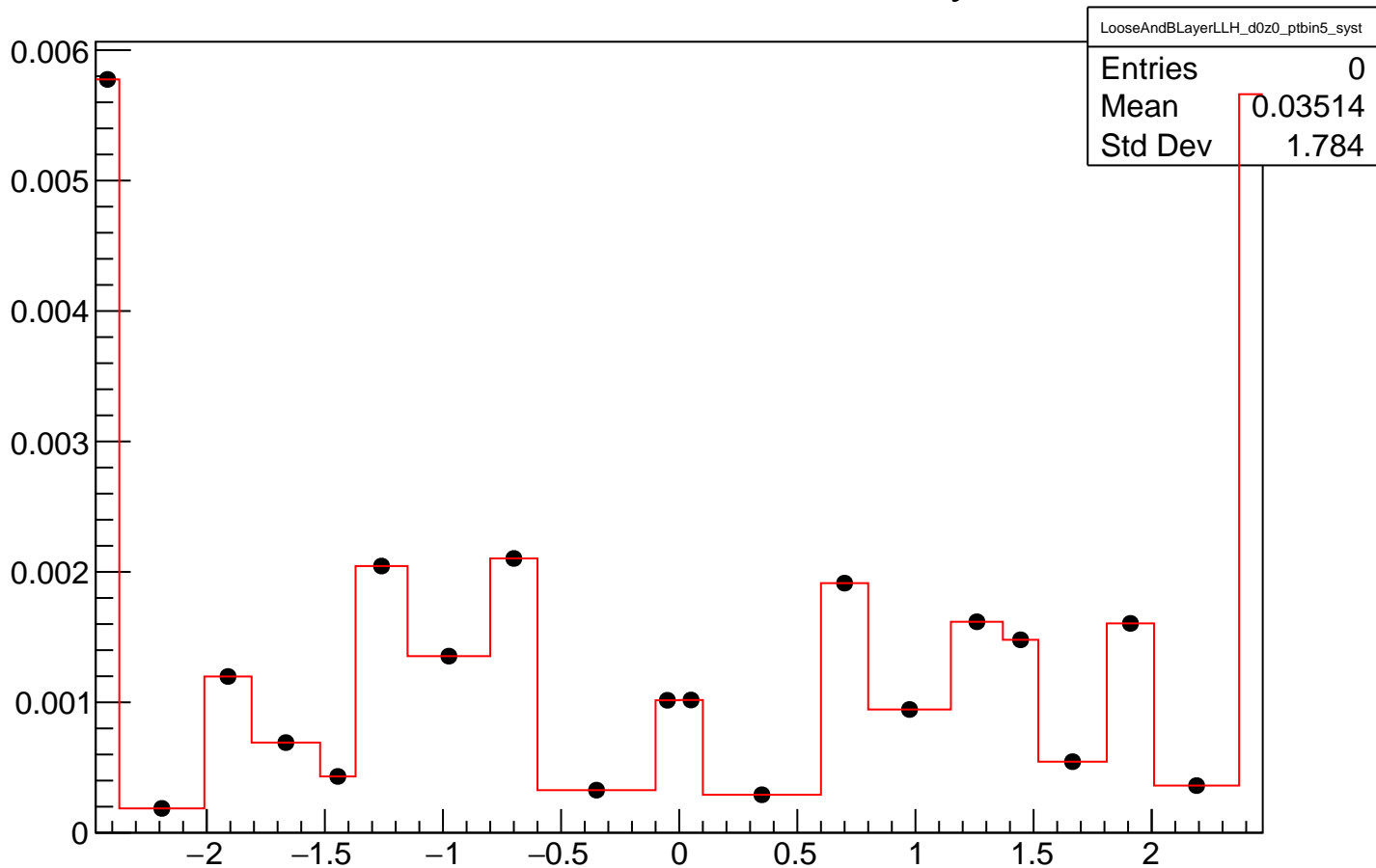
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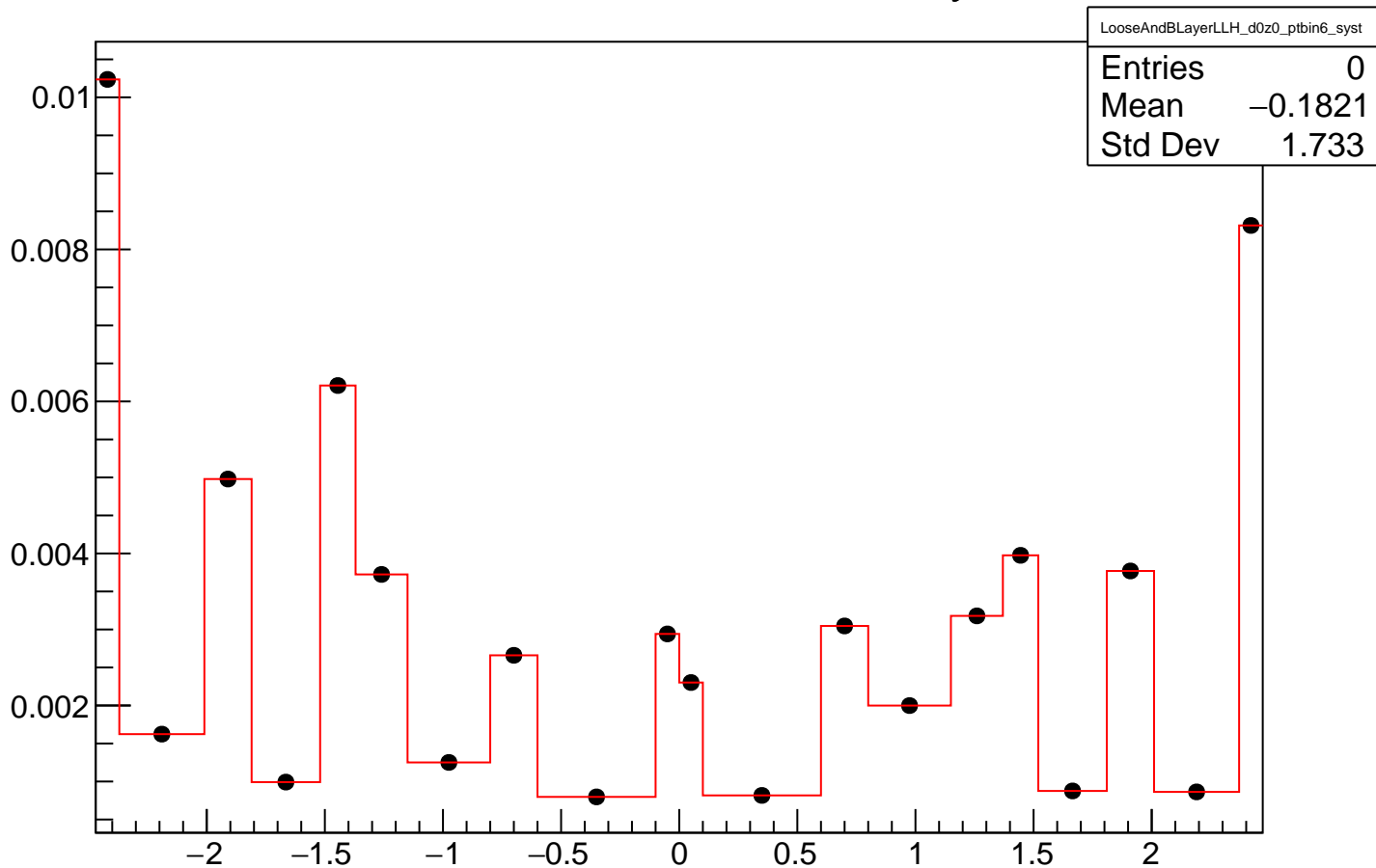
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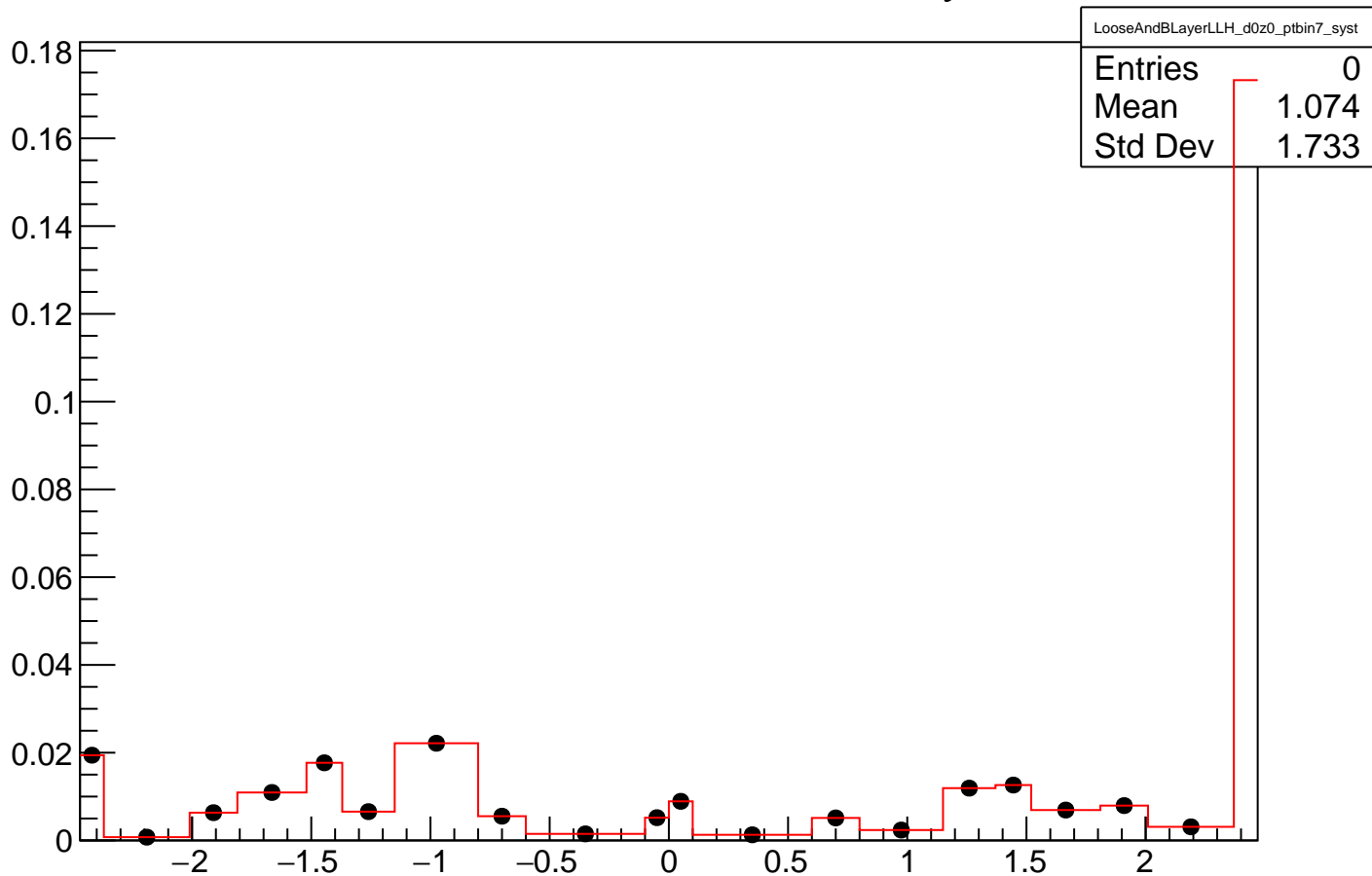
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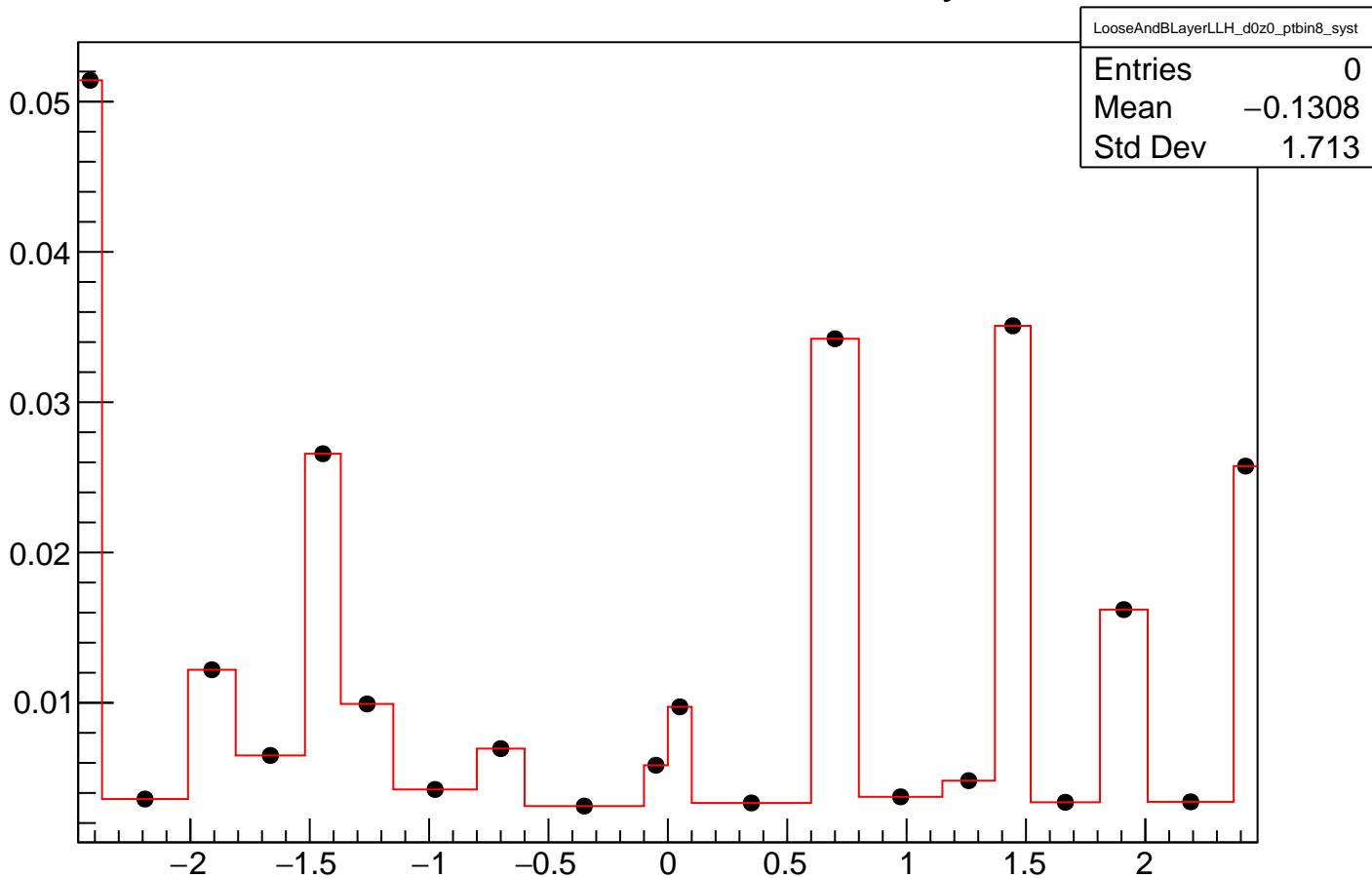
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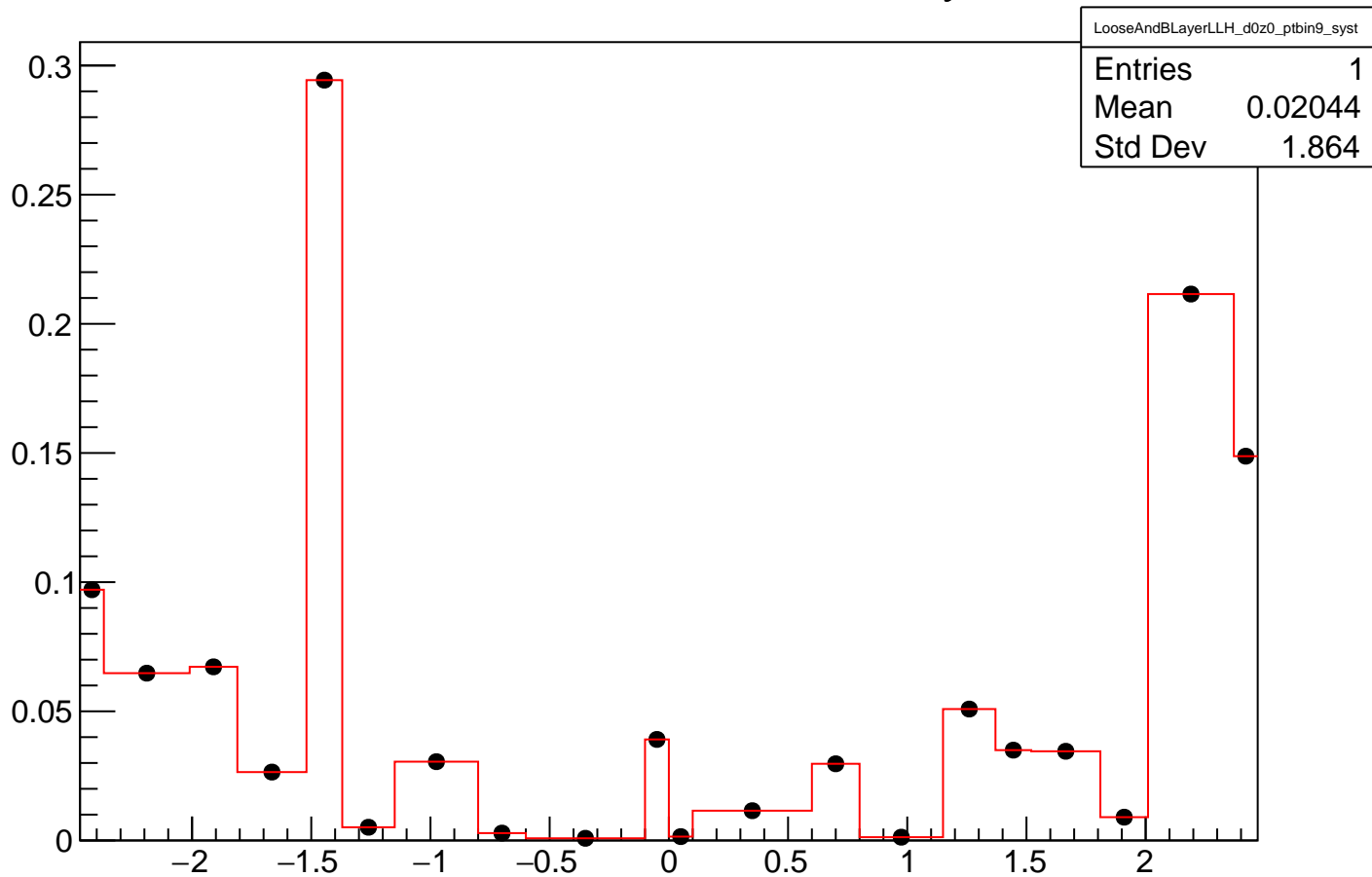
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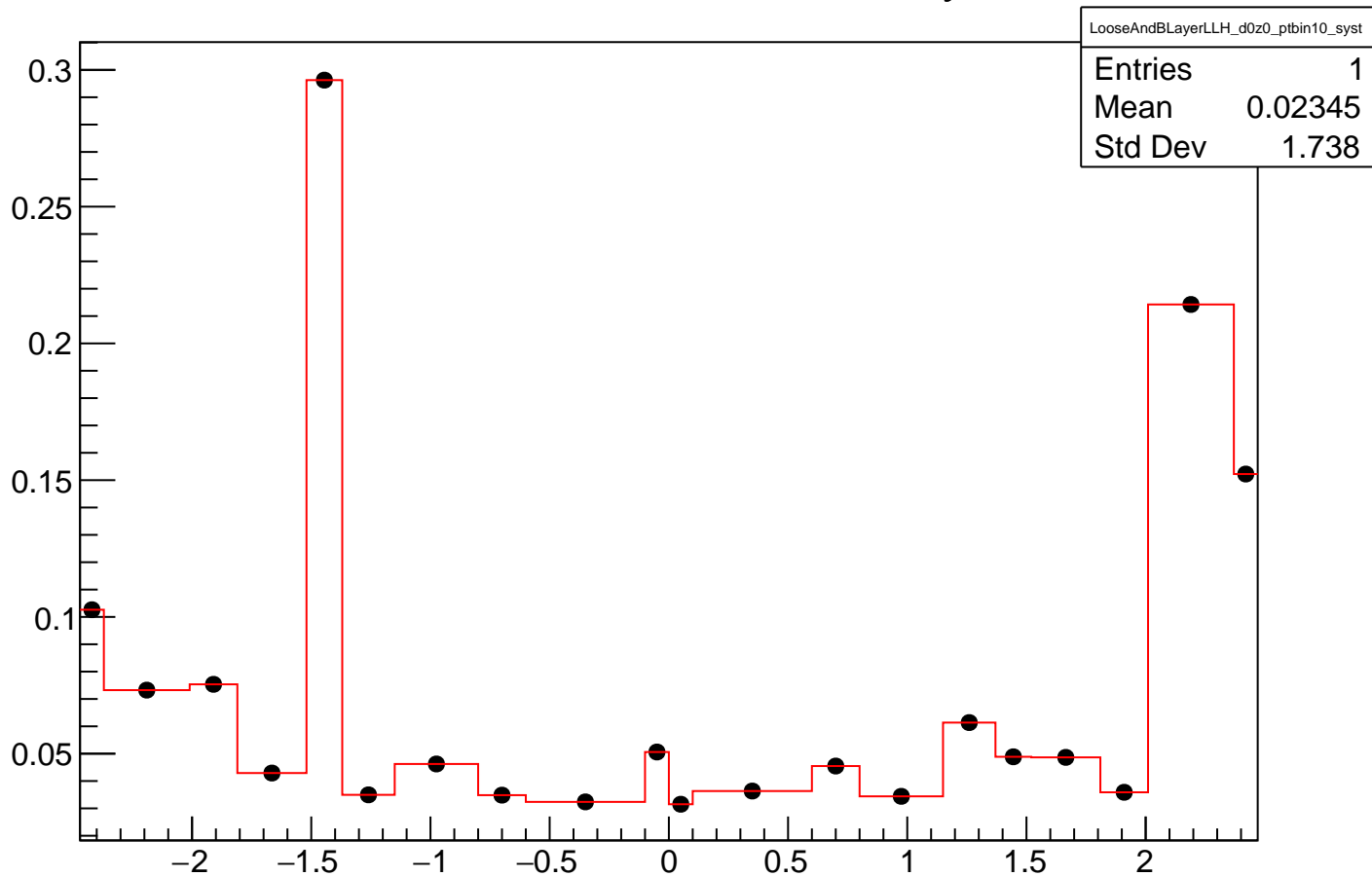
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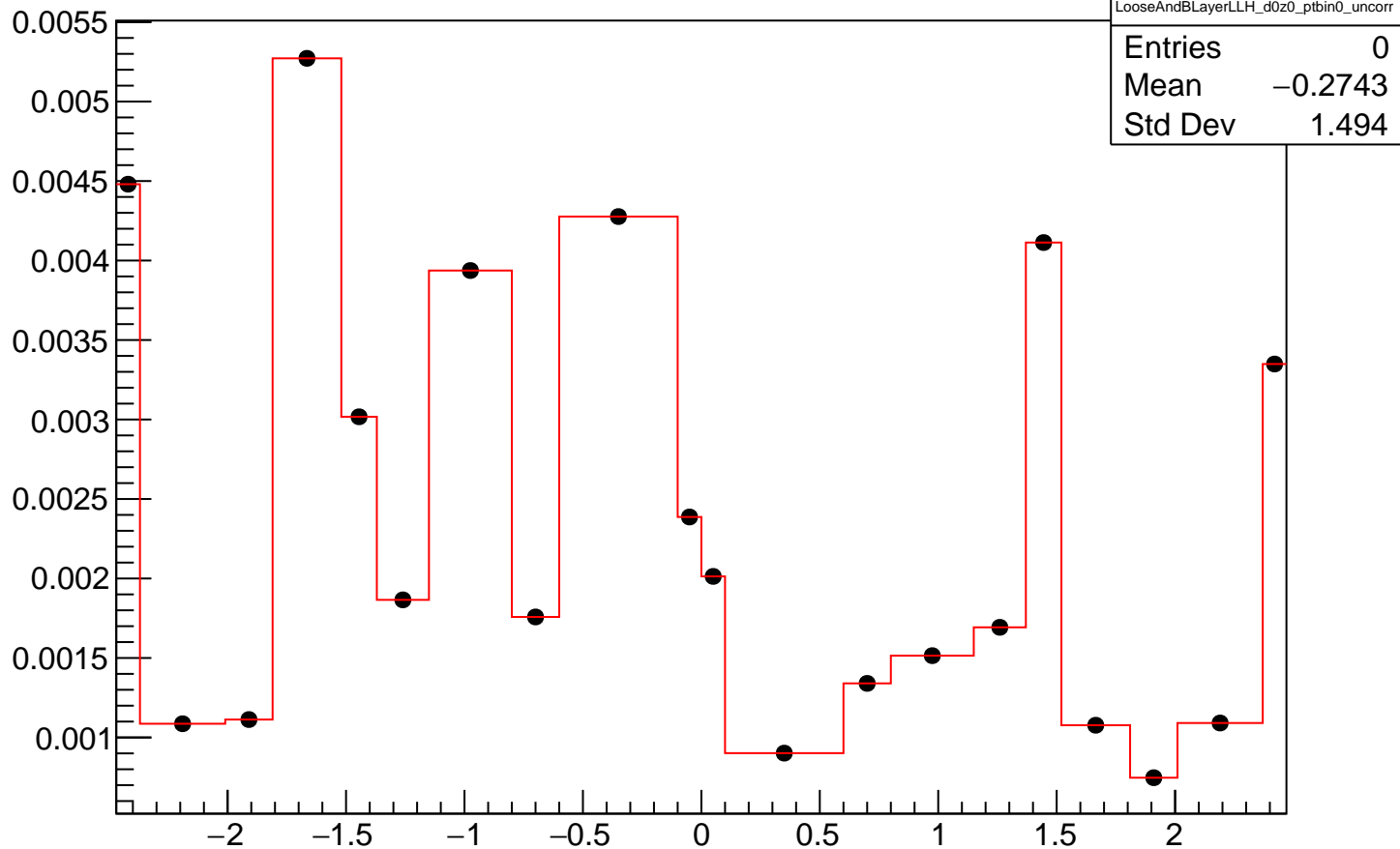
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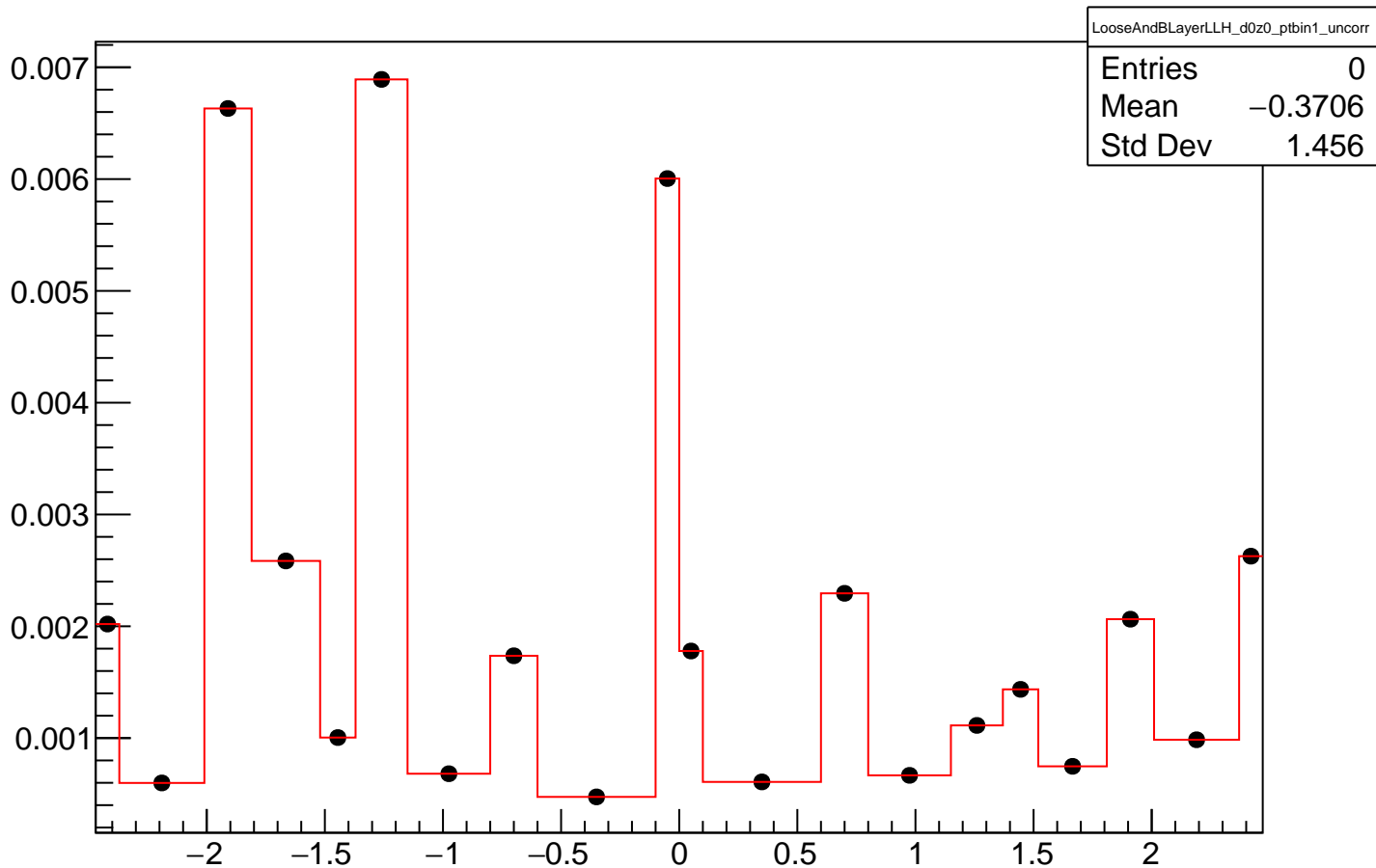
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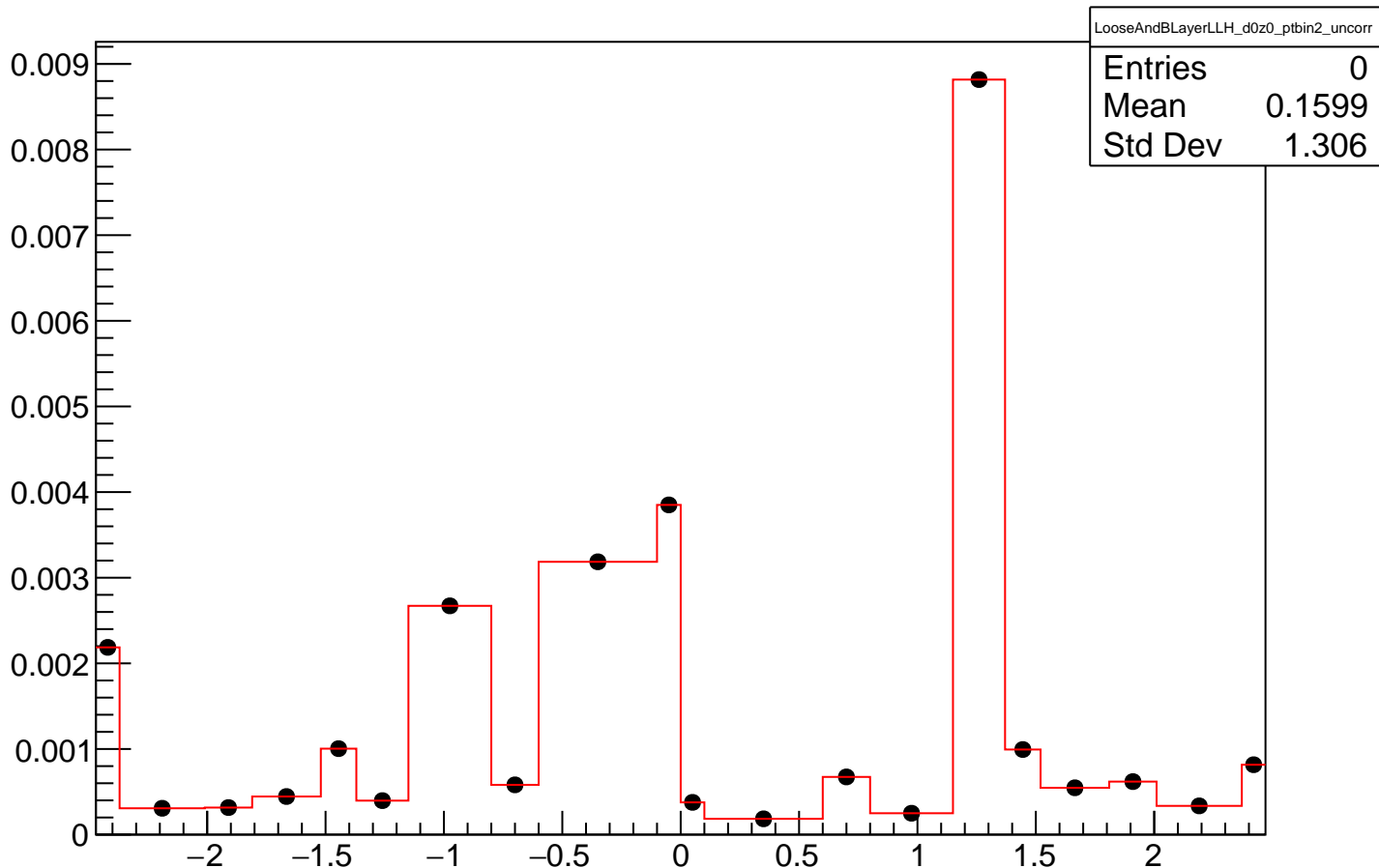
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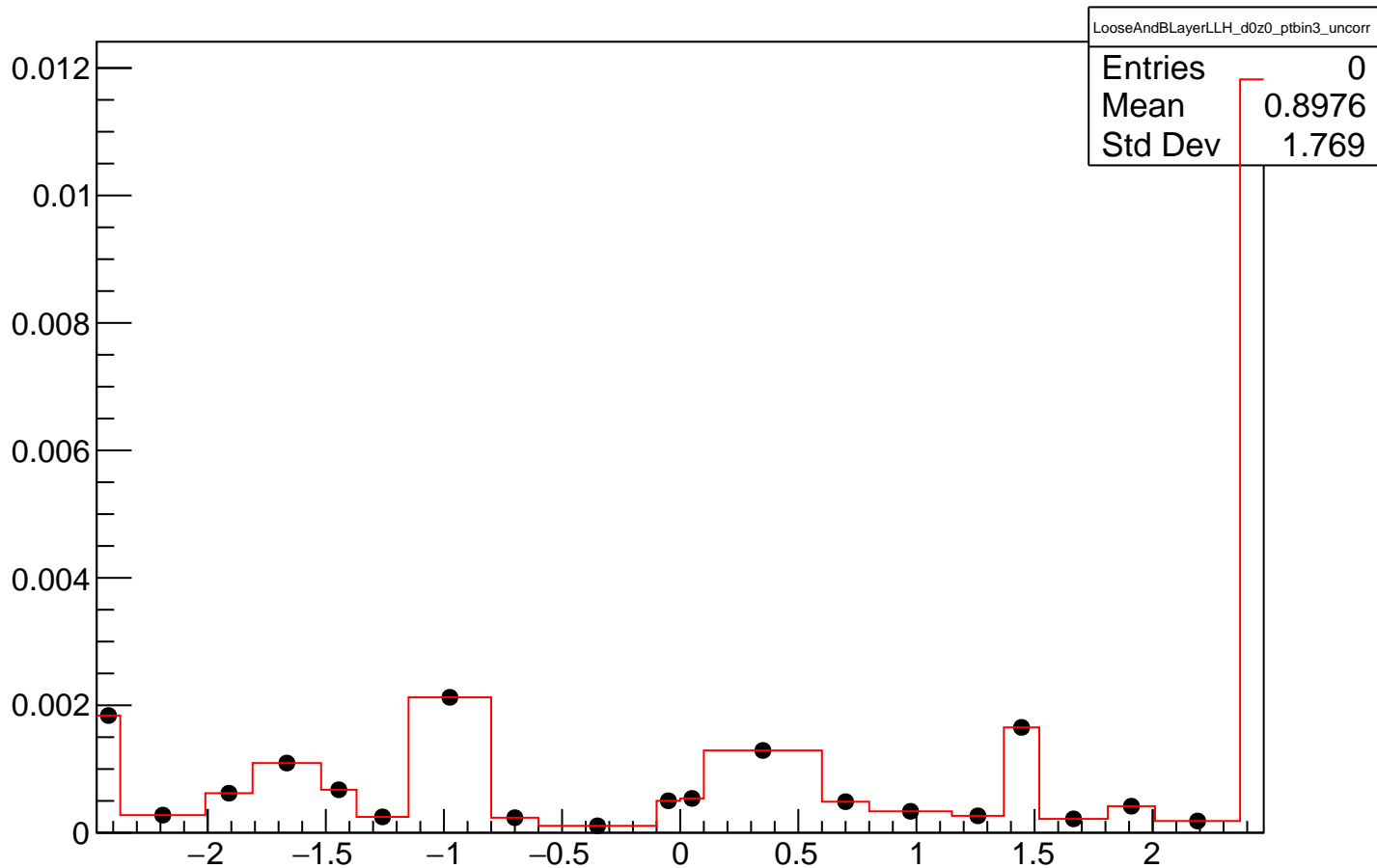
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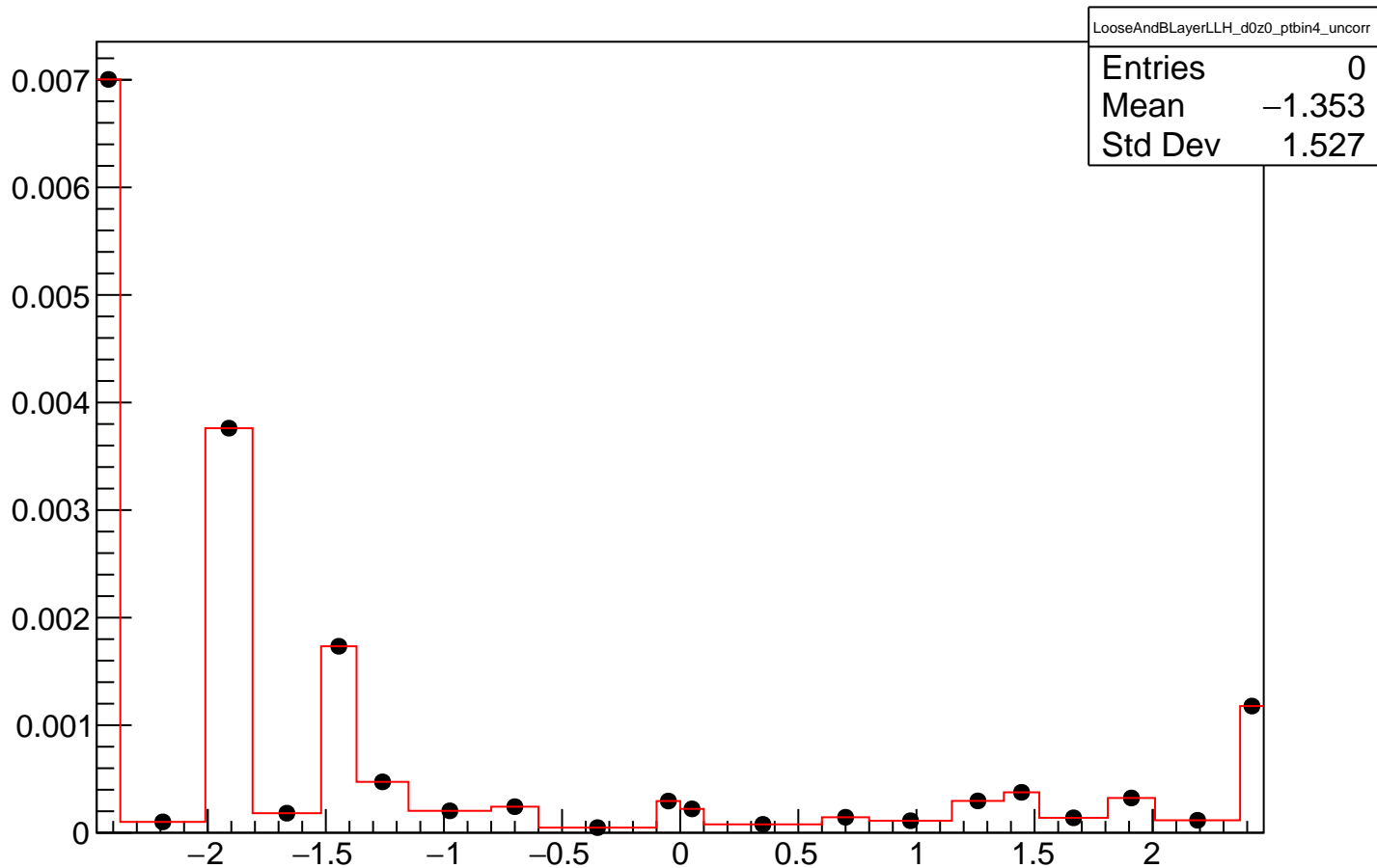
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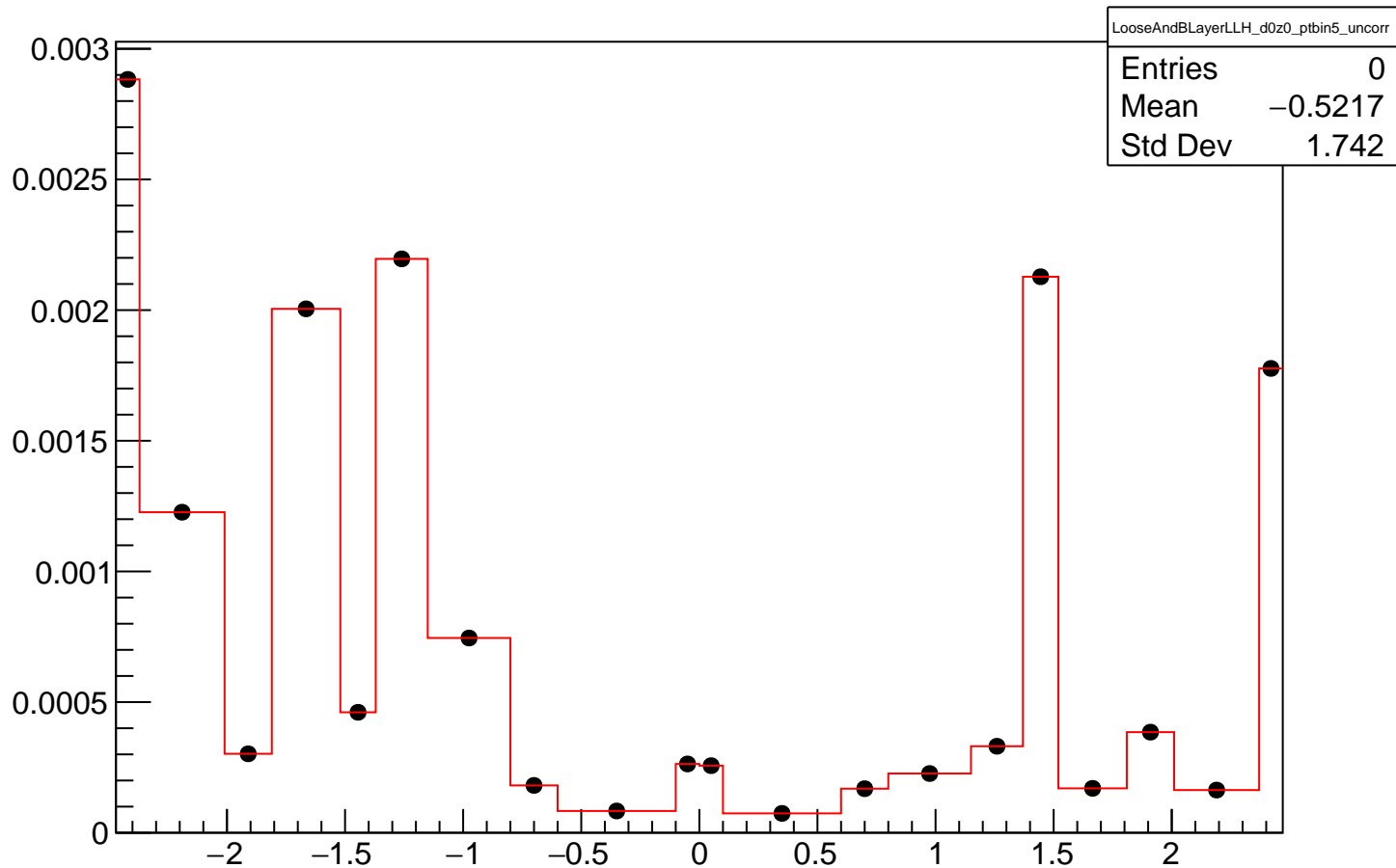
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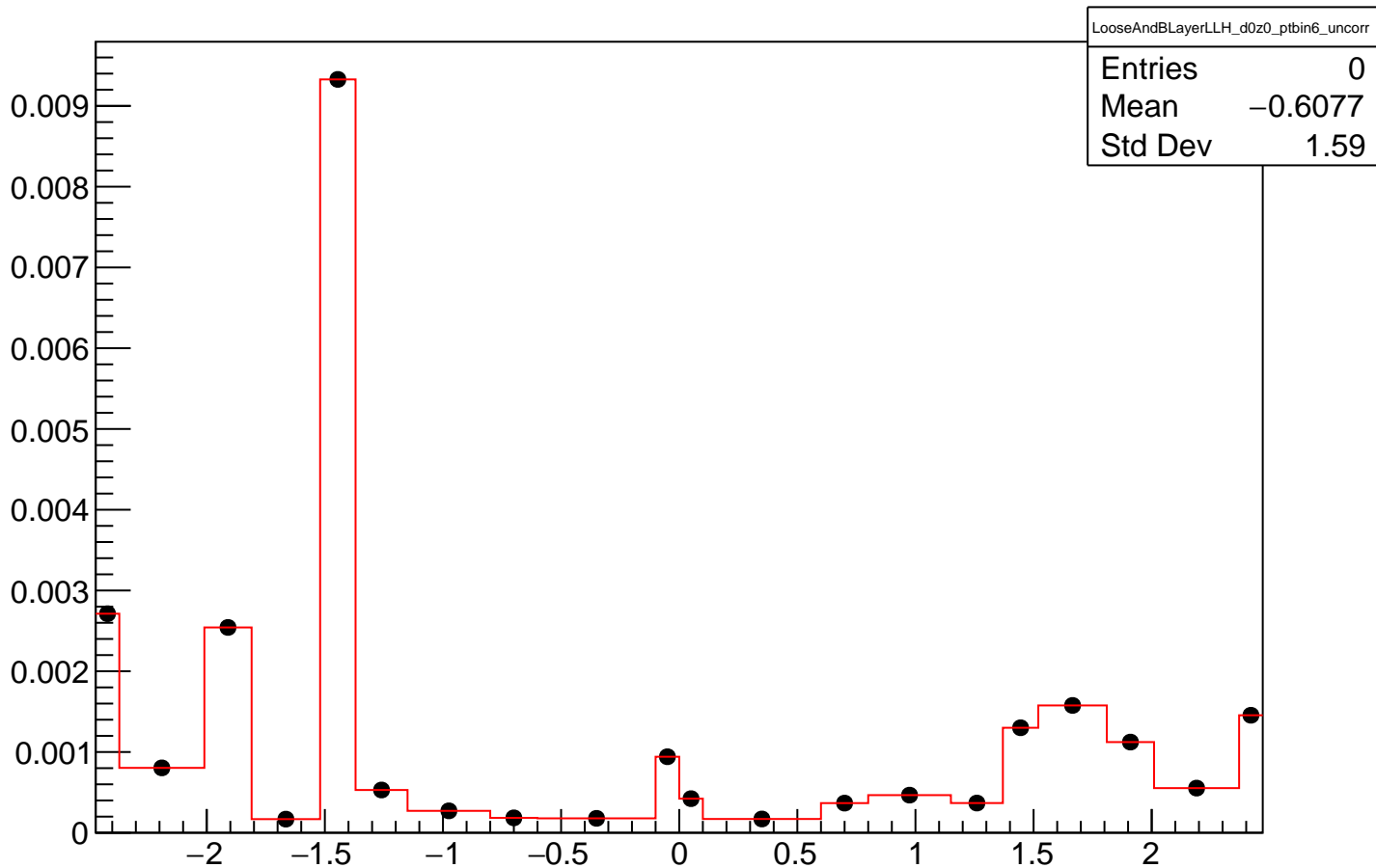
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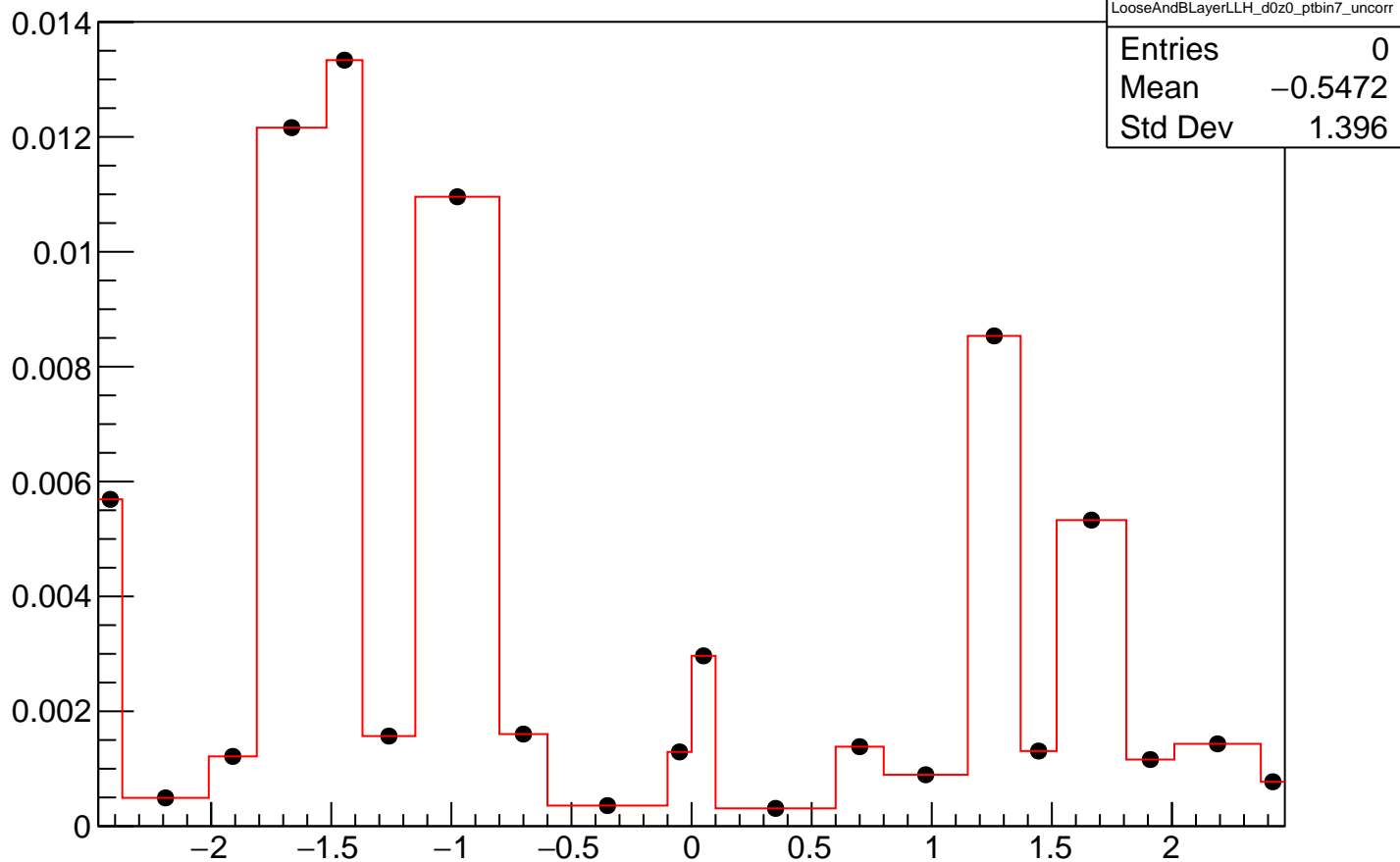
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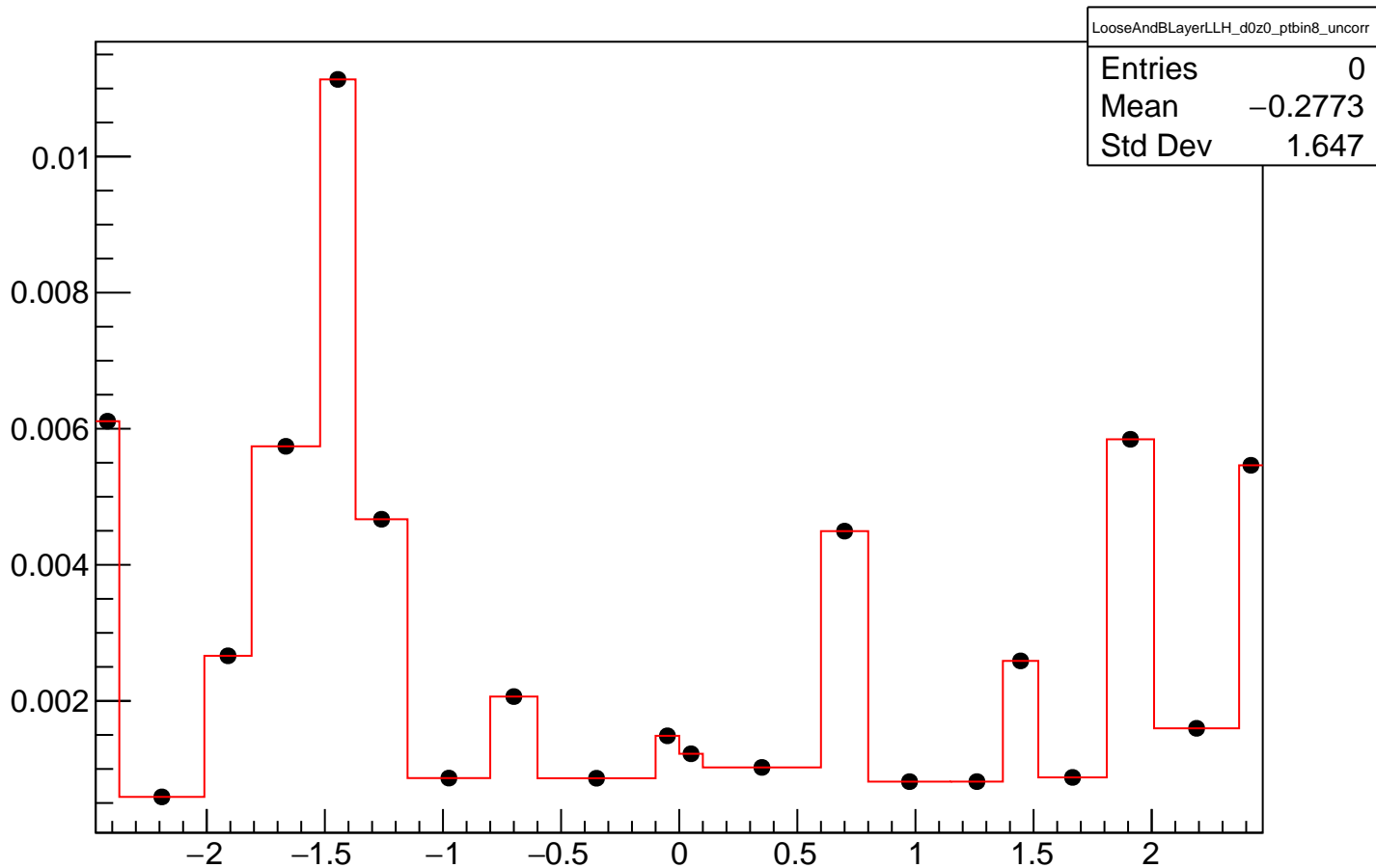
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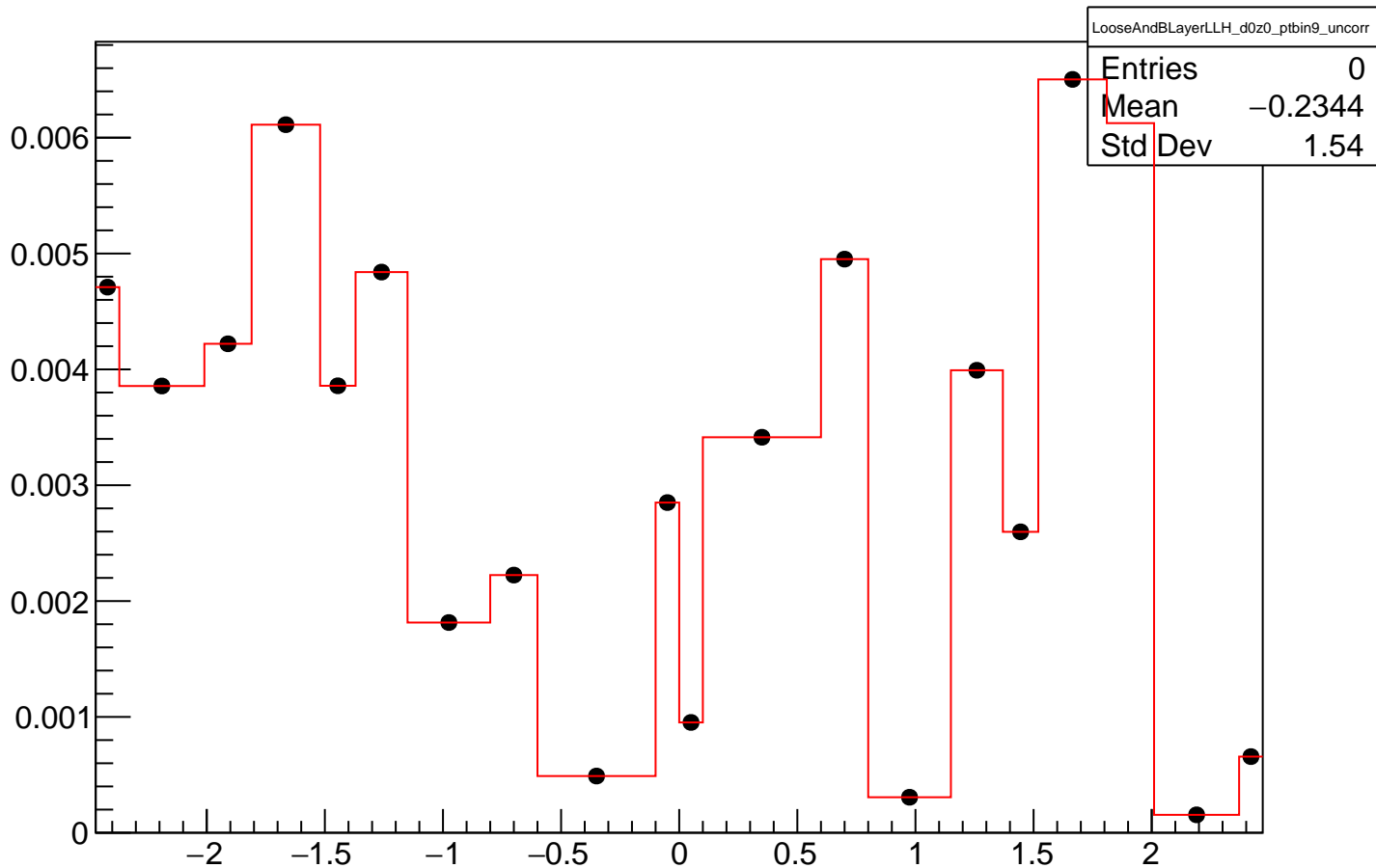
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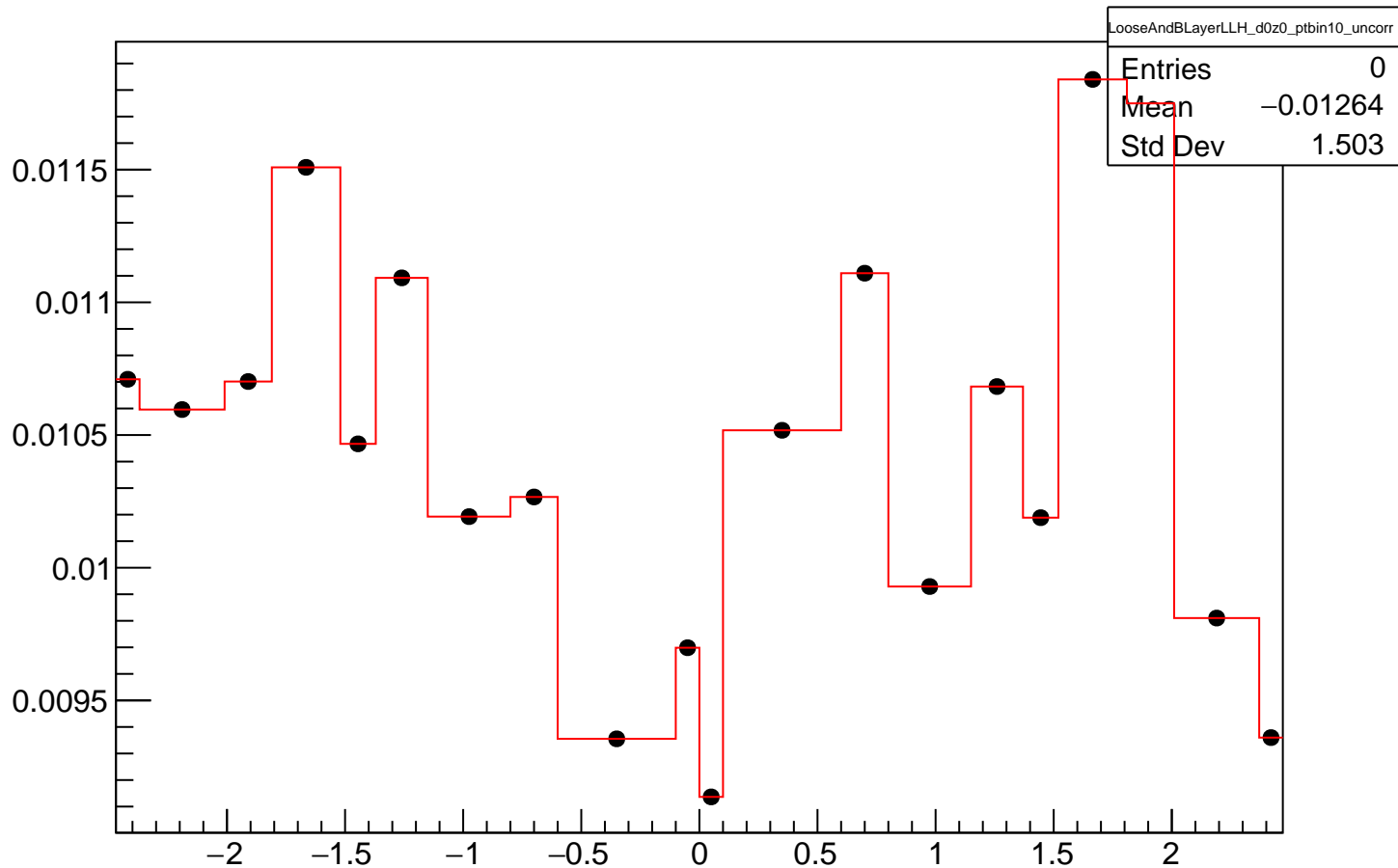
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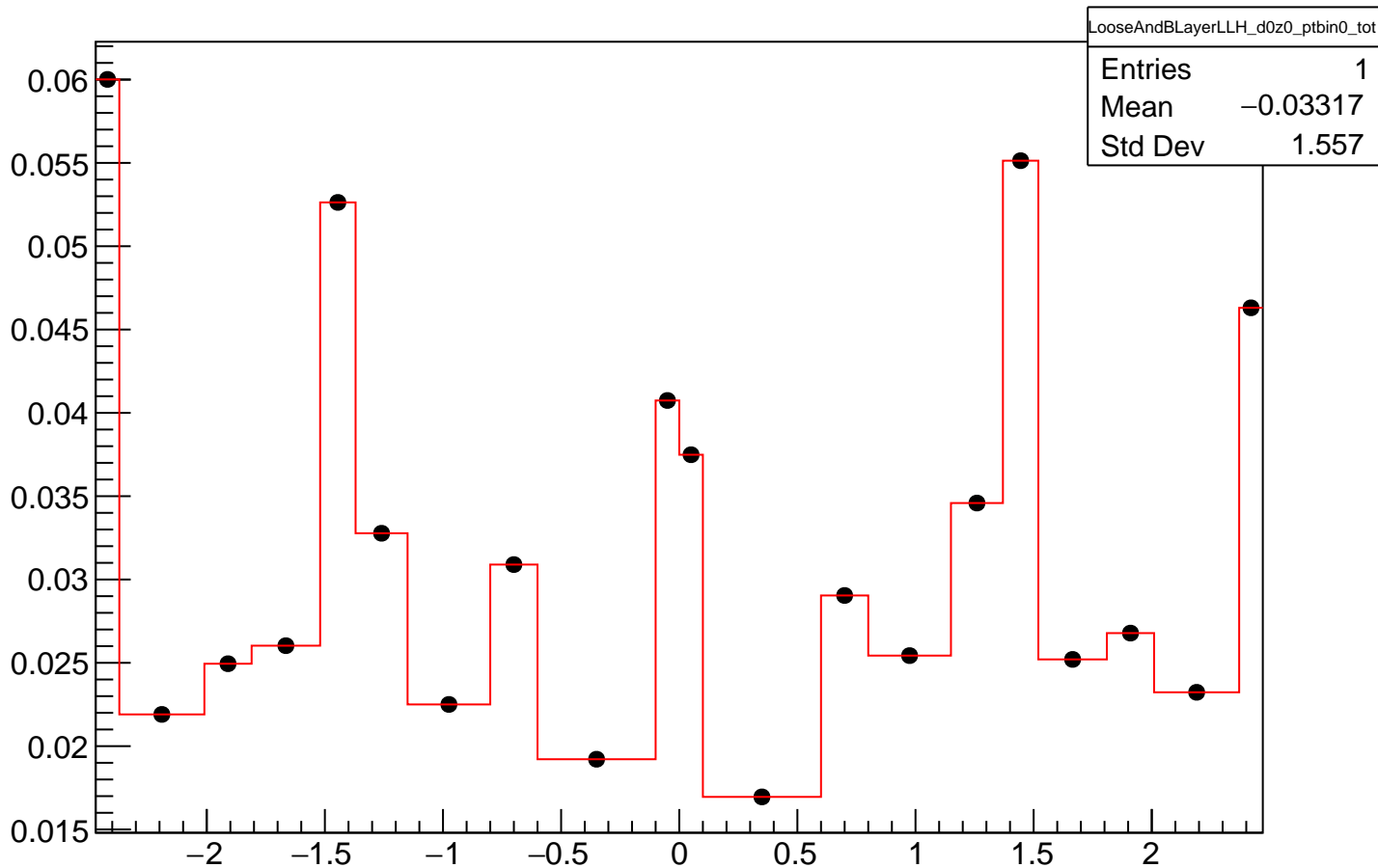
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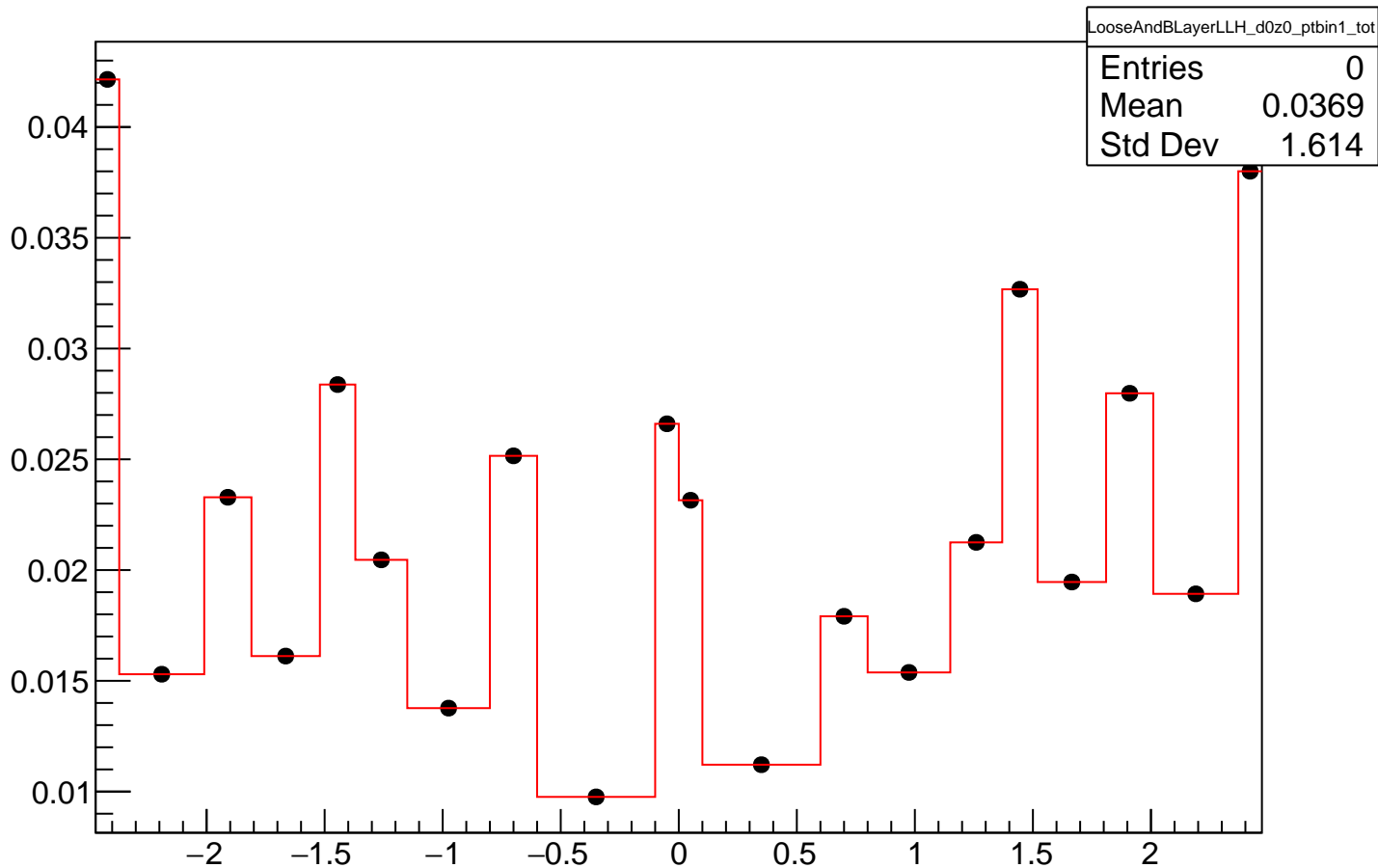
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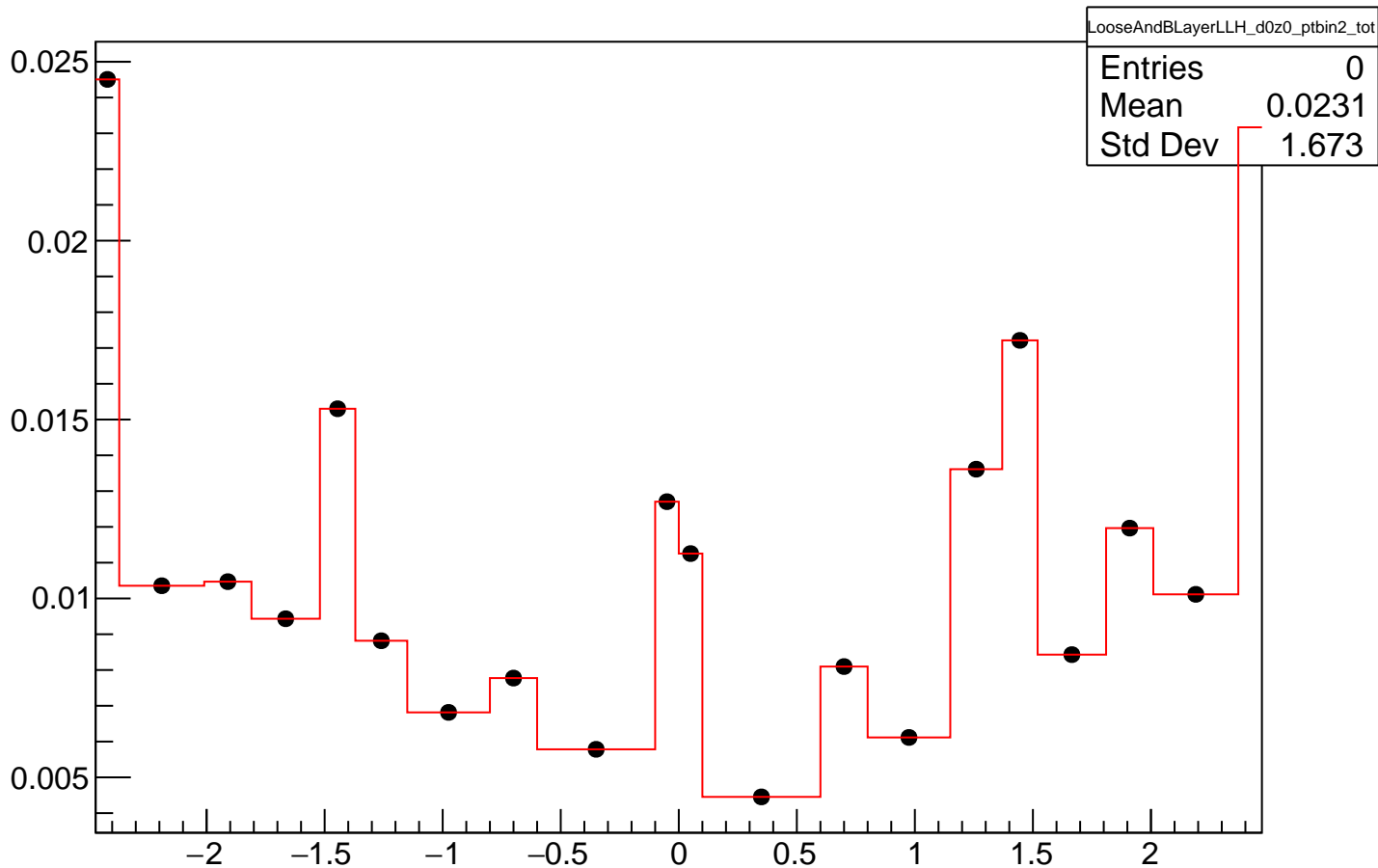
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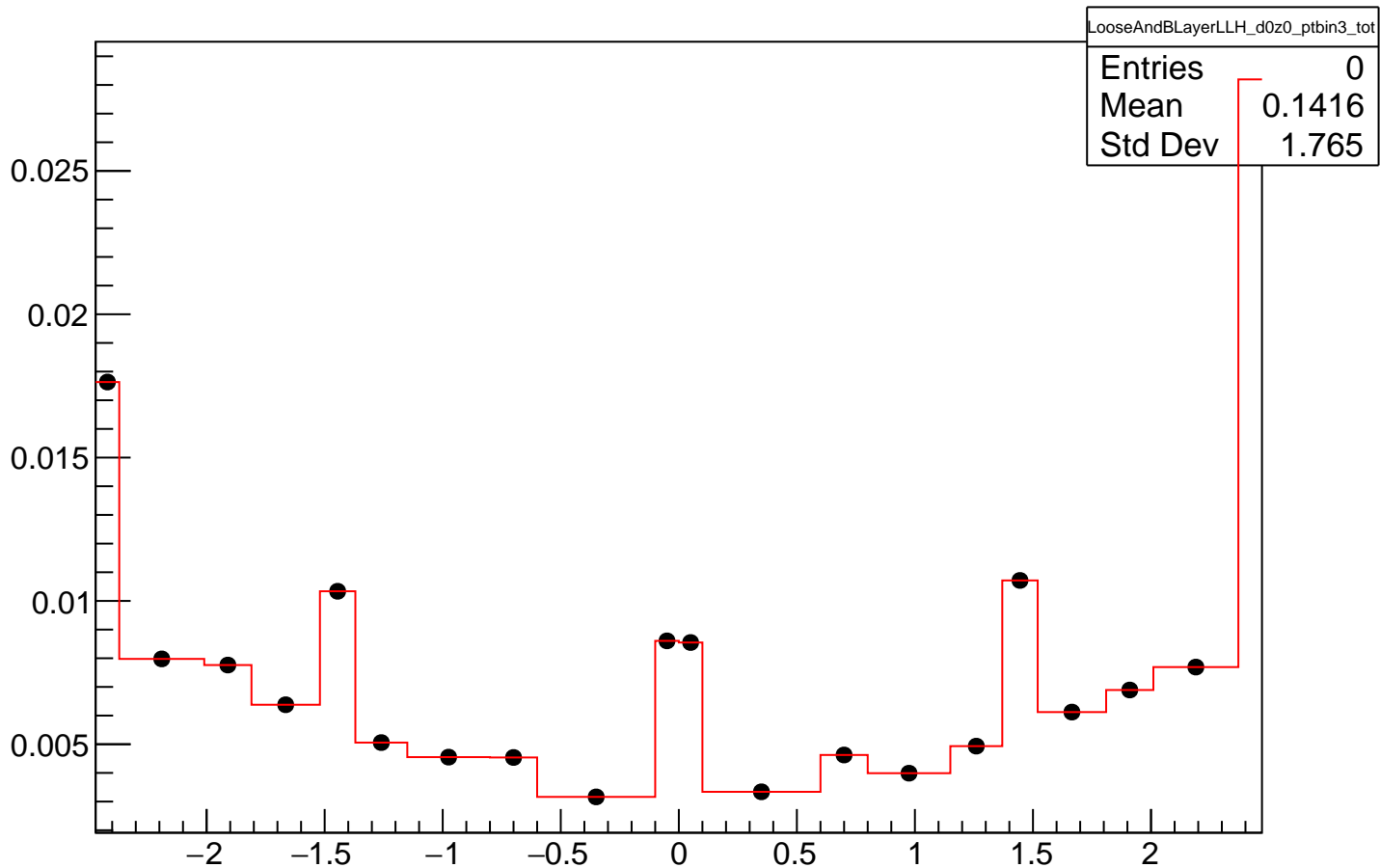
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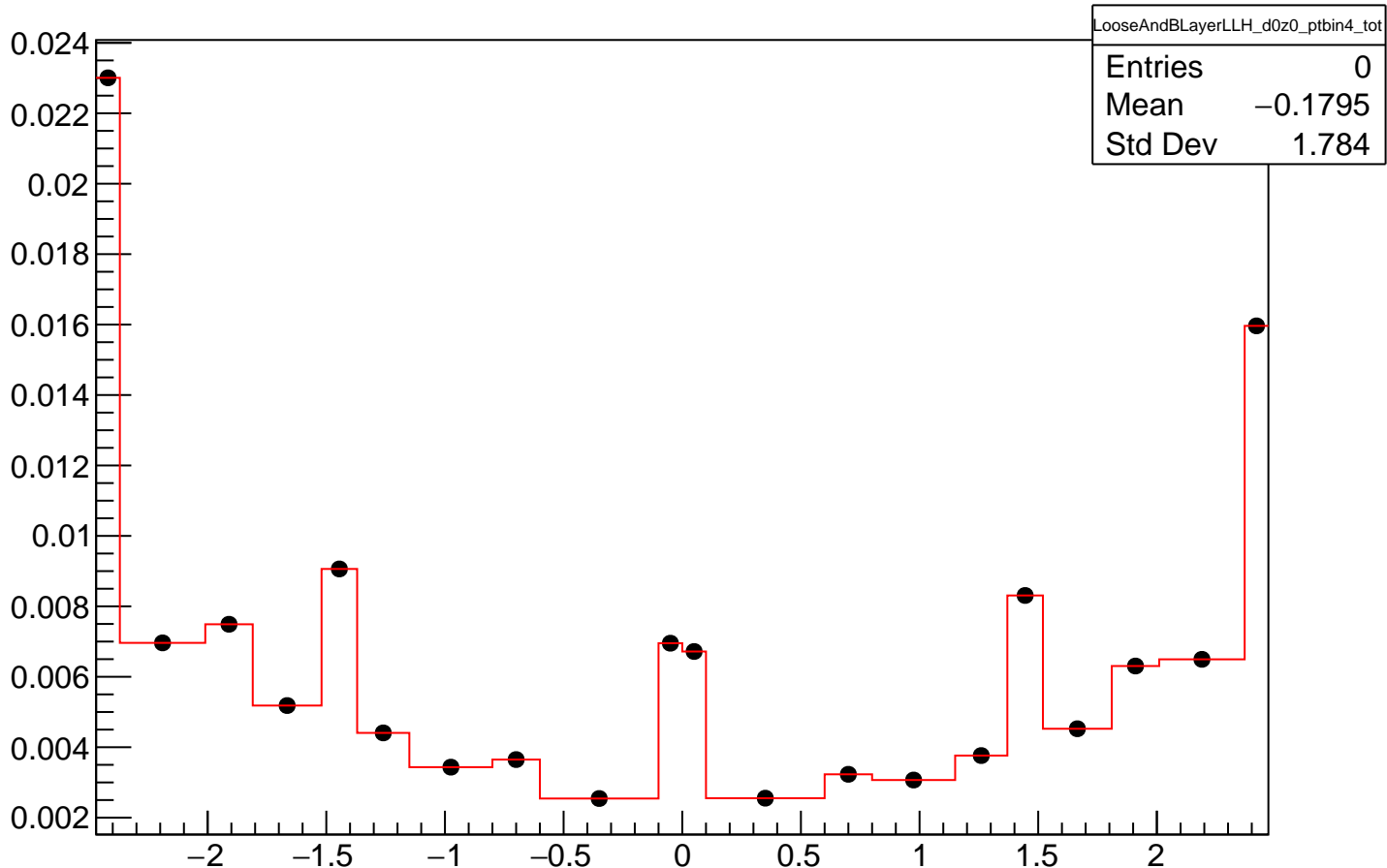
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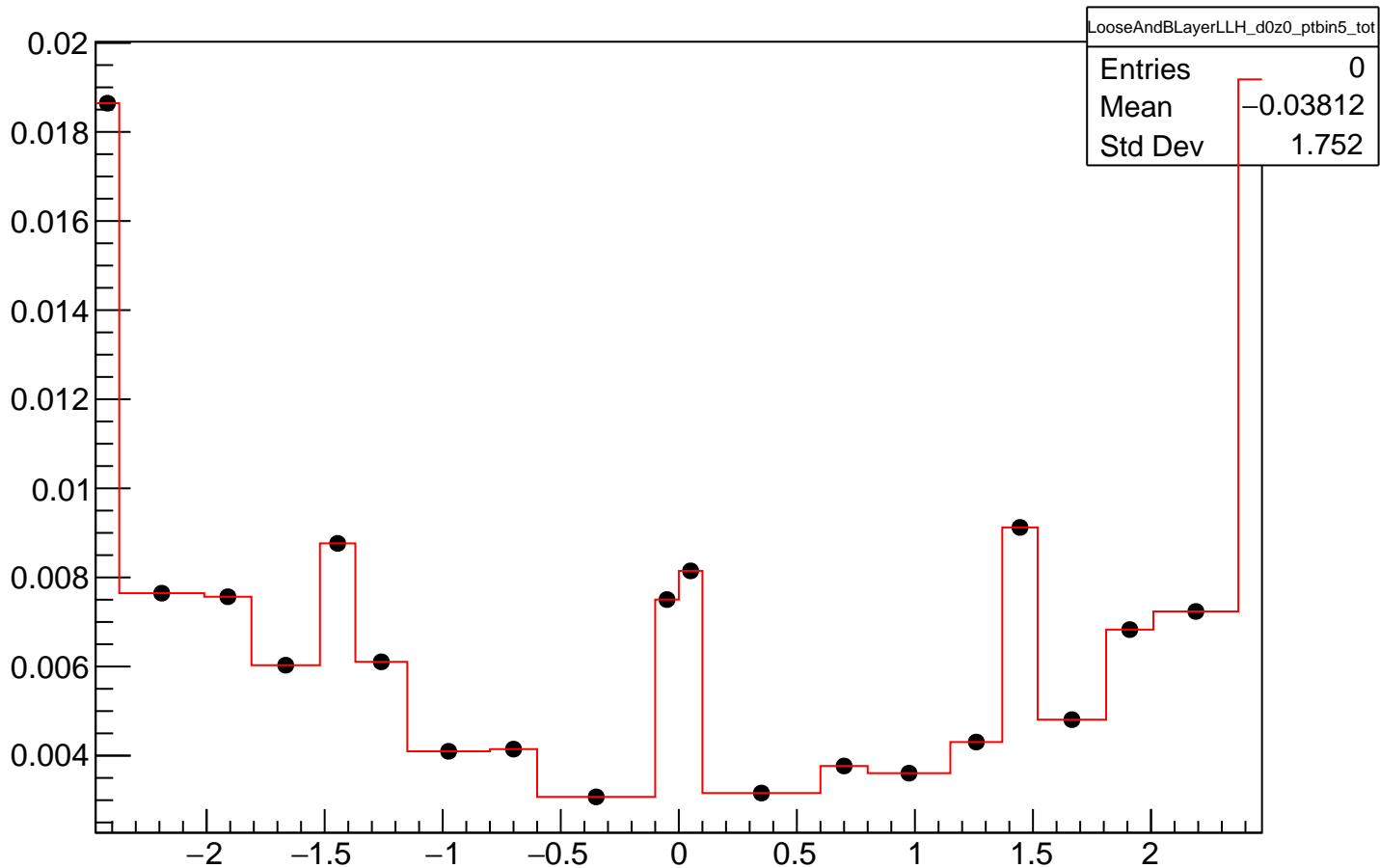
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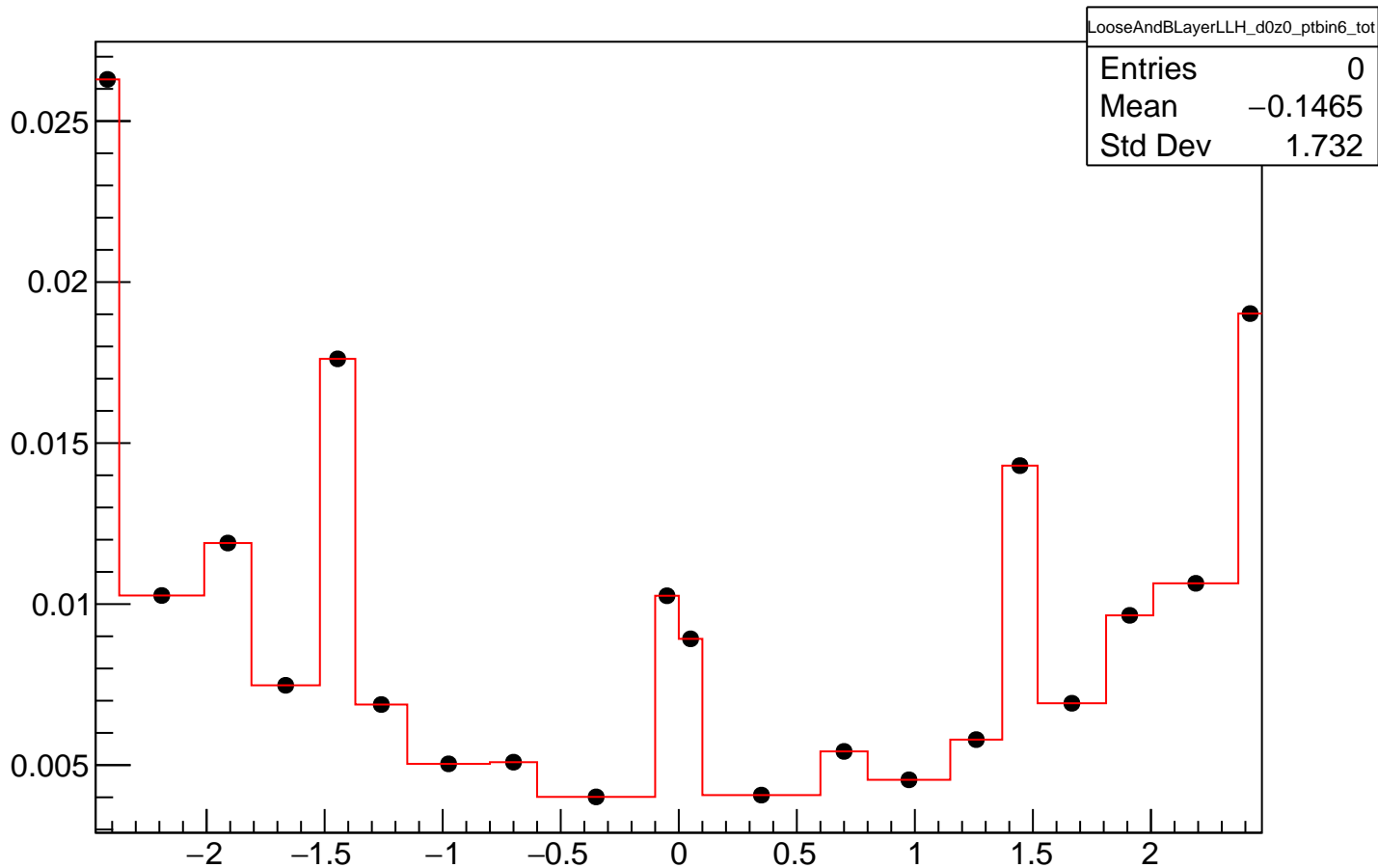
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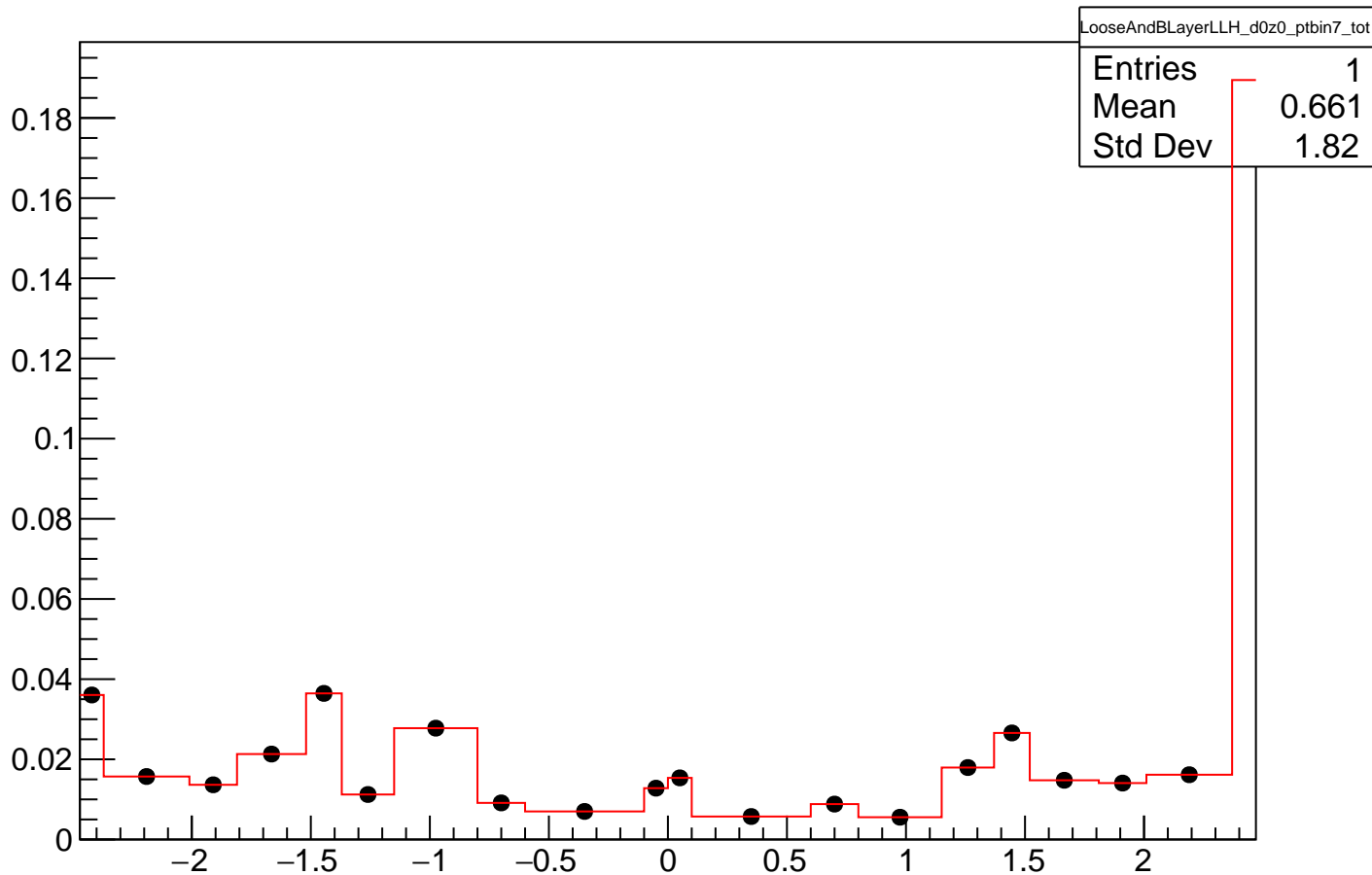
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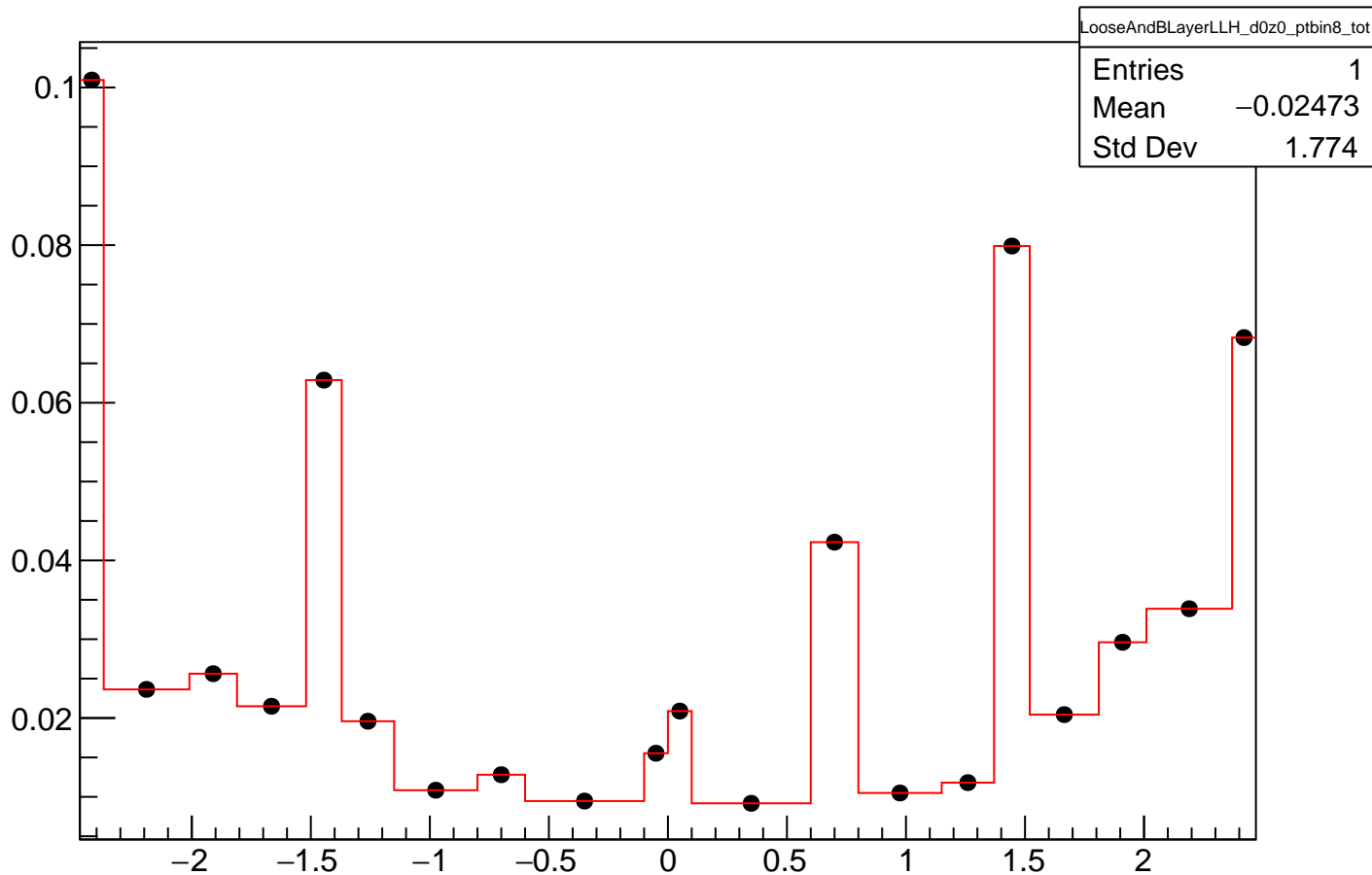
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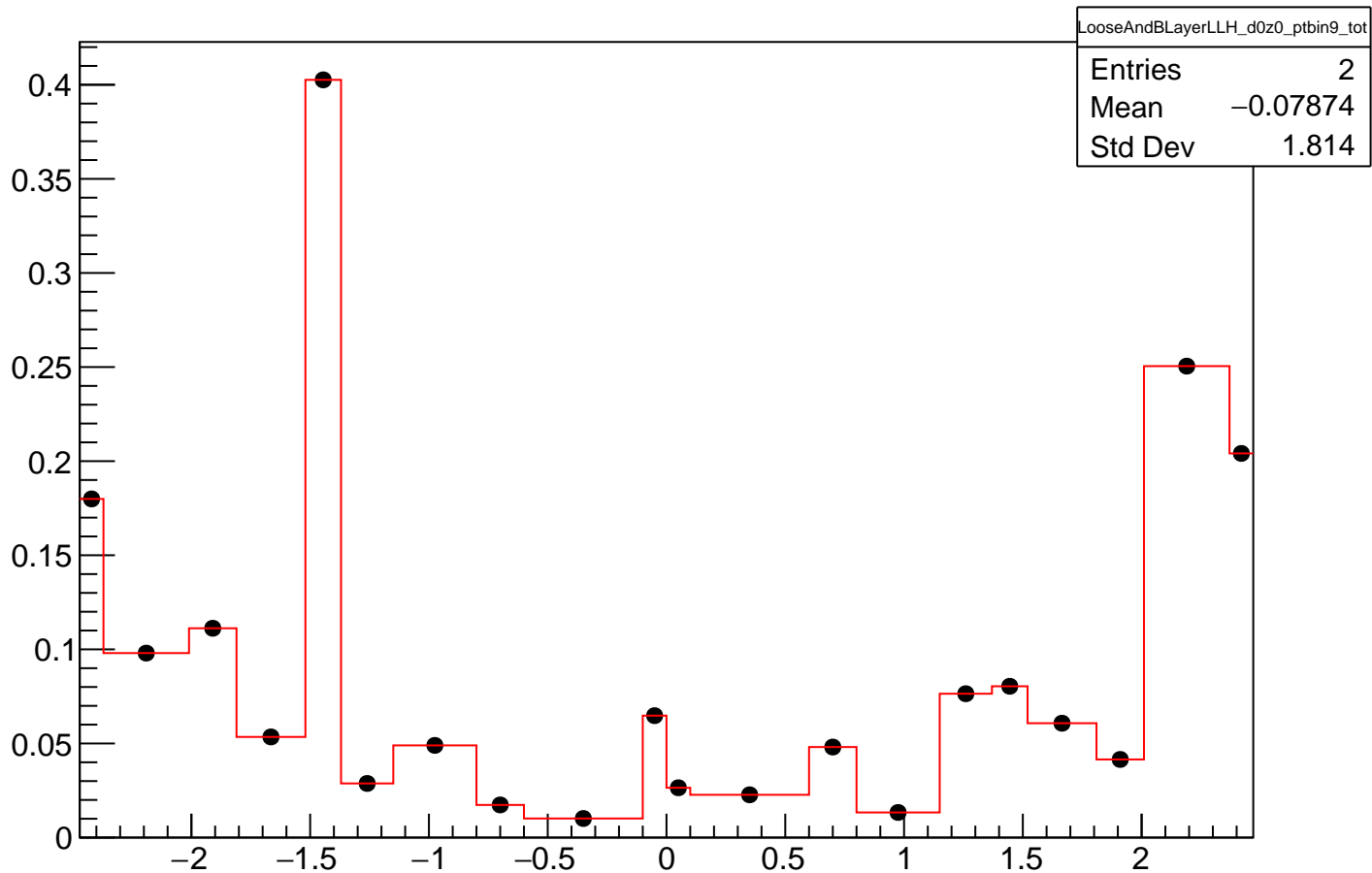
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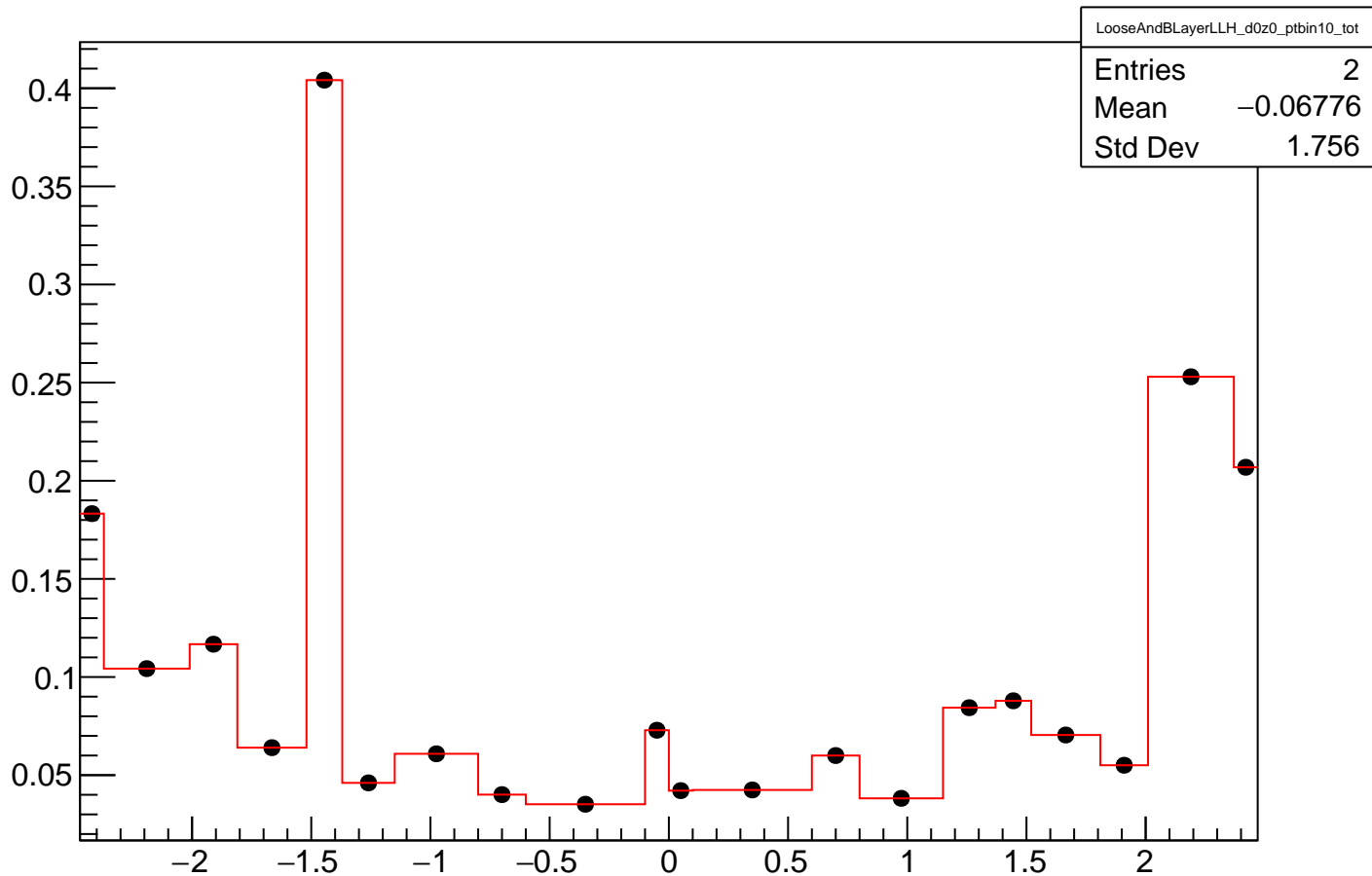
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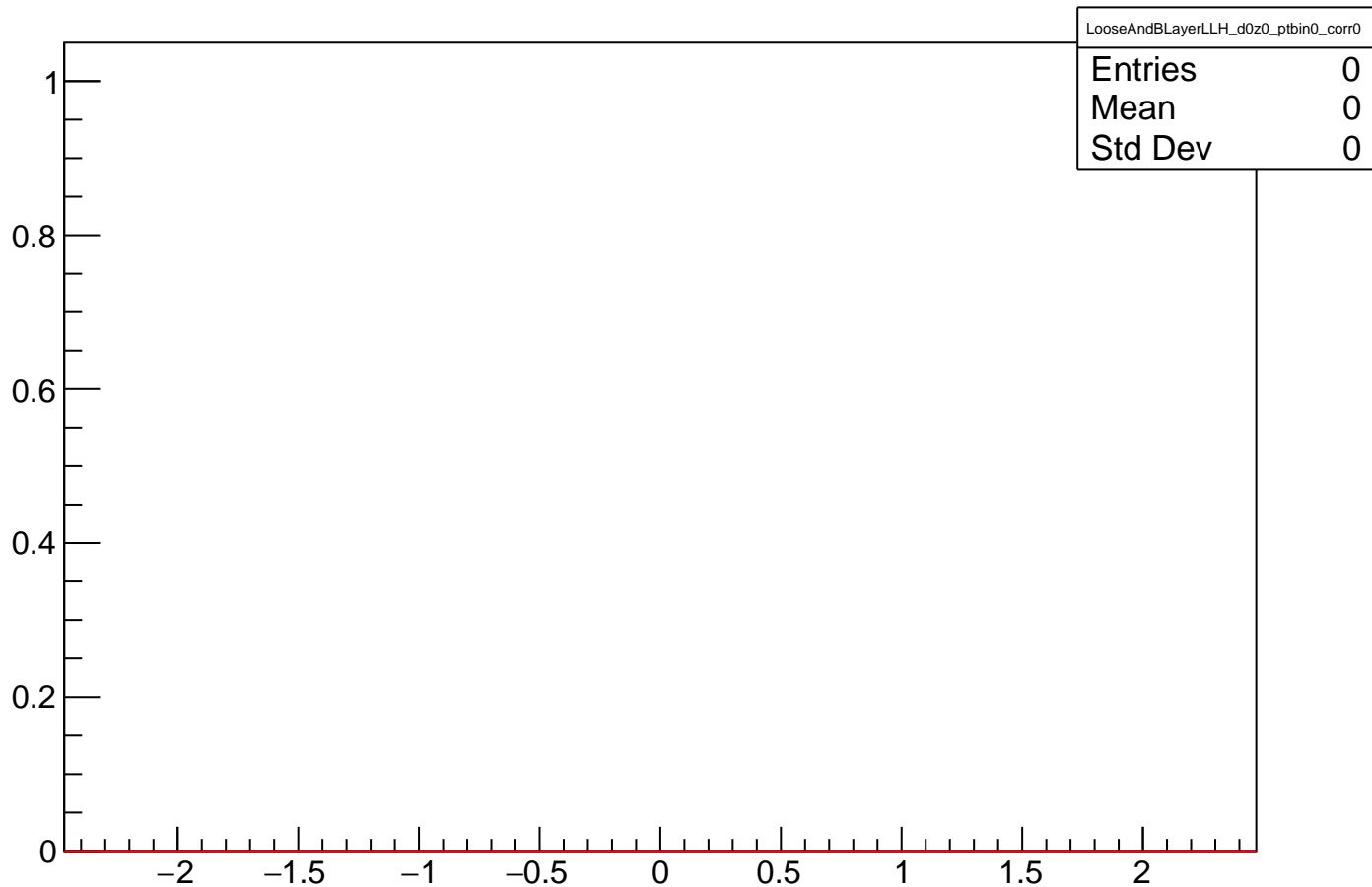
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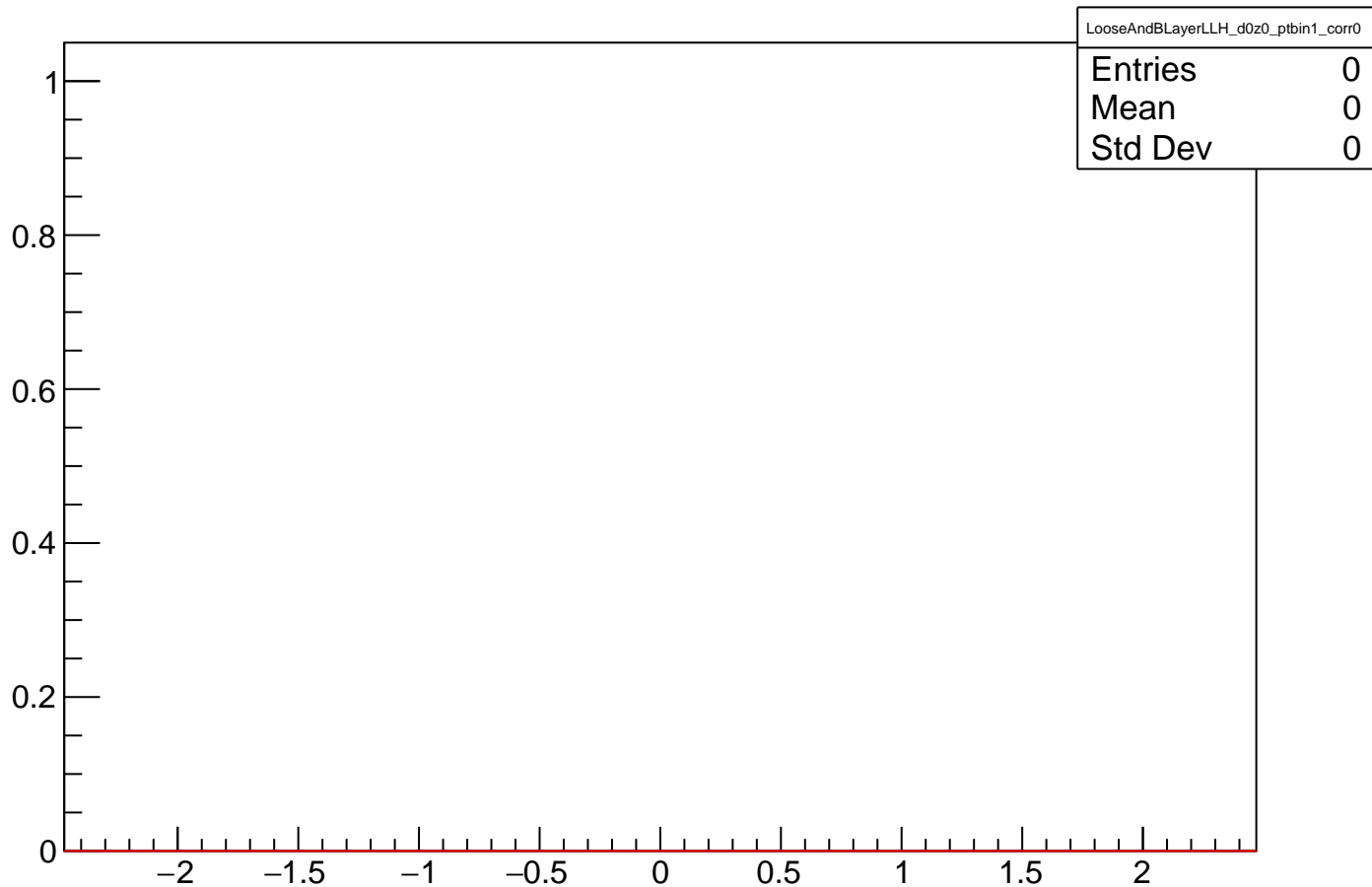
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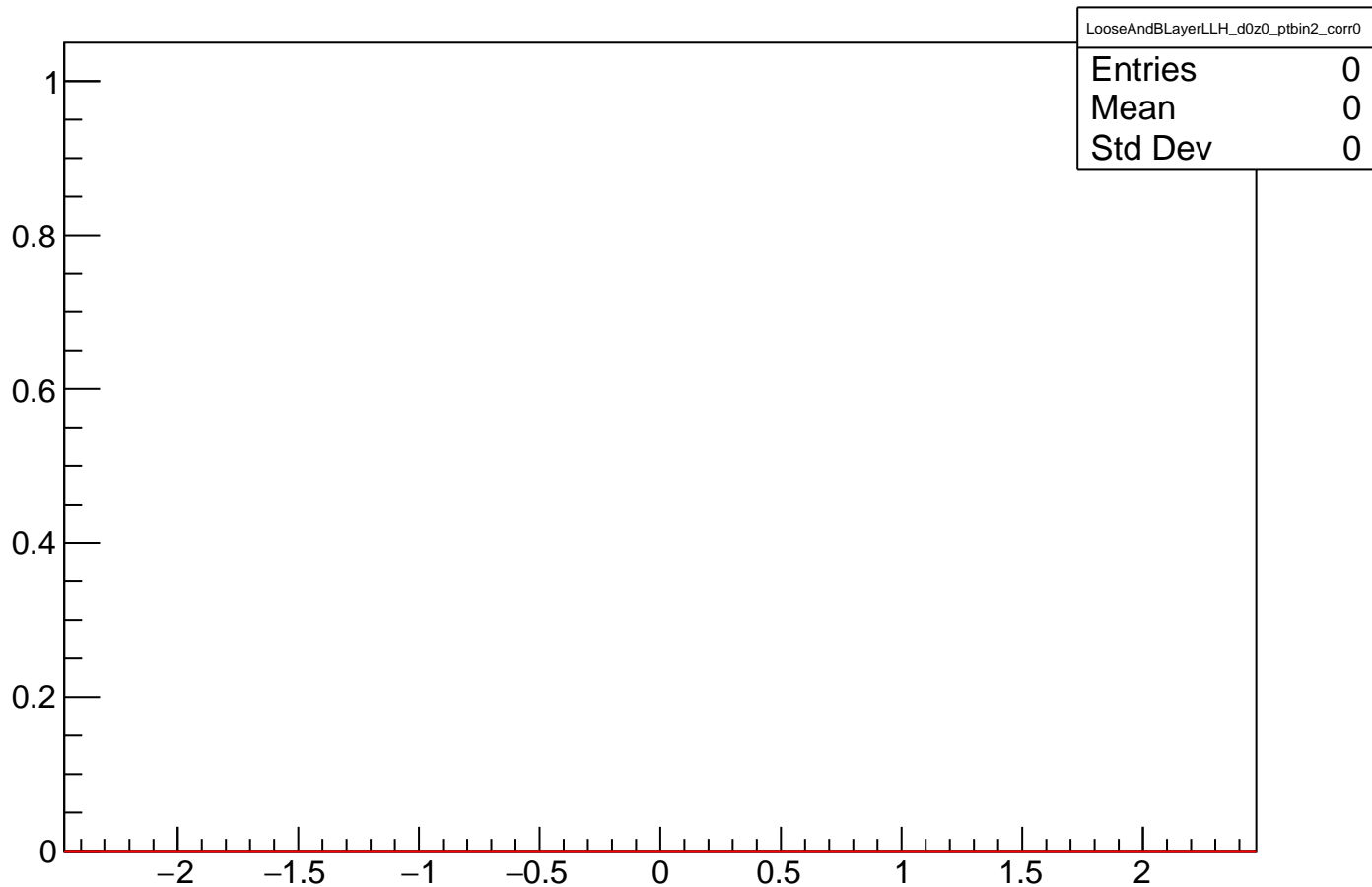
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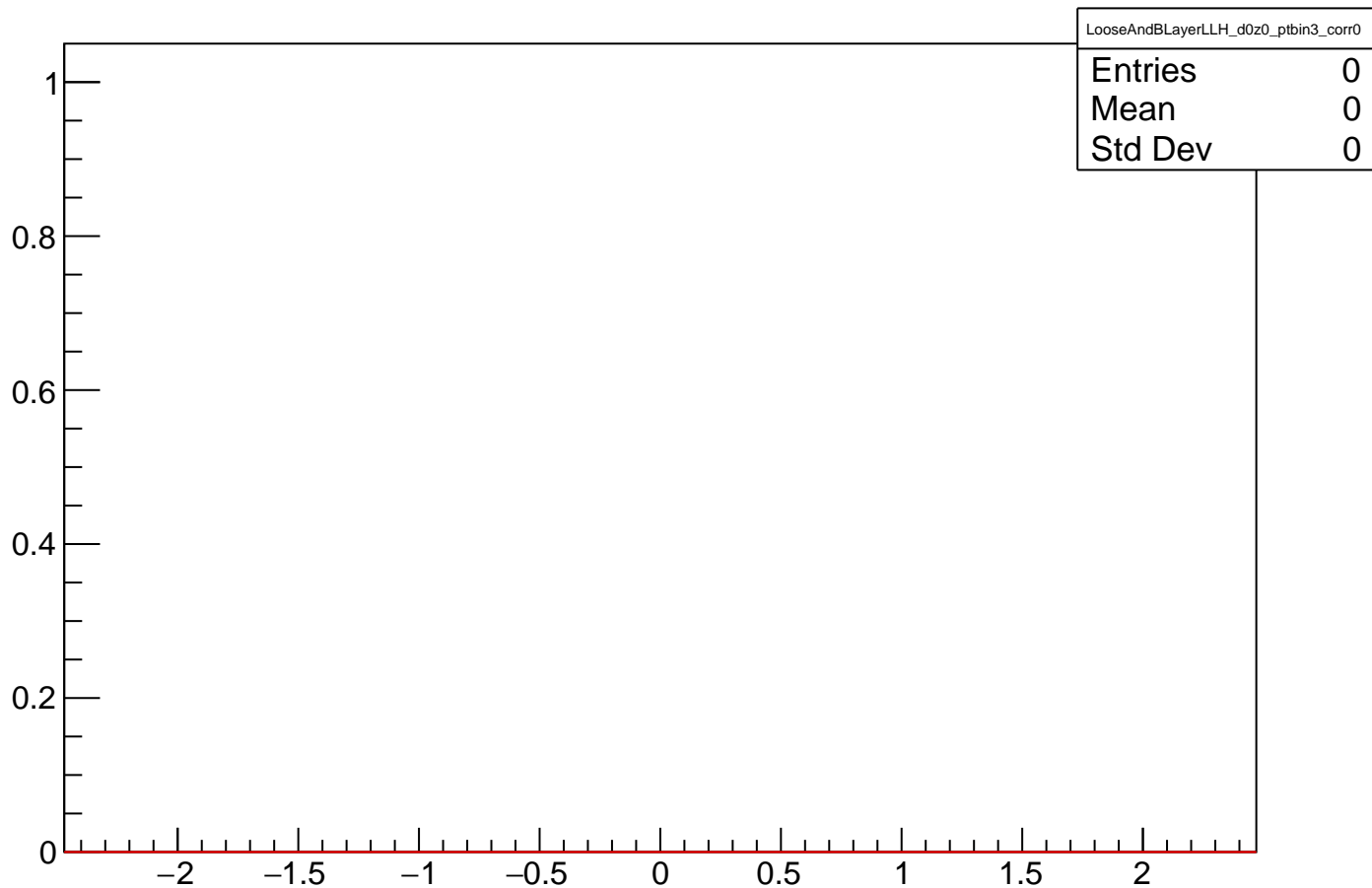
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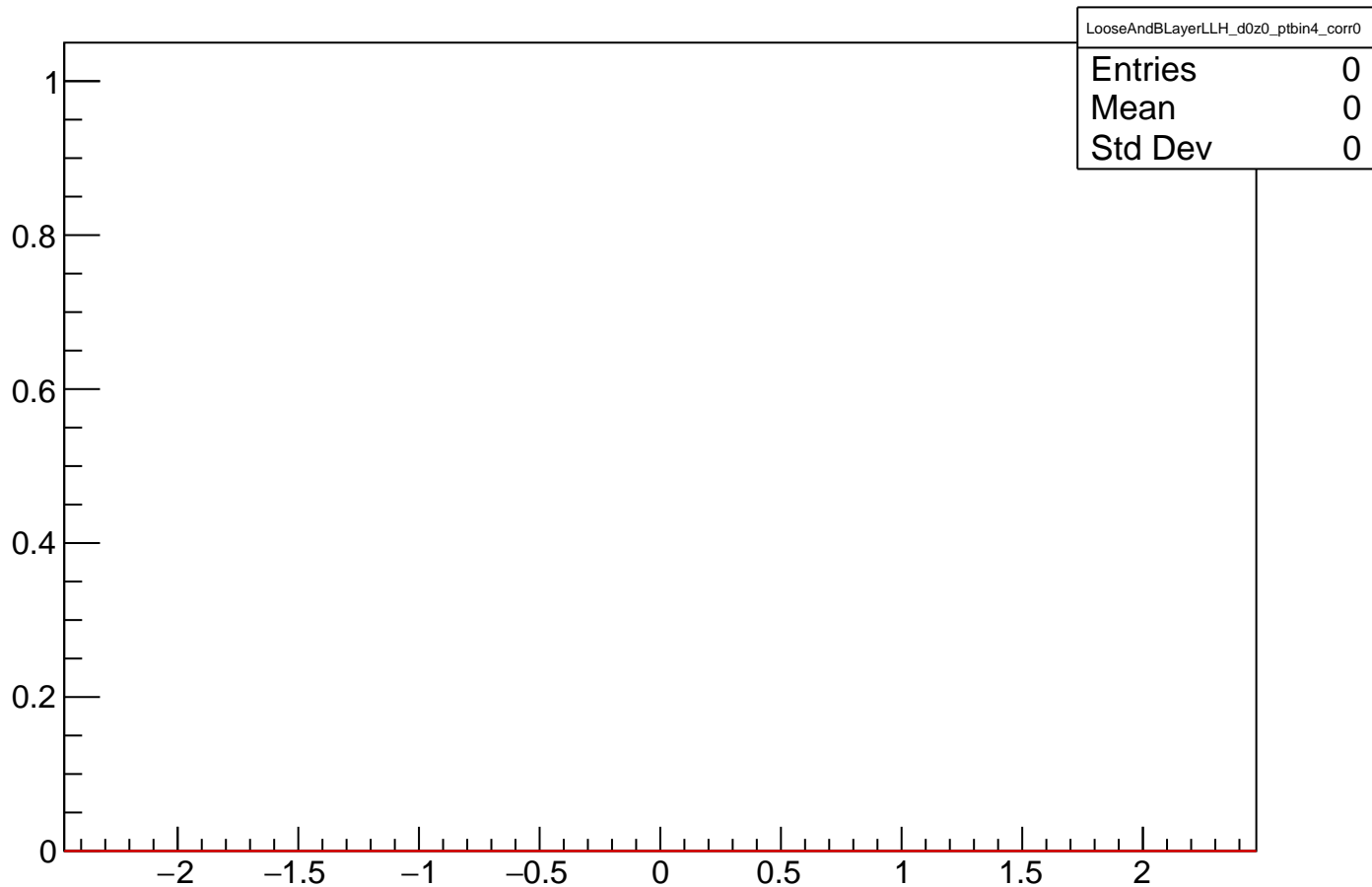
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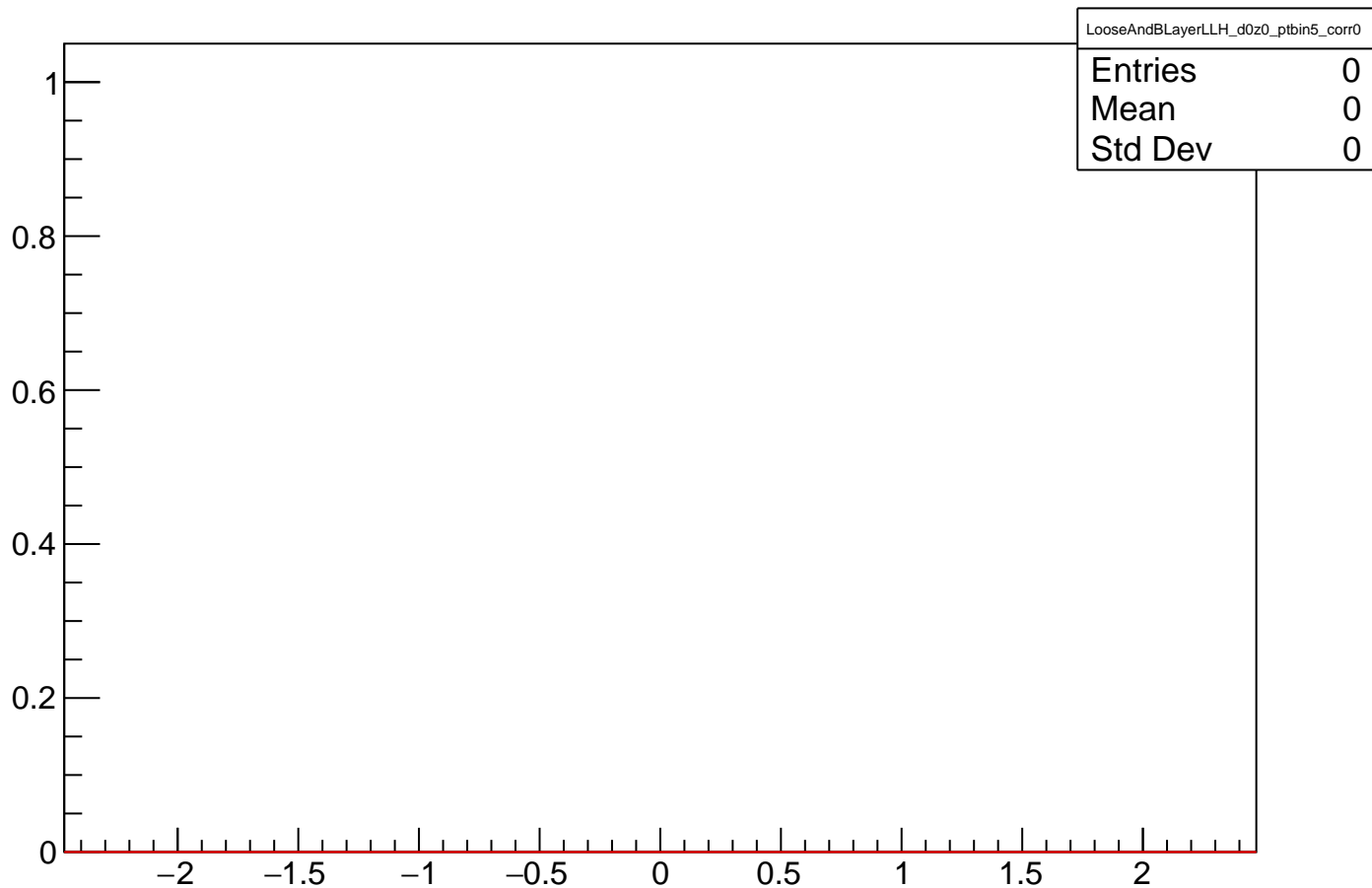
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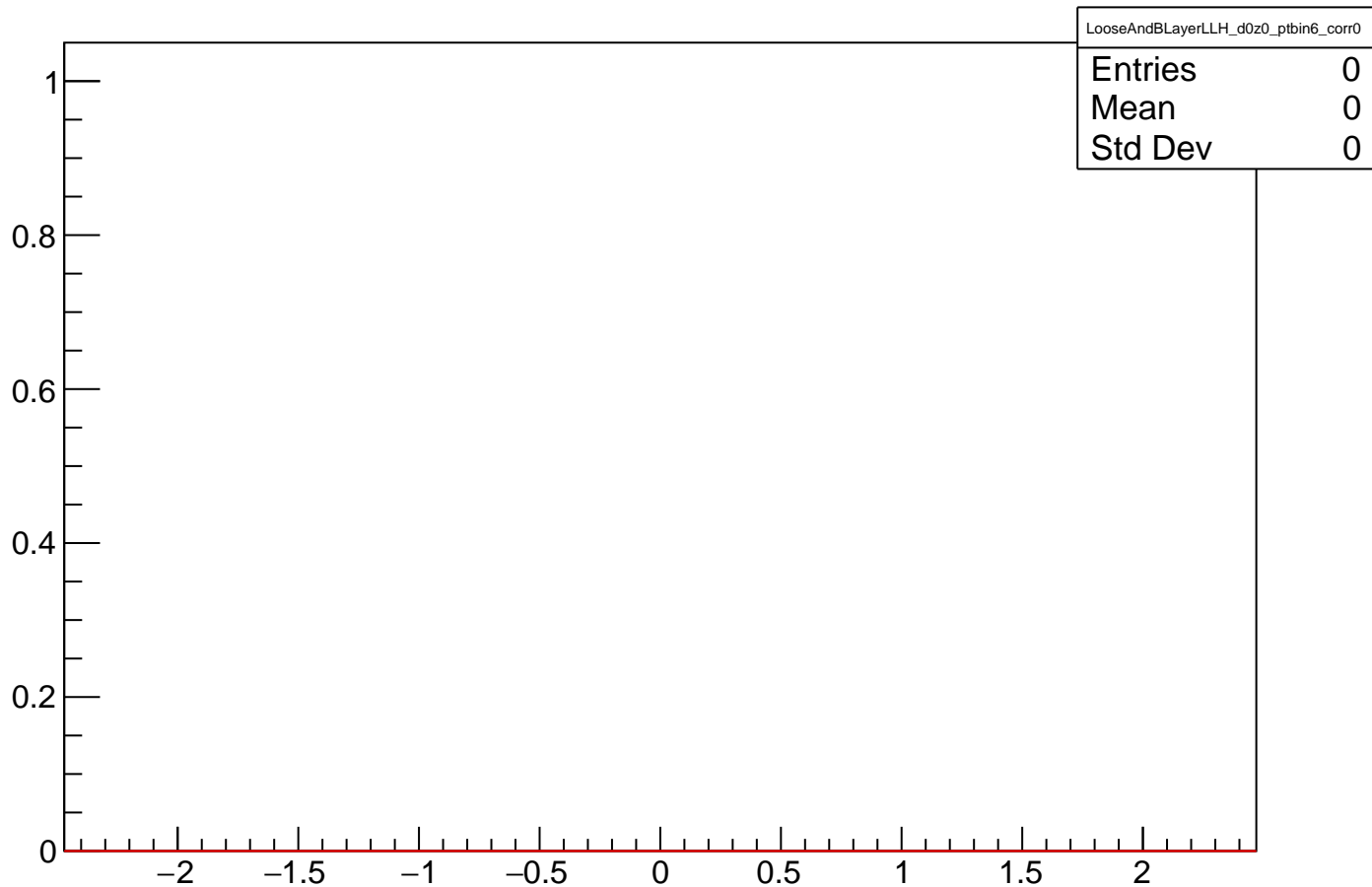
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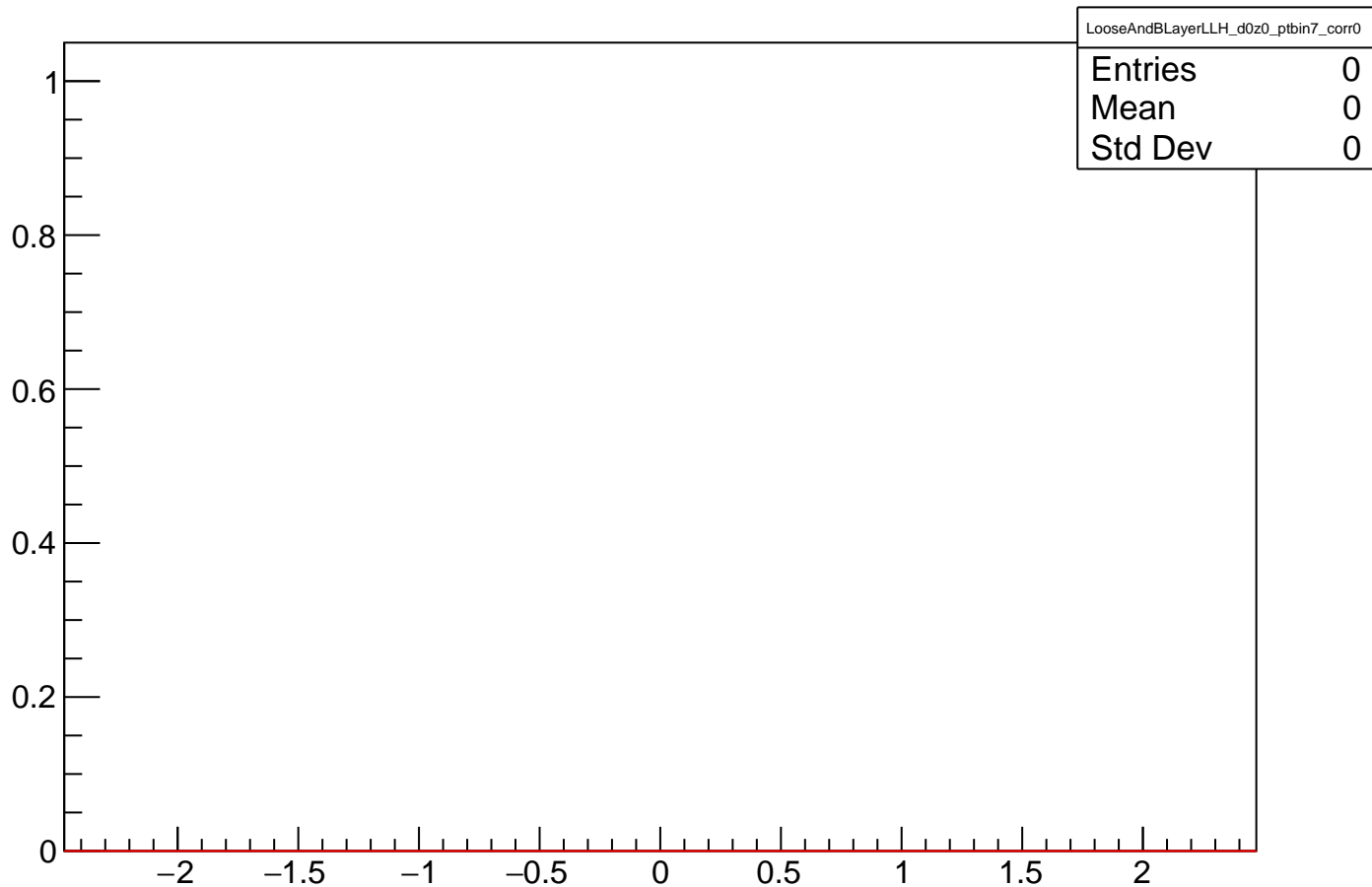
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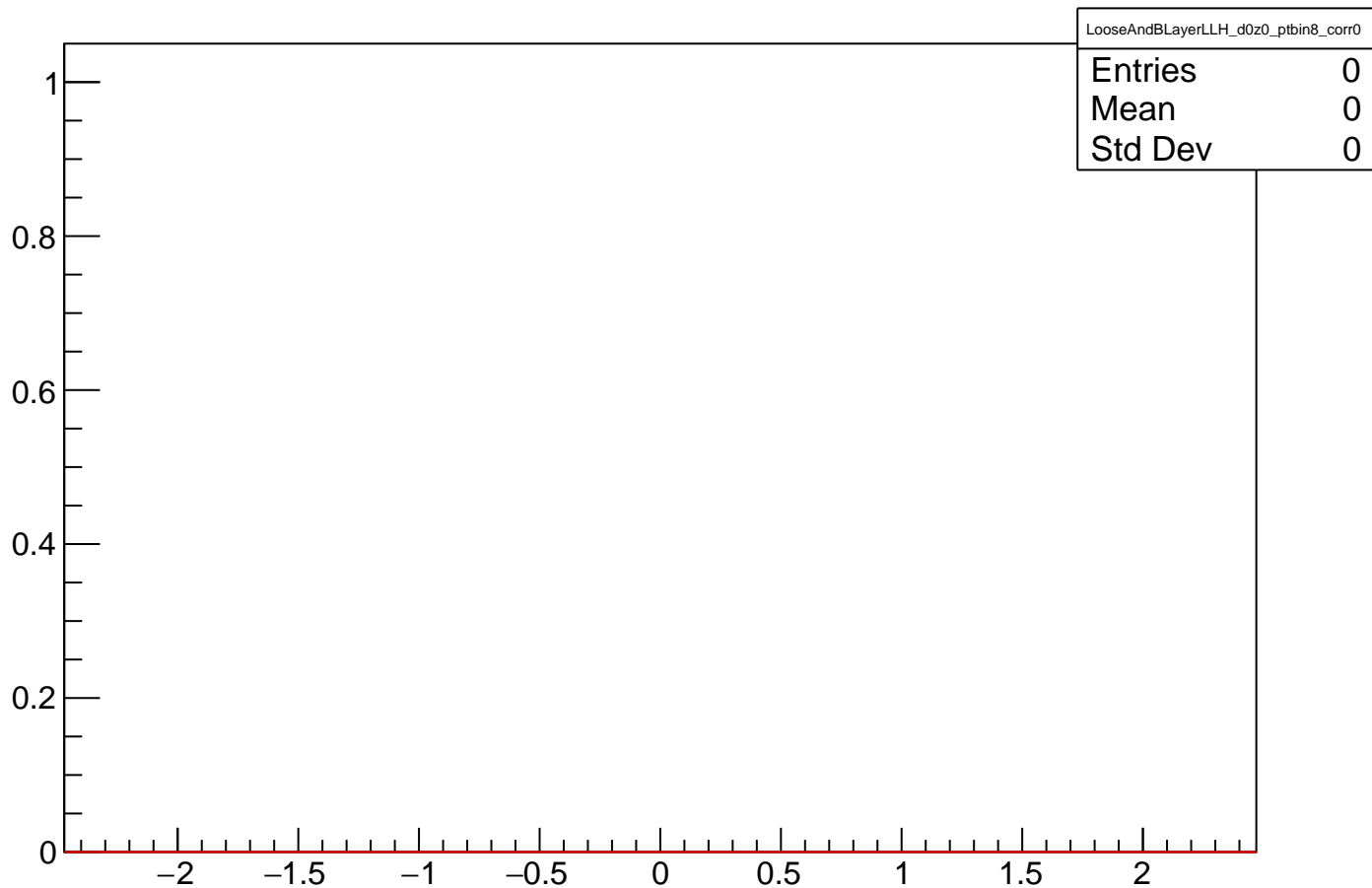
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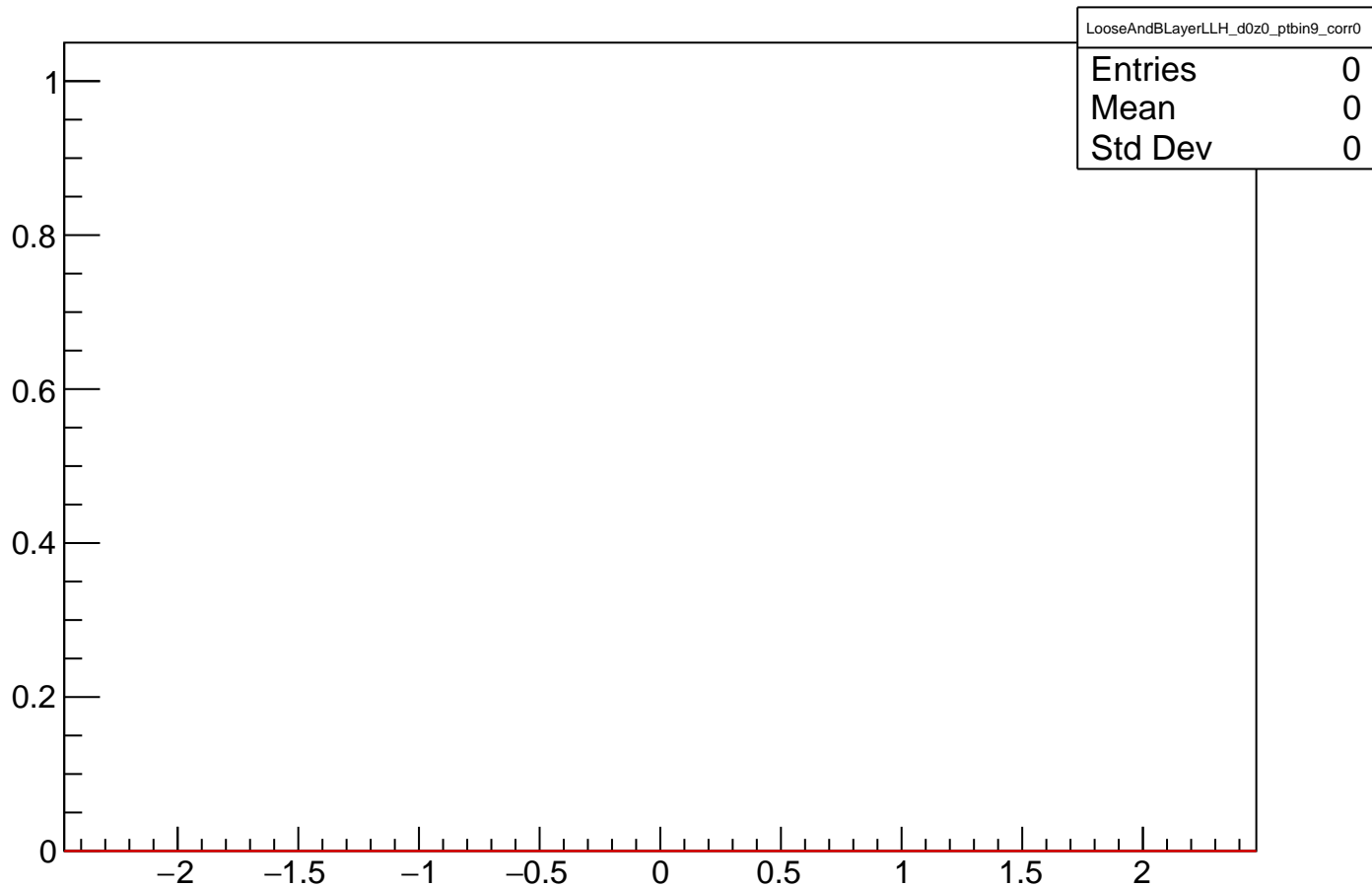
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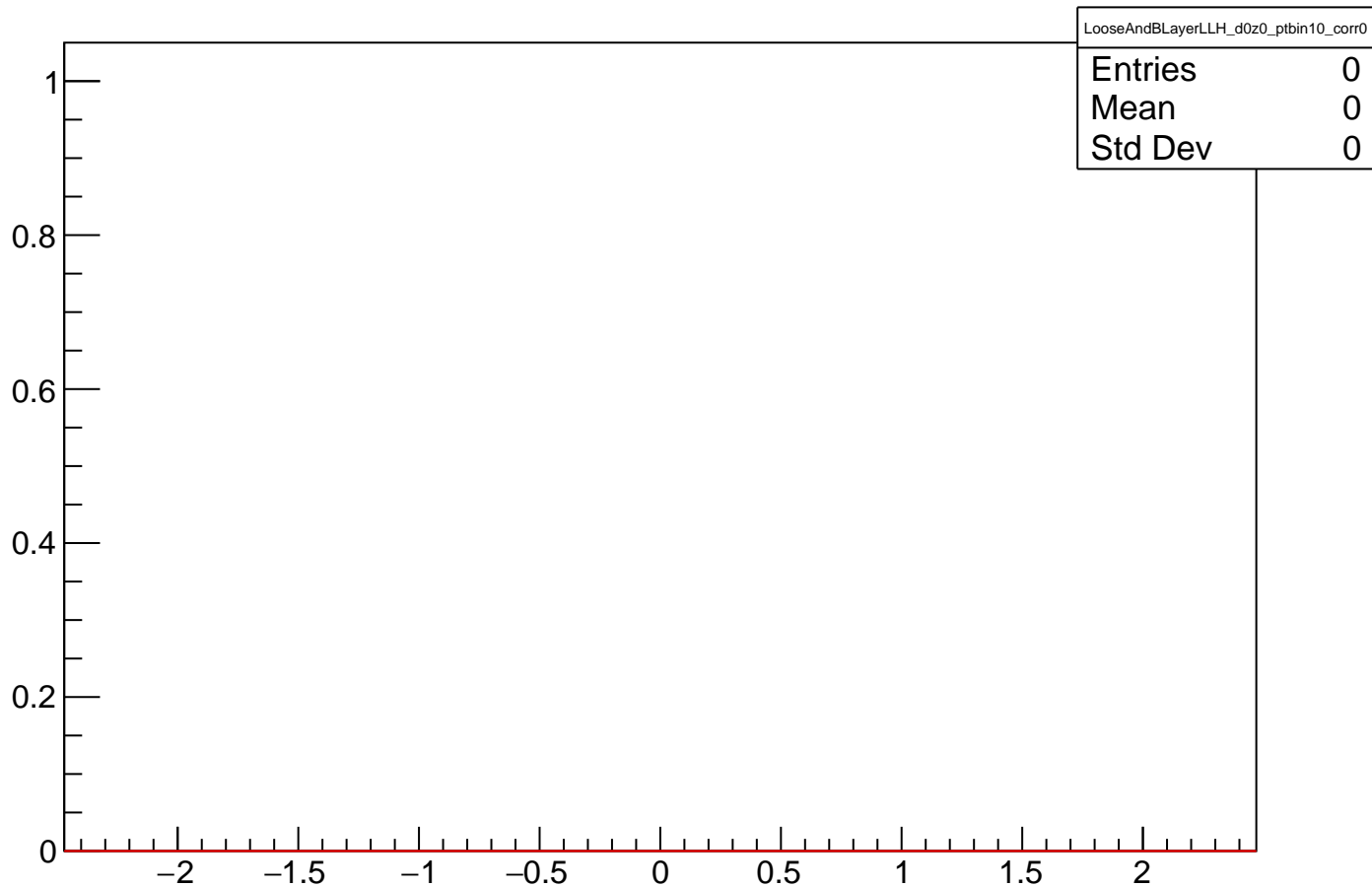
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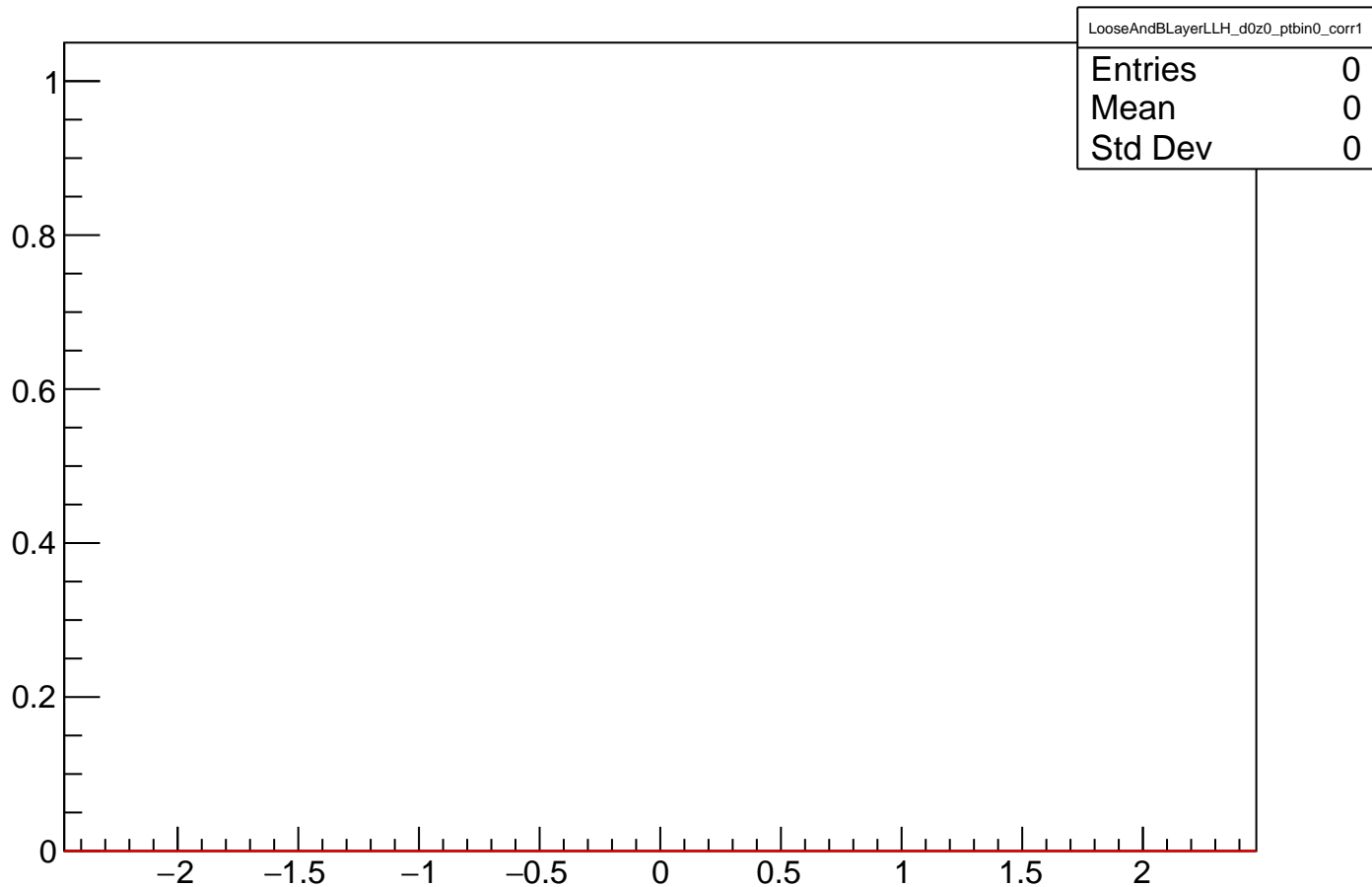
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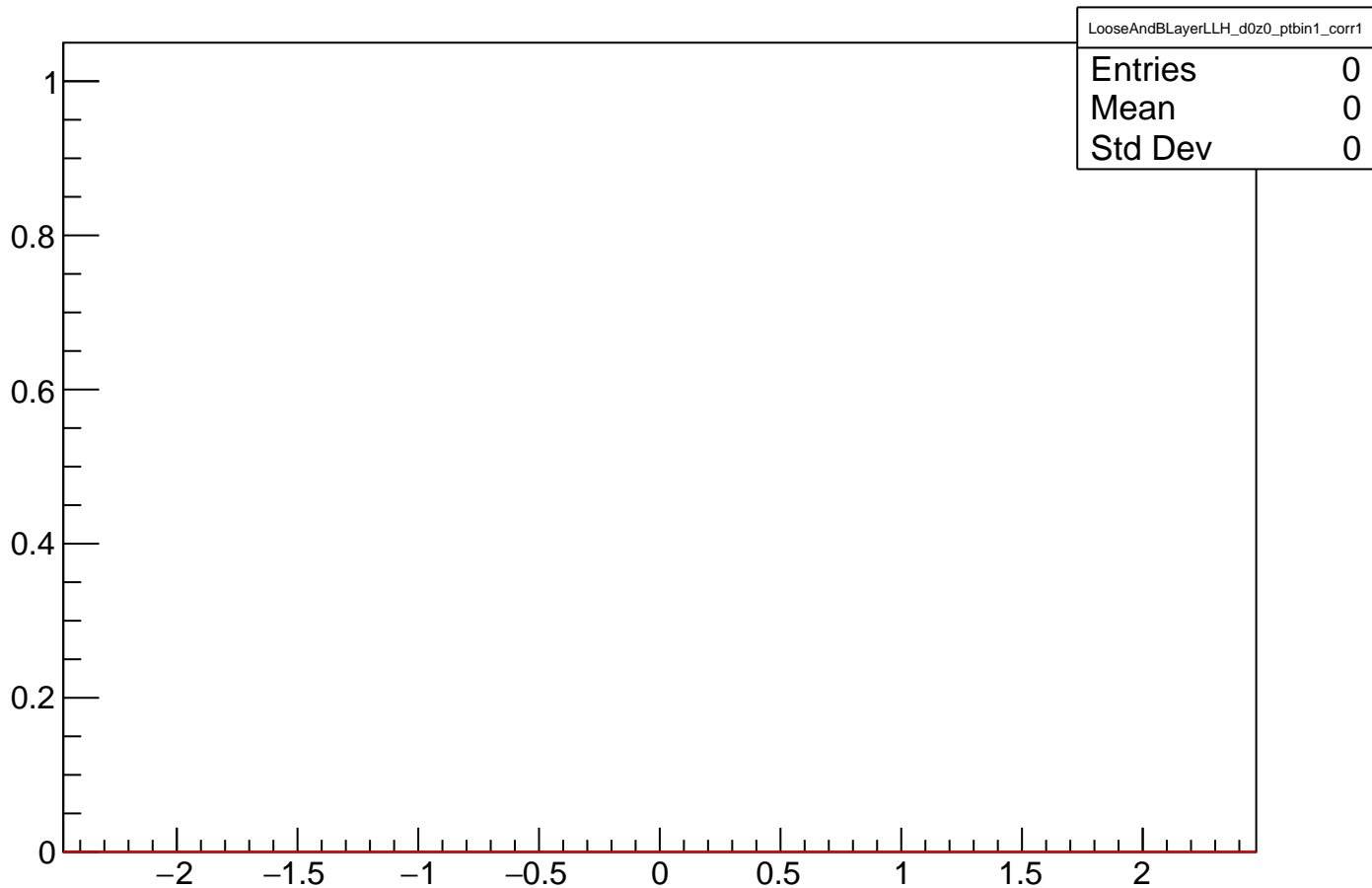
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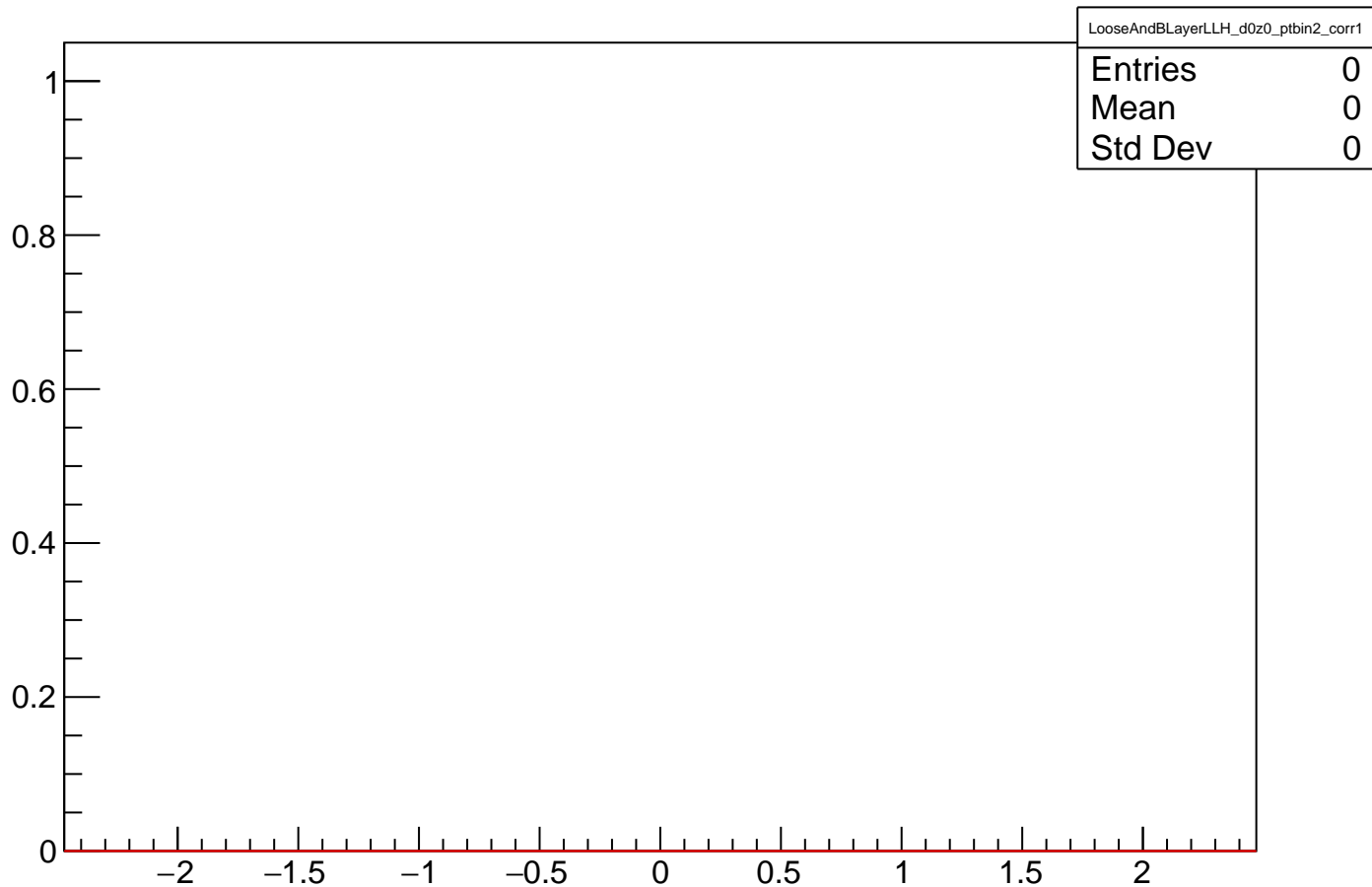
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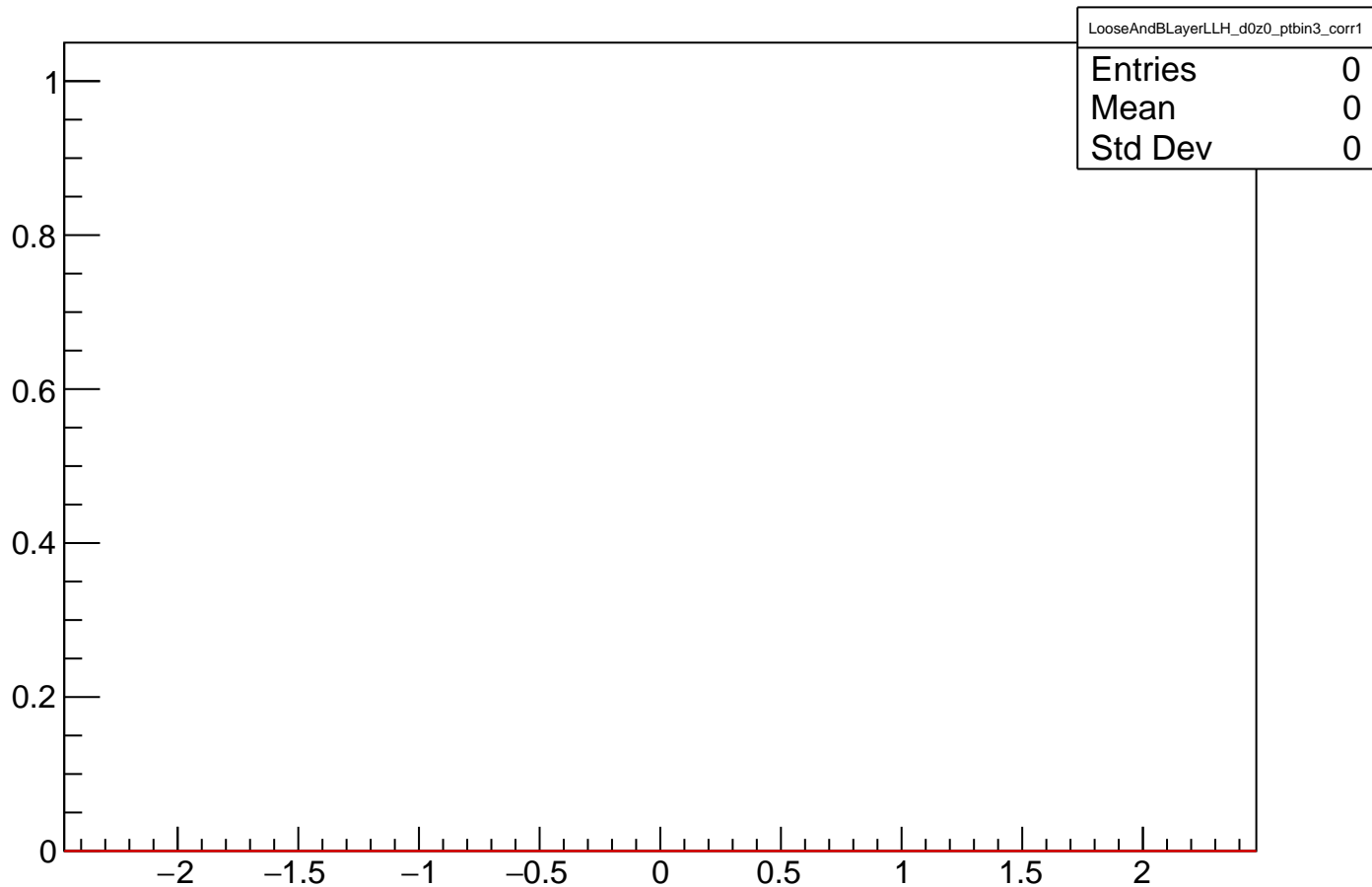
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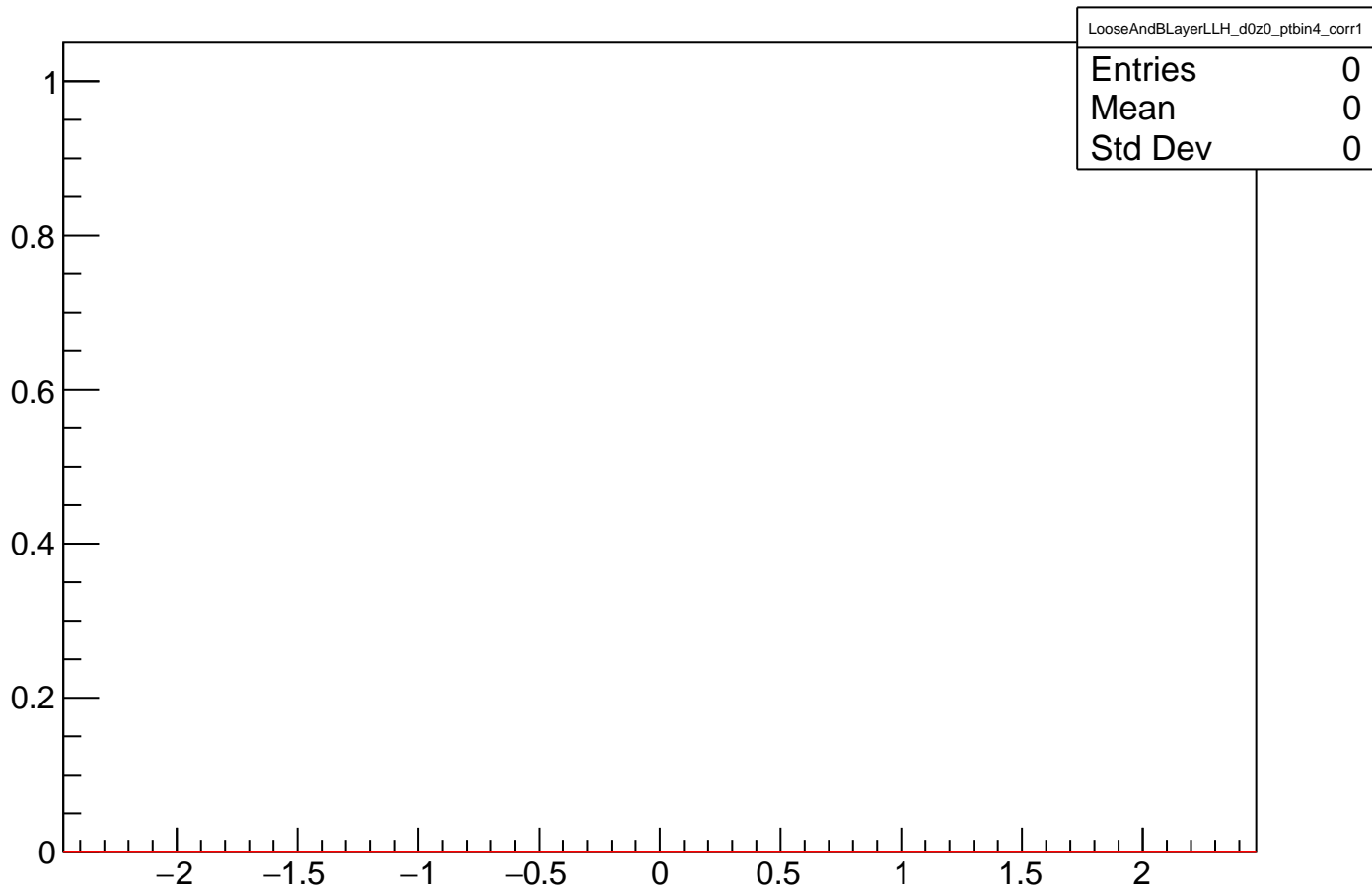
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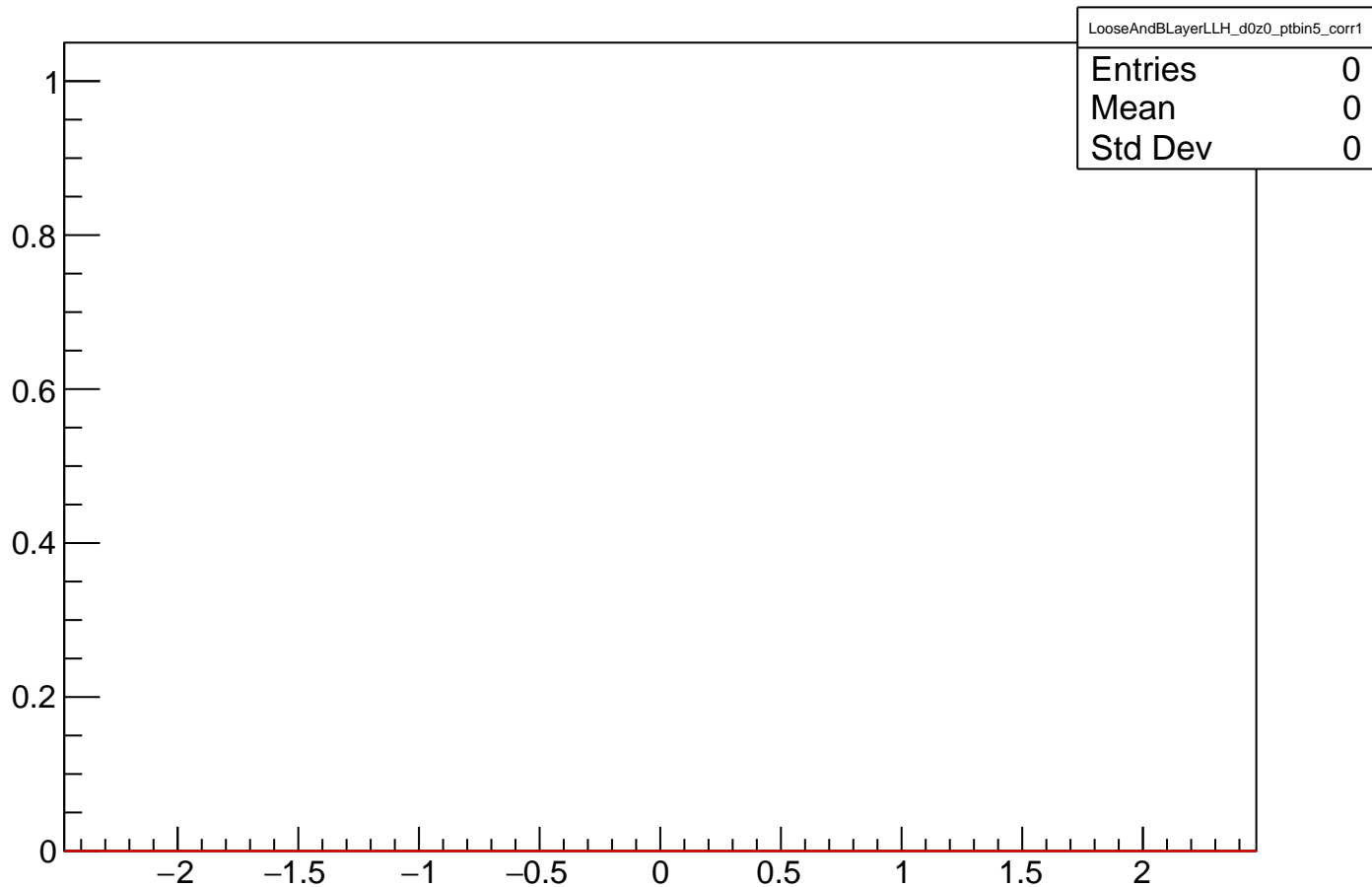
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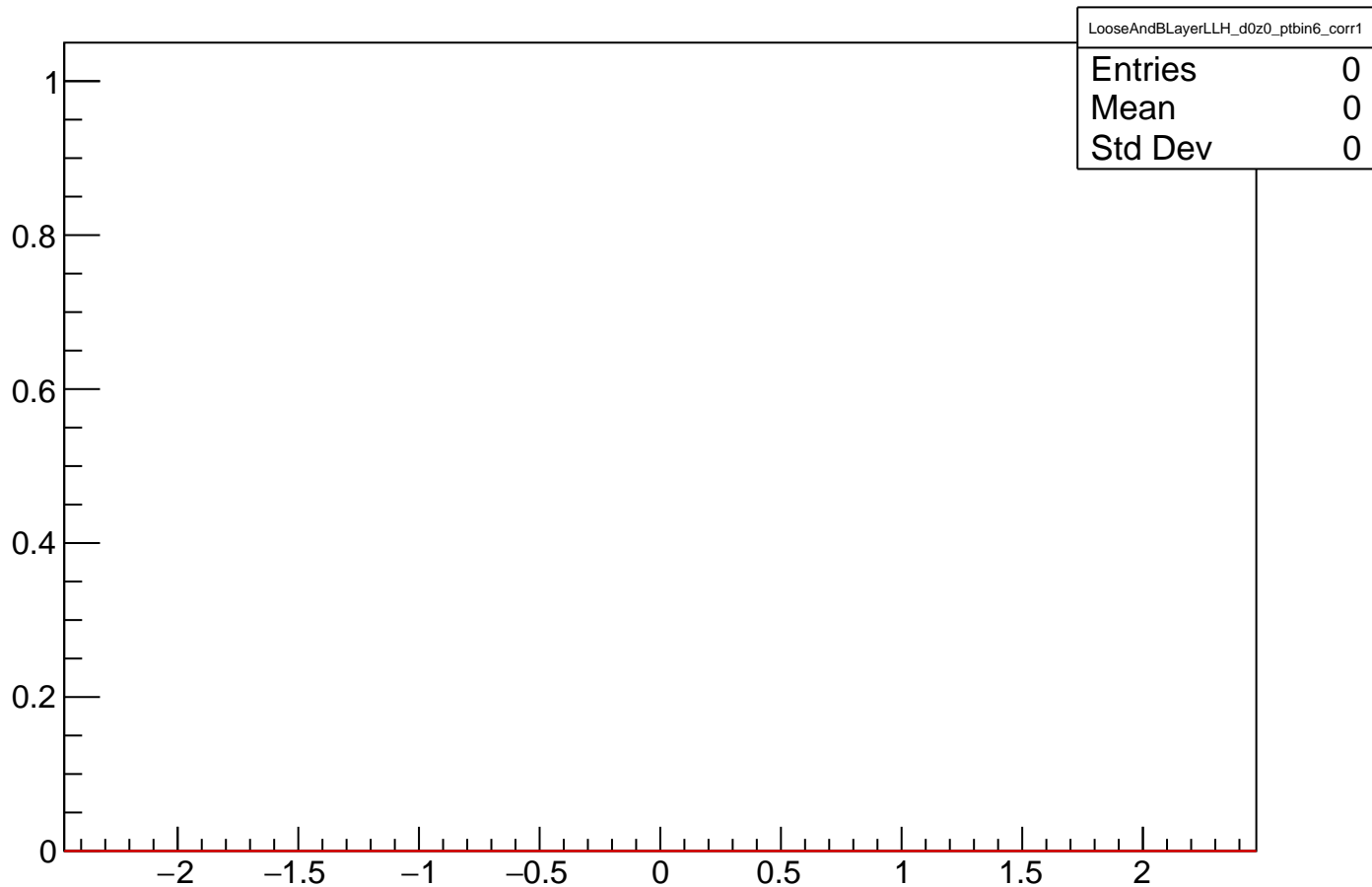
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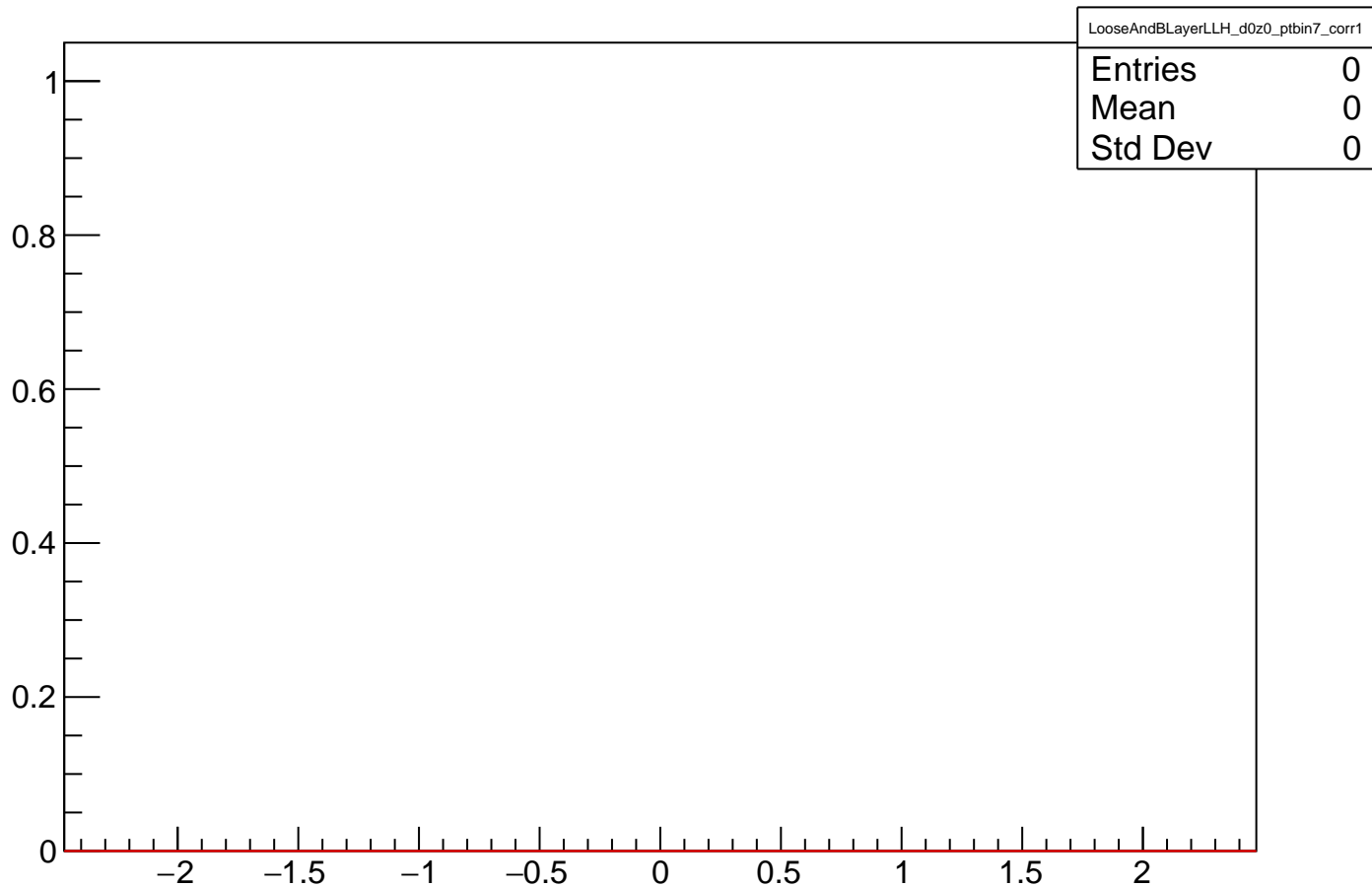
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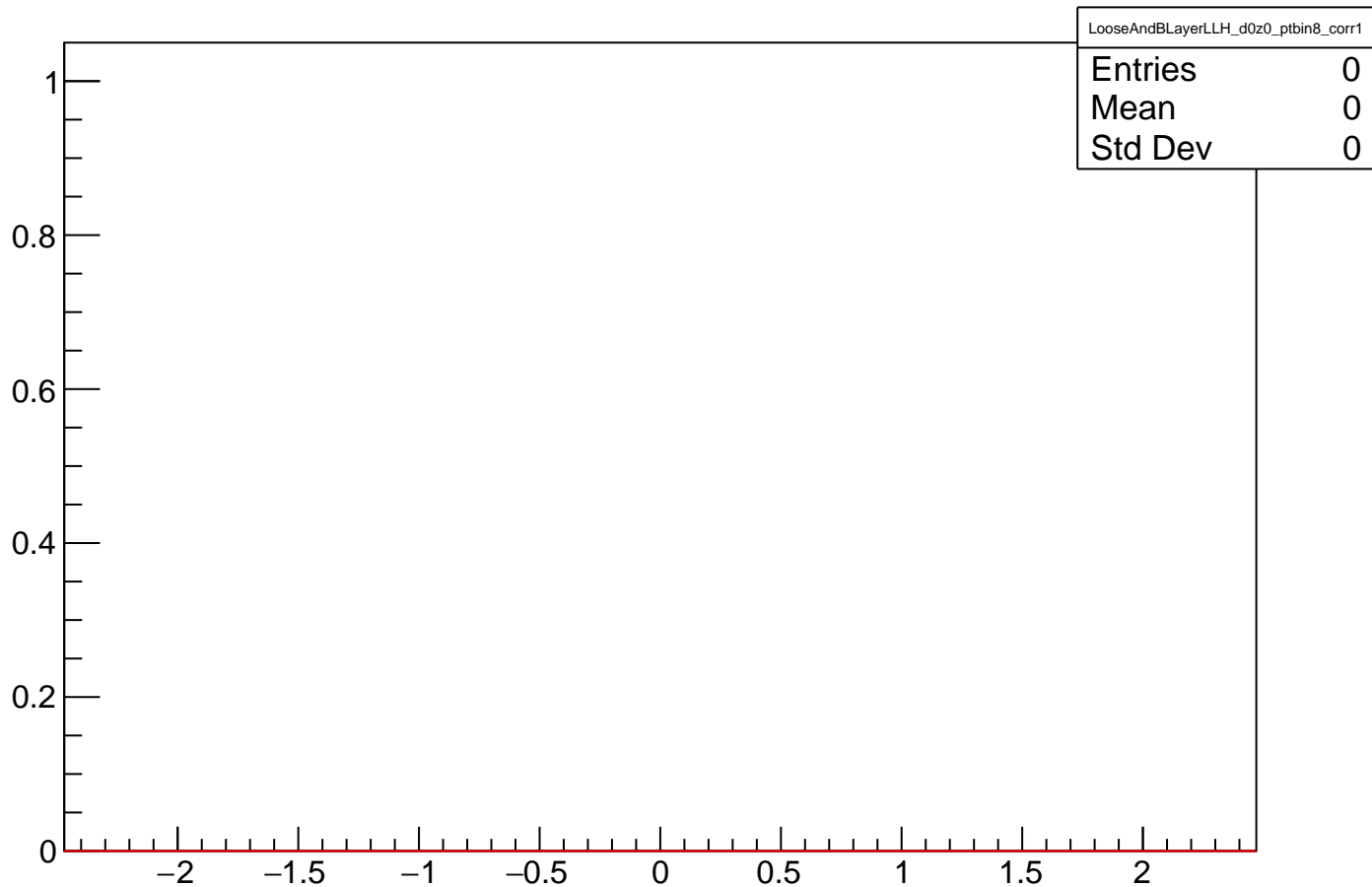
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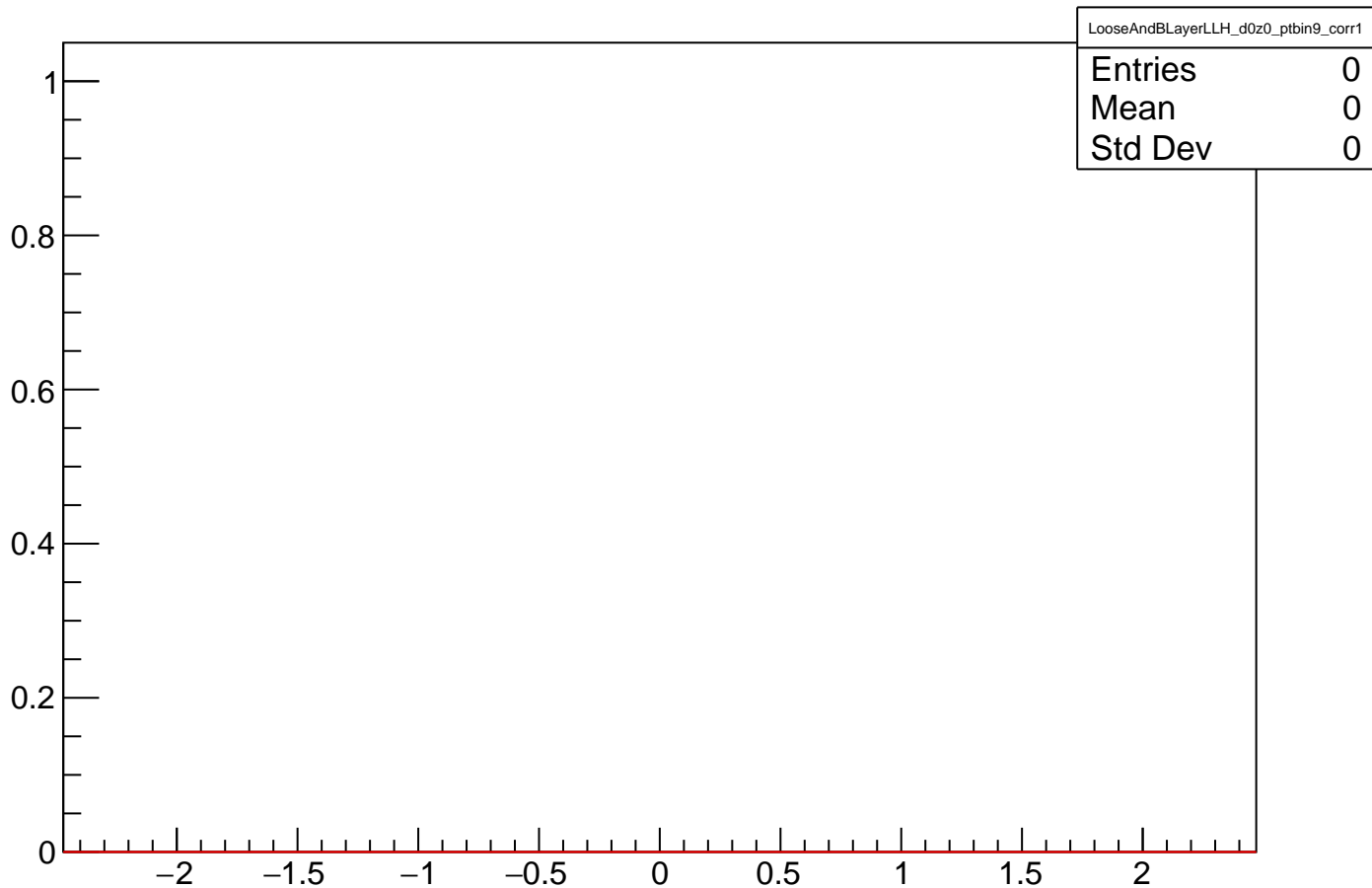
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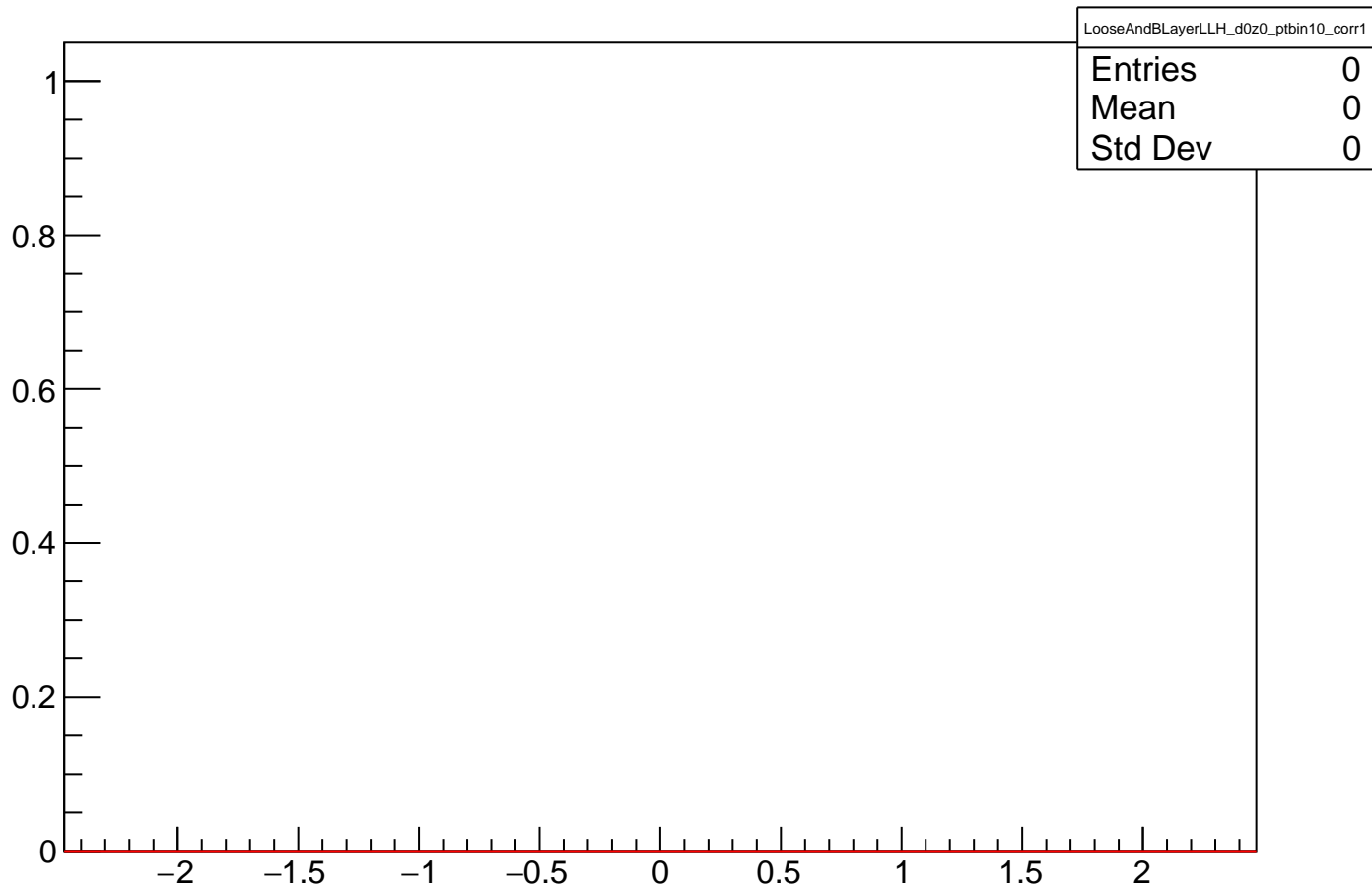
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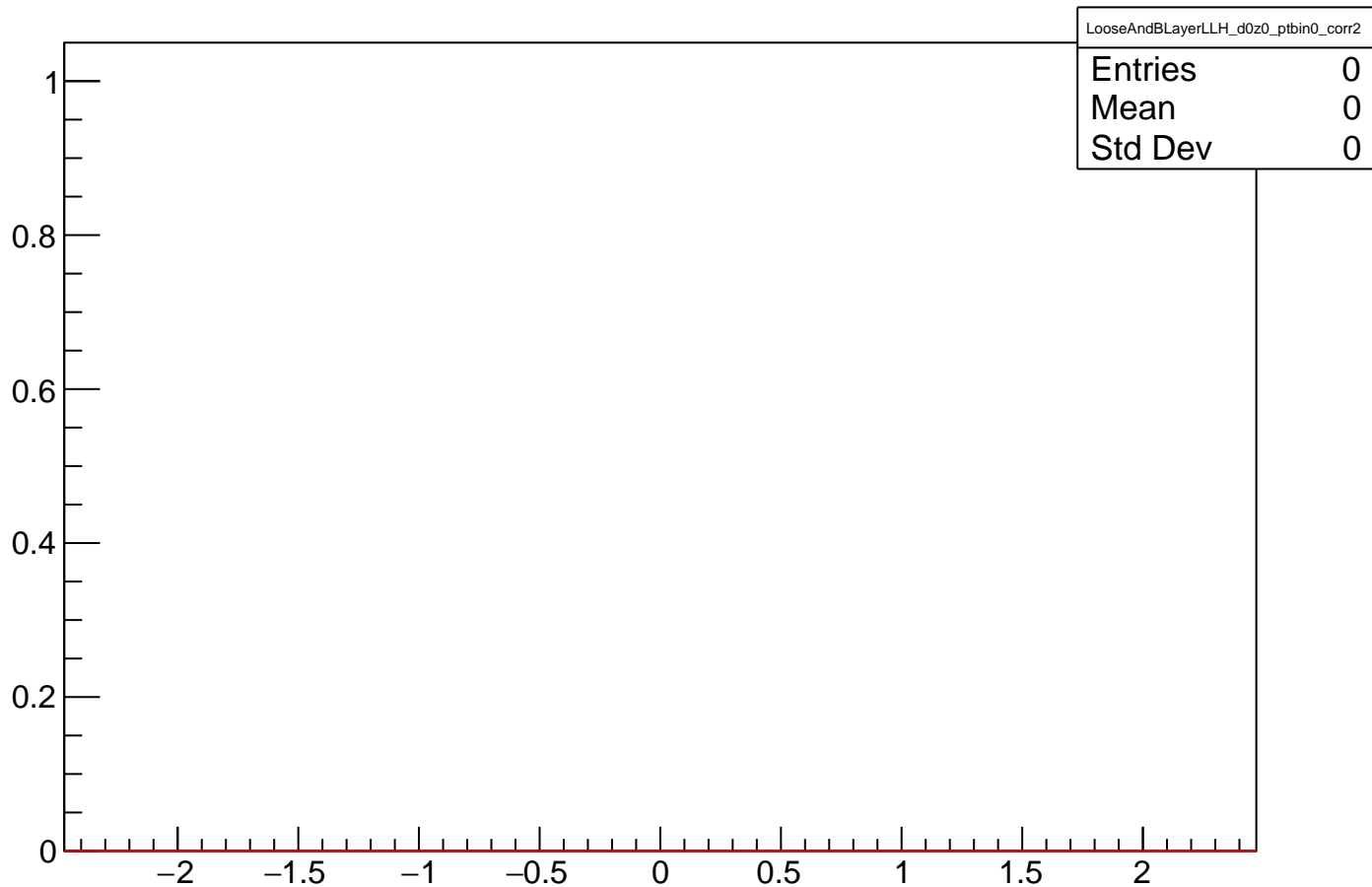
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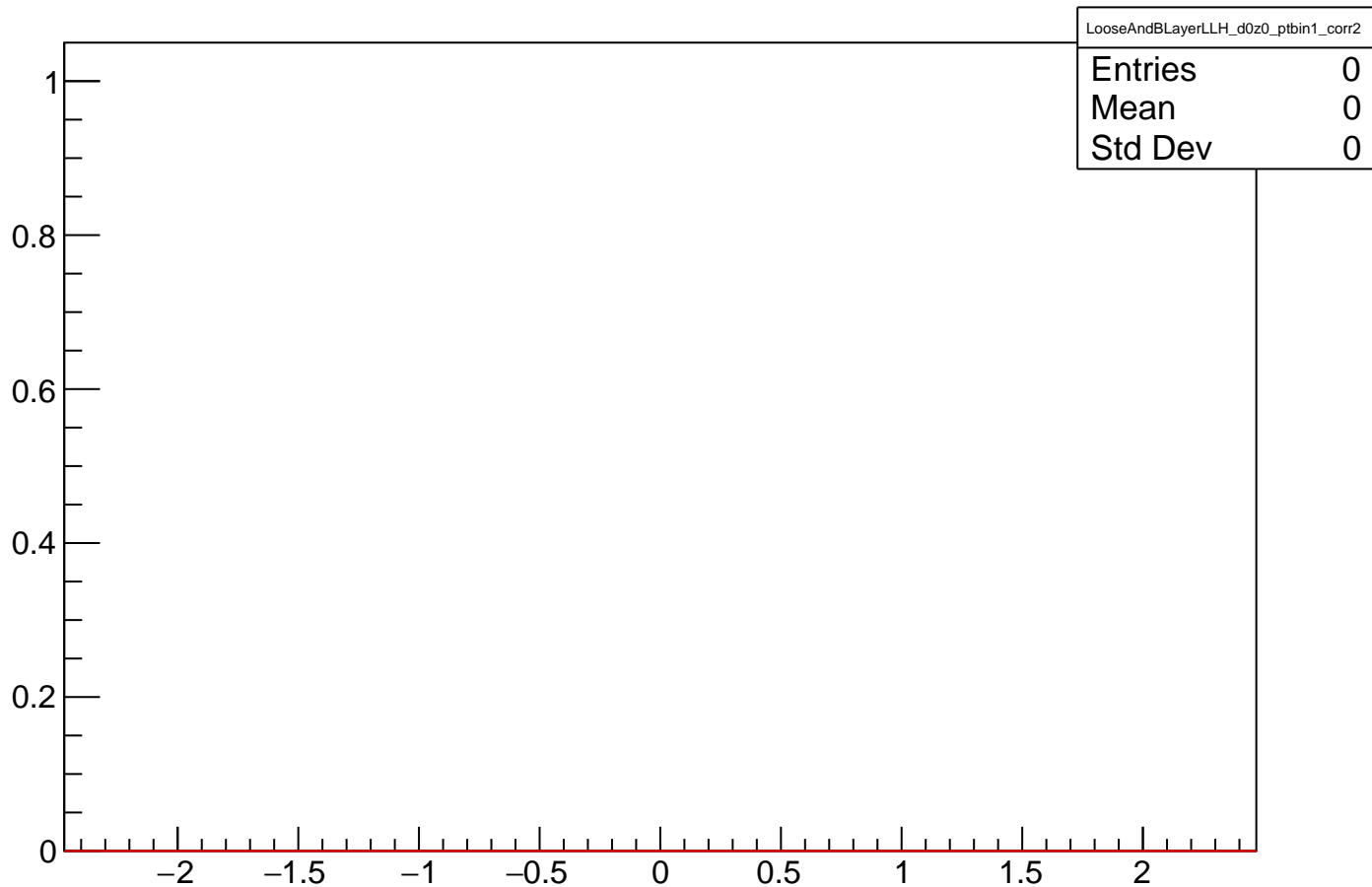
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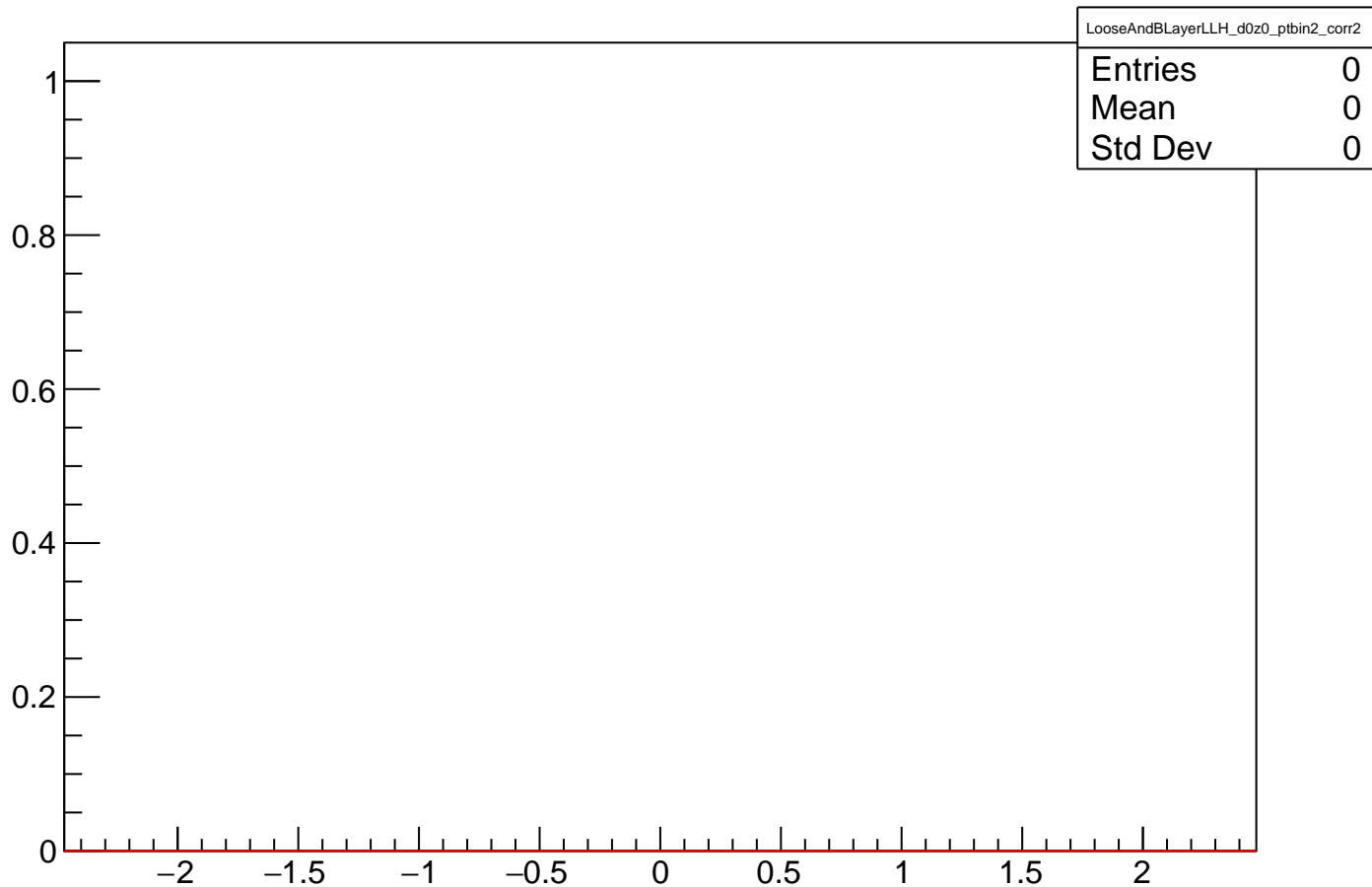
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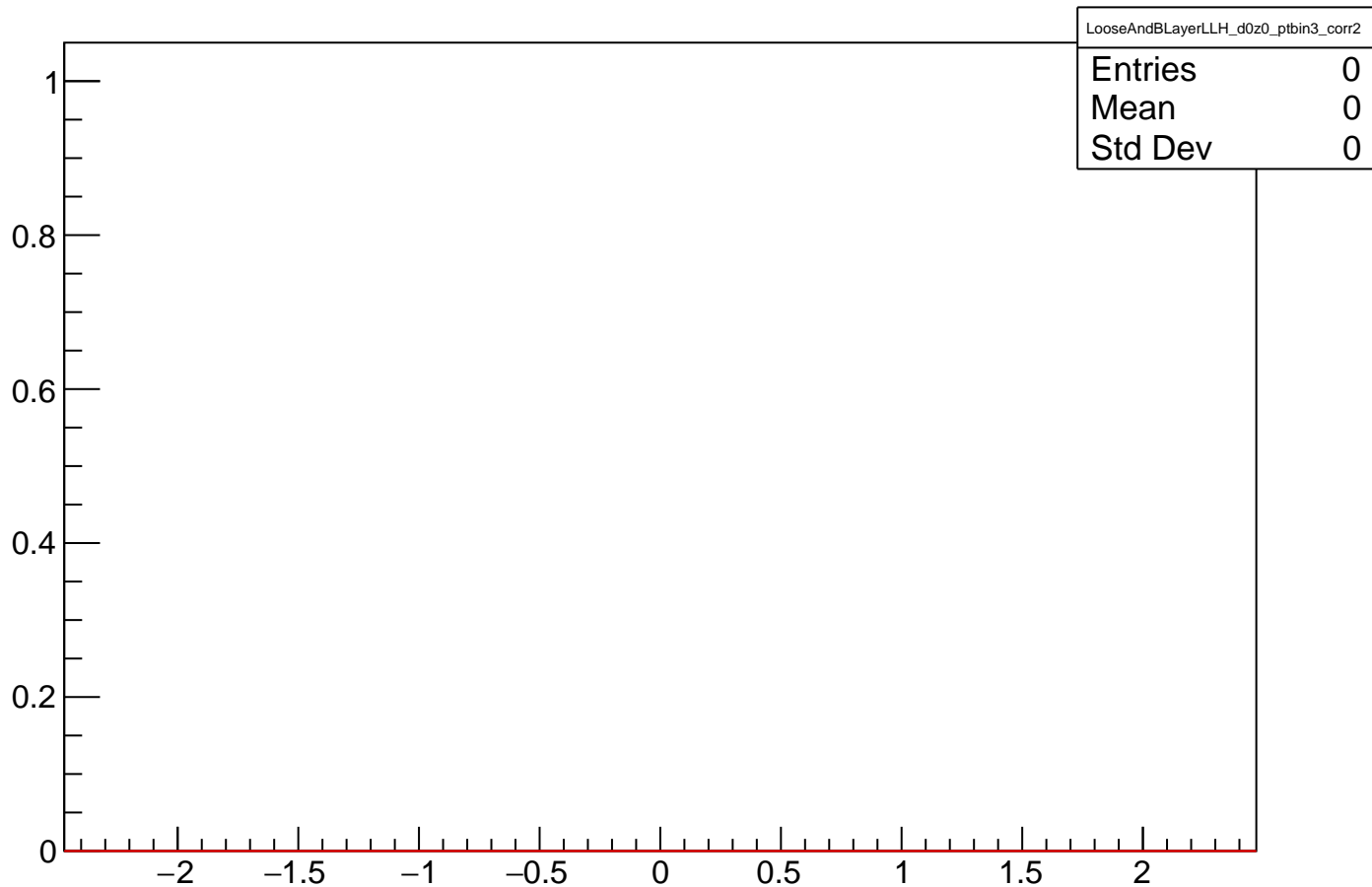
FullSim_LooseLLH_d0z0_corr2



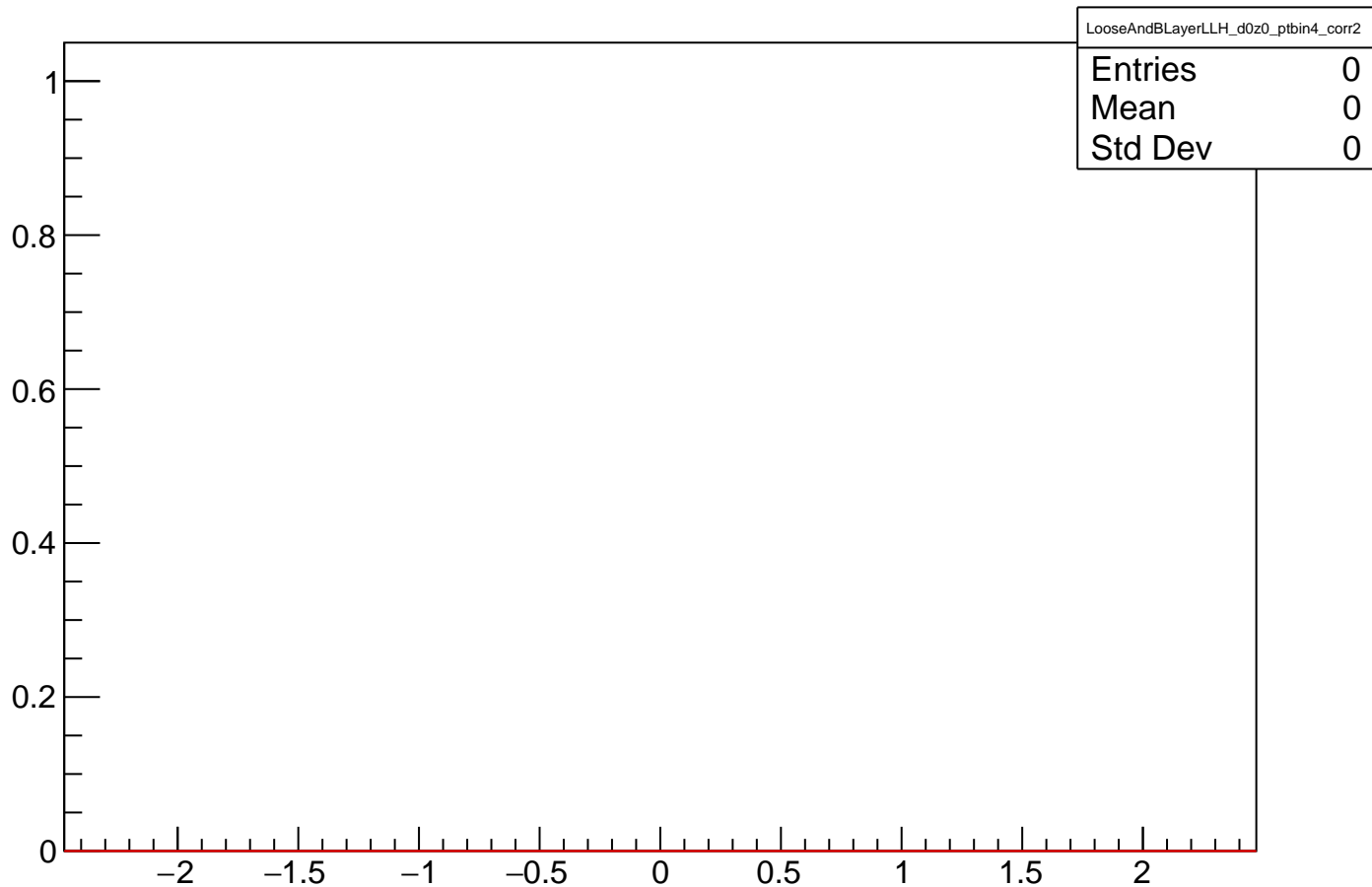
FullSim_LooseLLH_d0z0_corr2



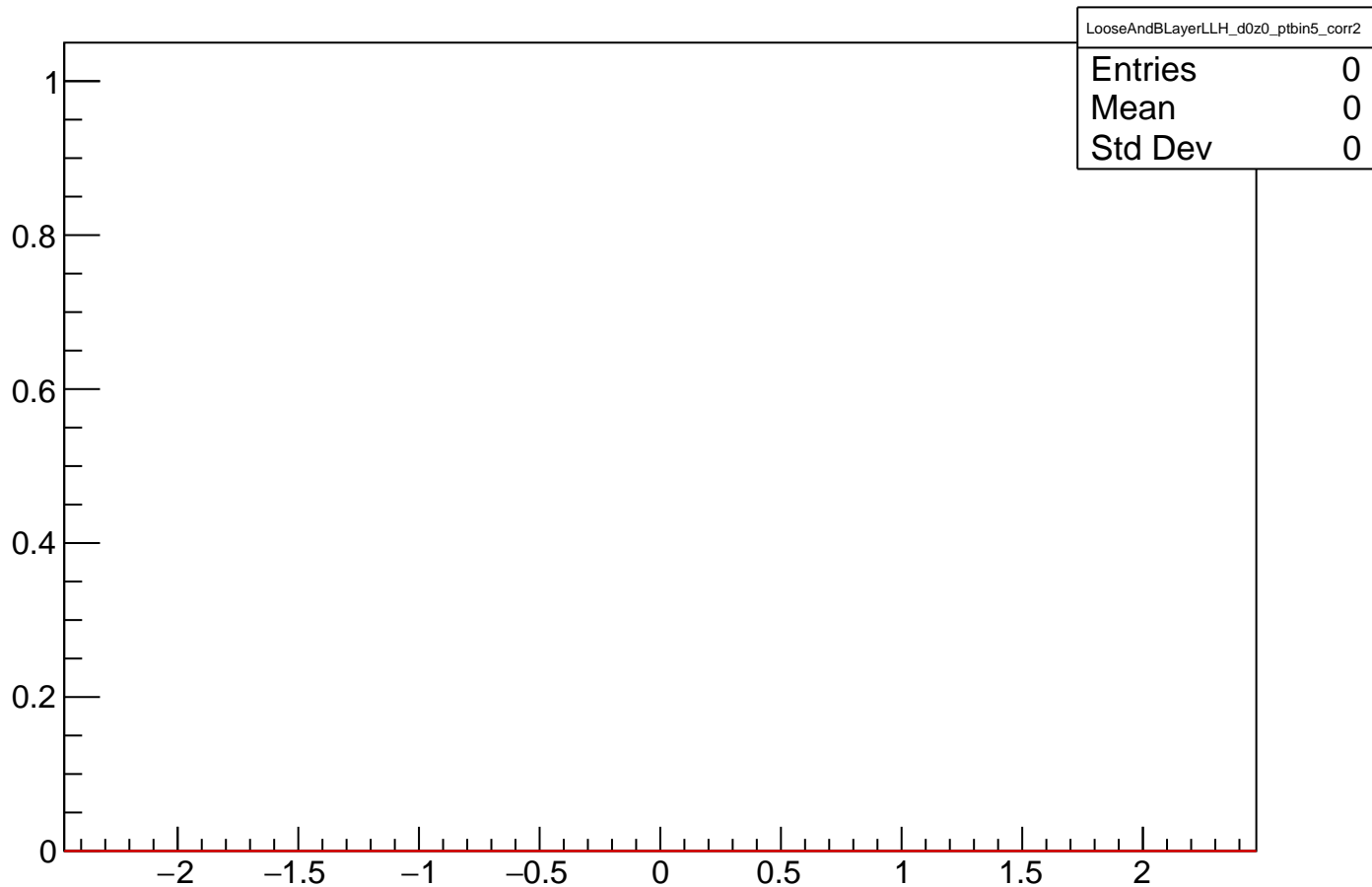
FullSim_LooseLLH_d0z0_corr2



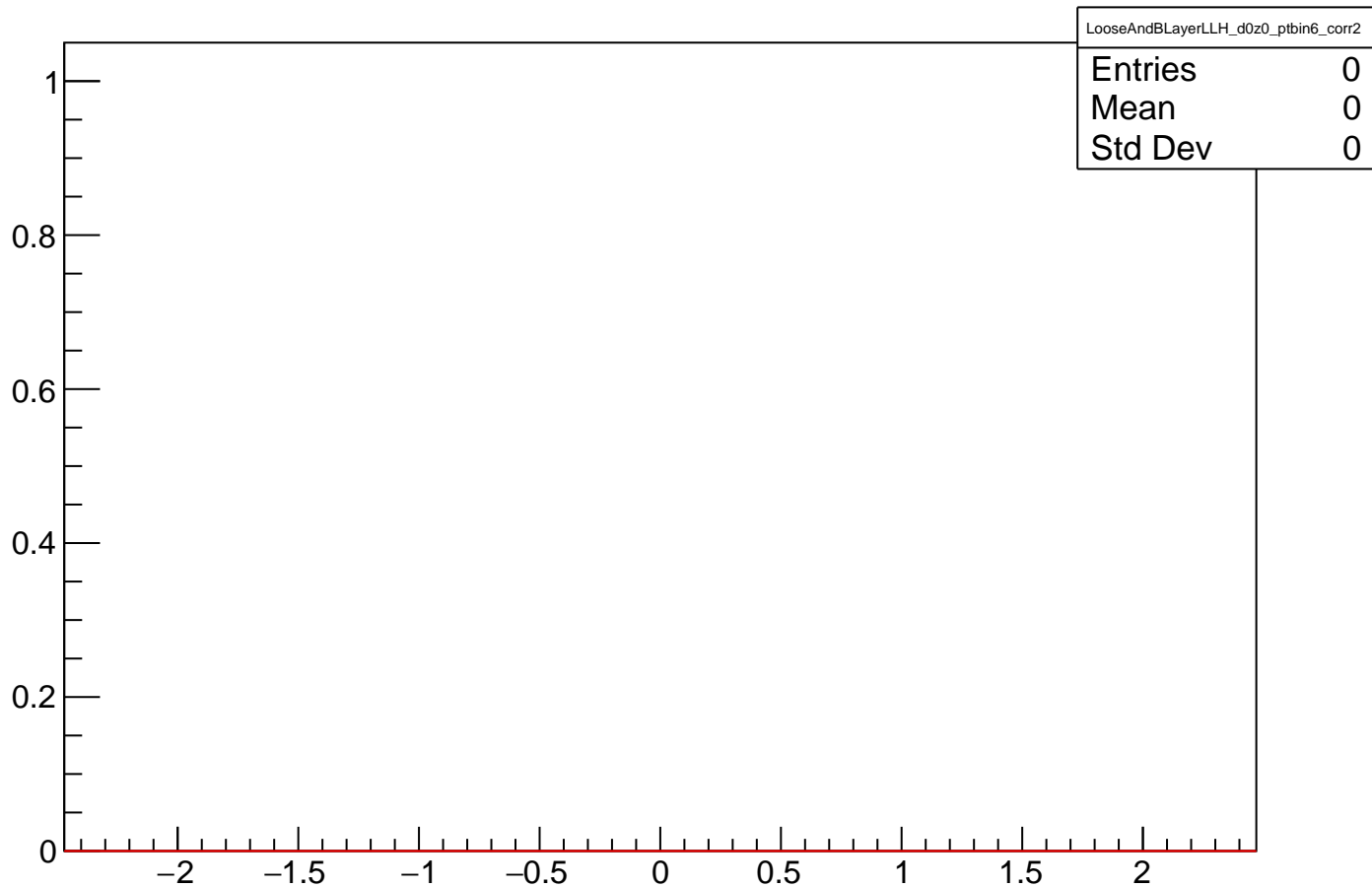
FullSim_LooseLLH_d0z0_corr2



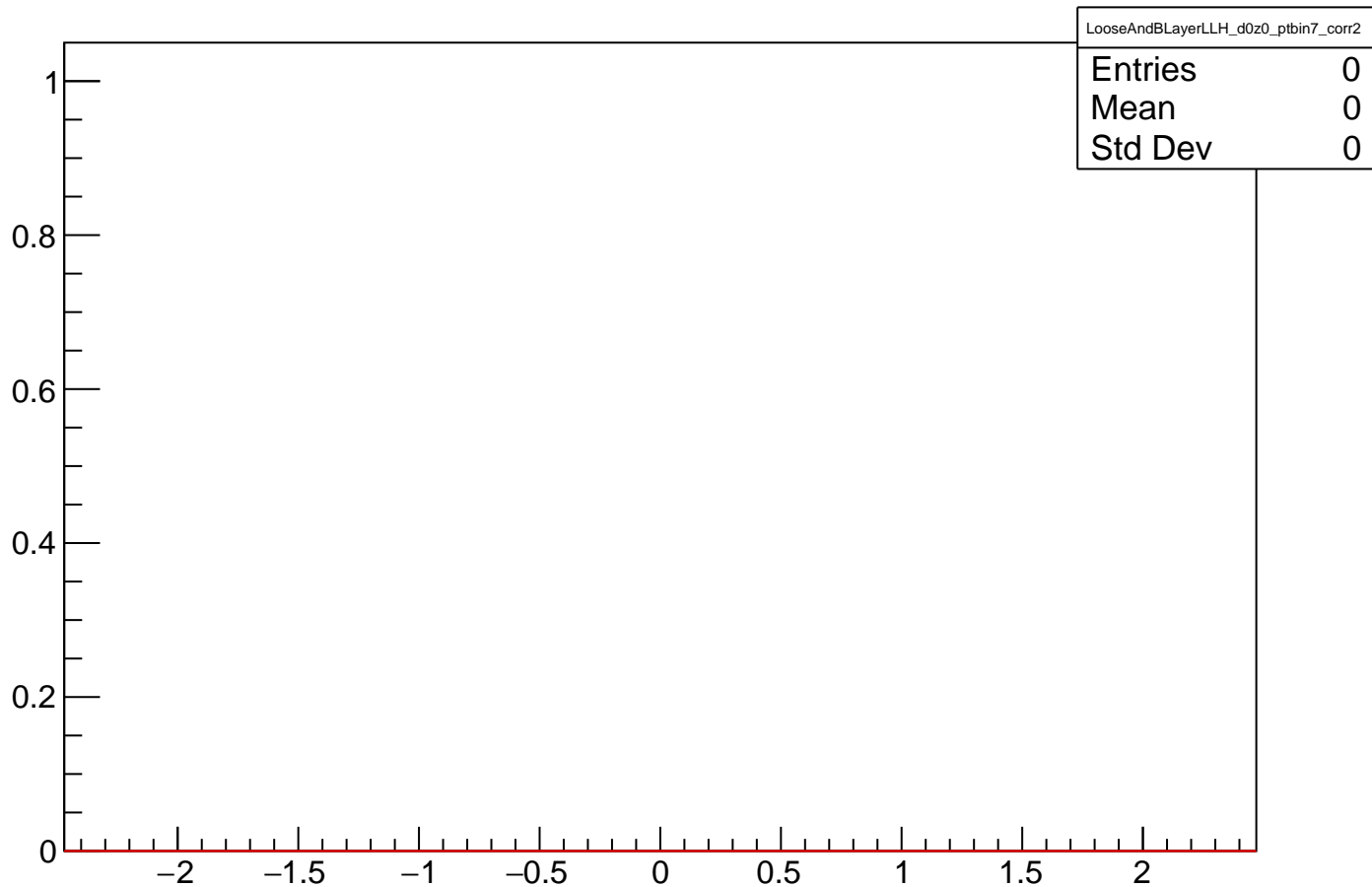
FullSim_LooseLLH_d0z0_corr2



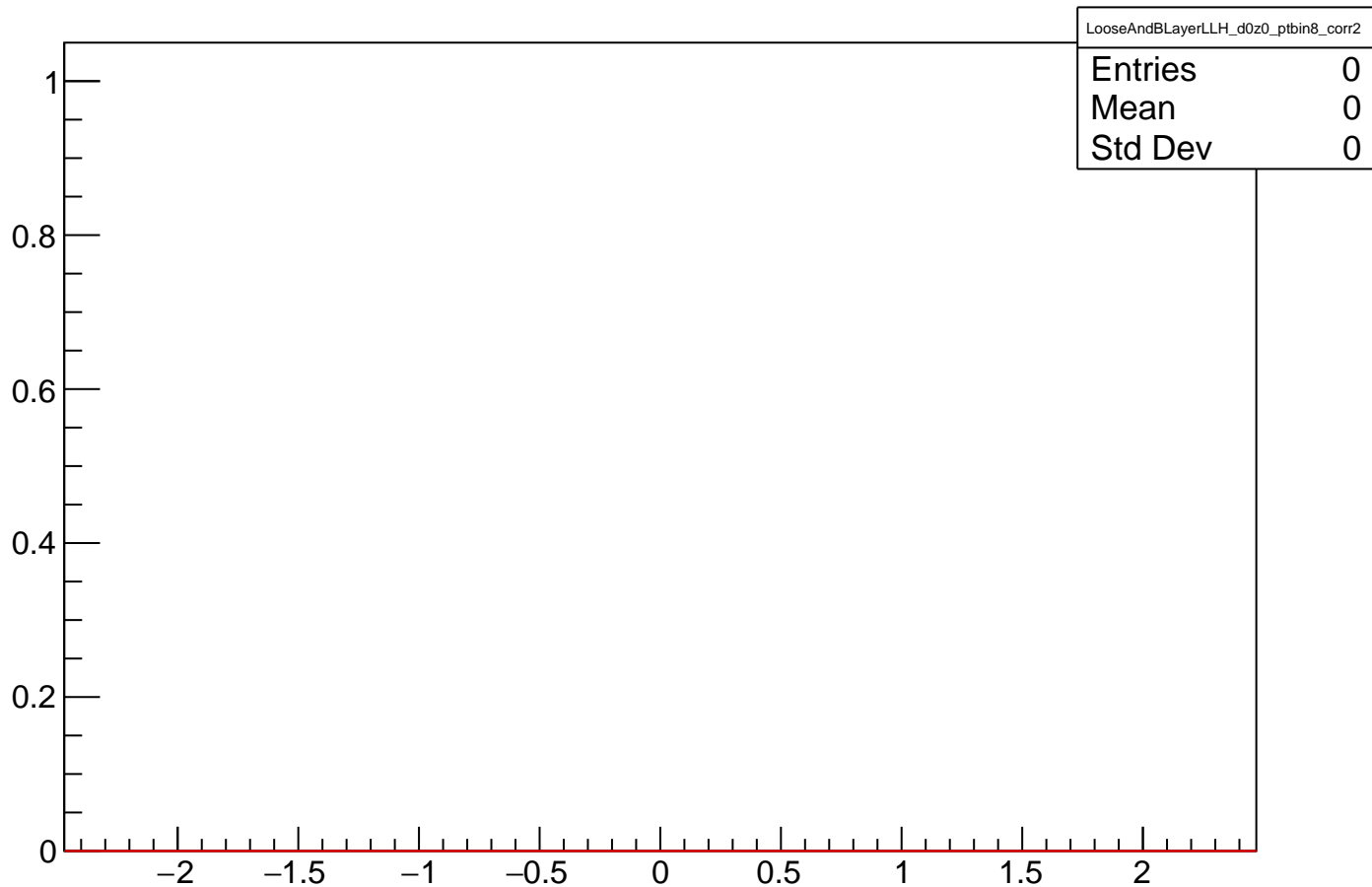
FullSim_LooseLLH_d0z0_corr2



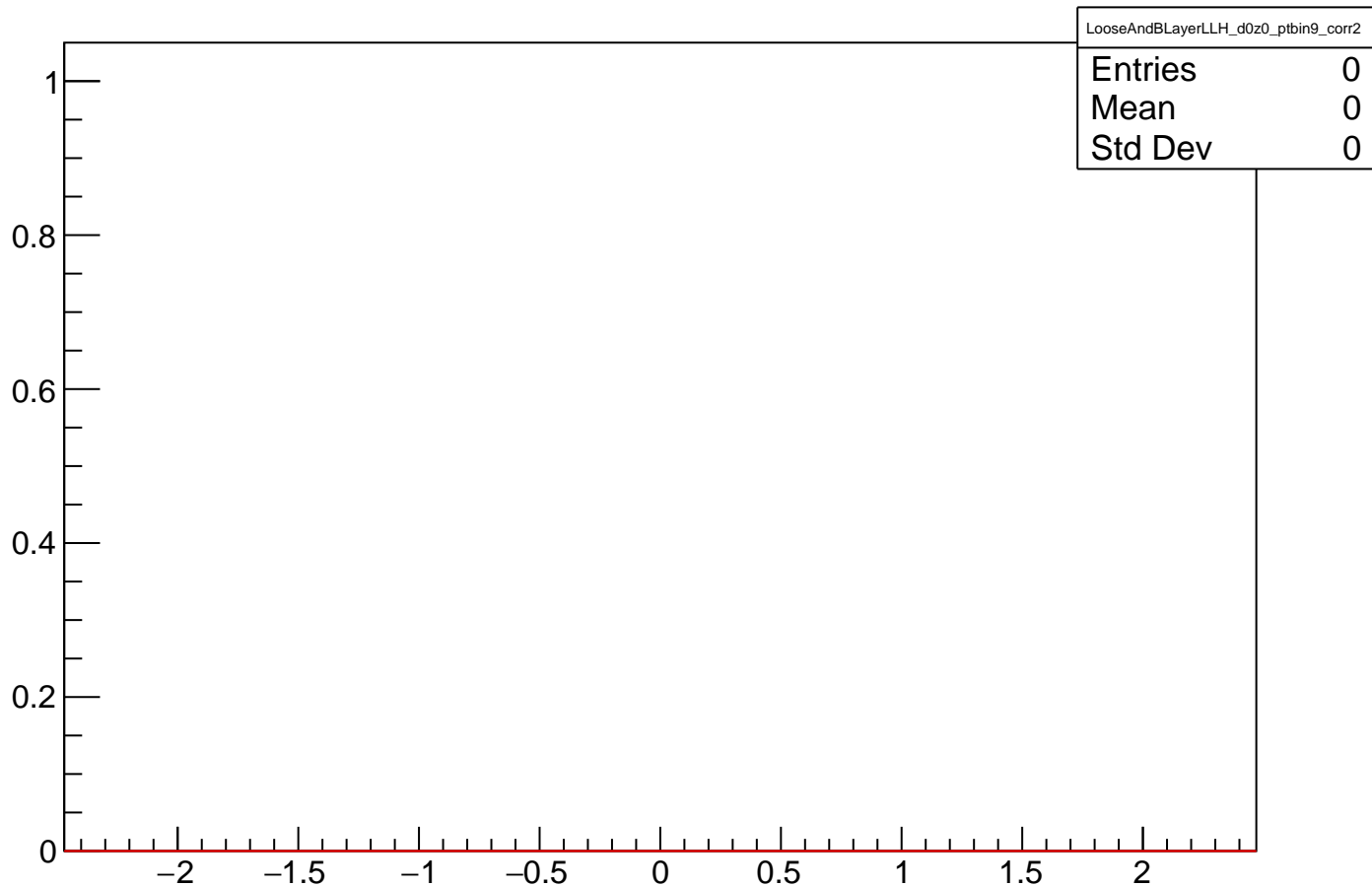
FullSim_LooseLLH_d0z0_corr2



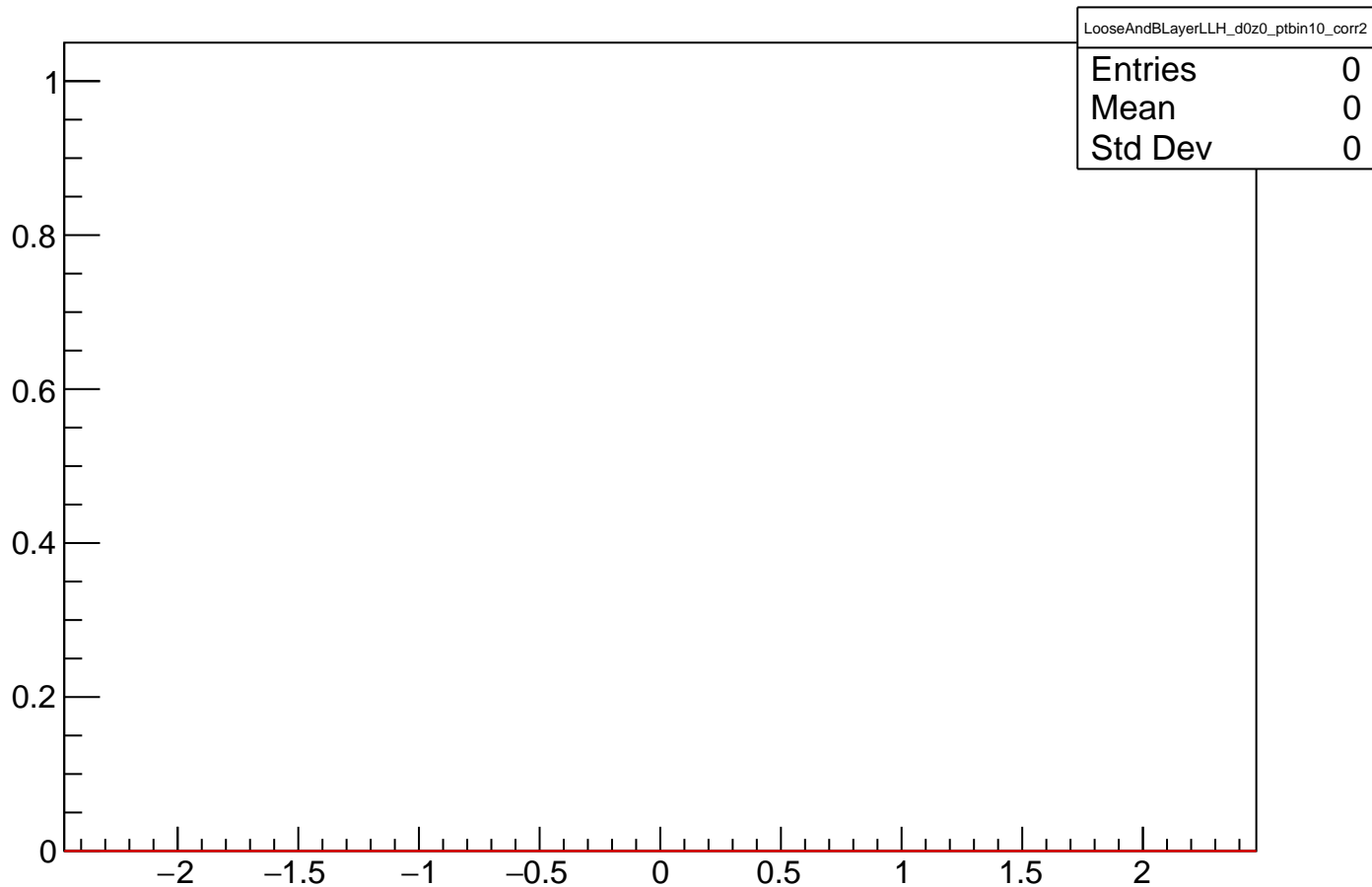
FullSim_LooseLLH_d0z0_corr2



FullSim_LooseLLH_d0z0_corr2



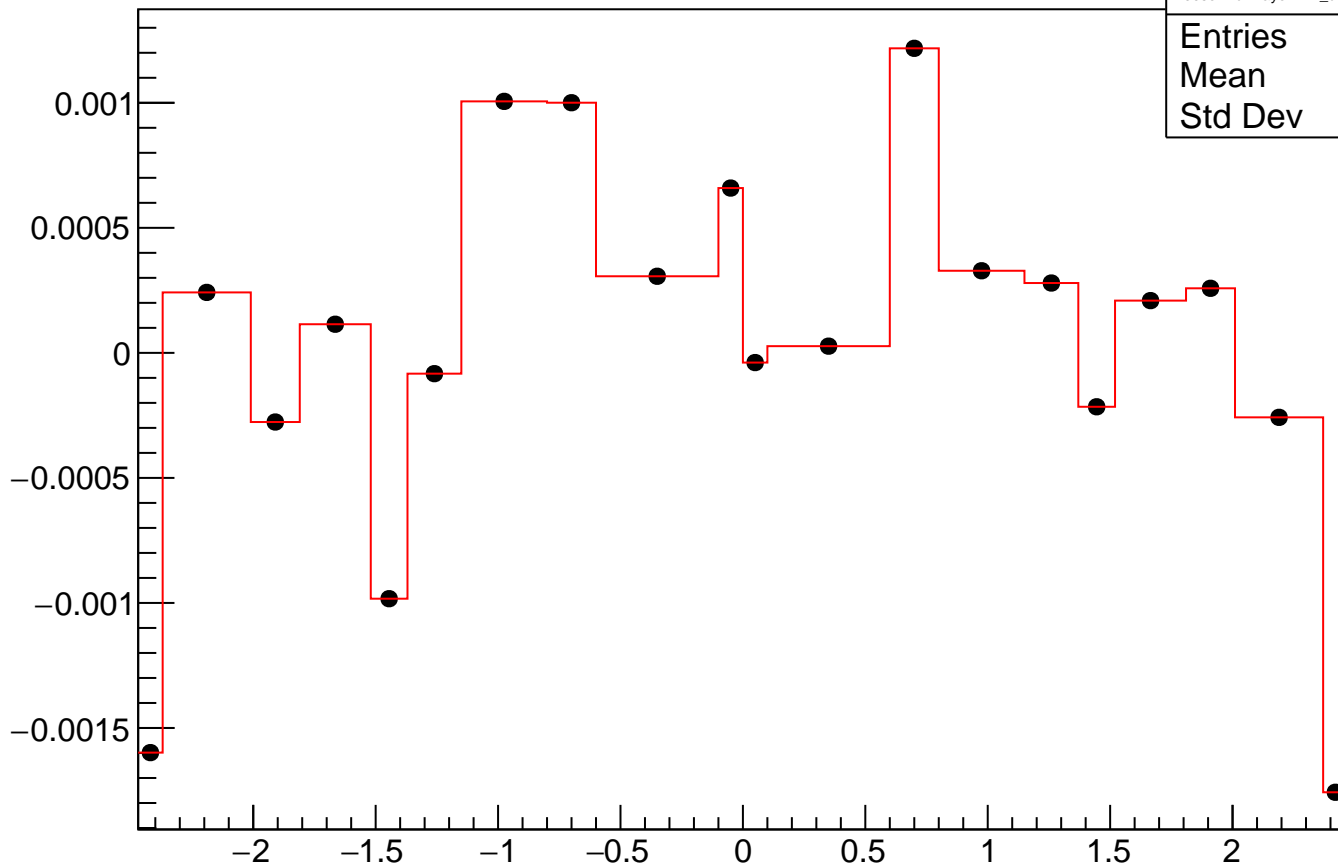
FullSim_LooseLLH_d0z0_corr2



FullSim_LooseLLH_d0z0_corr3

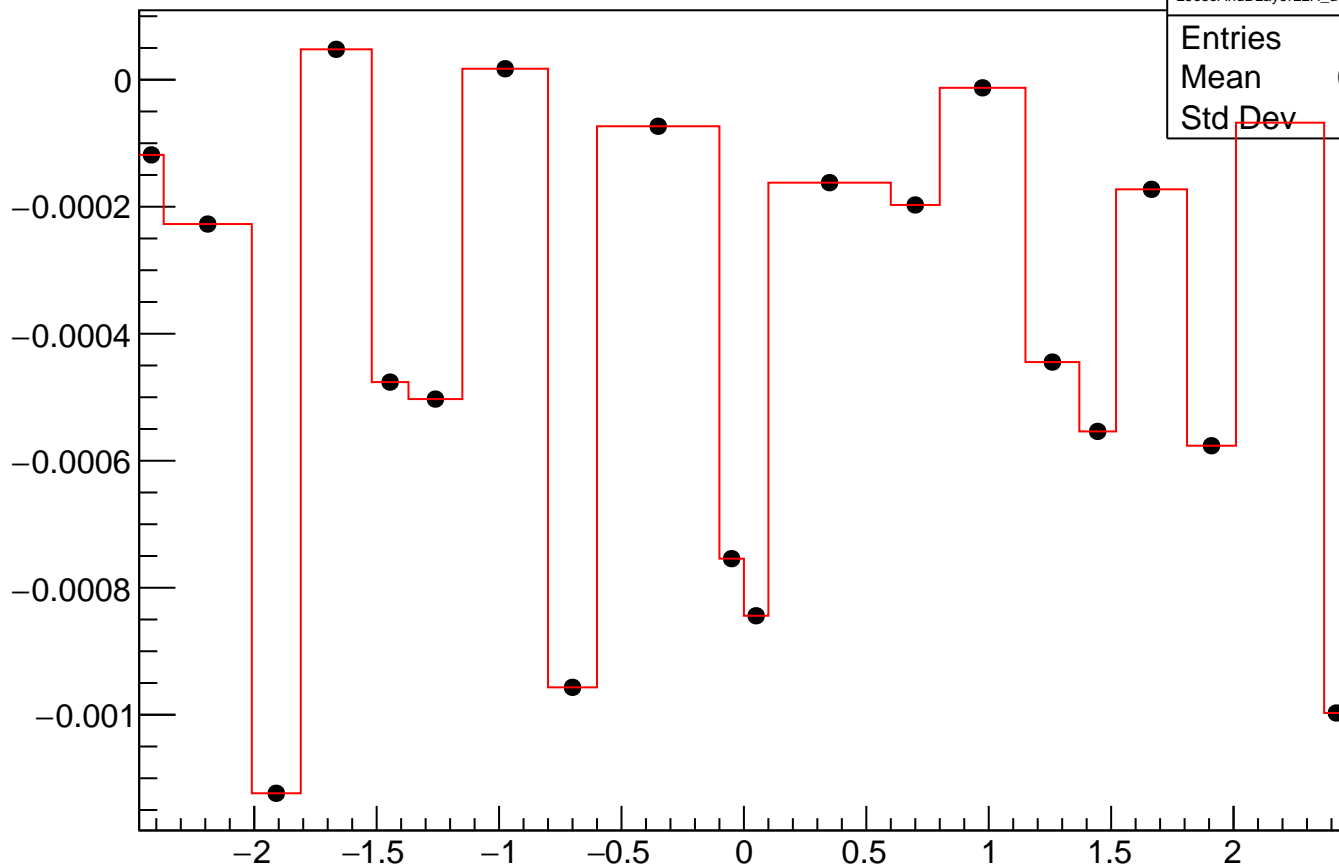
LooseAndBLayerLLH_d0z0_ptbin0_corr3

Entries	0
Mean	1.432
Std Dev	6.697

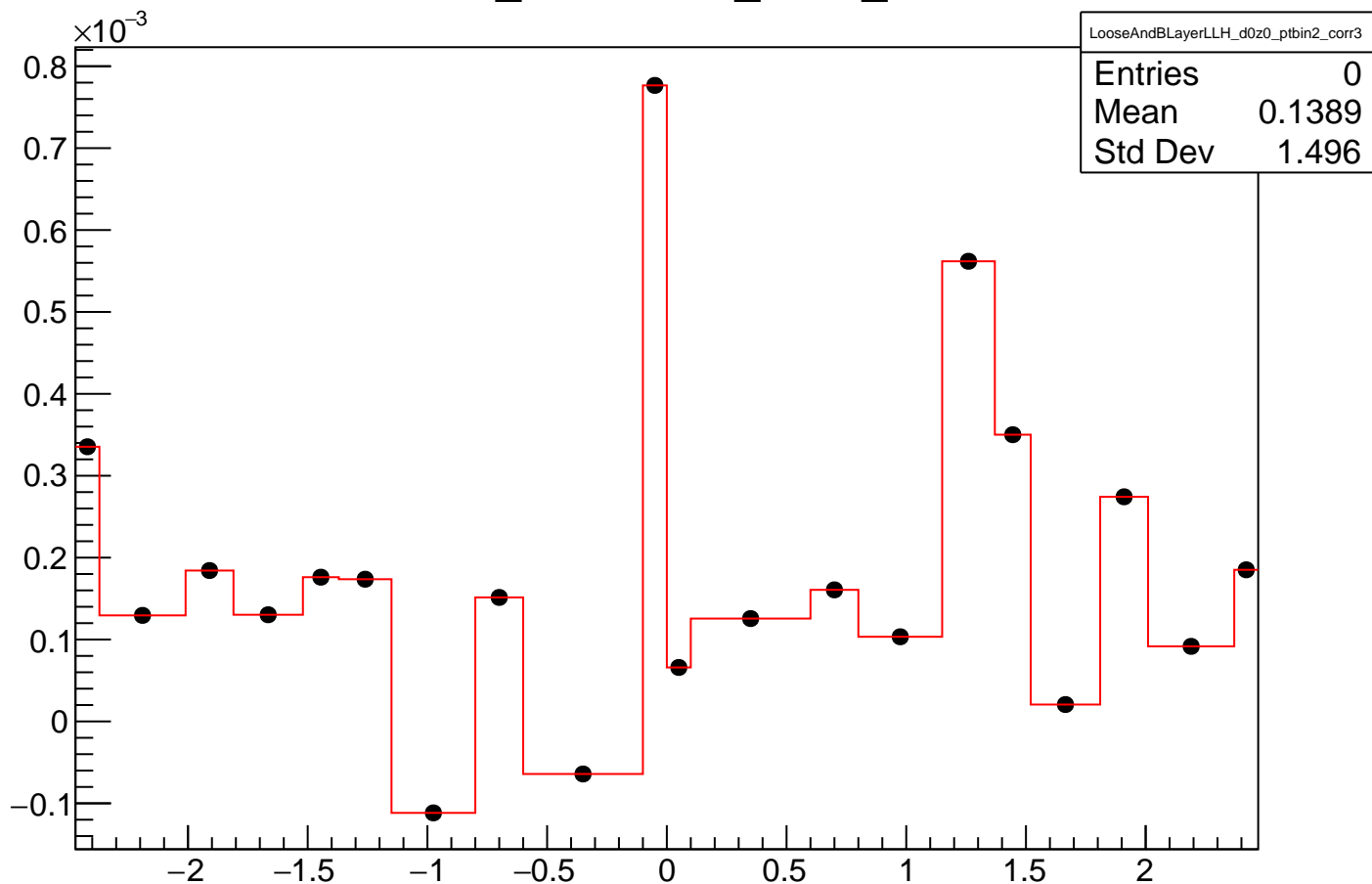


FullSim_LooseLLH_d0z0_corr3

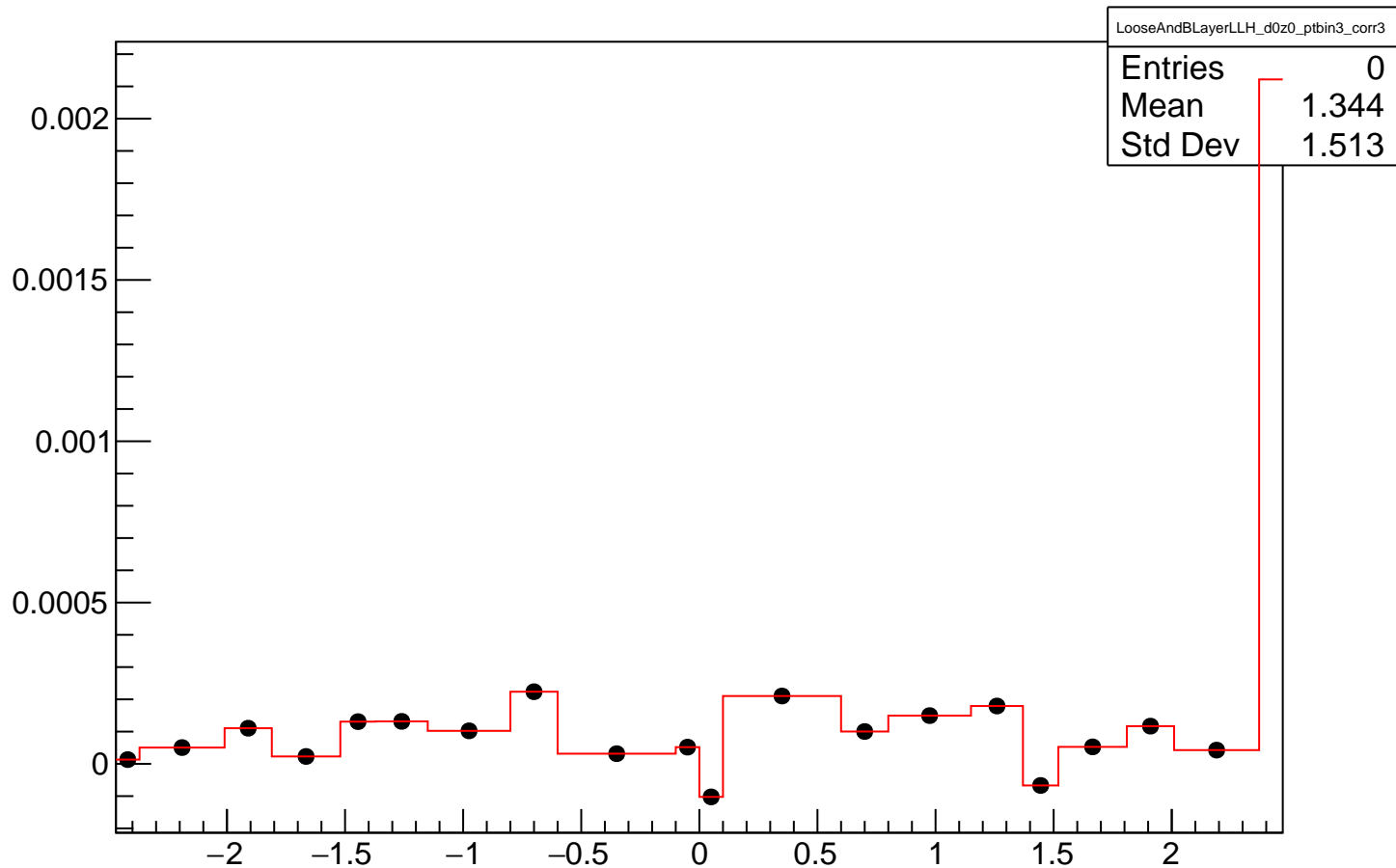
LooseAndBLayerLLH_d0z0_ptbin1_corr3	
Entries	0
Mean	0.08186
Std Dev	1.51



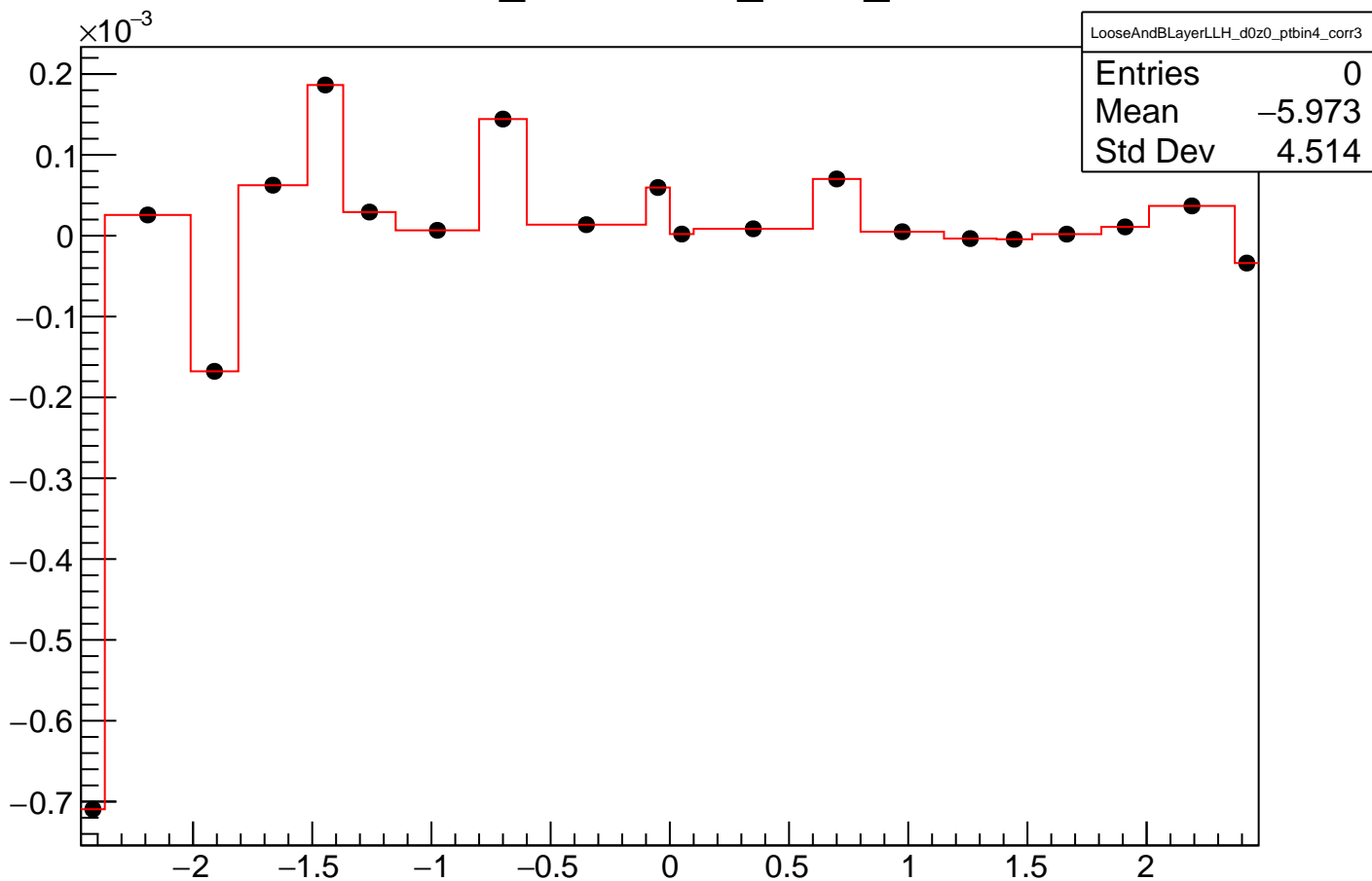
FullSim_LooseLLH_d0z0_corr3



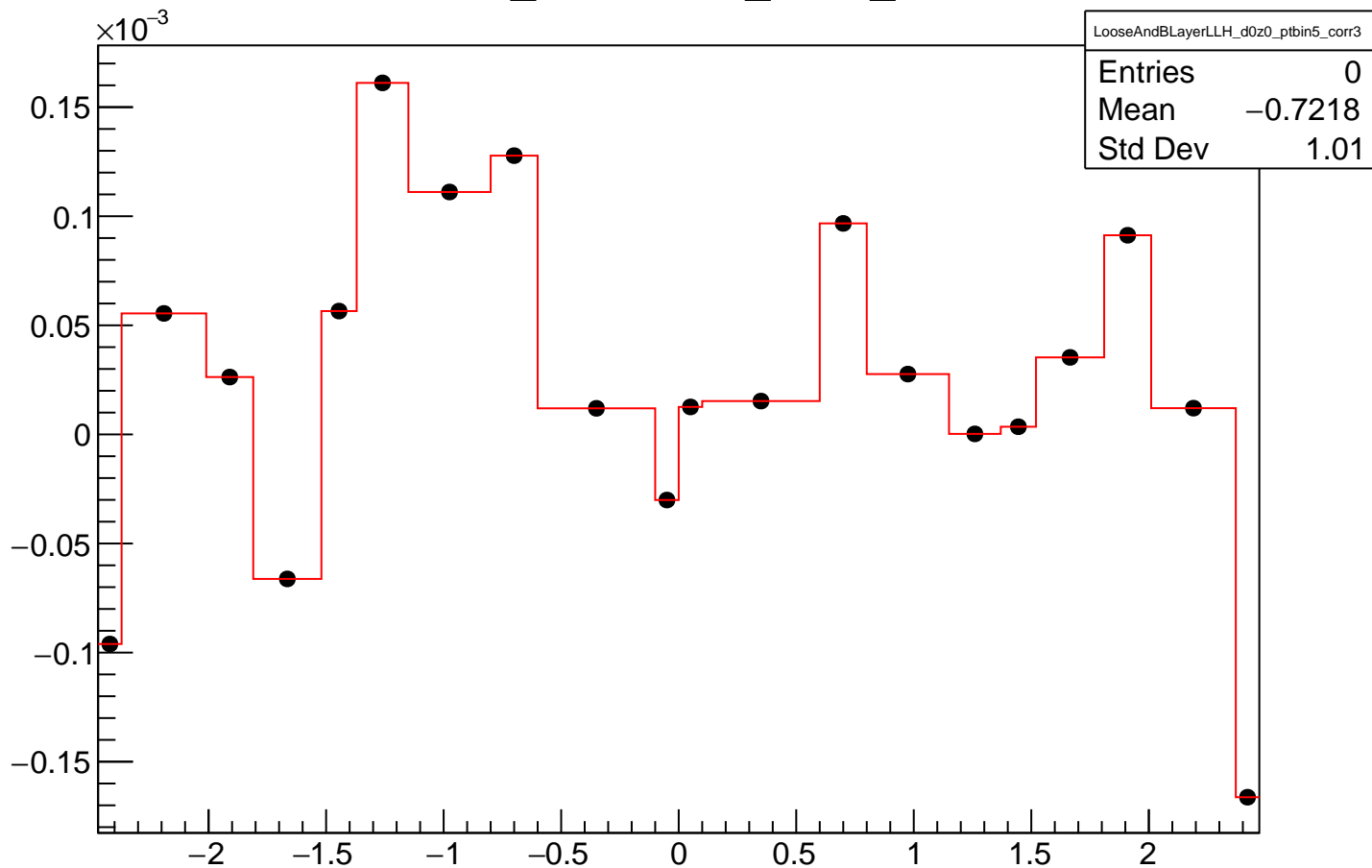
FullSim_LooseLLH_d0z0_corr3



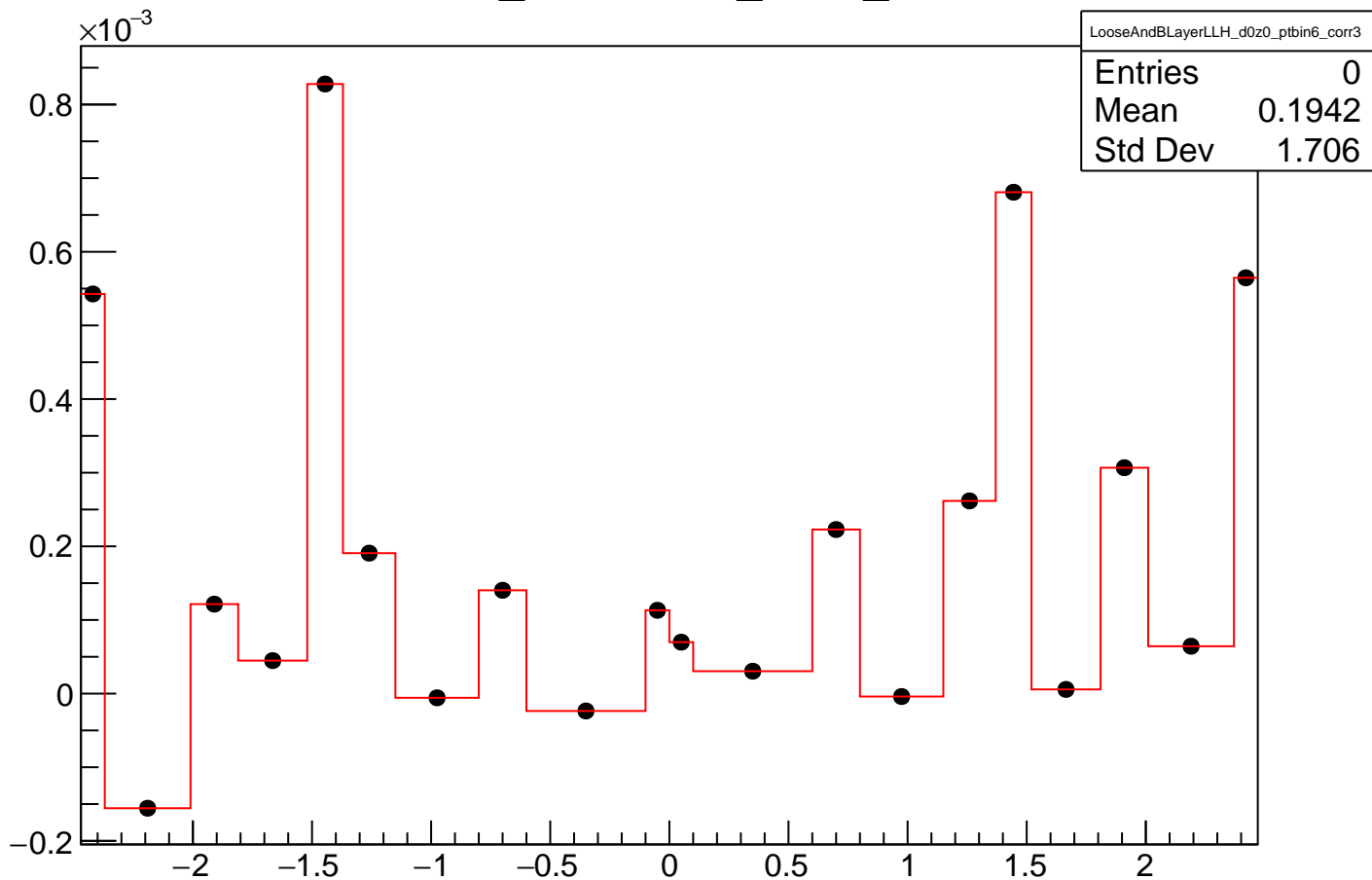
FullSim_LooseLLH_d0z0_corr3



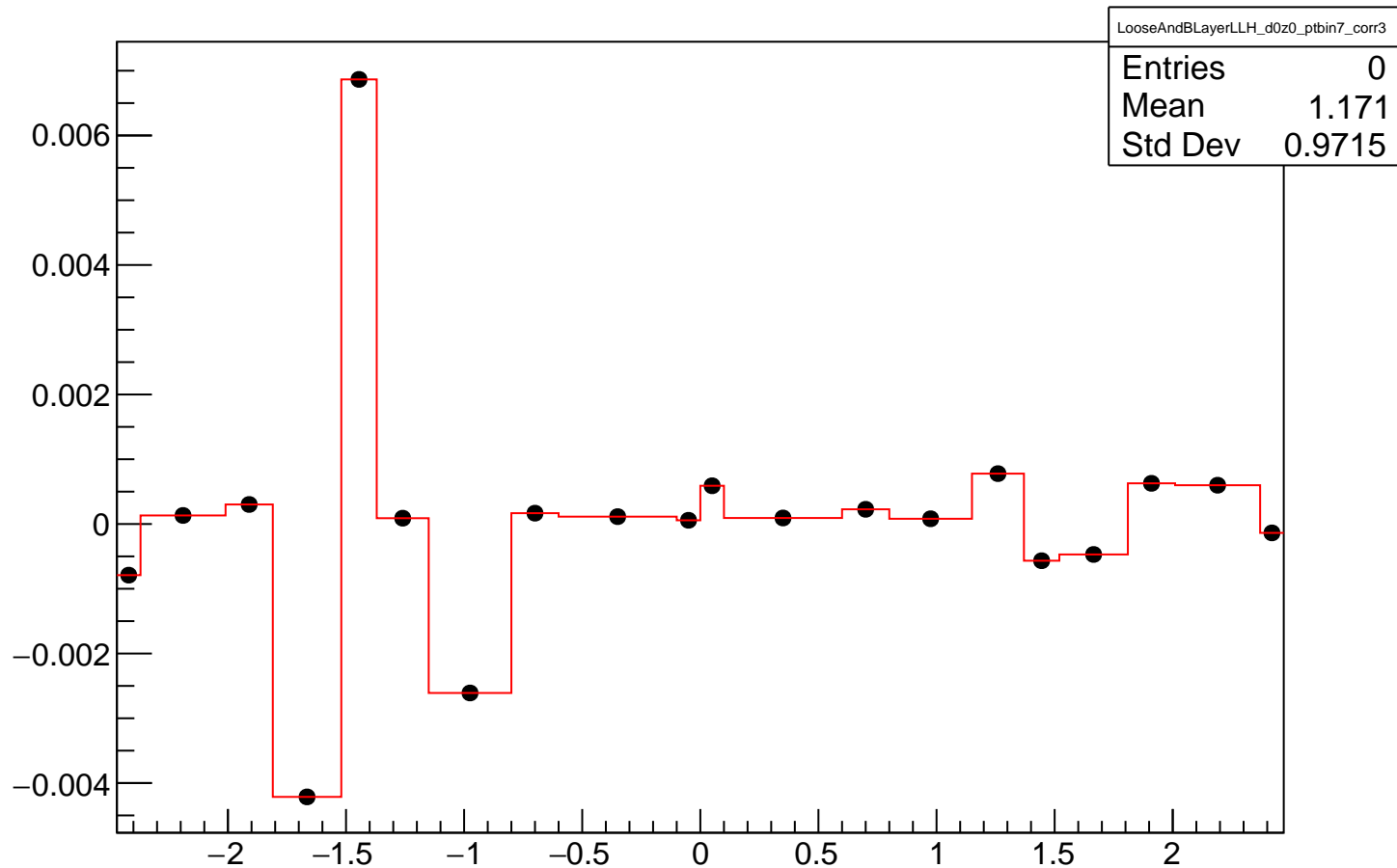
FullSim_LooseLLH_d0z0_corr3



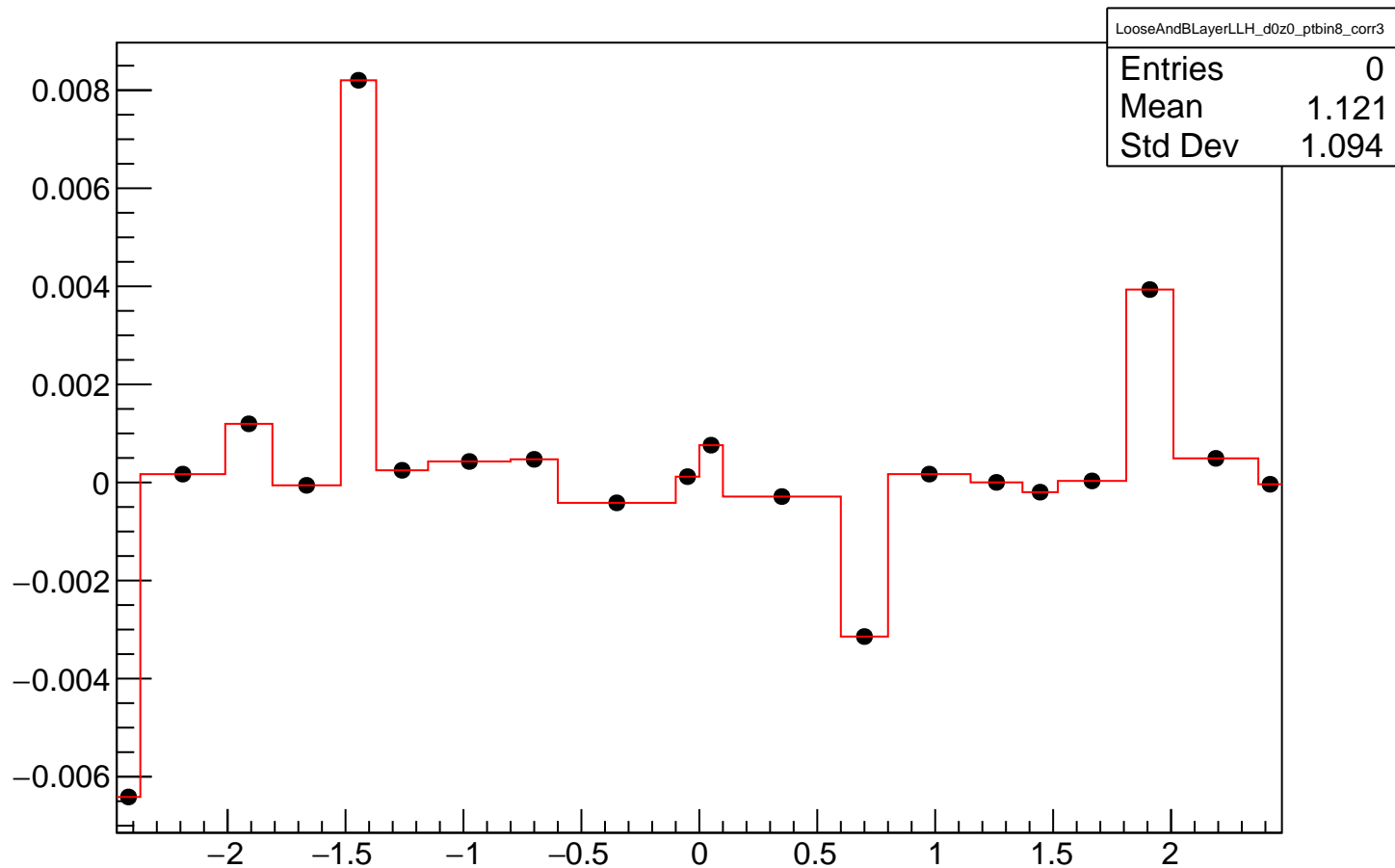
FullSim_LooseLLH_d0z0_corr3



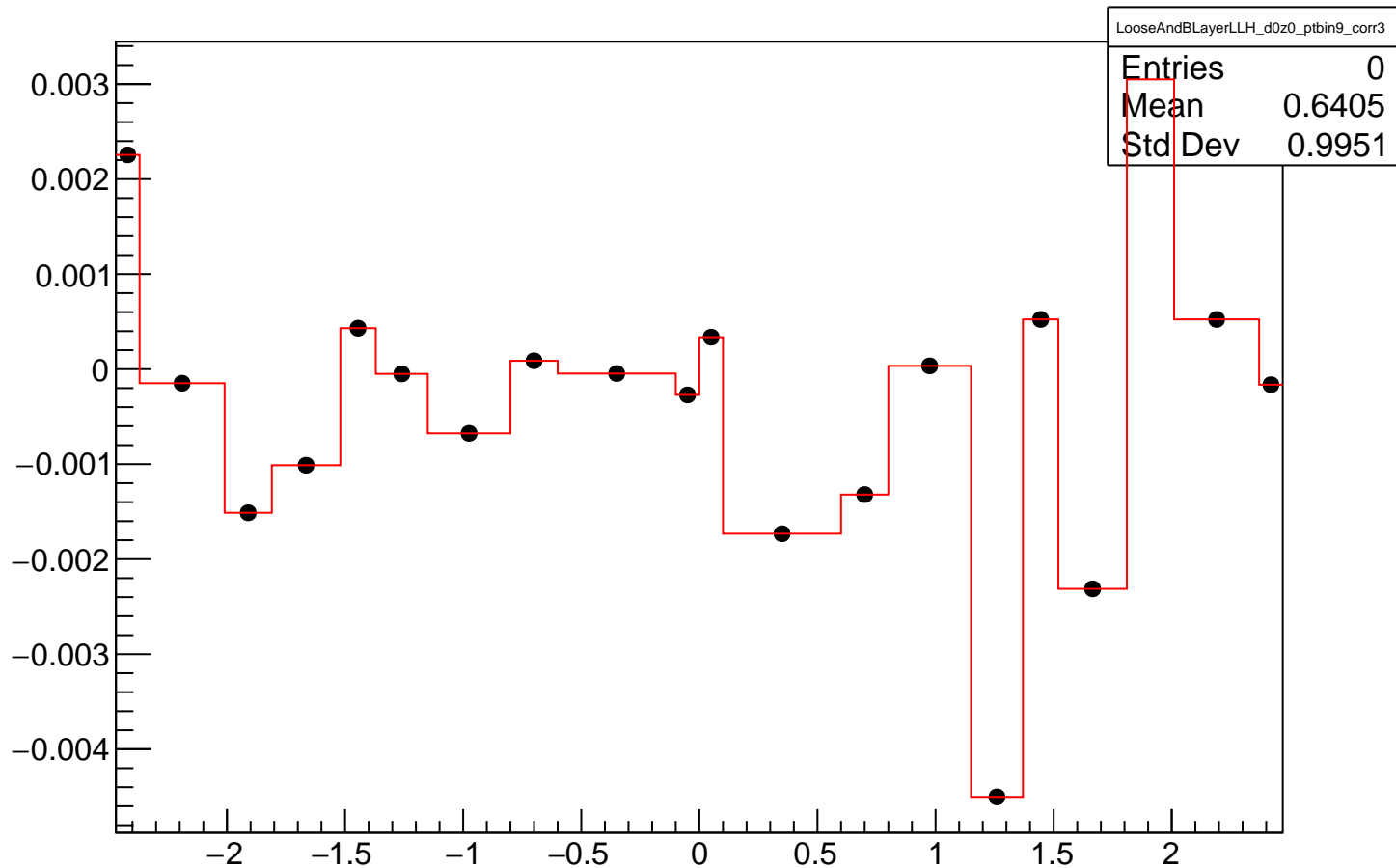
FullSim_LooseLLH_d0z0_corr3



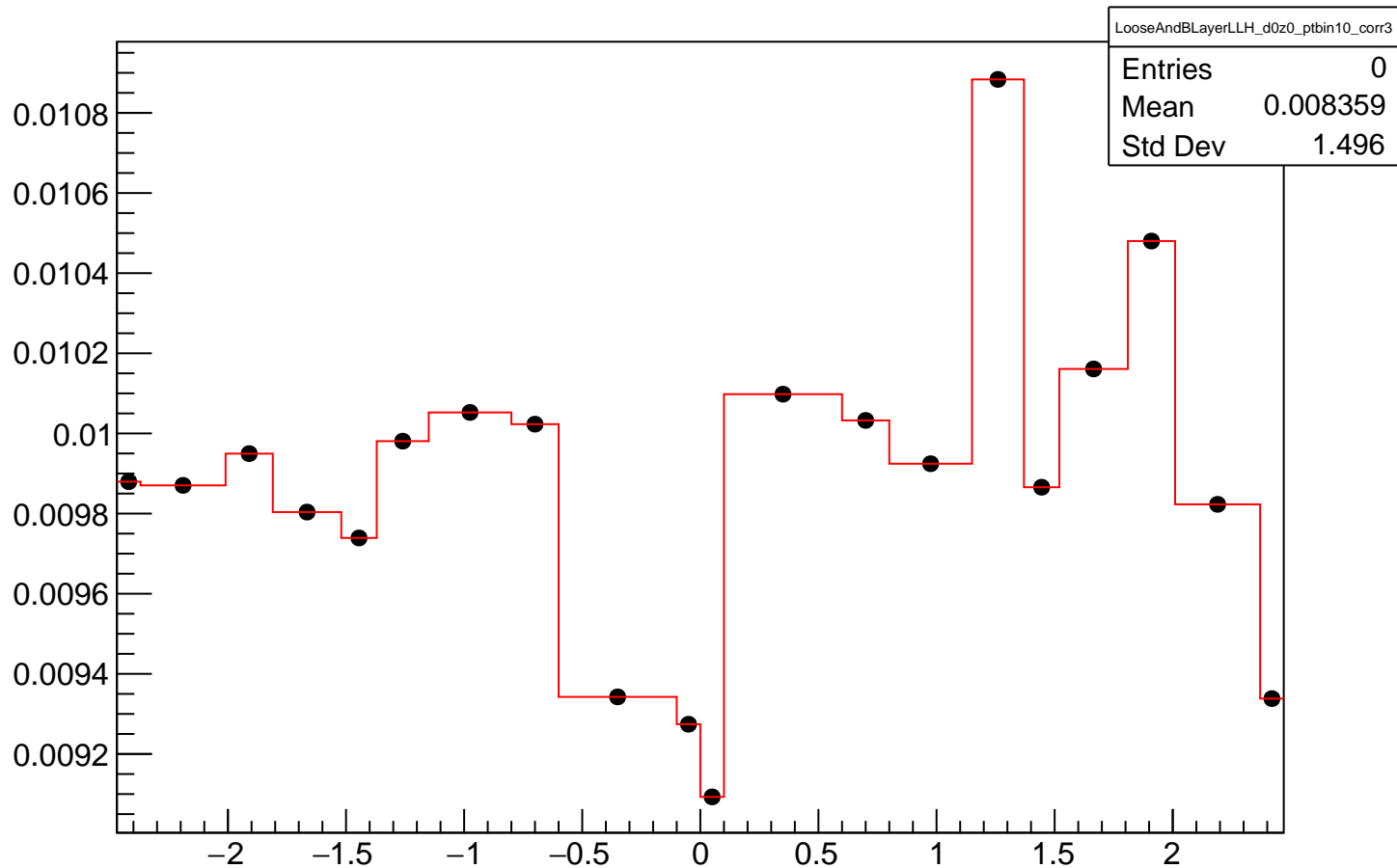
FullSim_LooseLLH_d0z0_corr3



FullSim_LooseLLH_d0z0_corr3



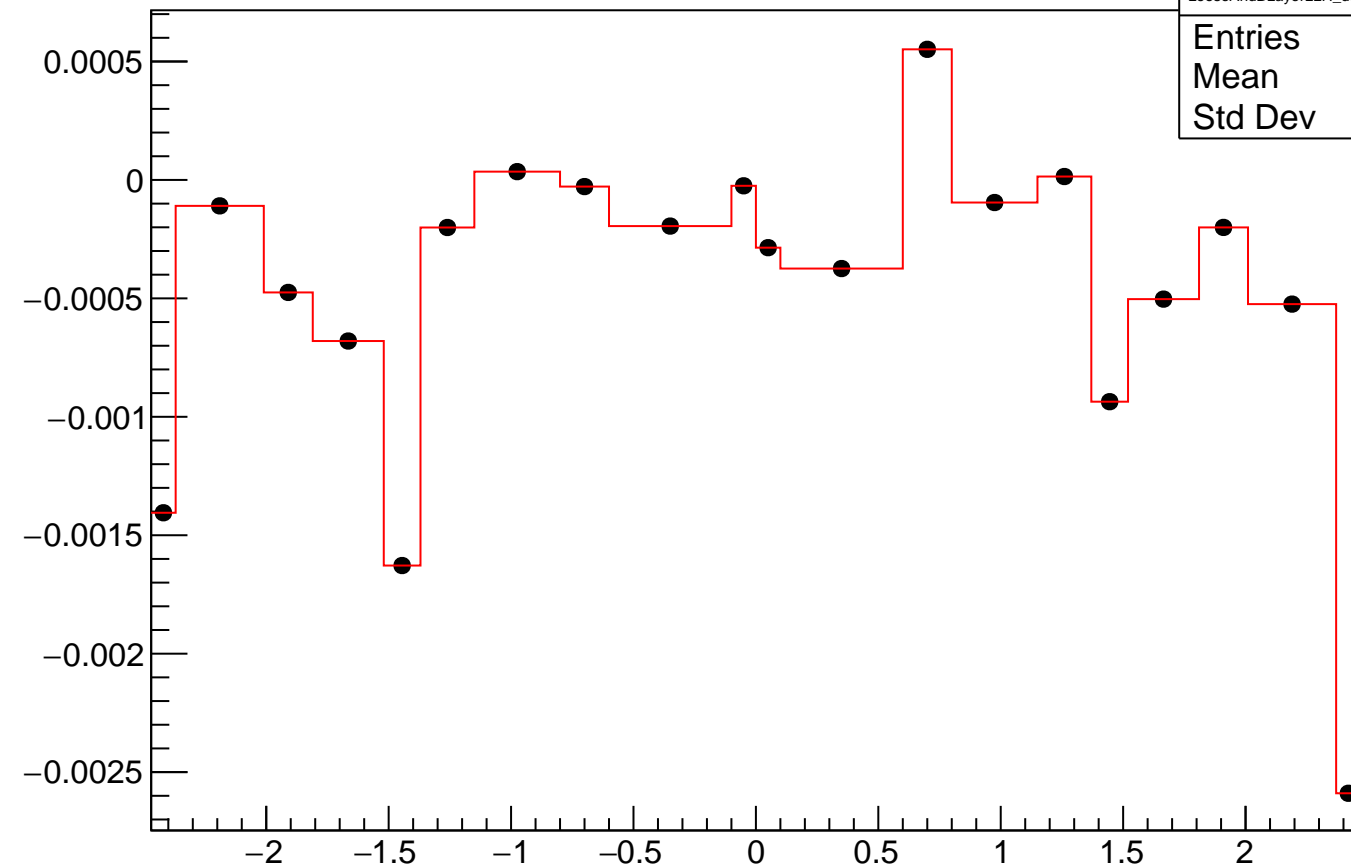
FullSim_LooseLLH_d0z0_corr3



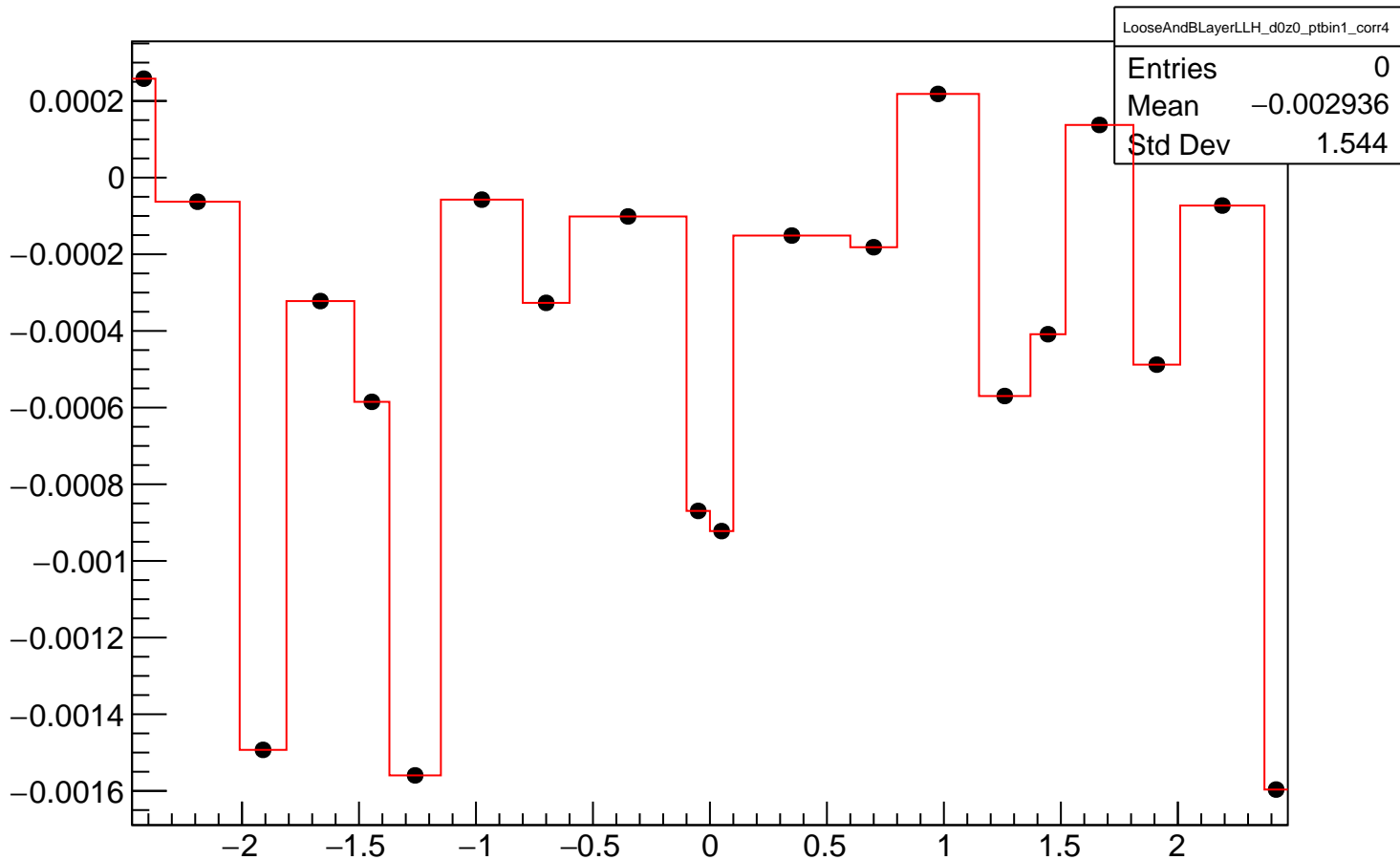
FullSim_LooseLLH_d0z0_corr4

LooseAndBLayerLLH_d0z0_ptbin0_corr4

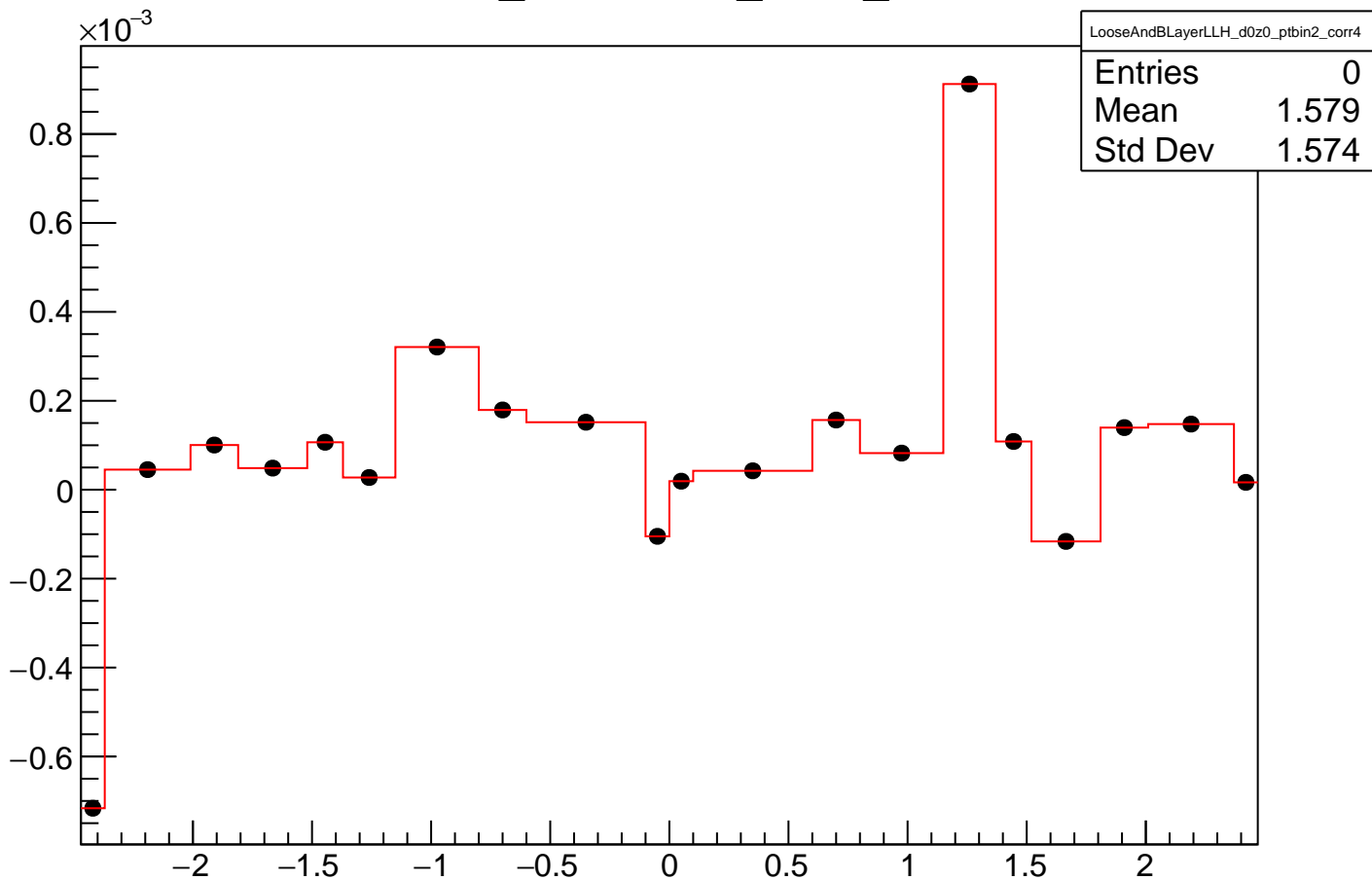
Entries	0
Mean	0.1535
Std Dev	1.97



FullSim_LooseLLH_d0z0_corr4

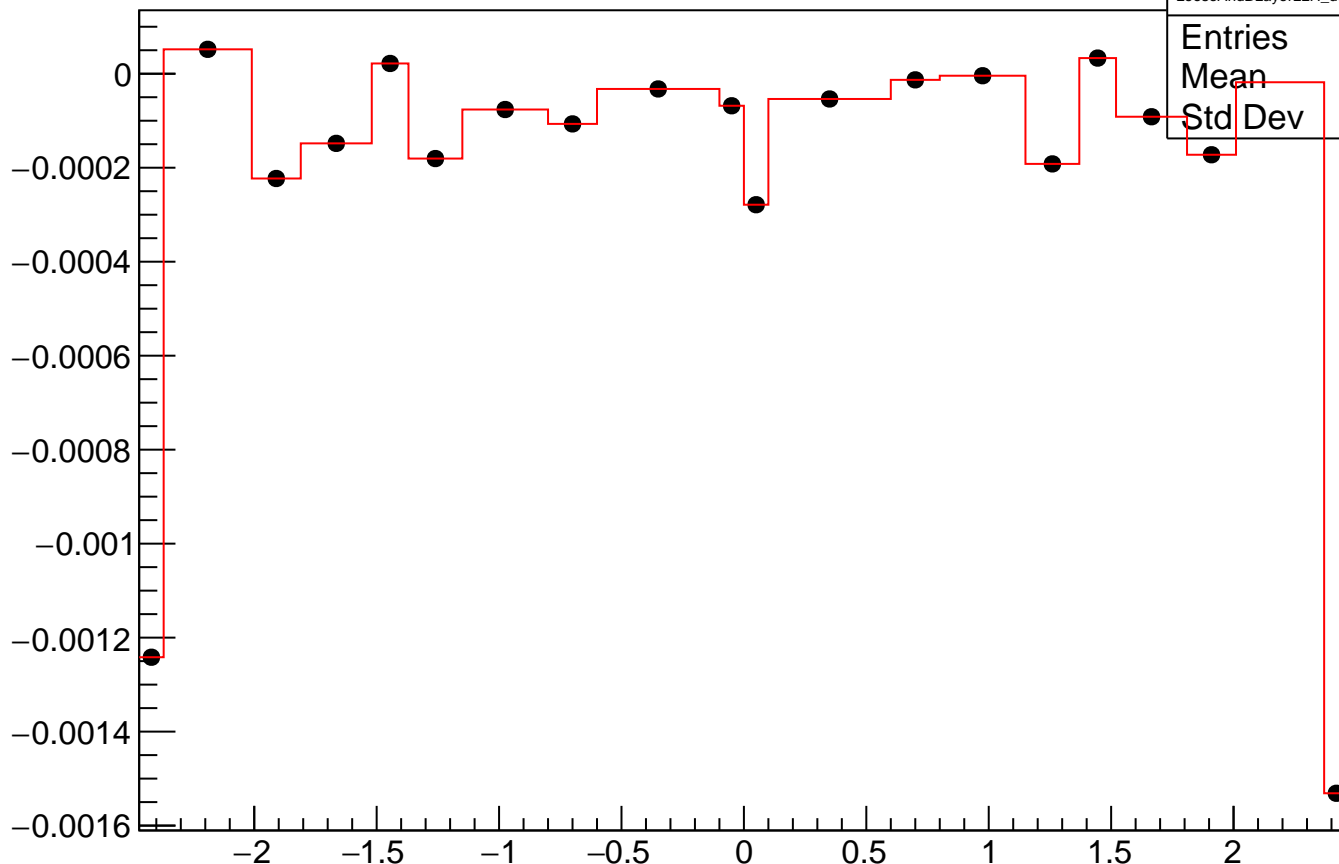


FullSim_LooseLLH_d0z0_corr4



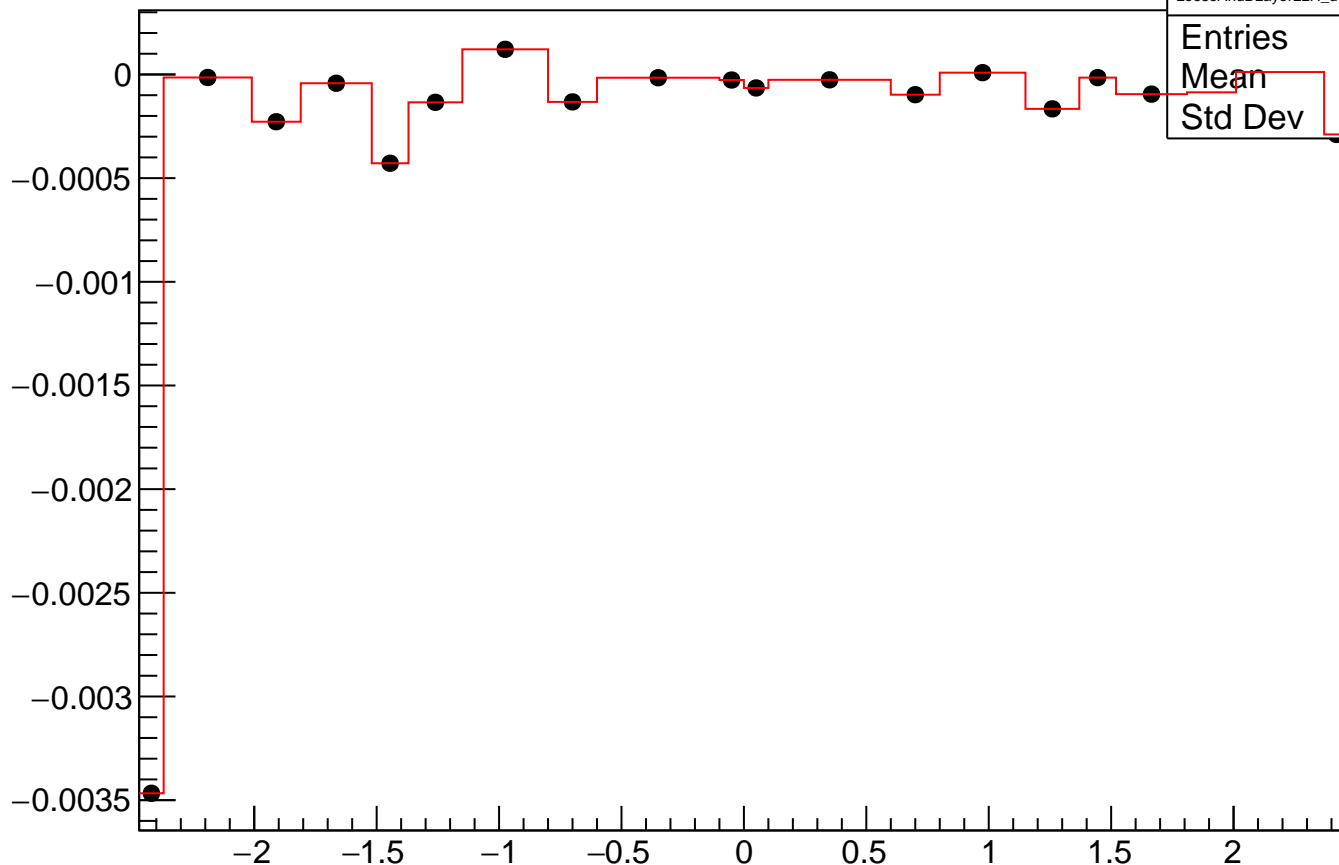
FullSim_LooseLLH_d0z0_corr4

LooseAndBLayerLLH_d0z0_ptbin3_corr4	
Entries	0
Mean	0.1256
Std Dev	2.082

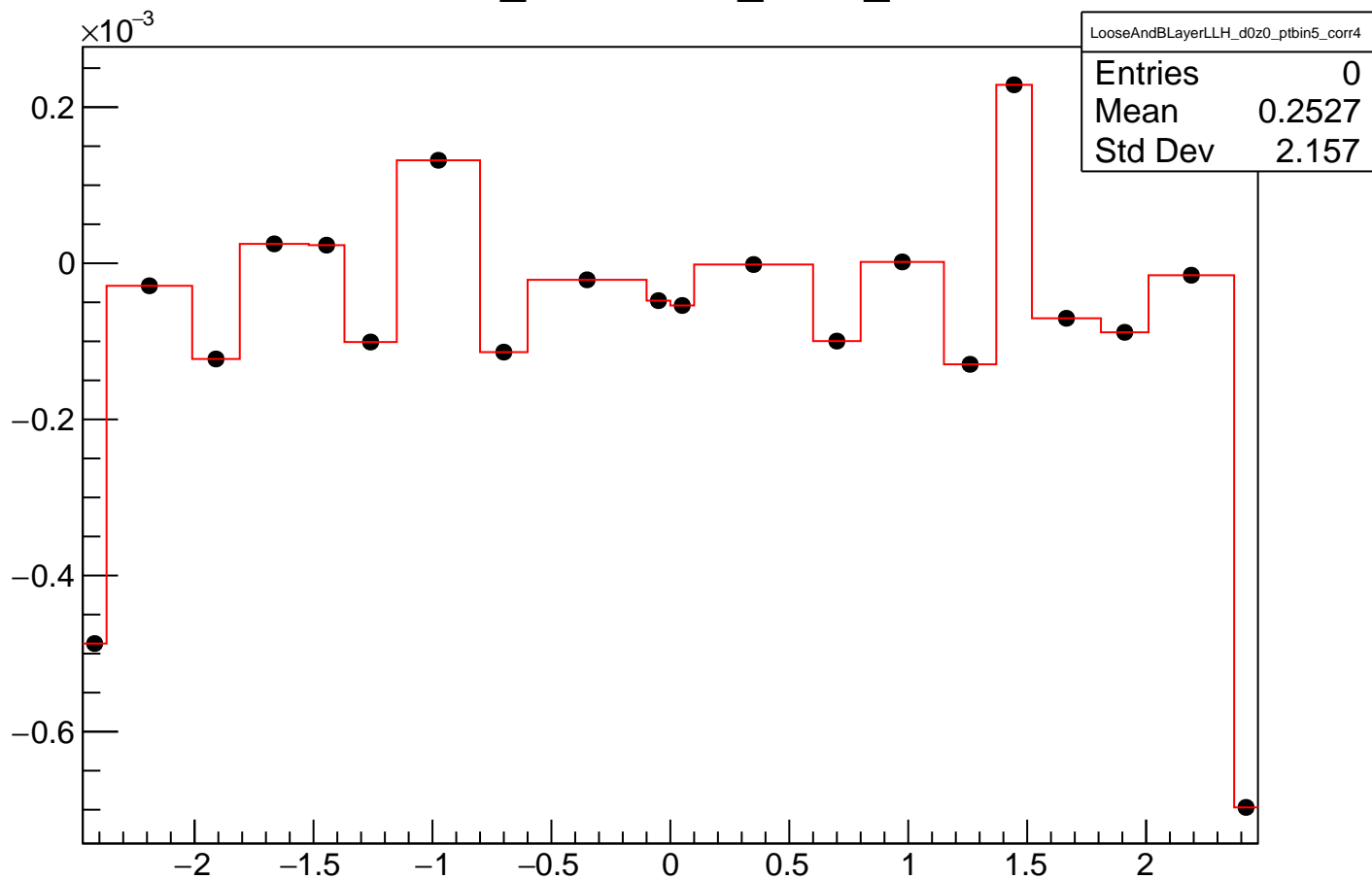


FullSim_LooseLLH_d0z0_corr4

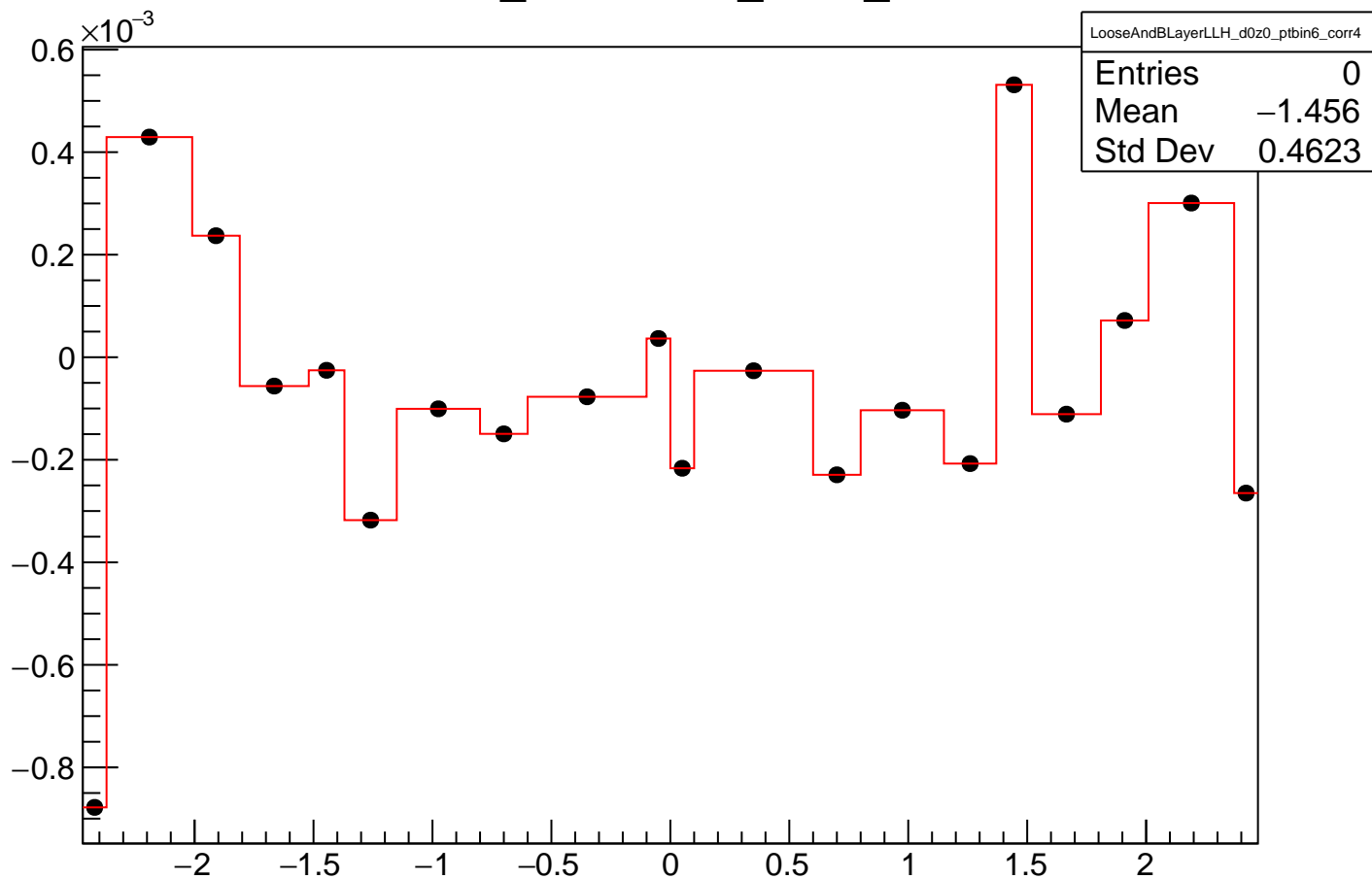
LooseAndBLayerLLH_d0z0_ptbin4_corr4	
Entries	0
Mean	-1.619
Std Dev	1.478



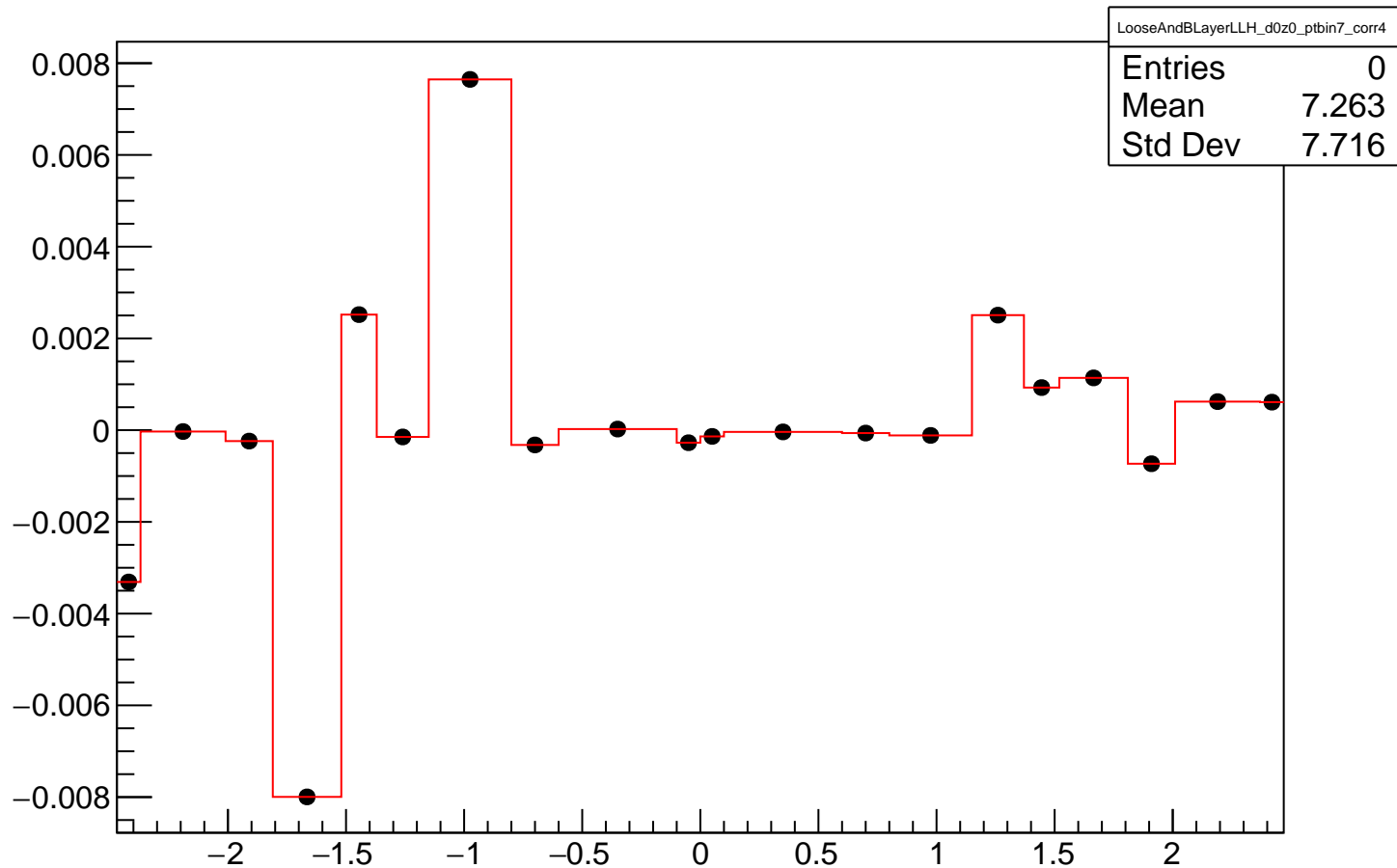
FullSim_LooseLLH_d0z0_corr4



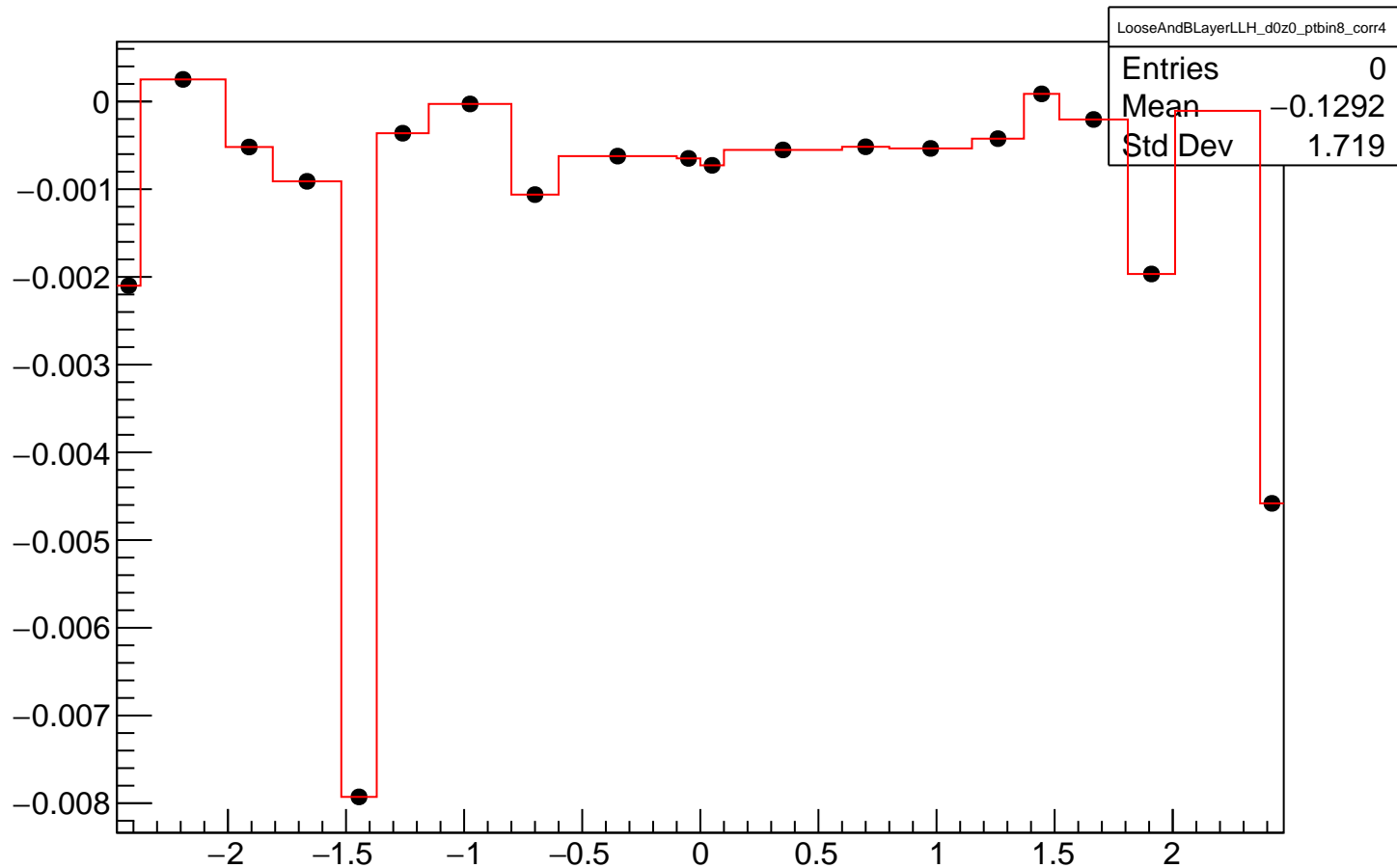
FullSim_LooseLLH_d0z0_corr4



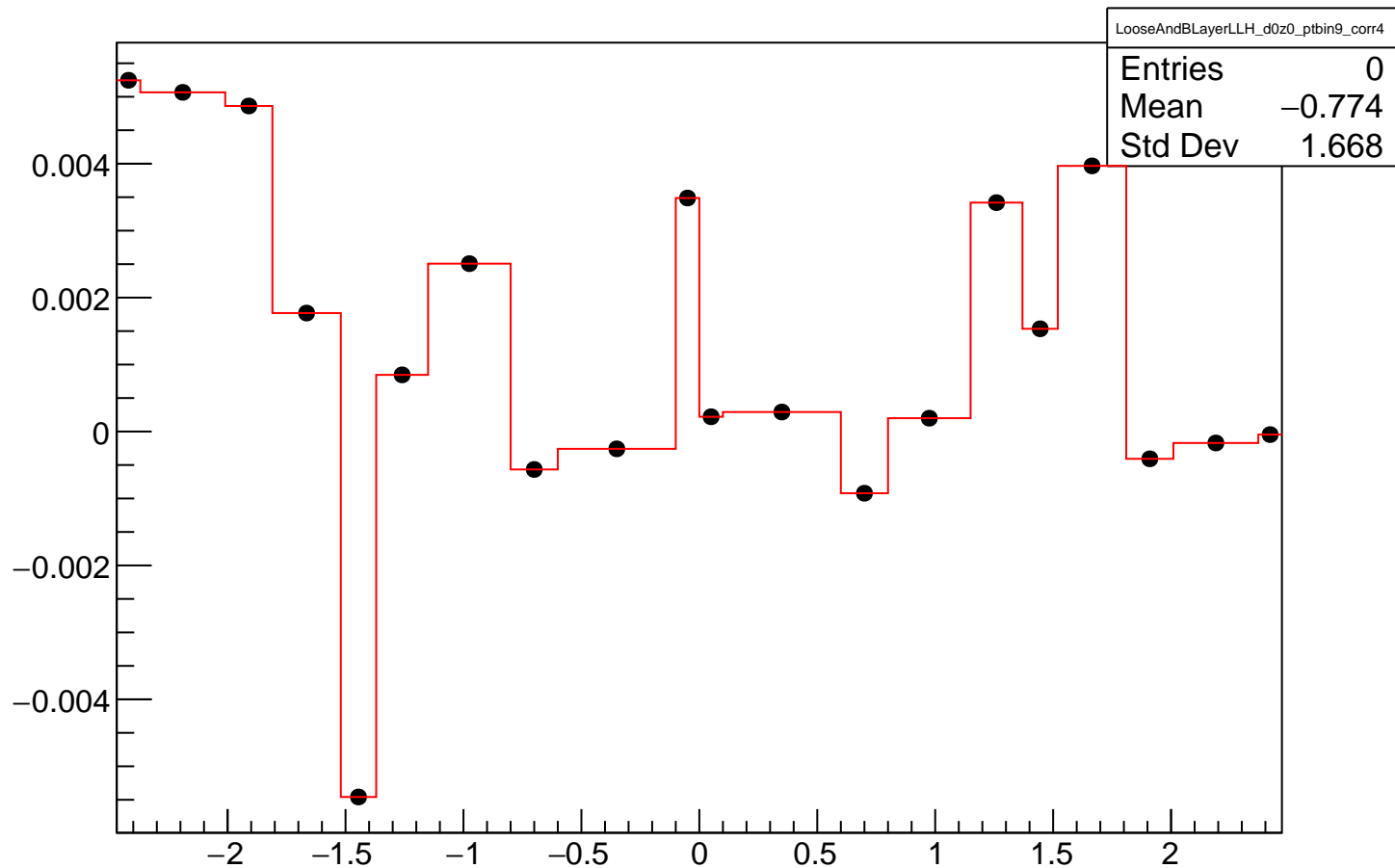
FullSim_LooseLLH_d0z0_corr4



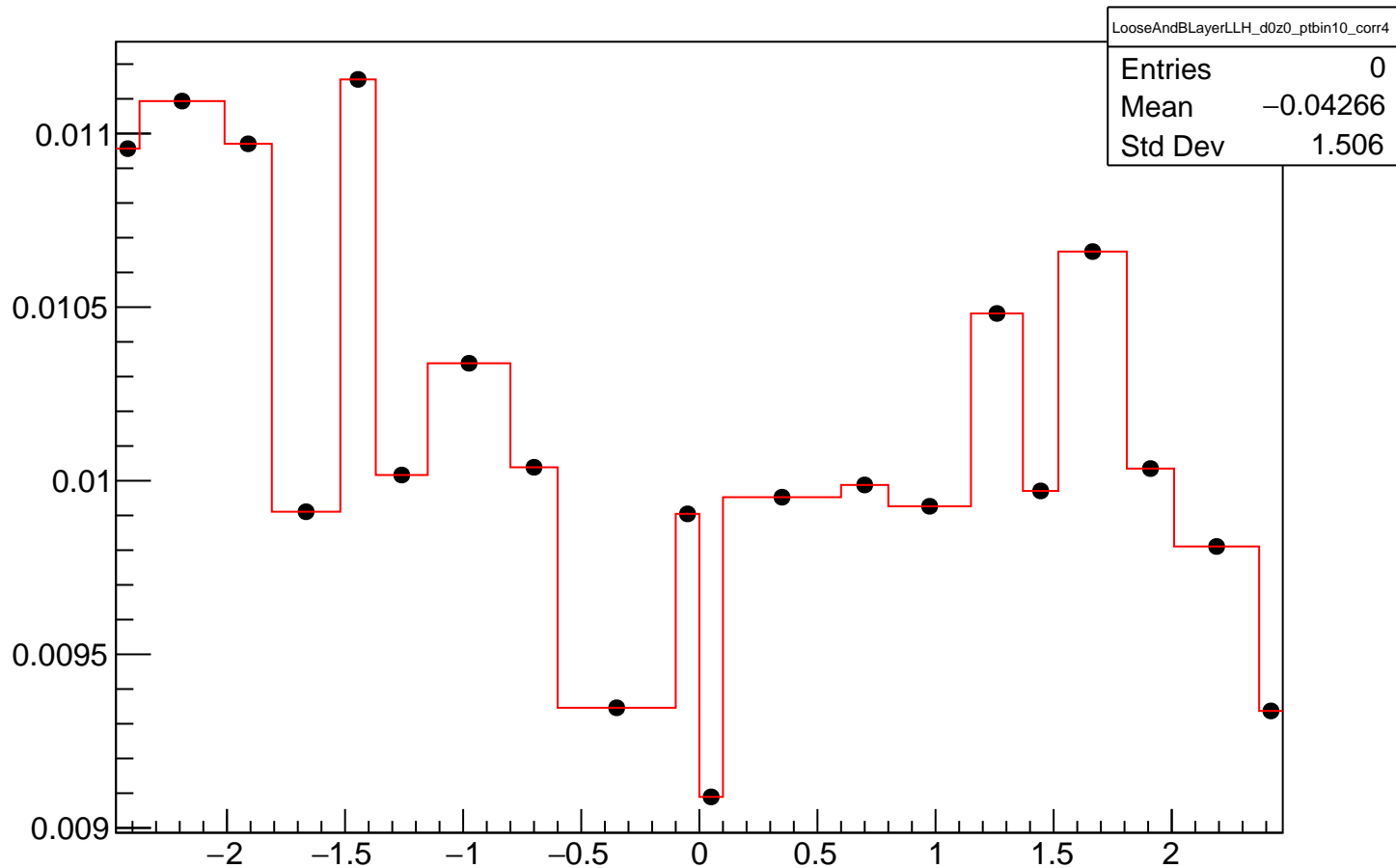
FullSim_LooseLLH_d0z0_corr4



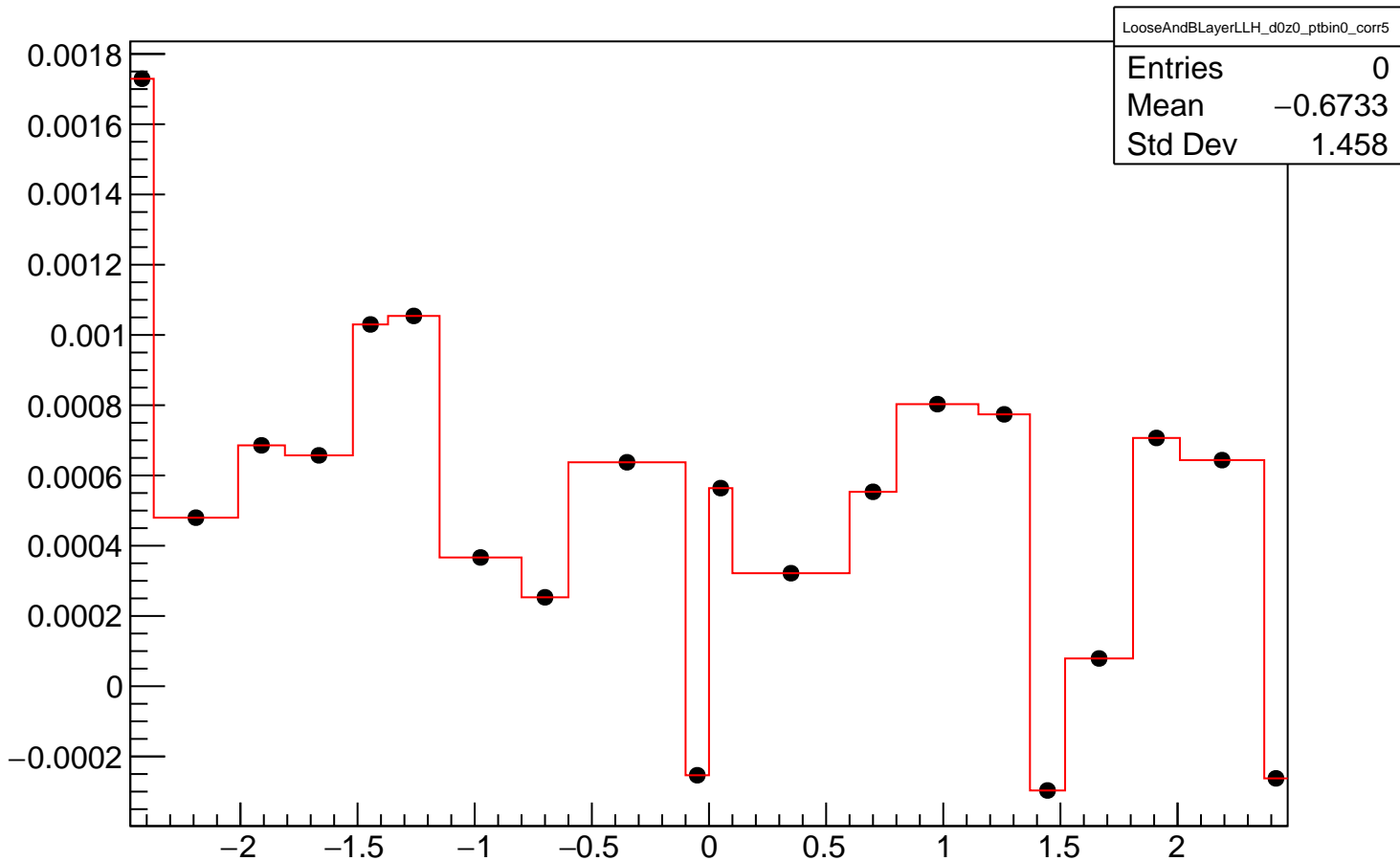
FullSim_LooseLLH_d0z0_corr4



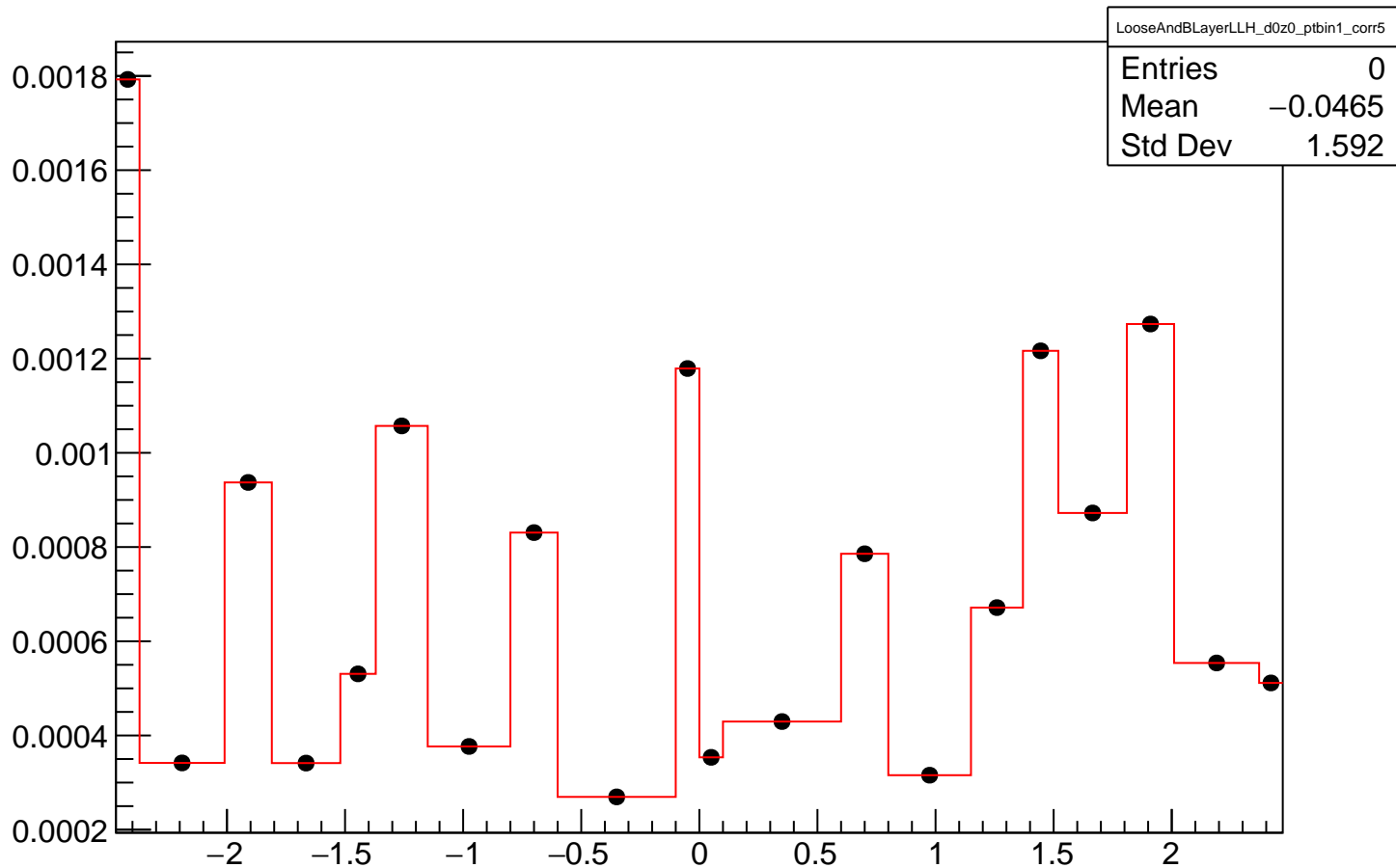
FullSim_LooseLLH_d0z0_corr4



FullSim_LooseLLH_d0z0_corr5

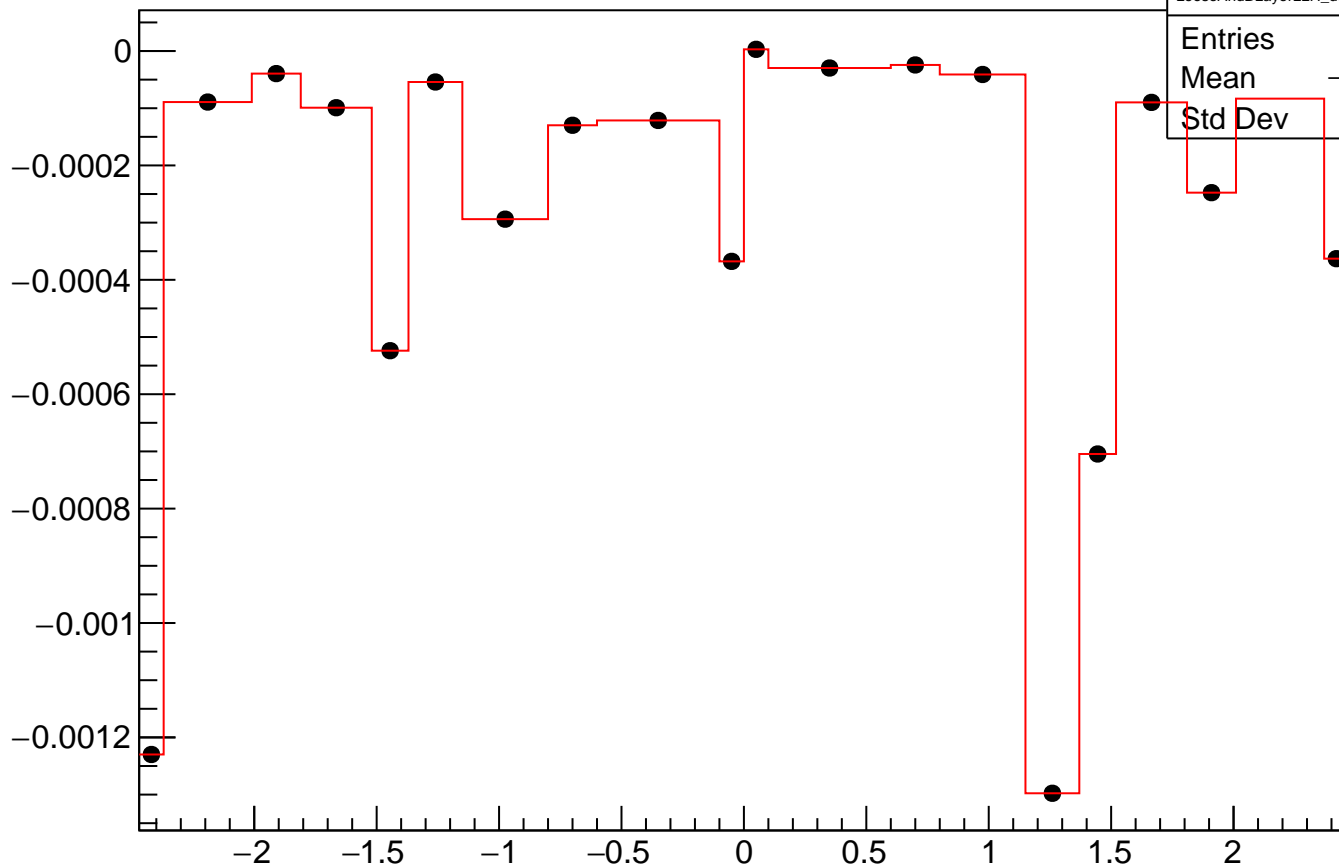


FullSim_LooseLLH_d0z0_corr5

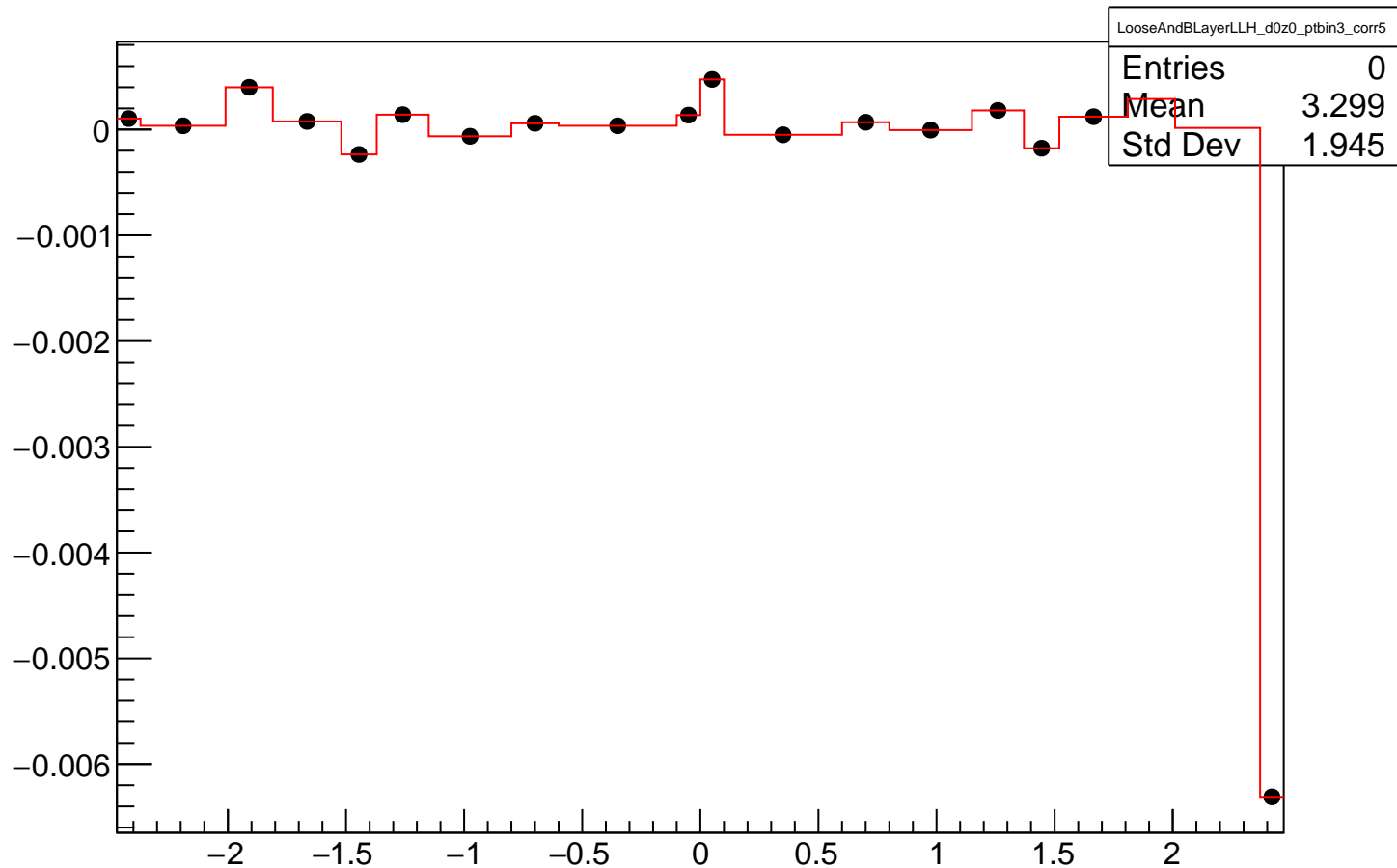


FullSim_LooseLLH_d0z0_corr5

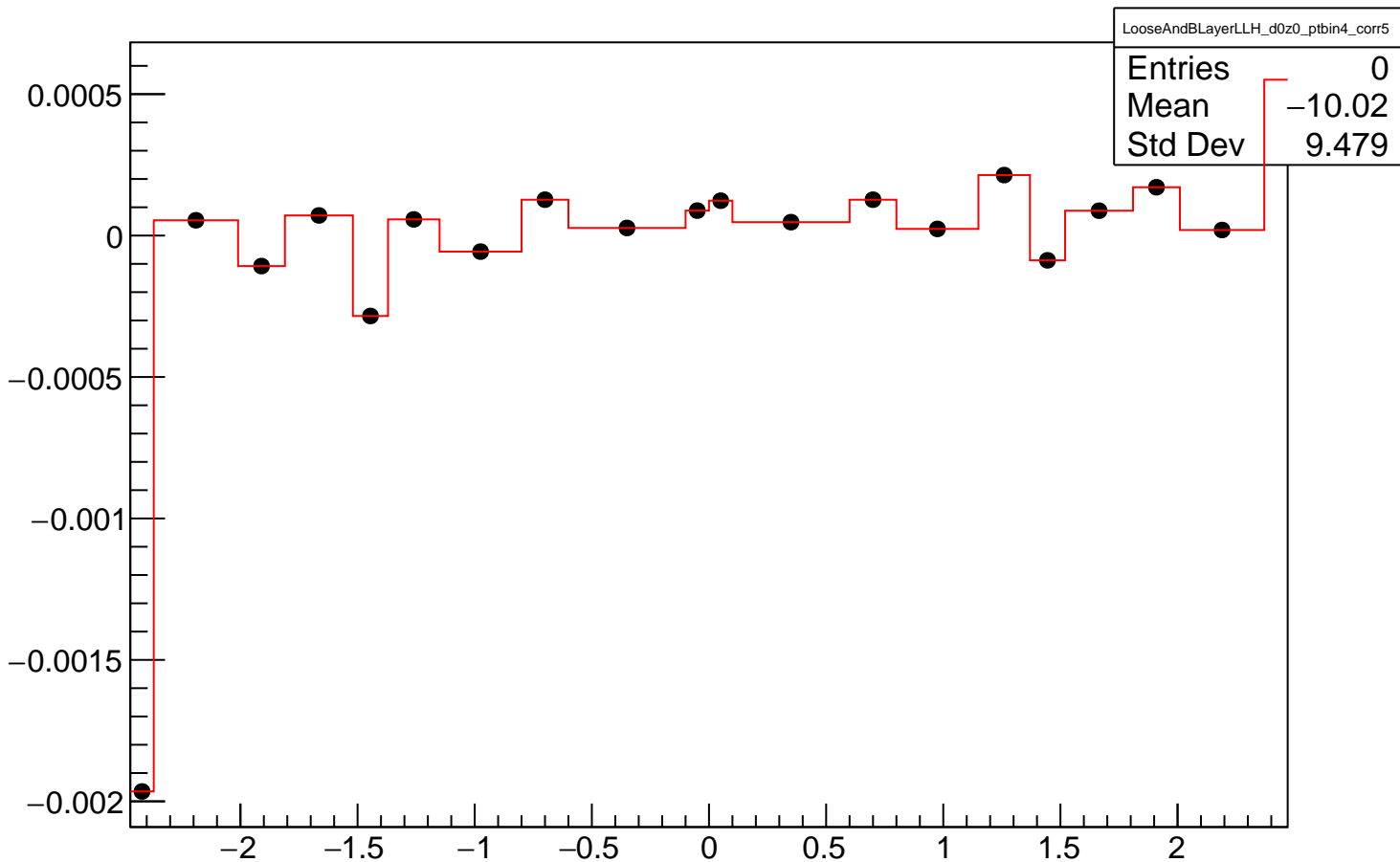
LooseAndBLayerLLH_d0z0_ptbin2_corr5	
Entries	0
Mean	-0.04669
Std Dev	1.7



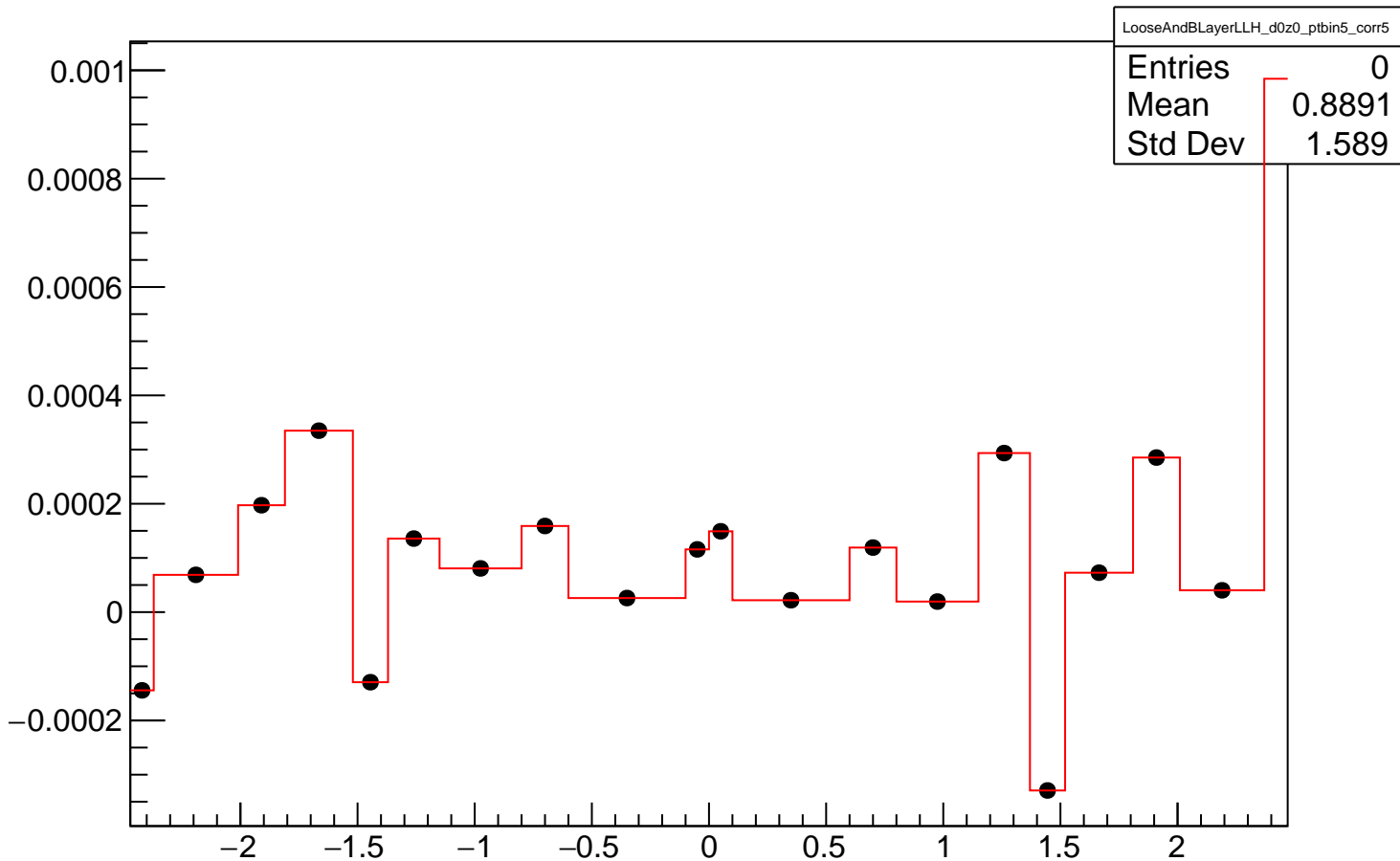
FullSim_LooseLLH_d0z0_corr5



FullSim_LooseLLH_d0z0_corr5



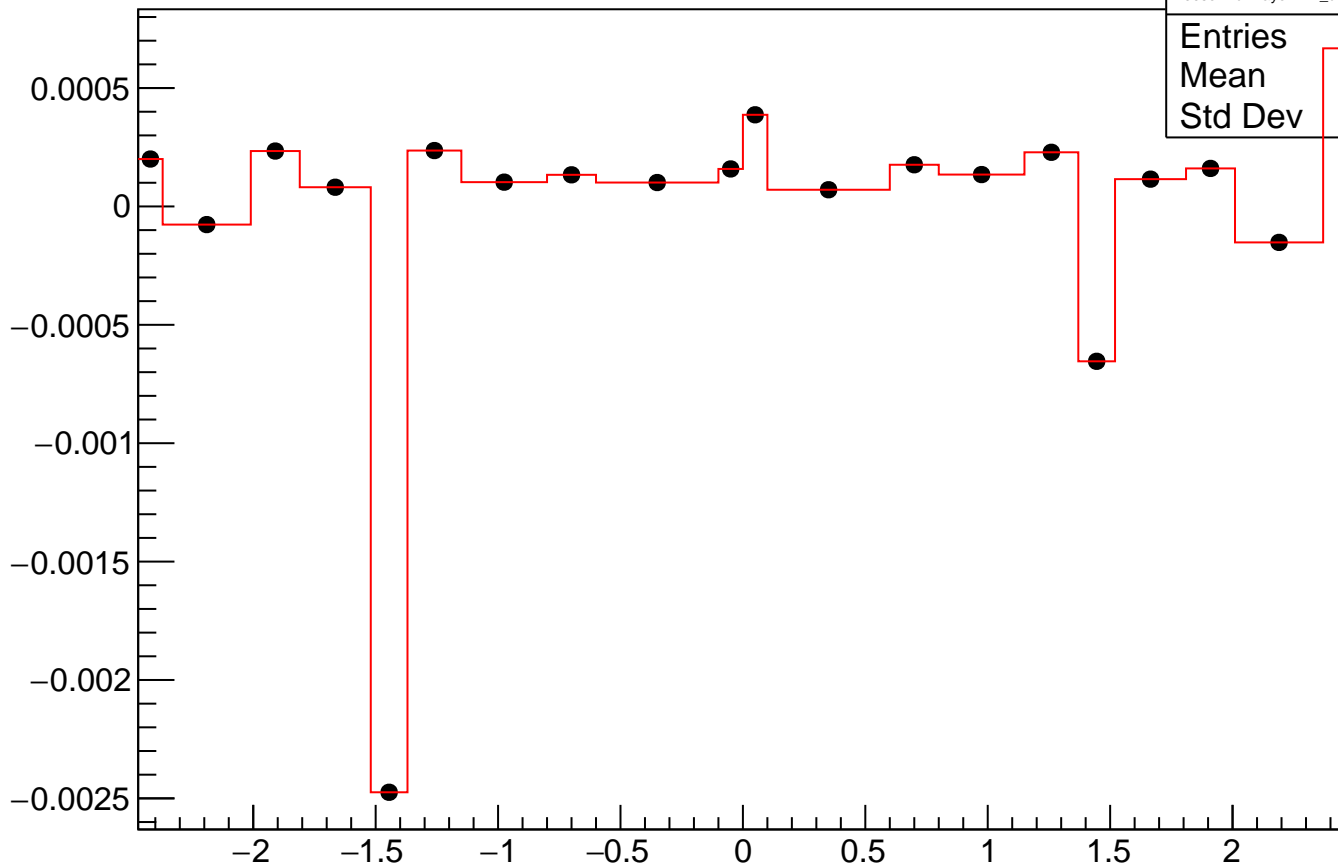
FullSim_LooseLLH_d0z0_corr5



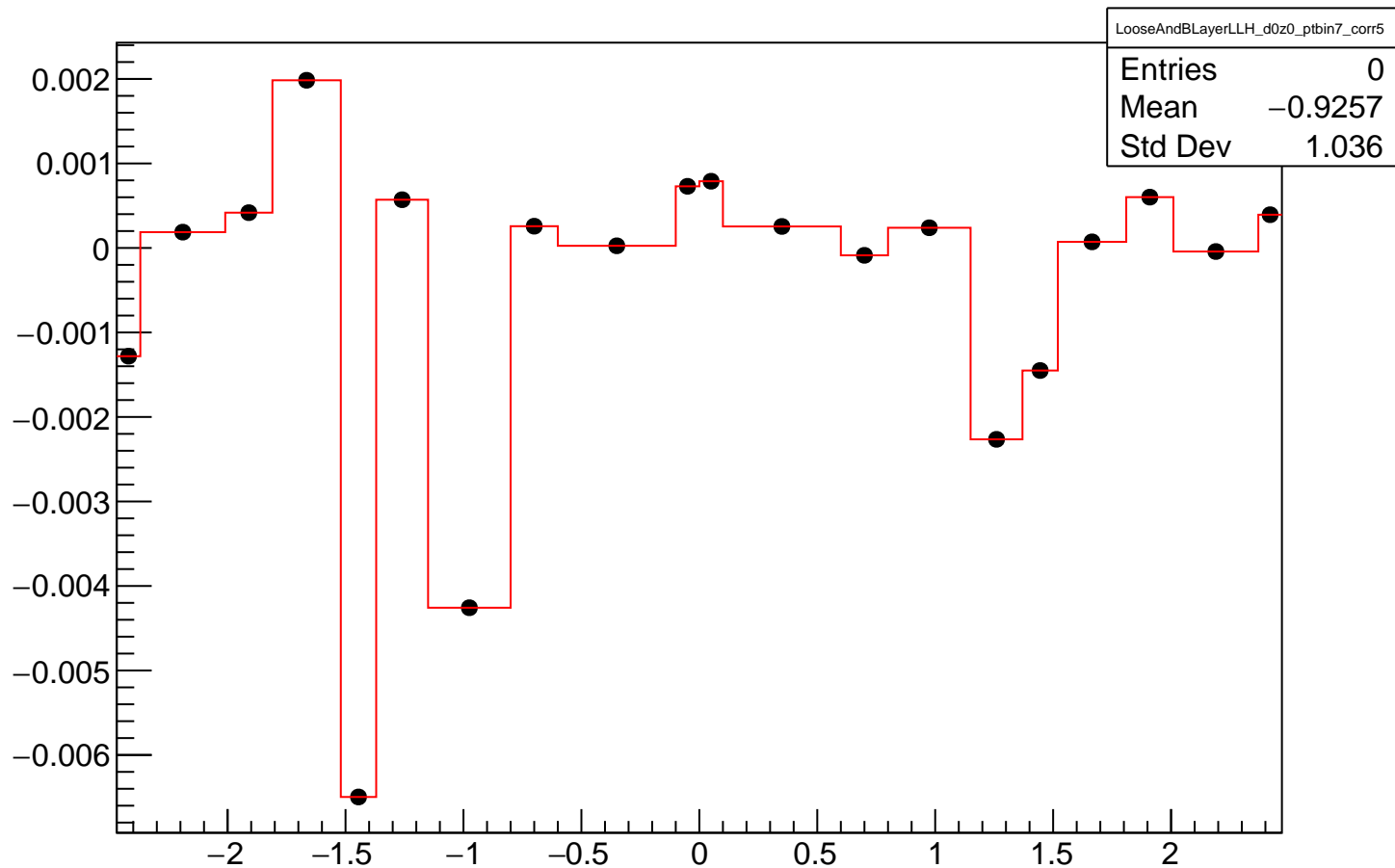
FullSim_LooseLLH_d0z0_corr5

LooseAndBLayerLLH_d0z0_ptbin6_corr5

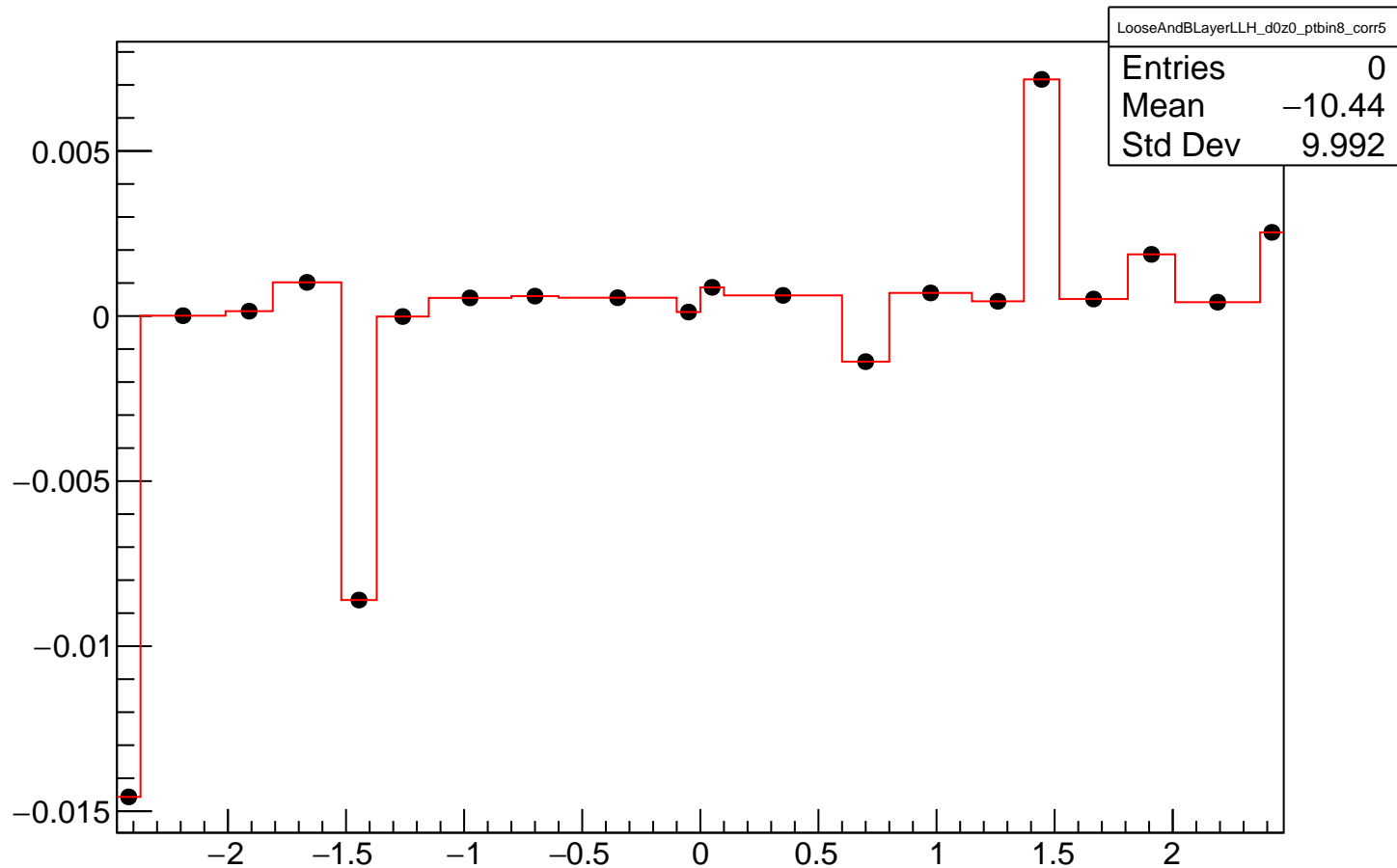
Entries	0
Mean	-20.99
Std Dev	21.07



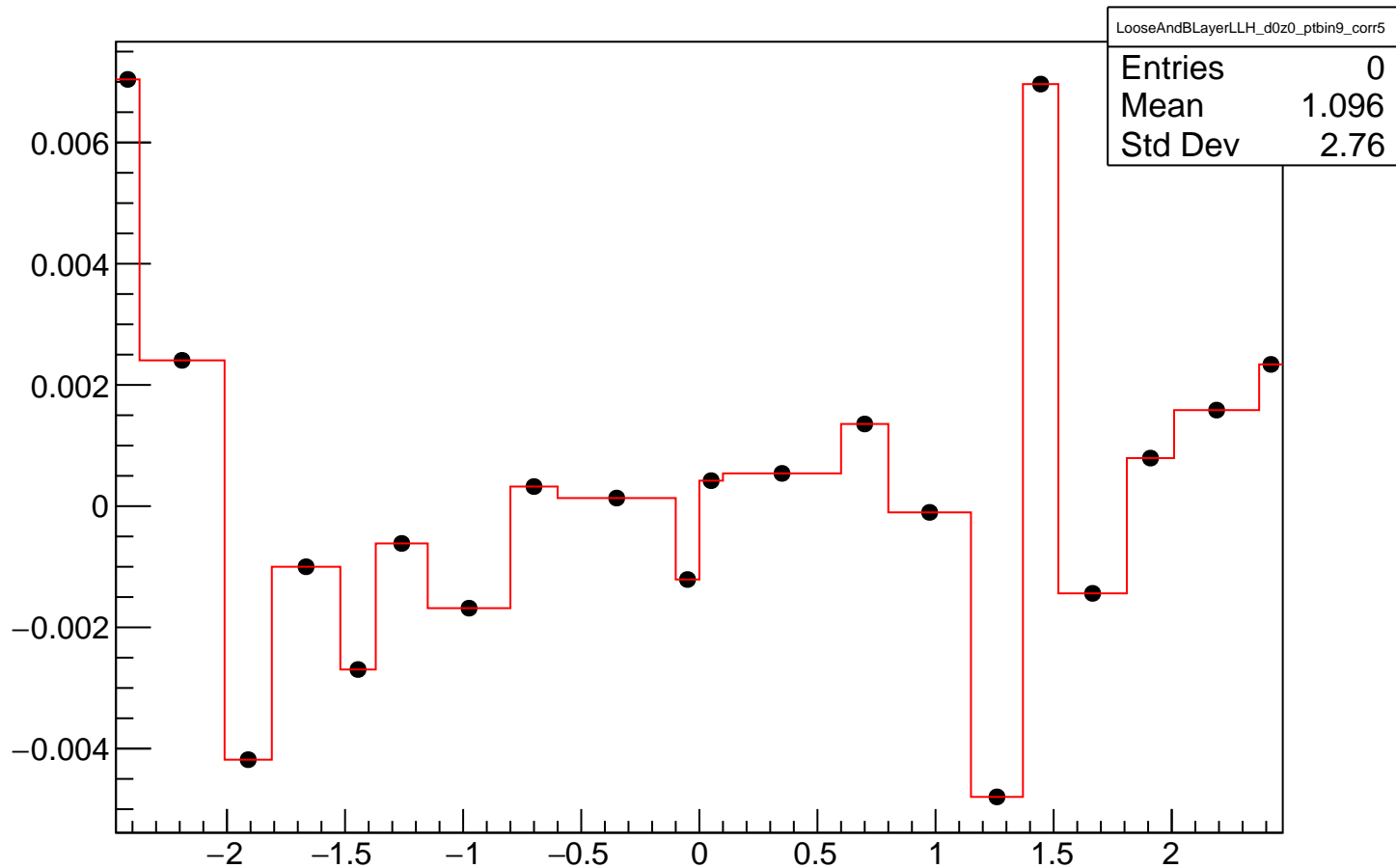
FullSim_LooseLLH_d0z0_corr5



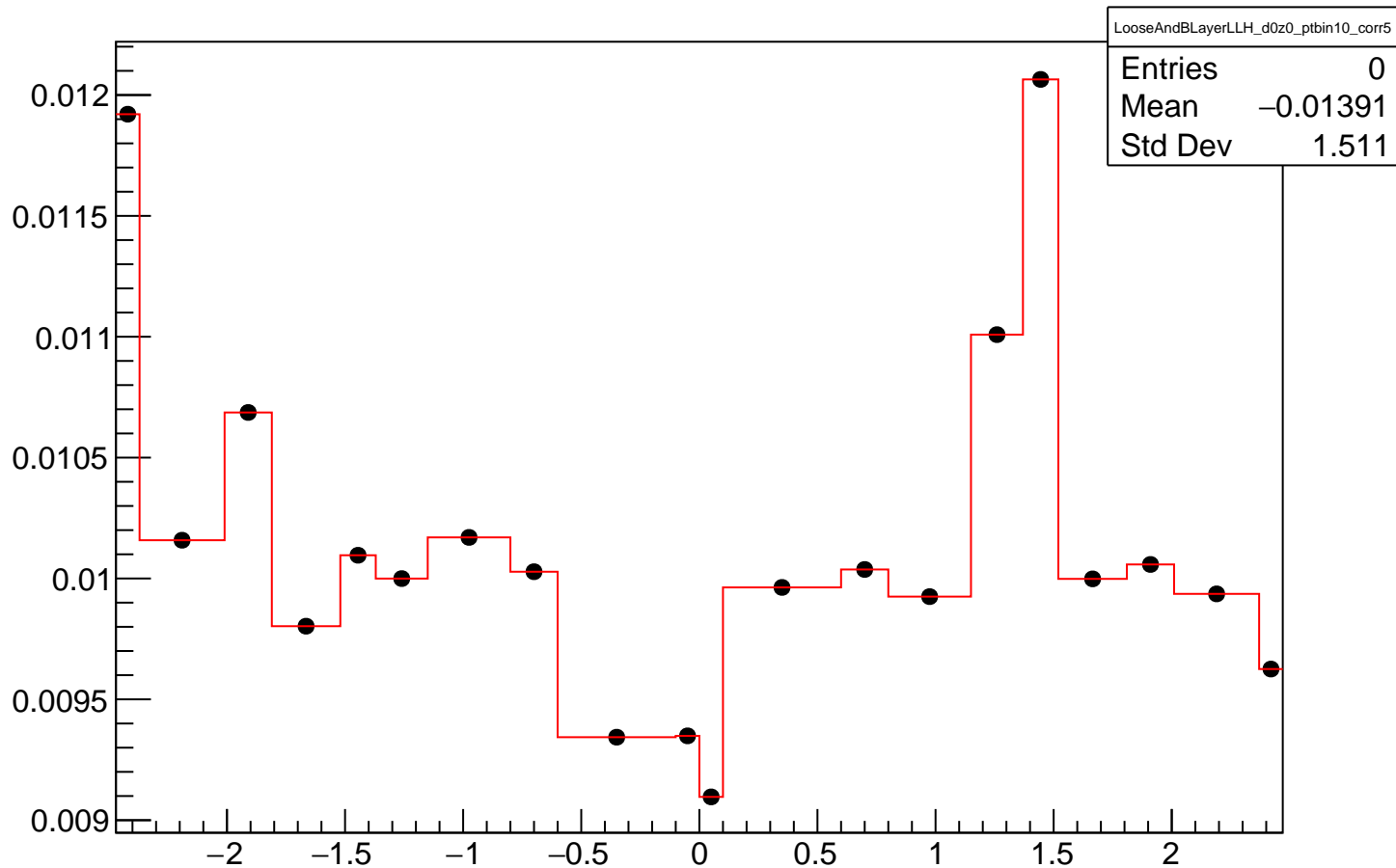
FullSim_LooseLLH_d0z0_corr5



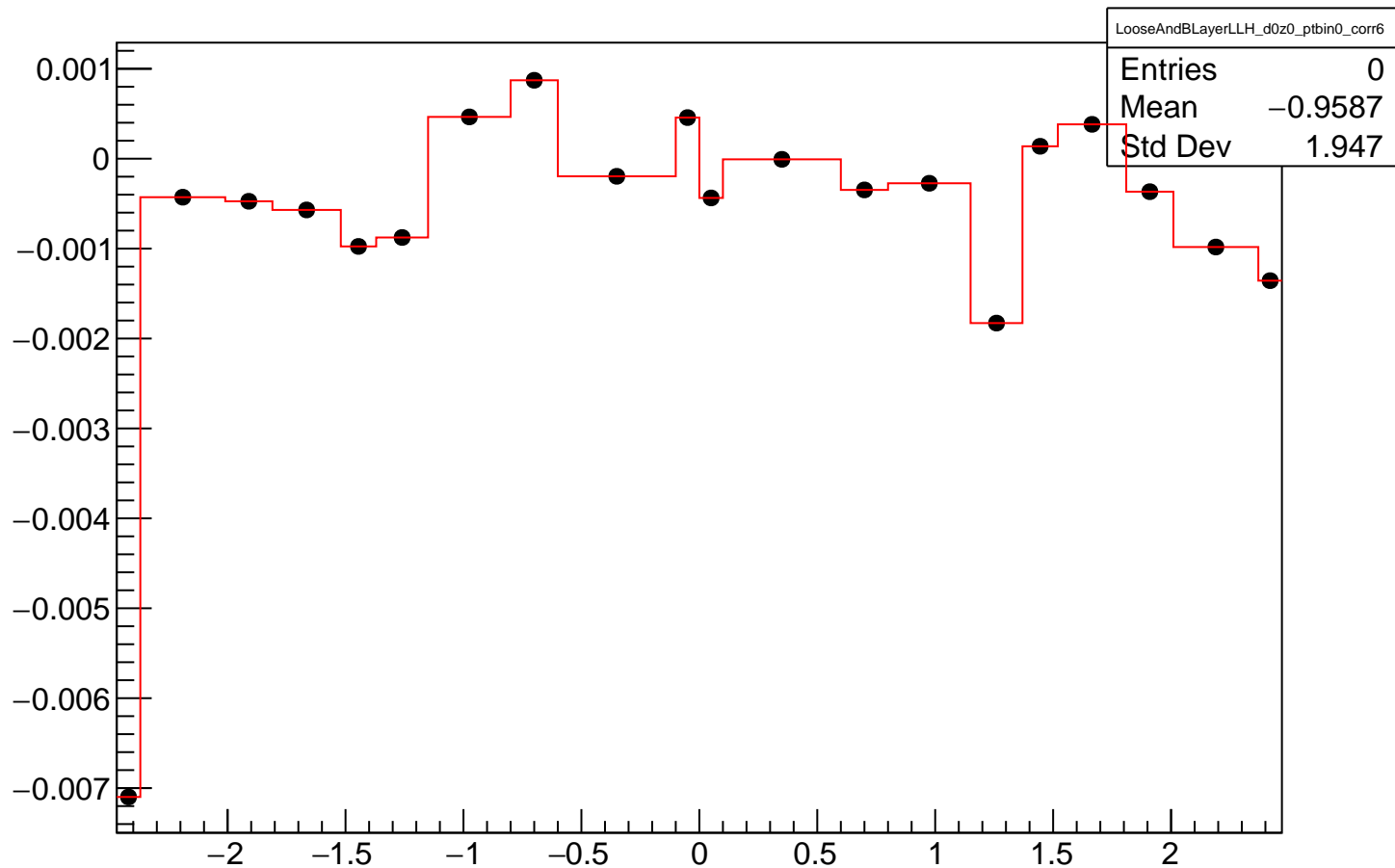
FullSim_LooseLLH_d0z0_corr5



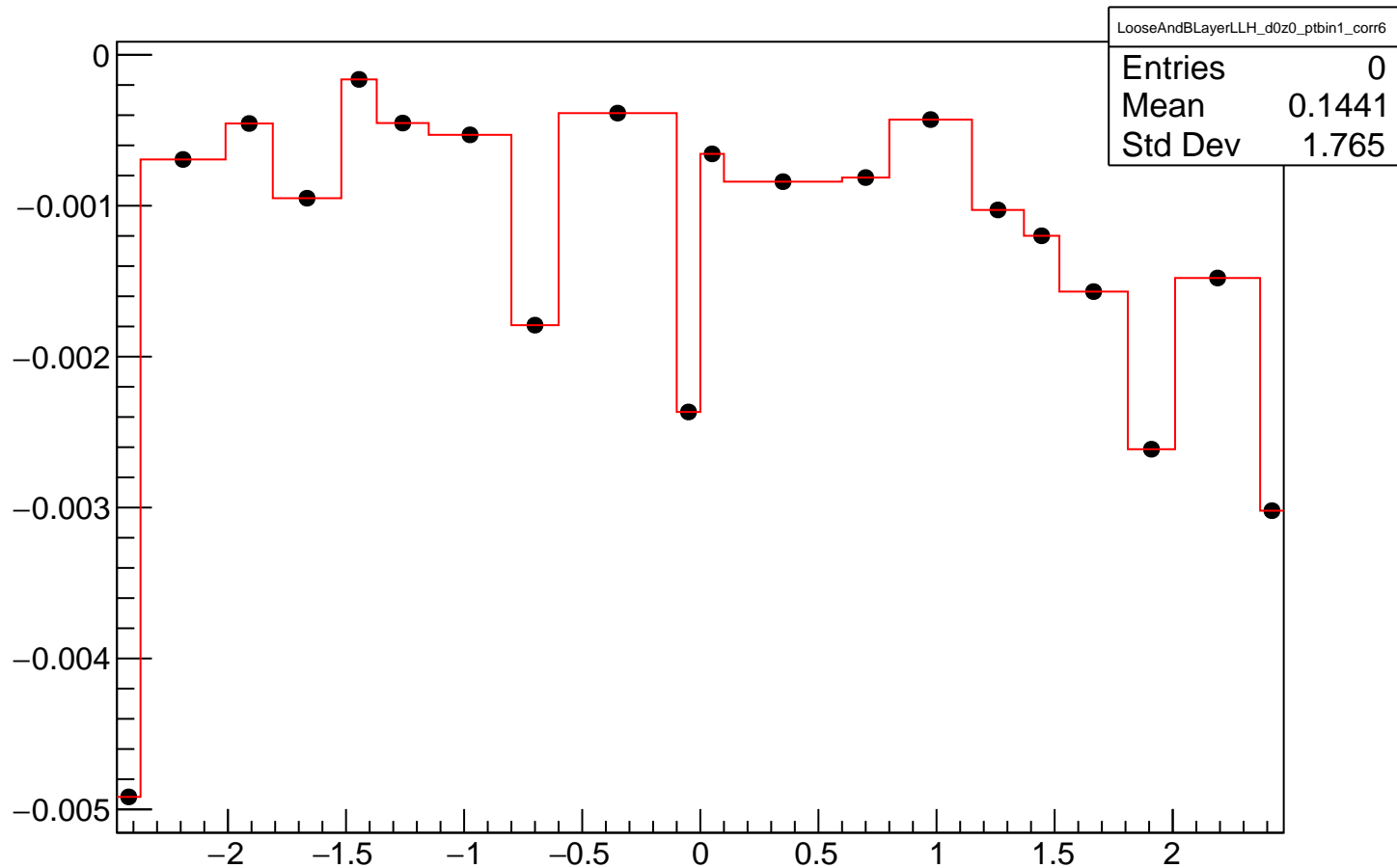
FullSim_LooseLLH_d0z0_corr5



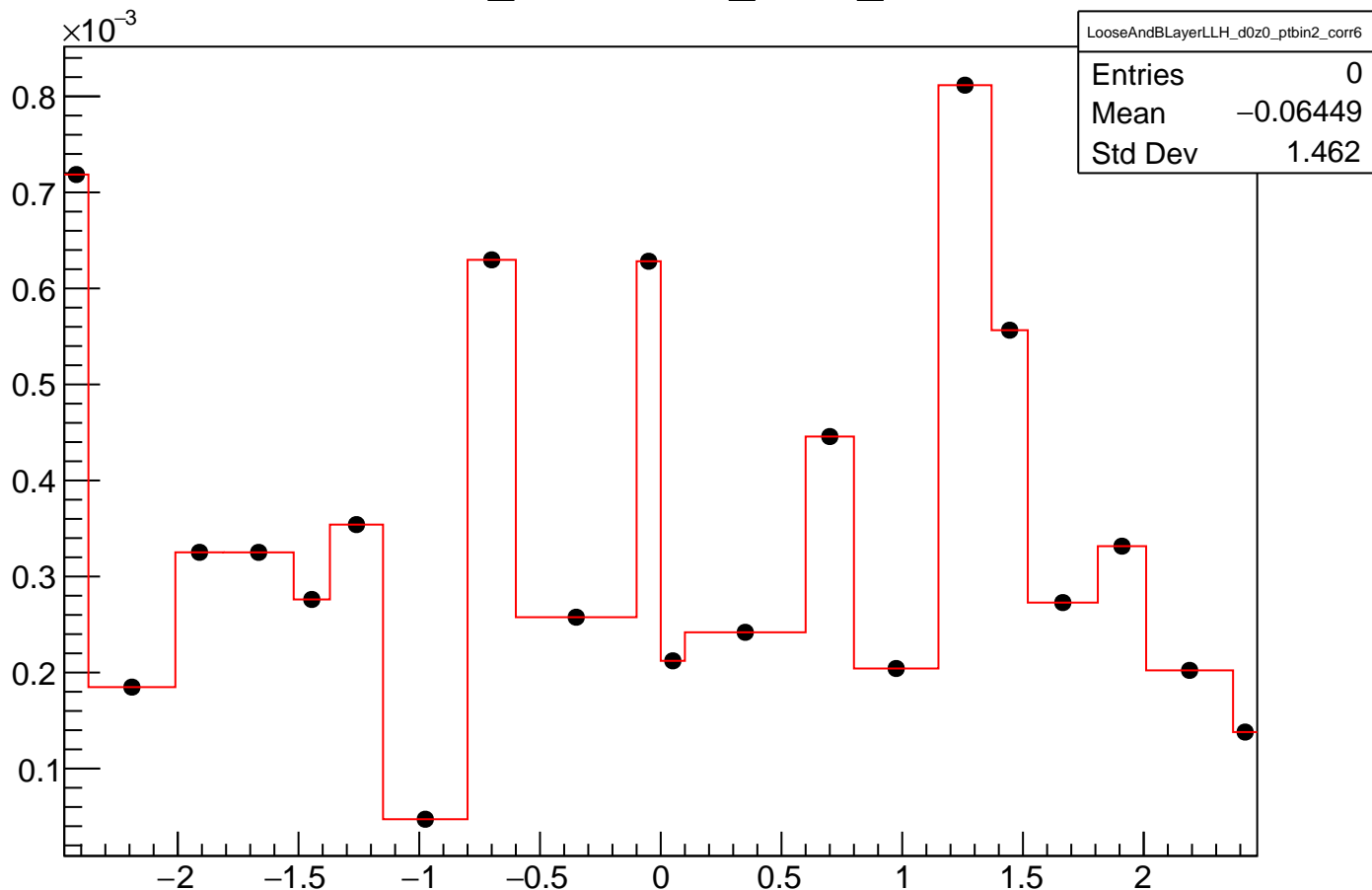
FullSim_LooseLLH_d0z0_corr6



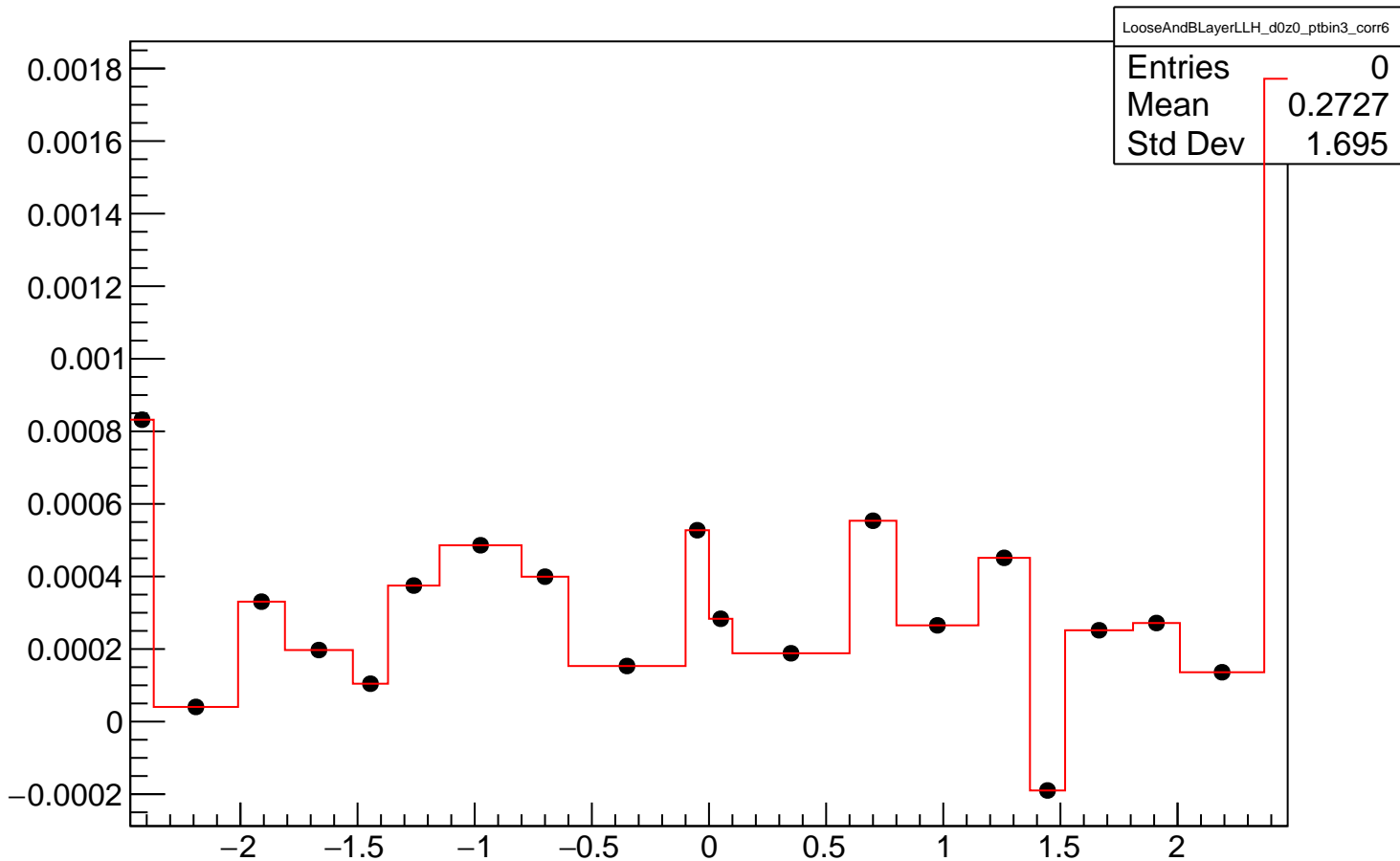
FullSim_LooseLLH_d0z0_corr6



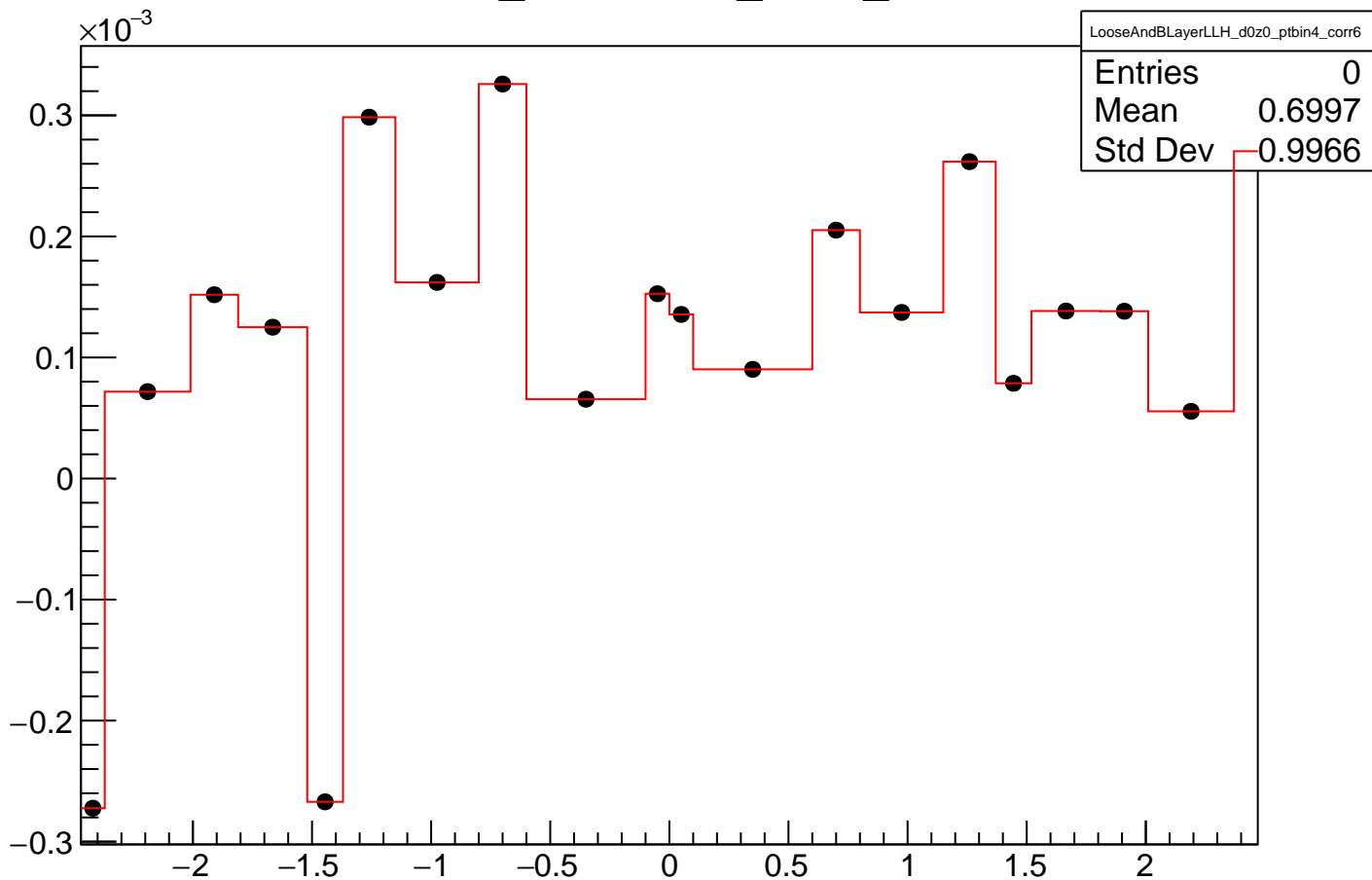
FullSim_LooseLLH_d0z0_corr6



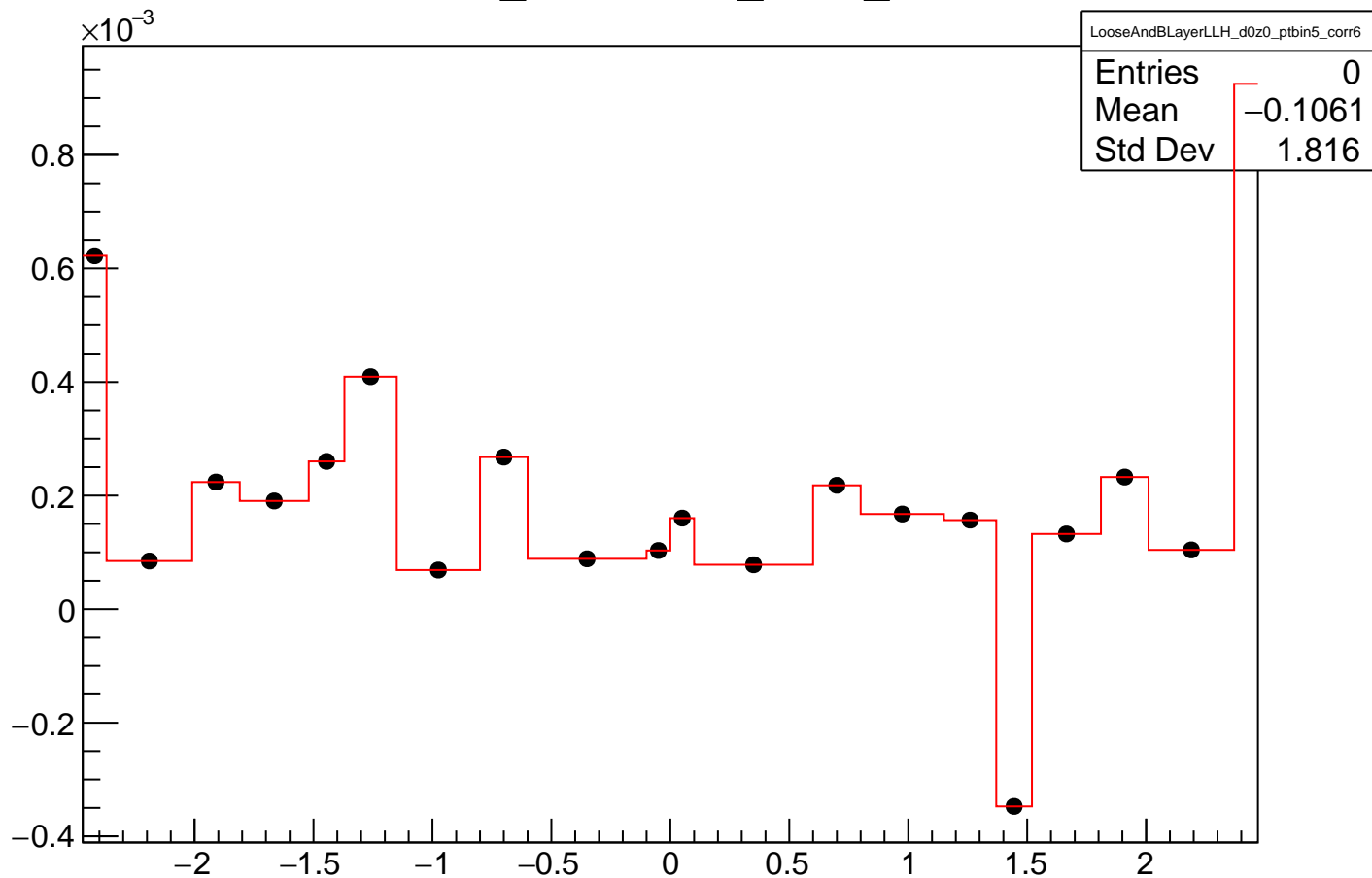
FullSim_LooseLLH_d0z0_corr6



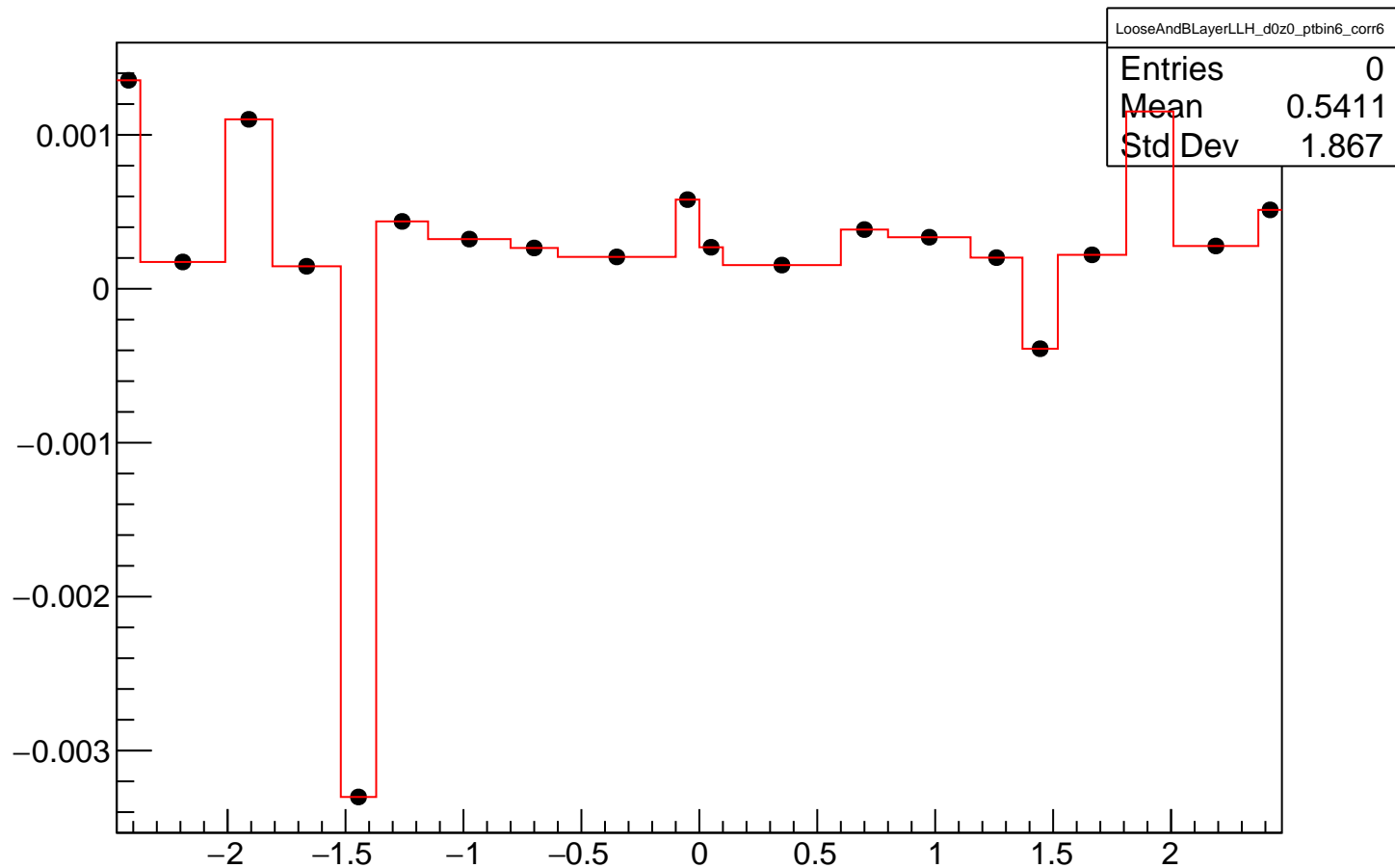
FullSim_LooseLLH_d0z0_corr6



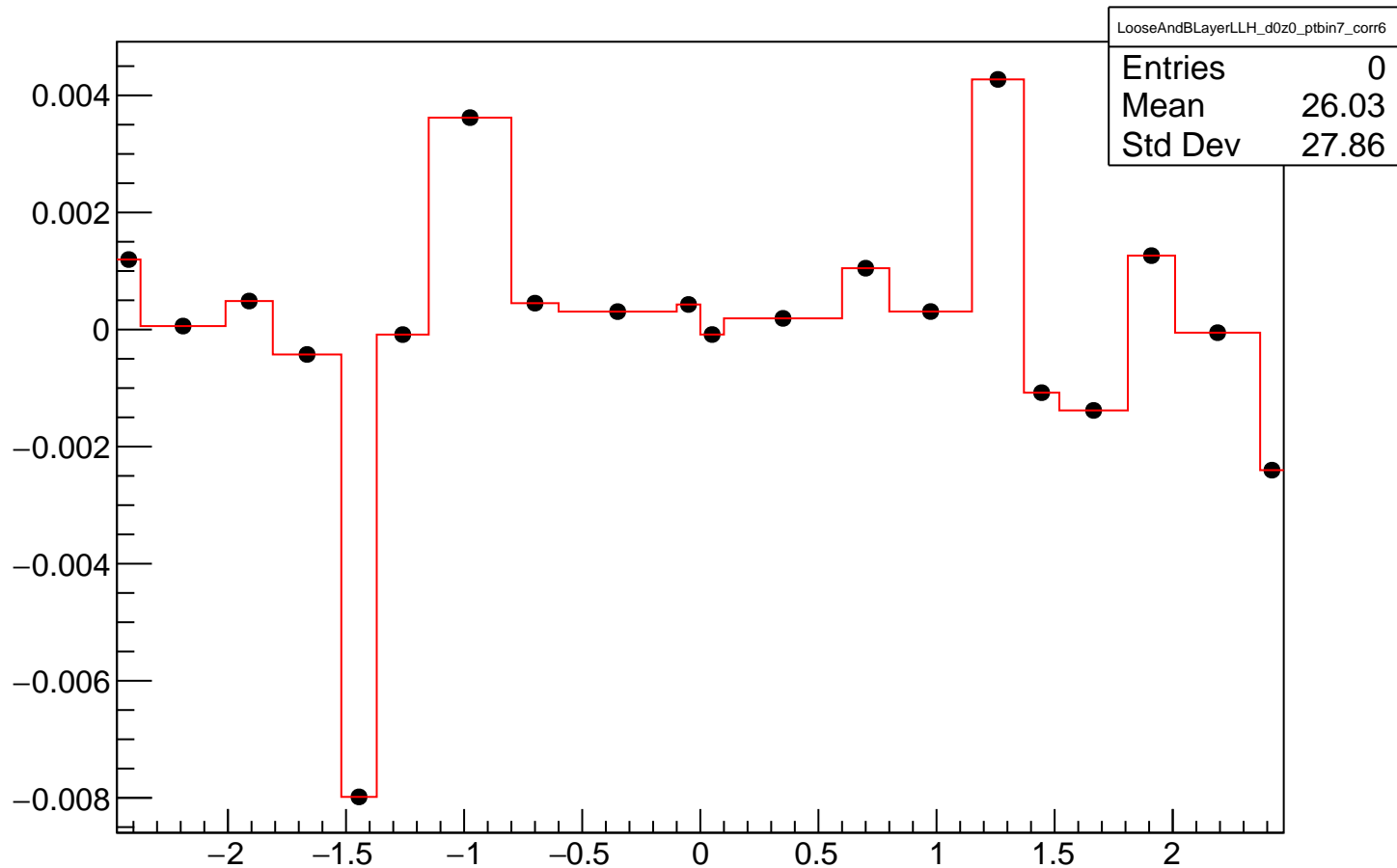
FullSim_LooseLLH_d0z0_corr6



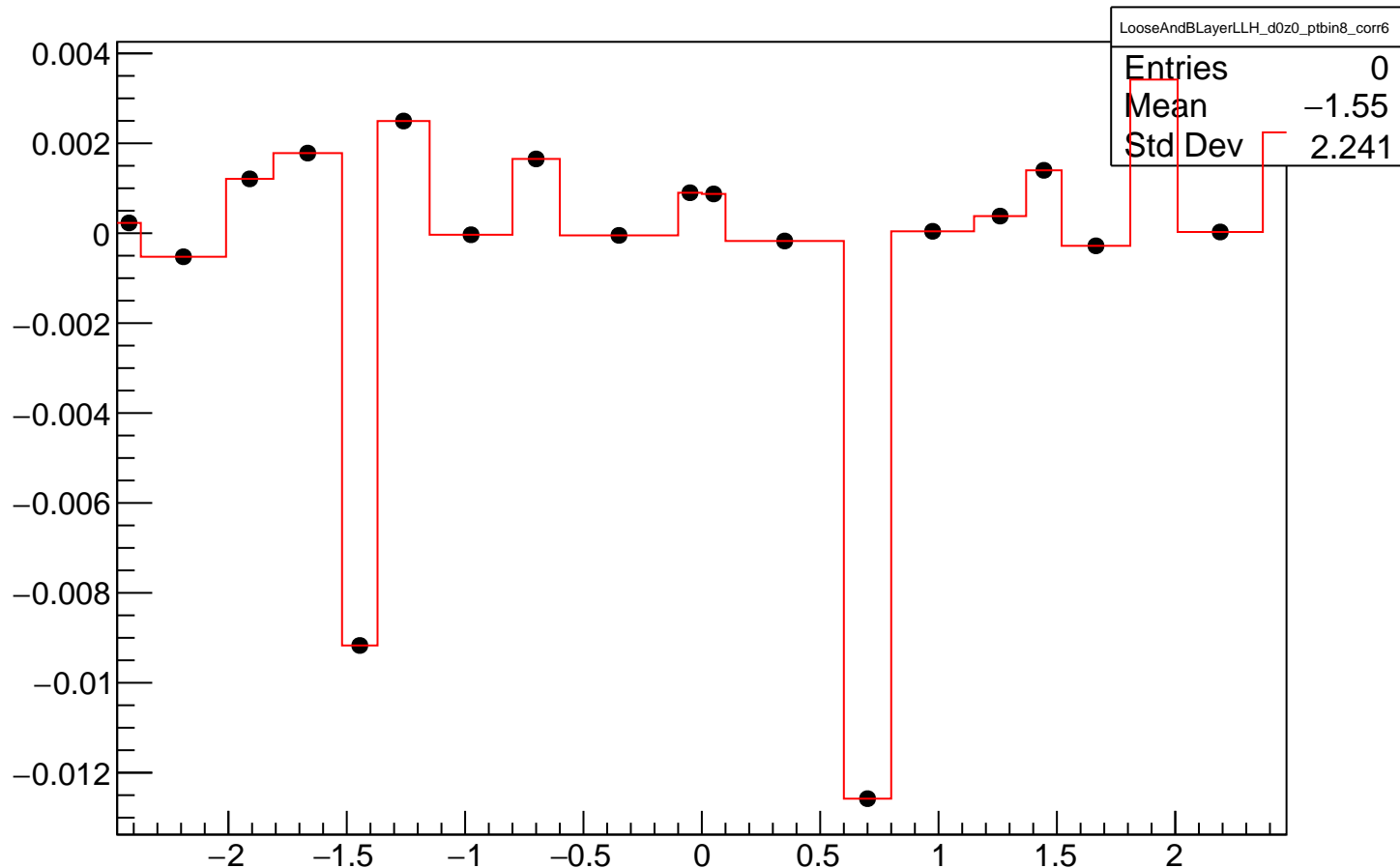
FullSim_LooseLLH_d0z0_corr6



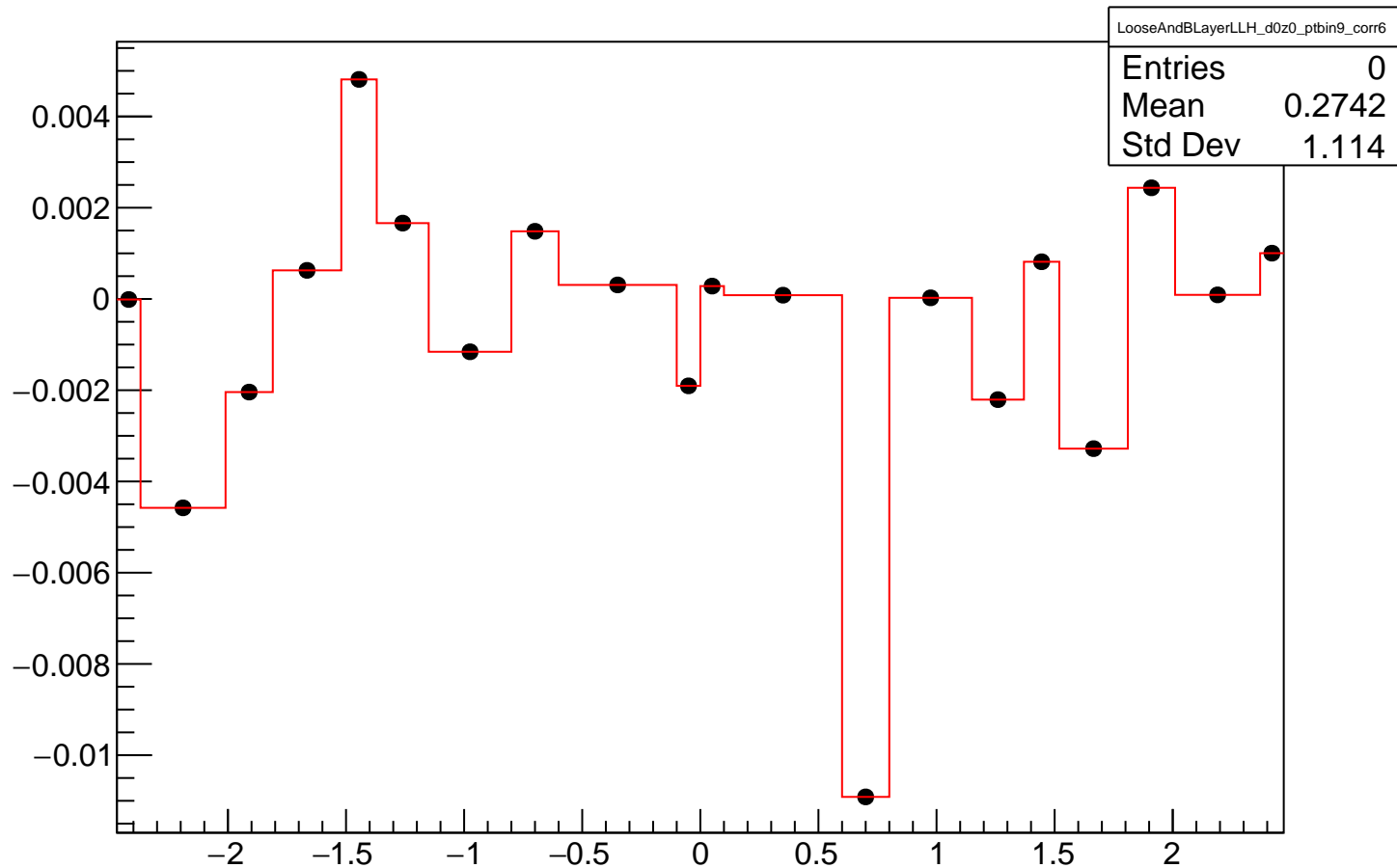
FullSim_LooseLLH_d0z0_corr6



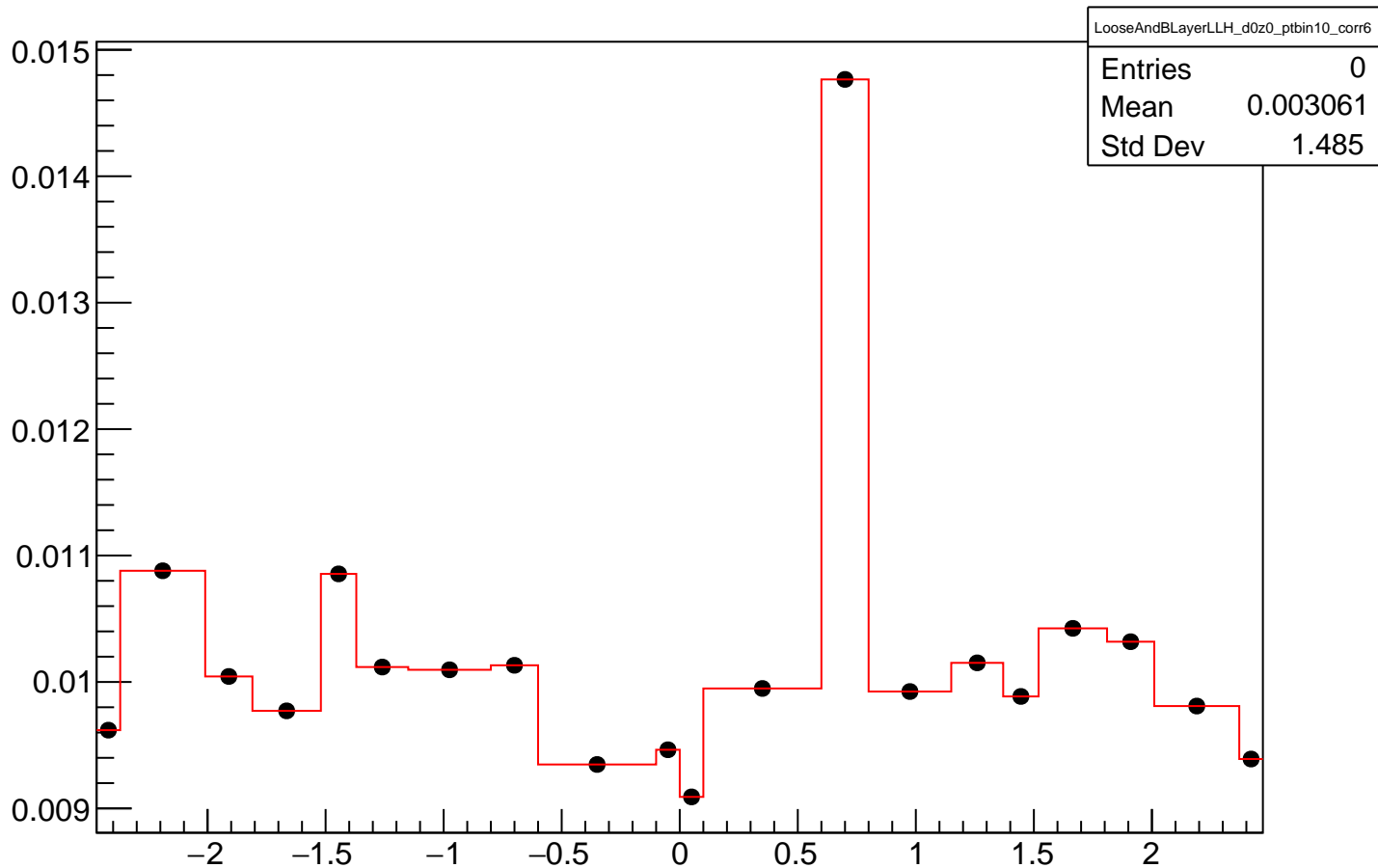
FullSim_LooseLLH_d0z0_corr6



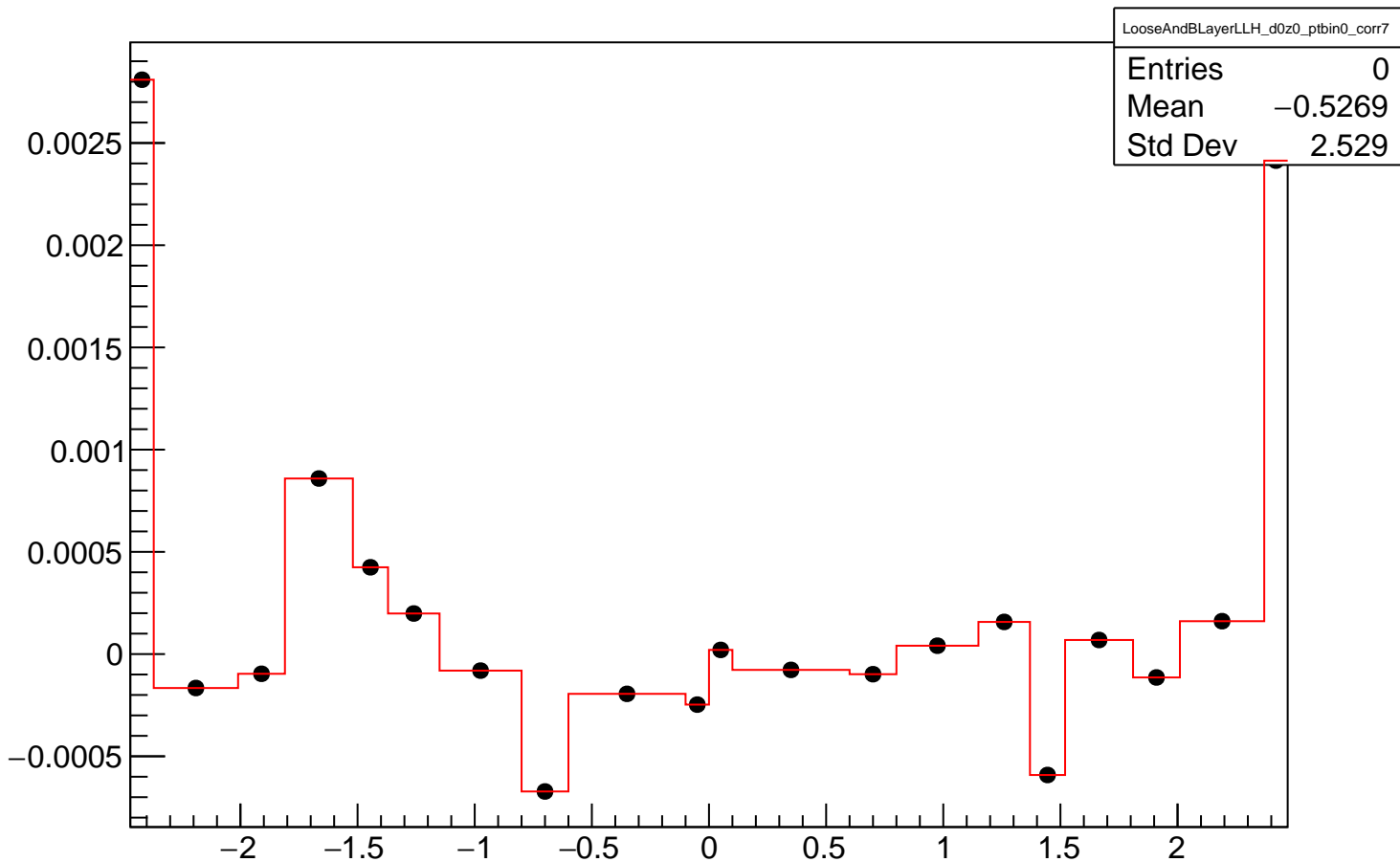
FullSim_LooseLLH_d0z0_corr6



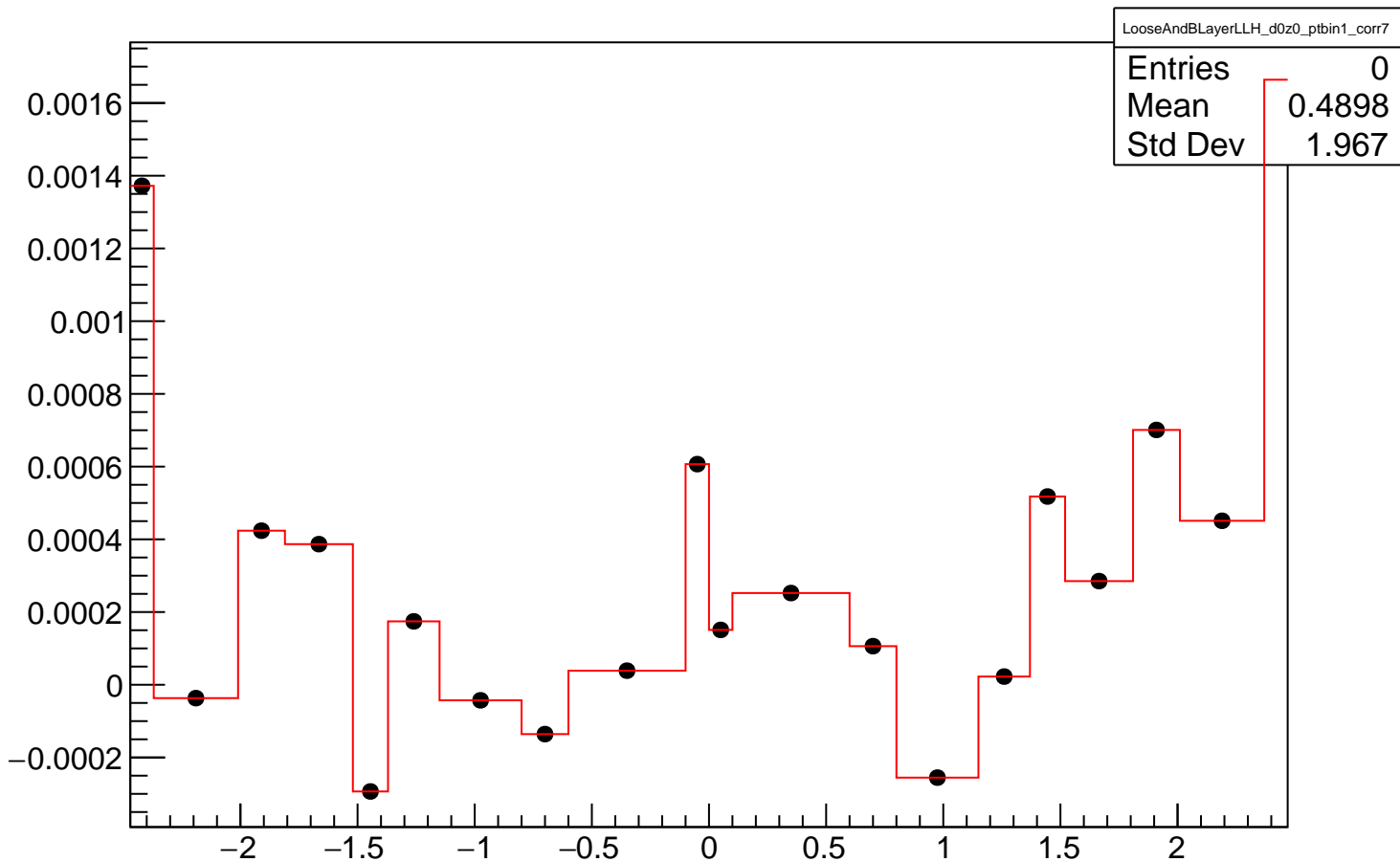
FullSim_LooseLLH_d0z0_corr6



FullSim_LooseLLH_d0z0_corr7



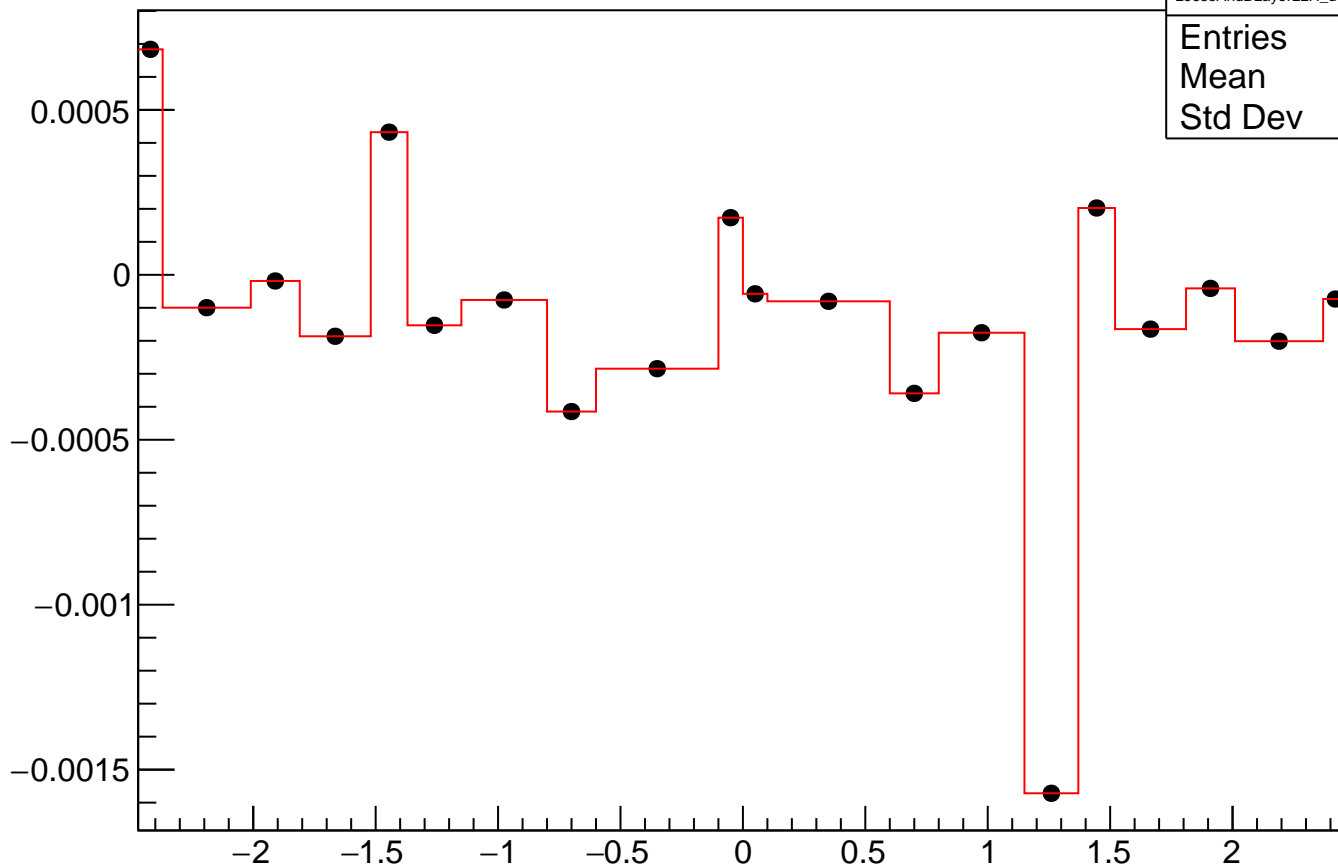
FullSim_LooseLLH_d0z0_corr7



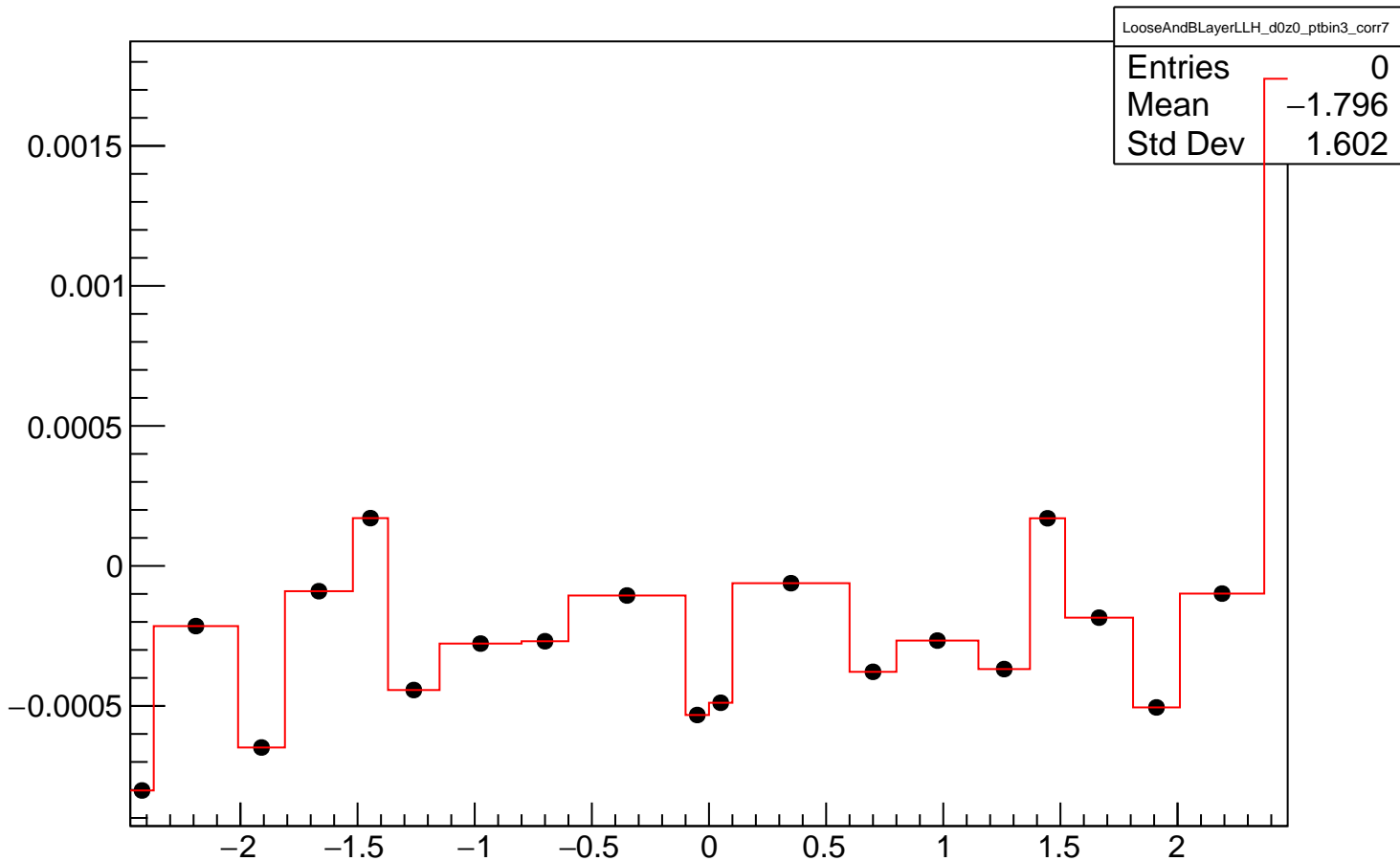
FullSim_LooseLLH_d0z0_corr7

LooseAndBLayerLLH_d0z0_ptbin2_corr7

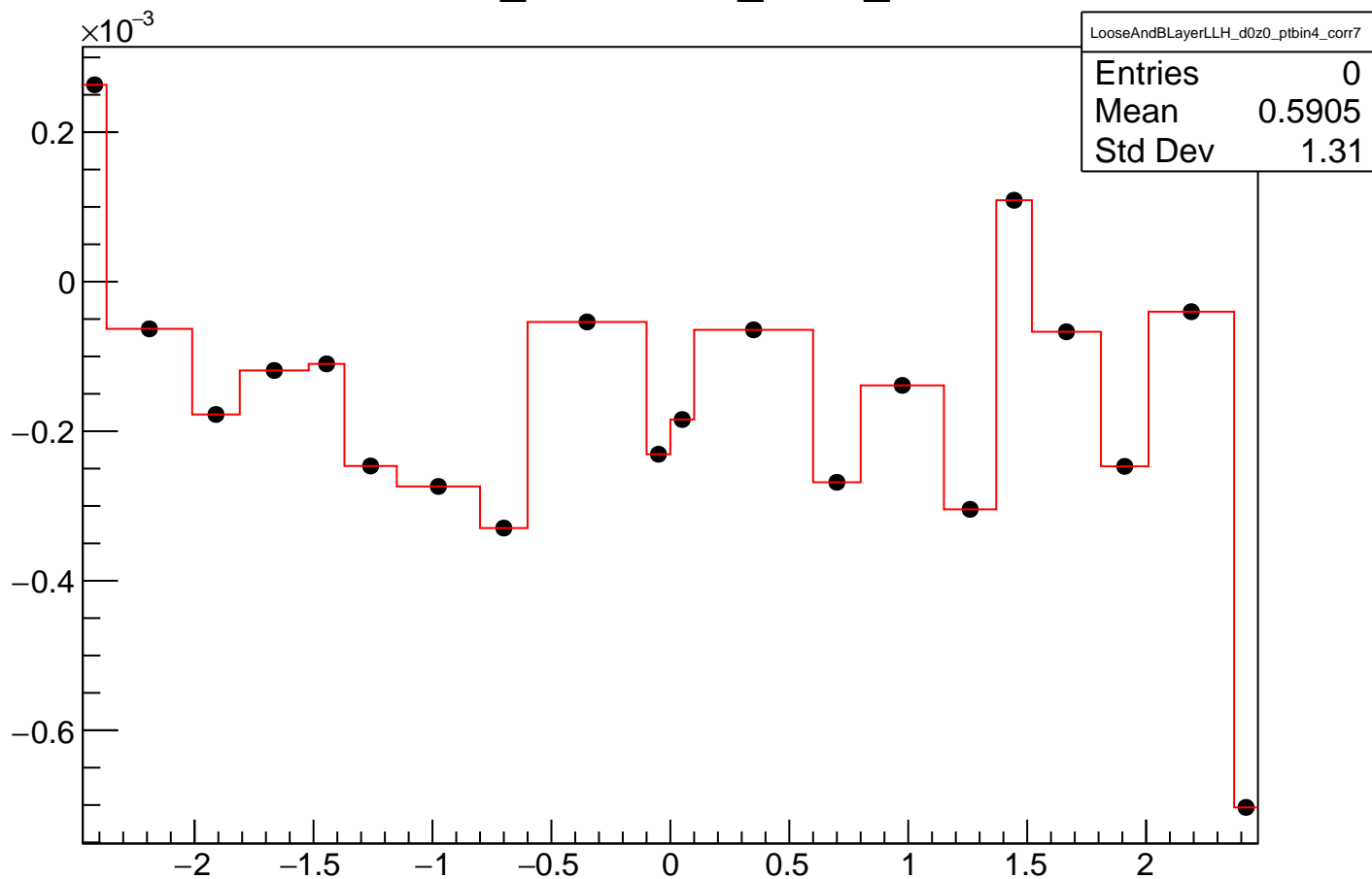
Entries	0
Mean	1.695
Std Dev	1.554



FullSim_LooseLLH_d0z0_corr7



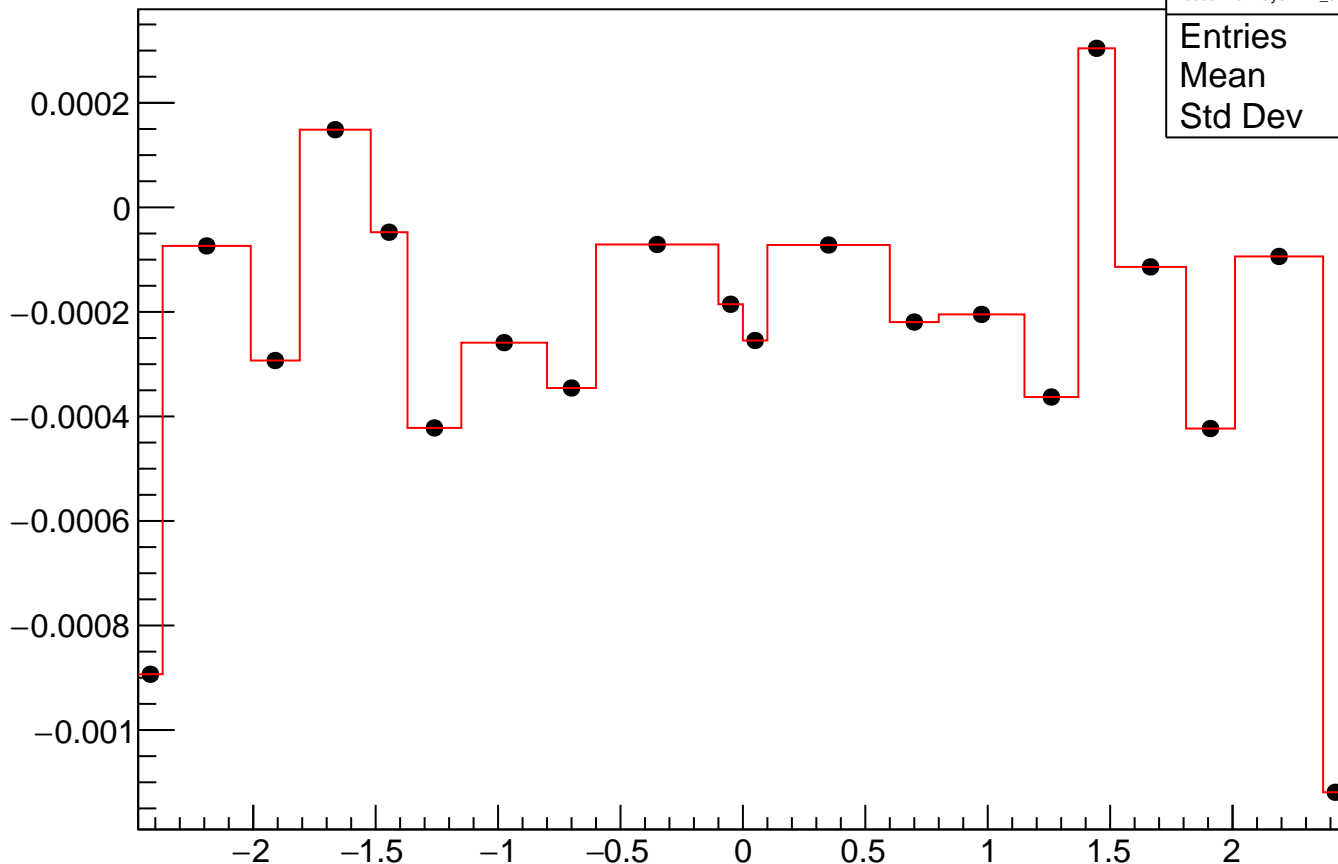
FullSim_LooseLLH_d0z0_corr7



FullSim_LooseLLH_d0z0_corr7

LooseAndBLayerLLH_d0z0_ptbin5_corr7

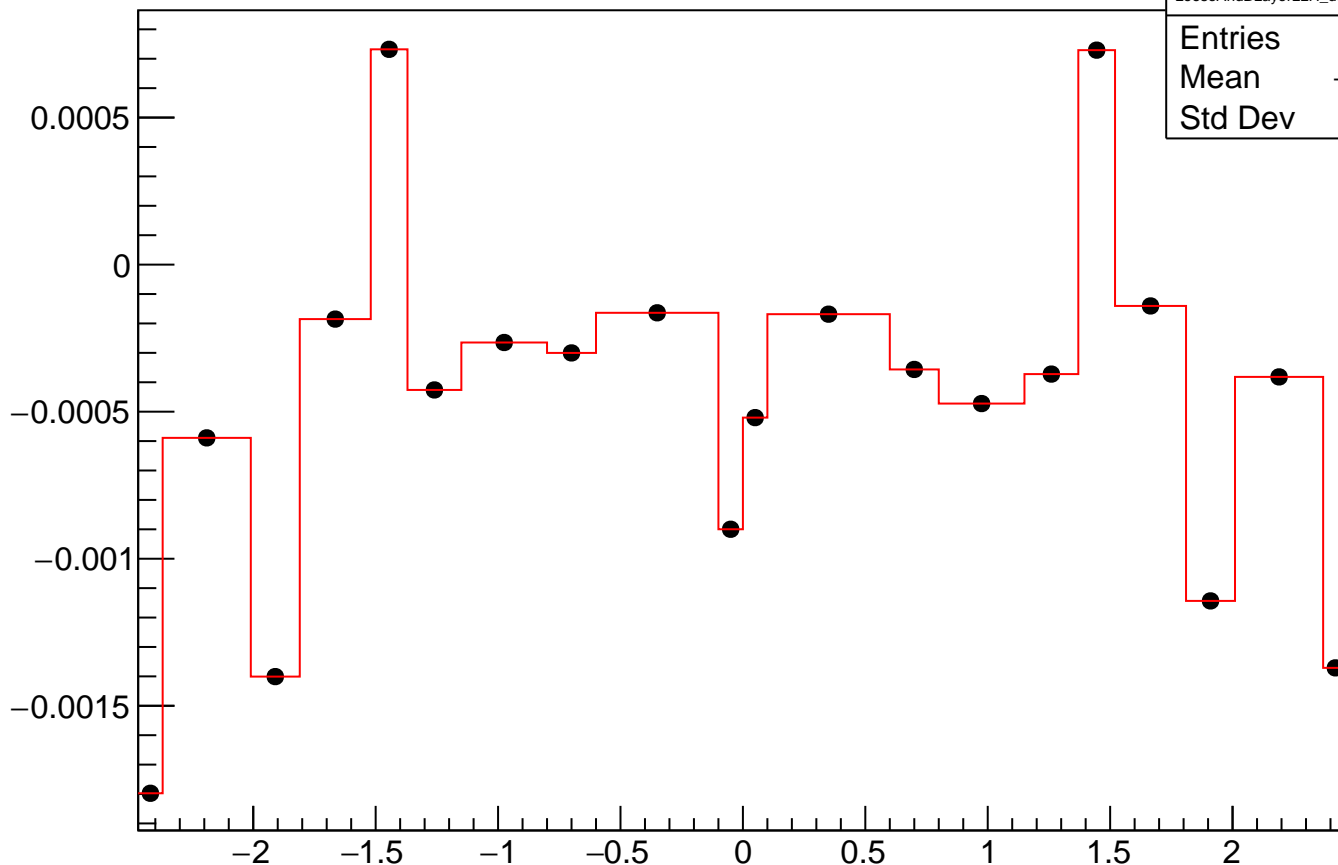
Entries	0
Mean	0.1112
Std Dev	1.816



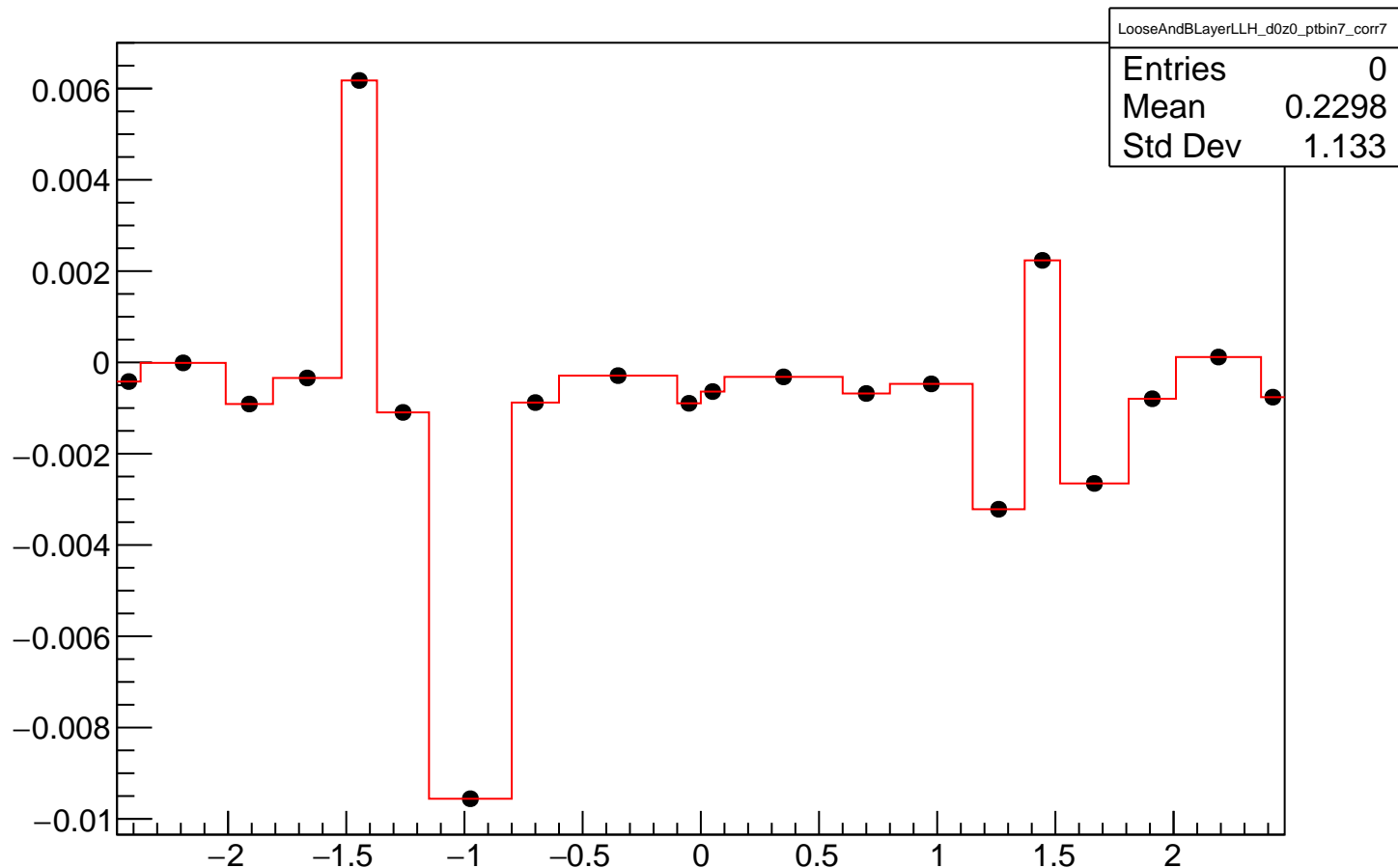
FullSim_LooseLLH_d0z0_corr7

LooseAndBLayerLLH_d0z0_ptbin6_corr7

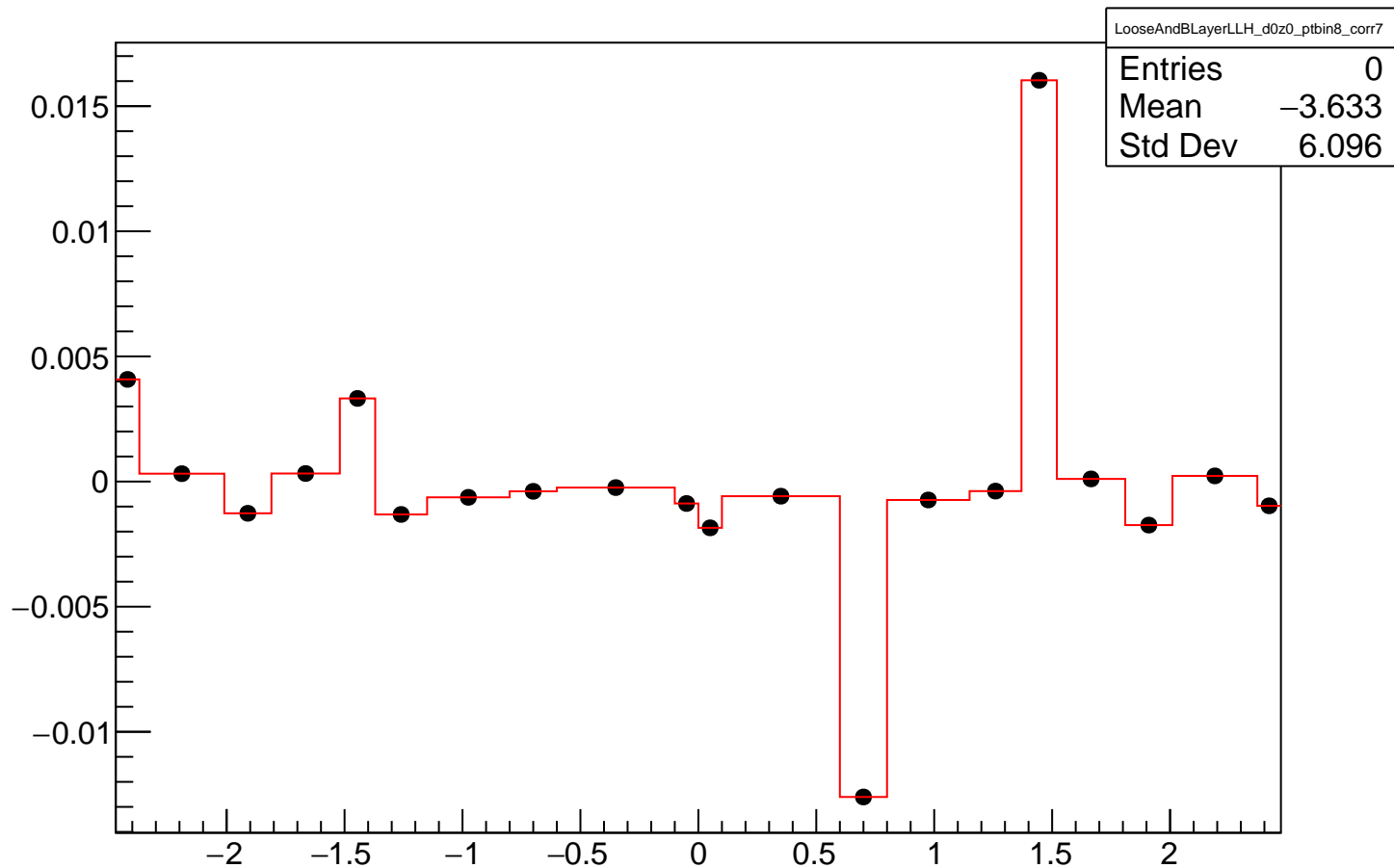
Entries	0
Mean	-0.1992
Std Dev	1.845



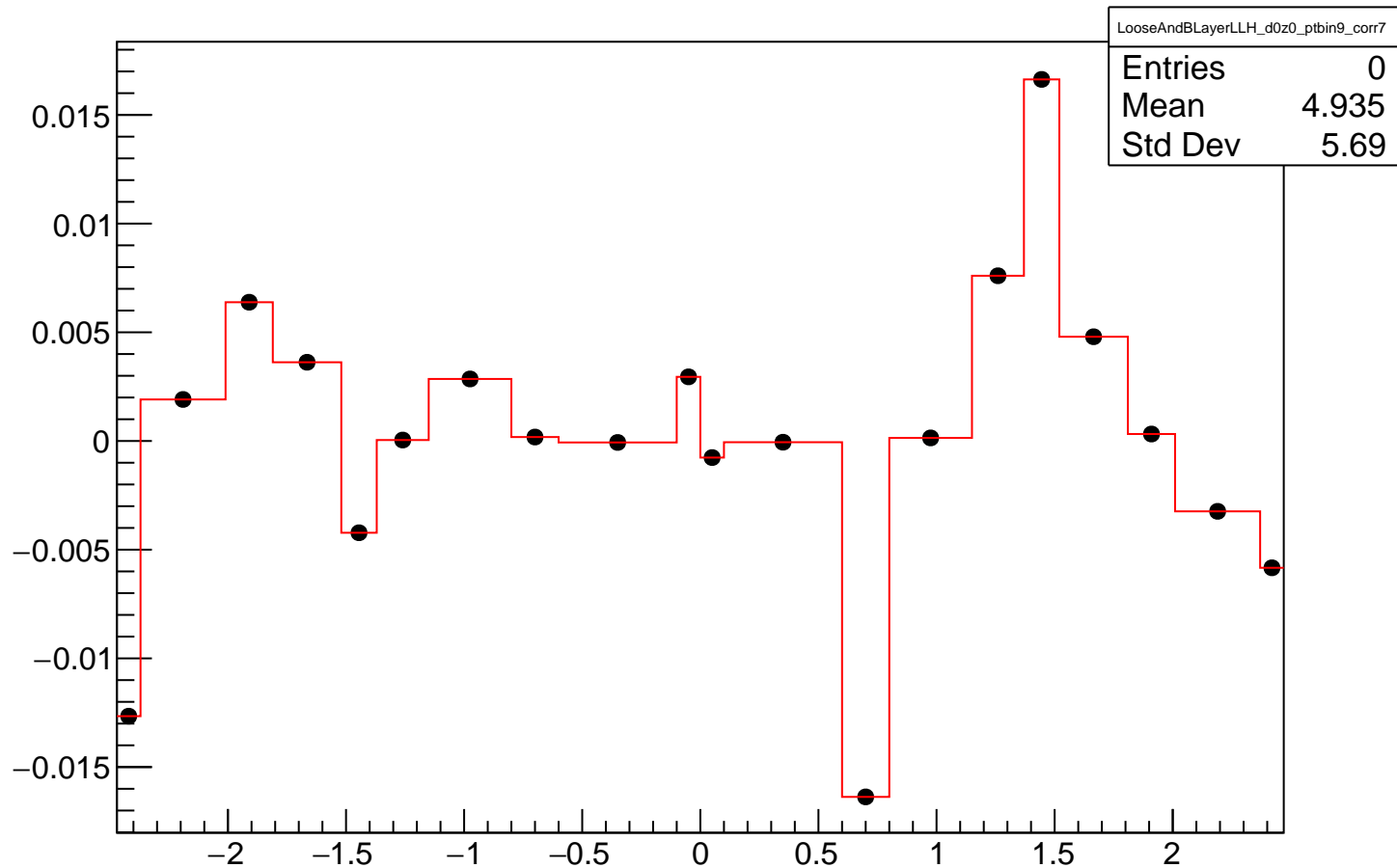
FullSim_LooseLLH_d0z0_corr7



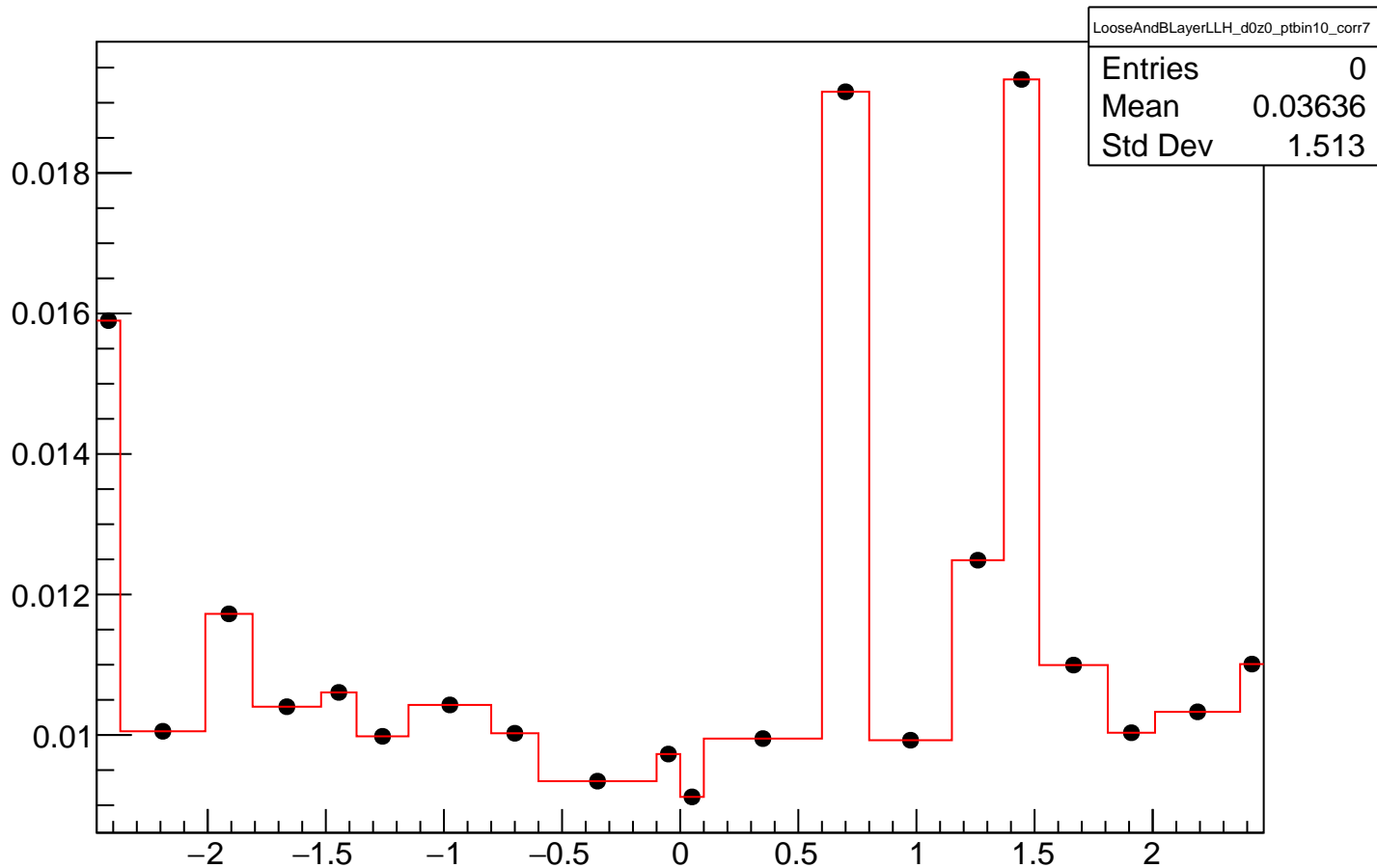
FullSim_LooseLLH_d0z0_corr7



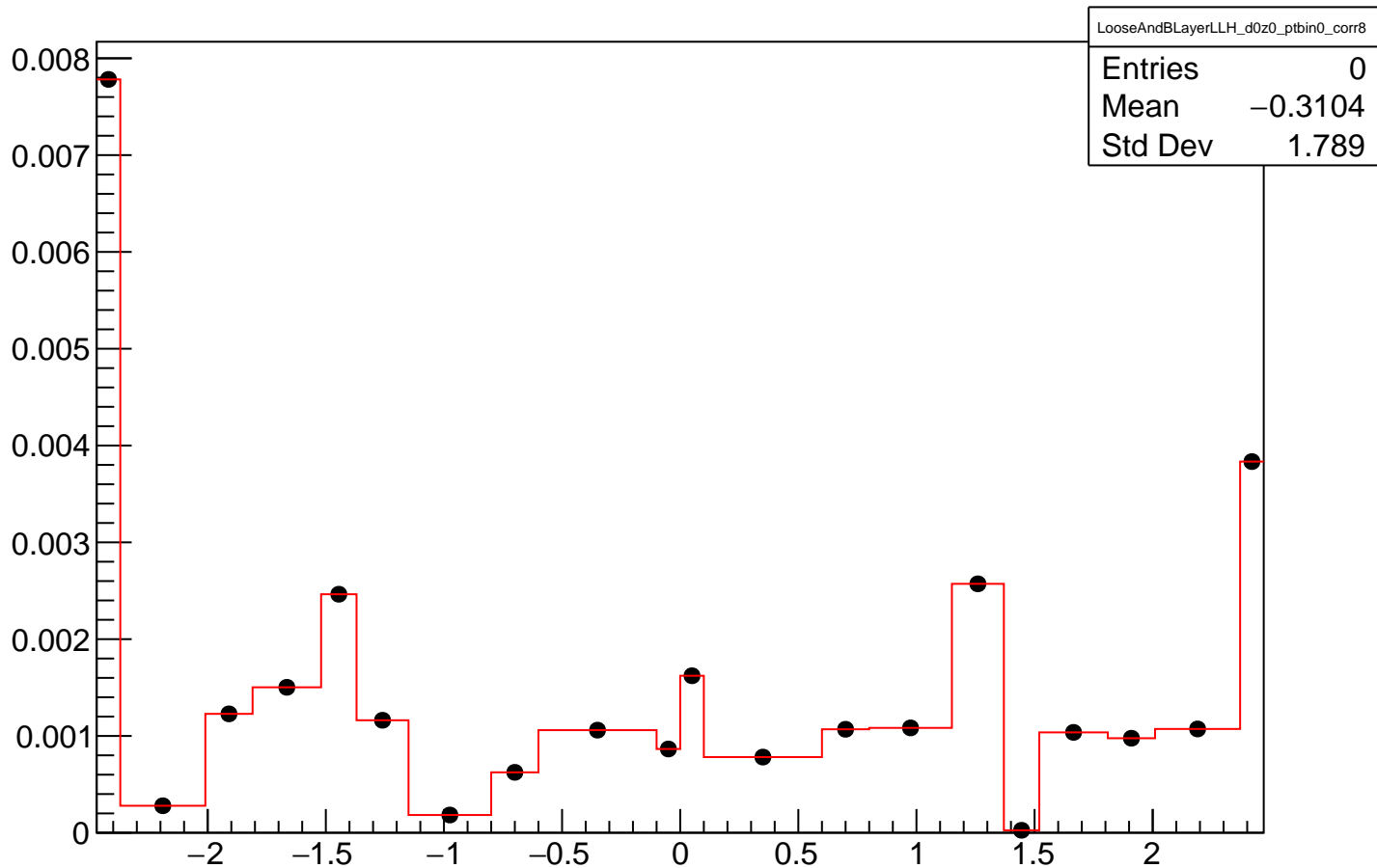
FullSim_LooseLLH_d0z0_corr7



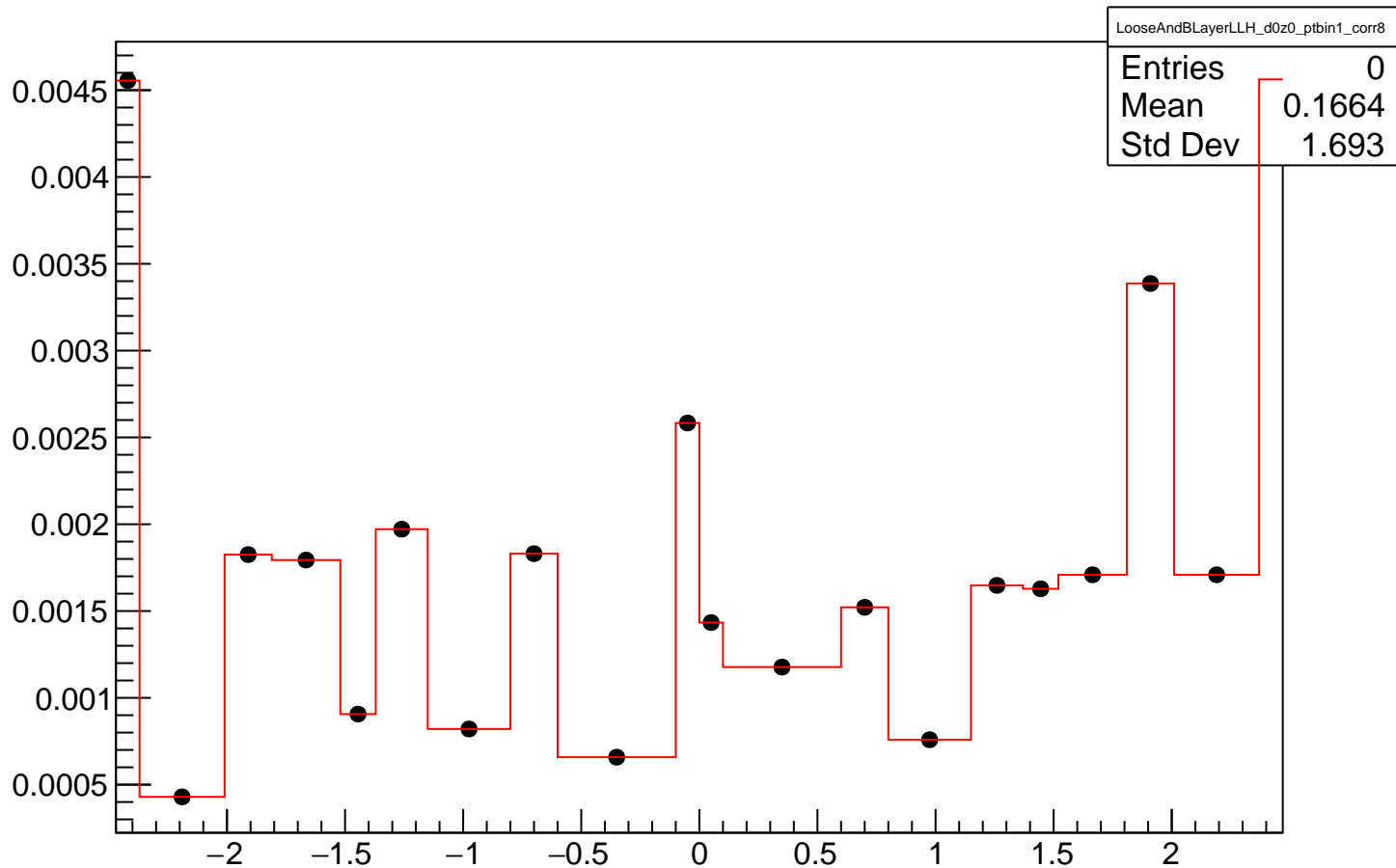
FullSim_LooseLLH_d0z0_corr7



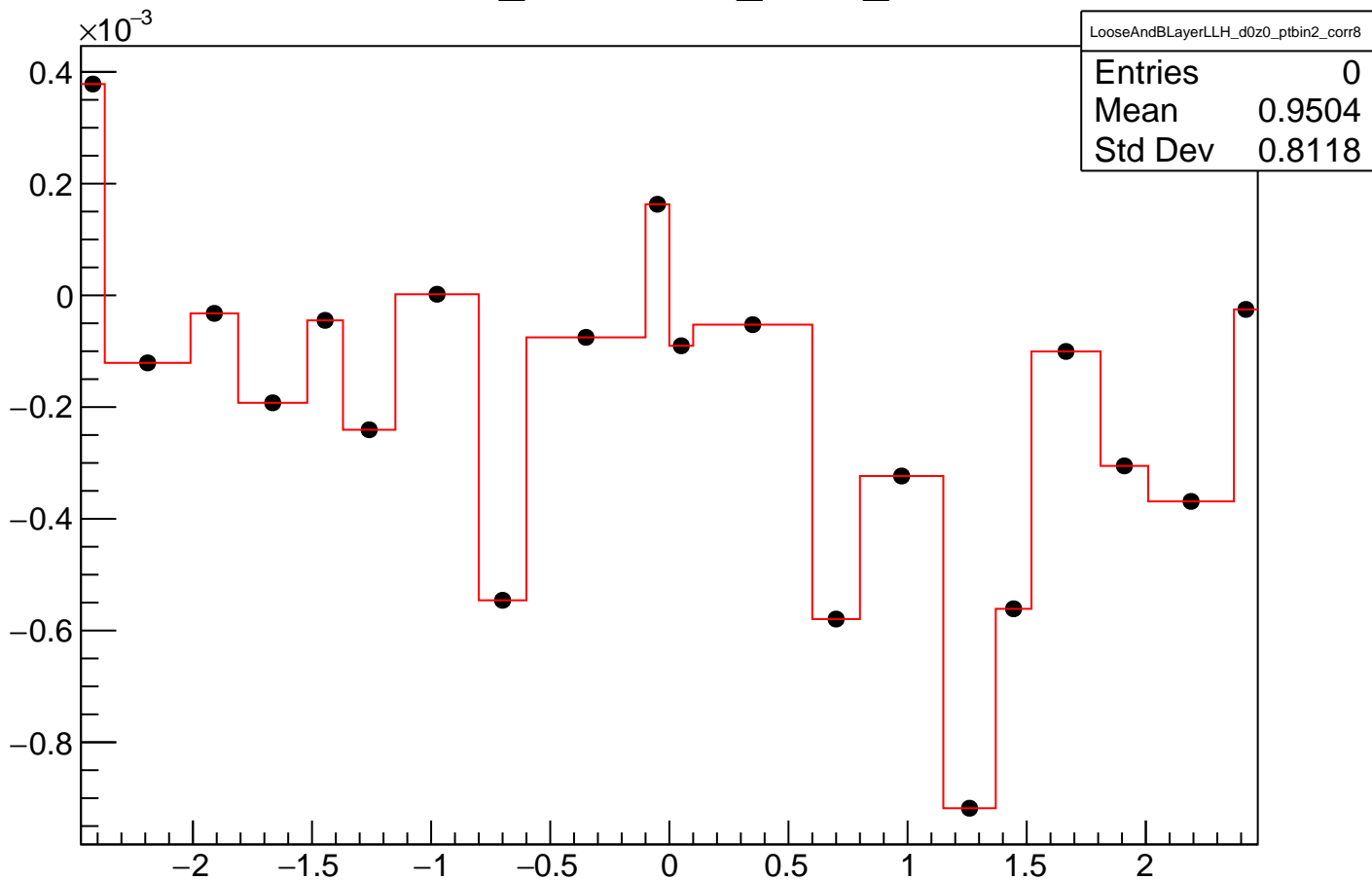
FullSim_LooseLLH_d0z0_corr8



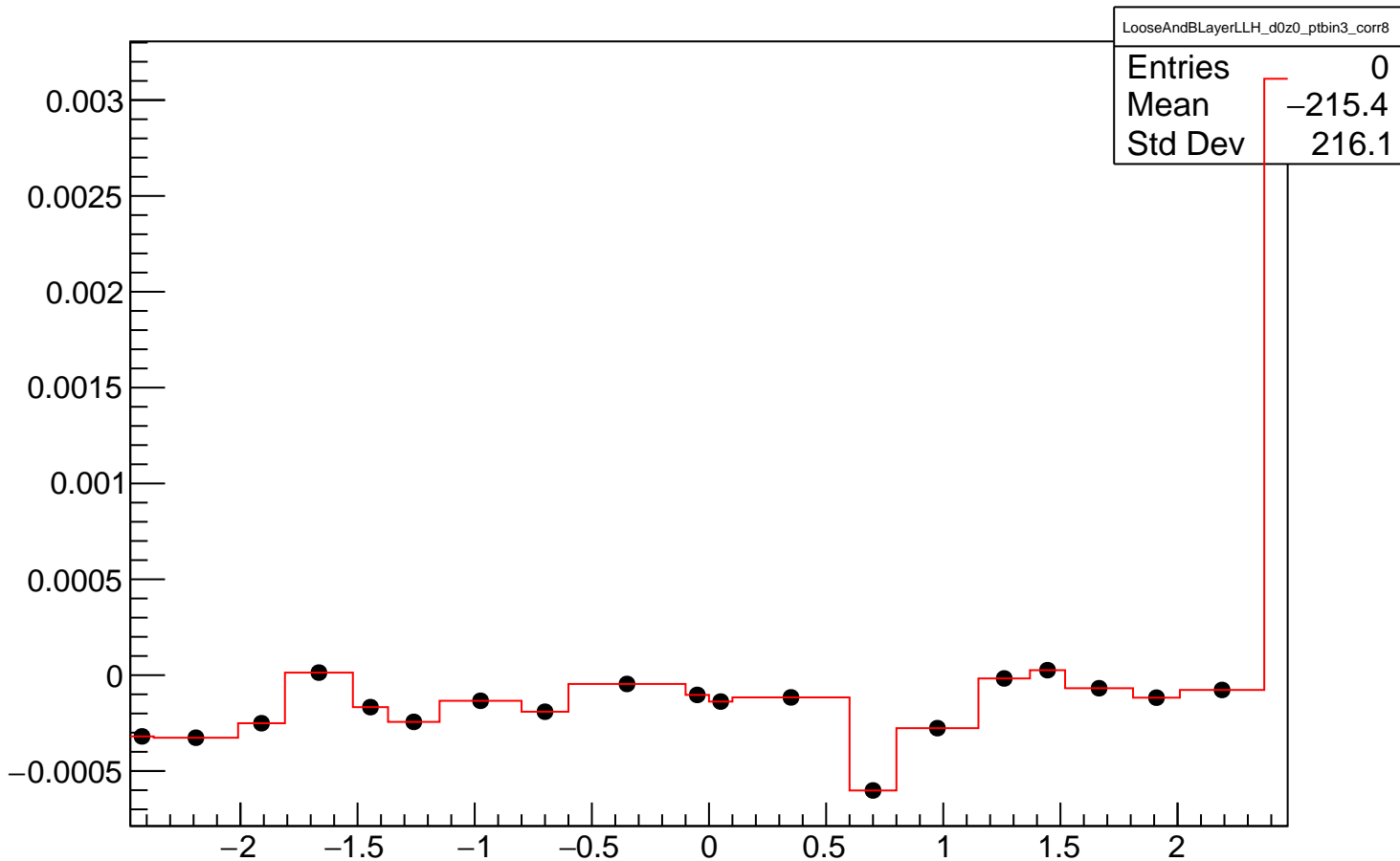
FullSim_LooseLLH_d0z0_corr8



FullSim_LooseLLH_d0z0_corr8



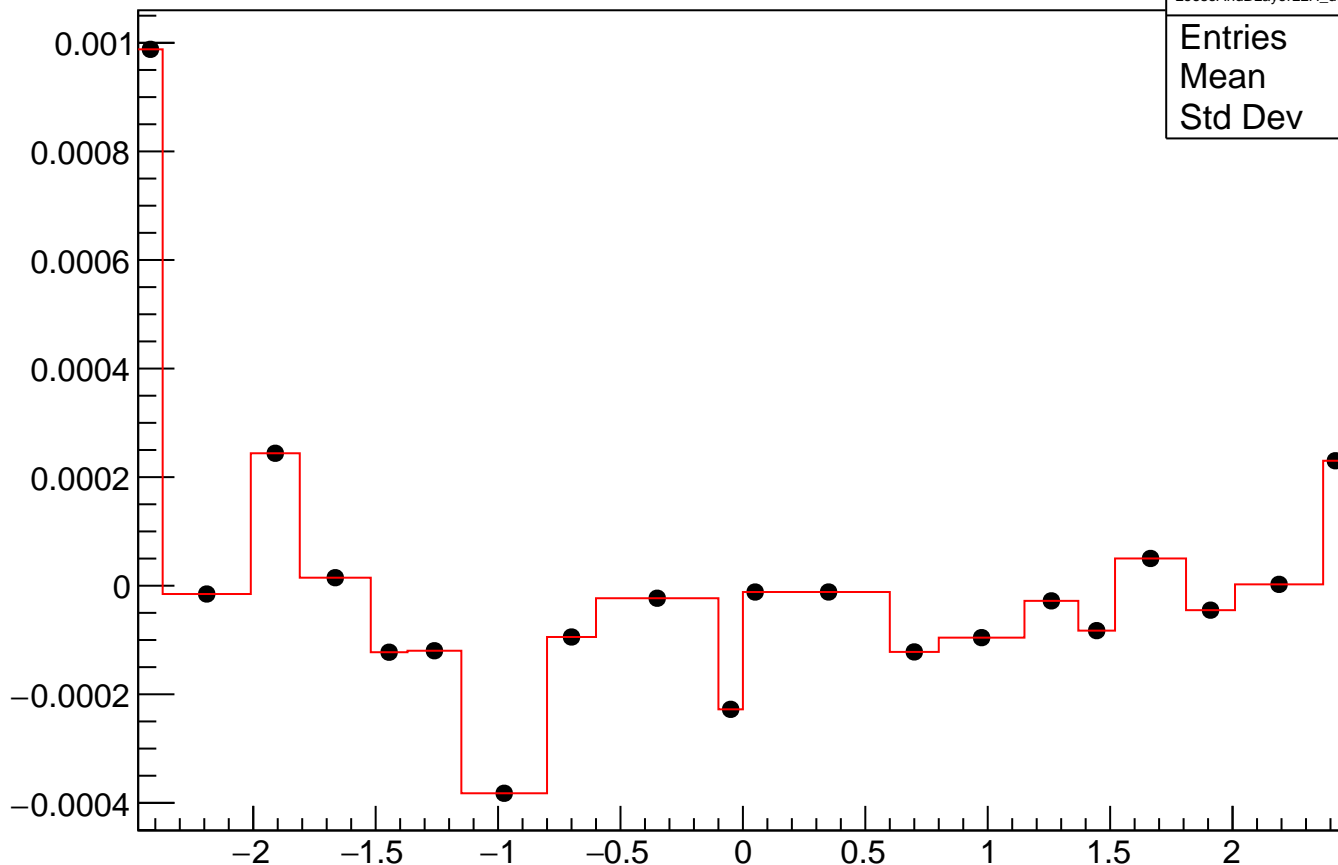
FullSim_LooseLLH_d0z0_corr8



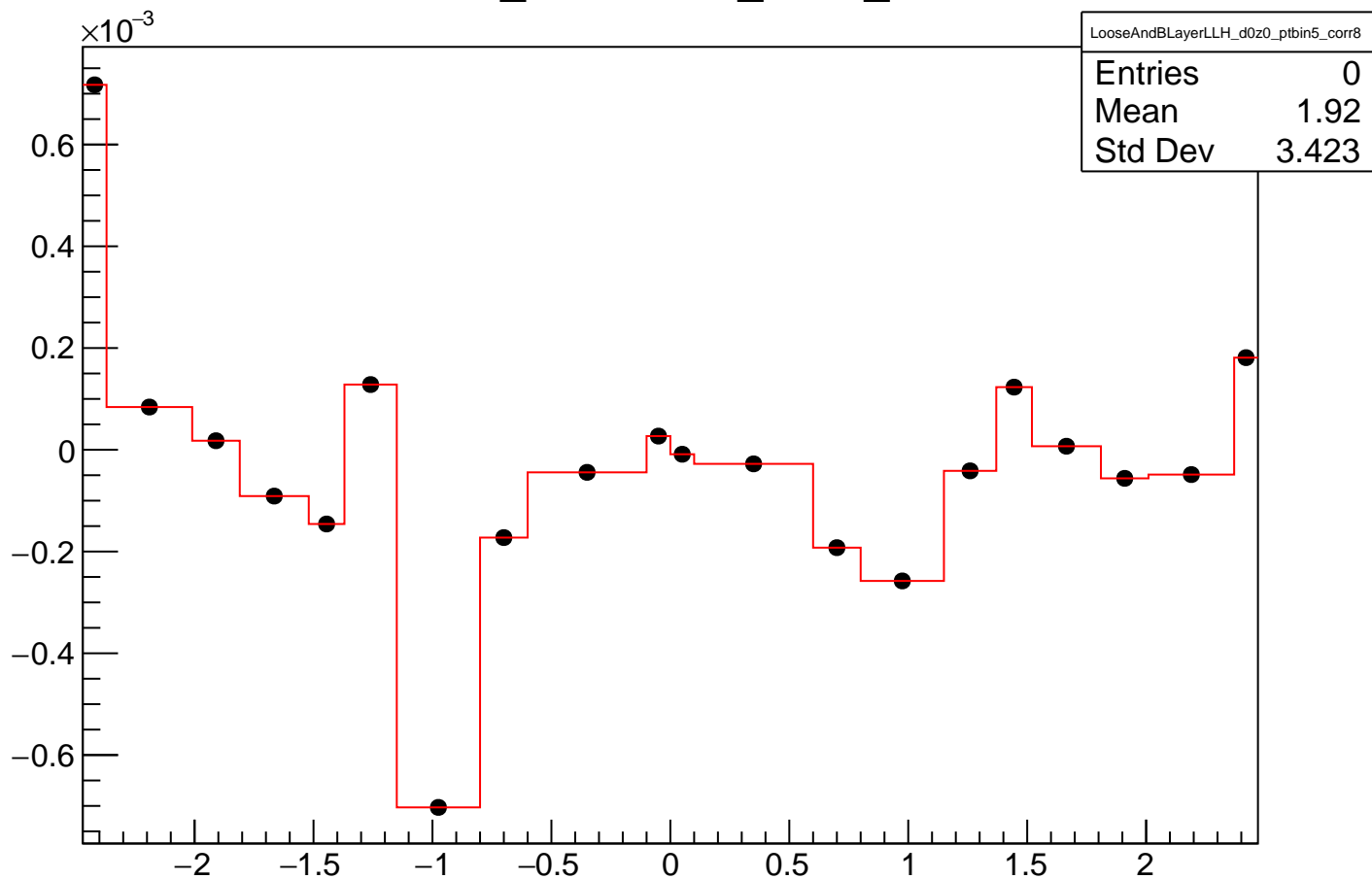
FullSim_LooseLLH_d0z0_corr8

LooseAndBLayerLLH_d0z0_ptbin4_corr8

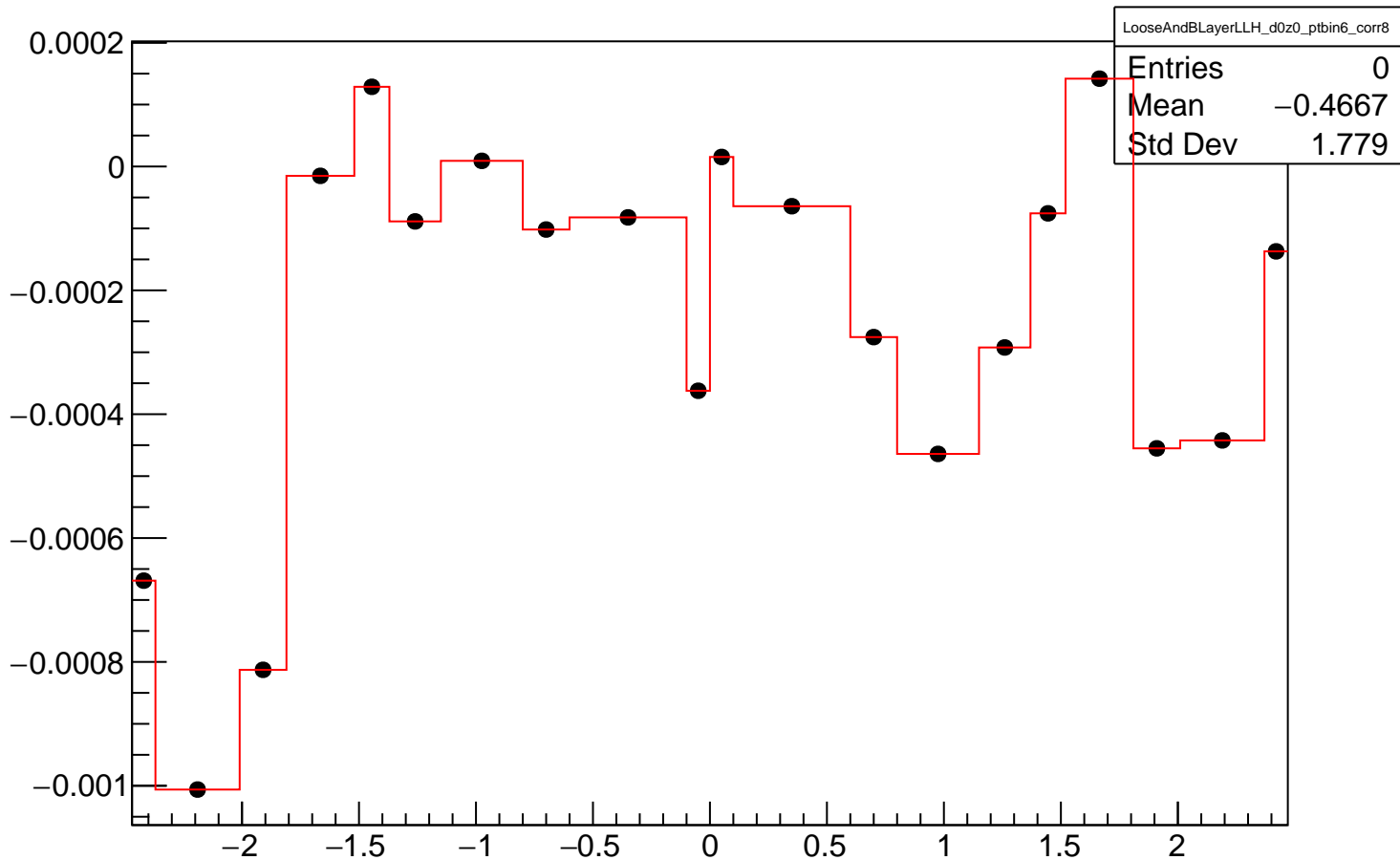
Entries	0
Mean	-12.43
Std Dev	10.43



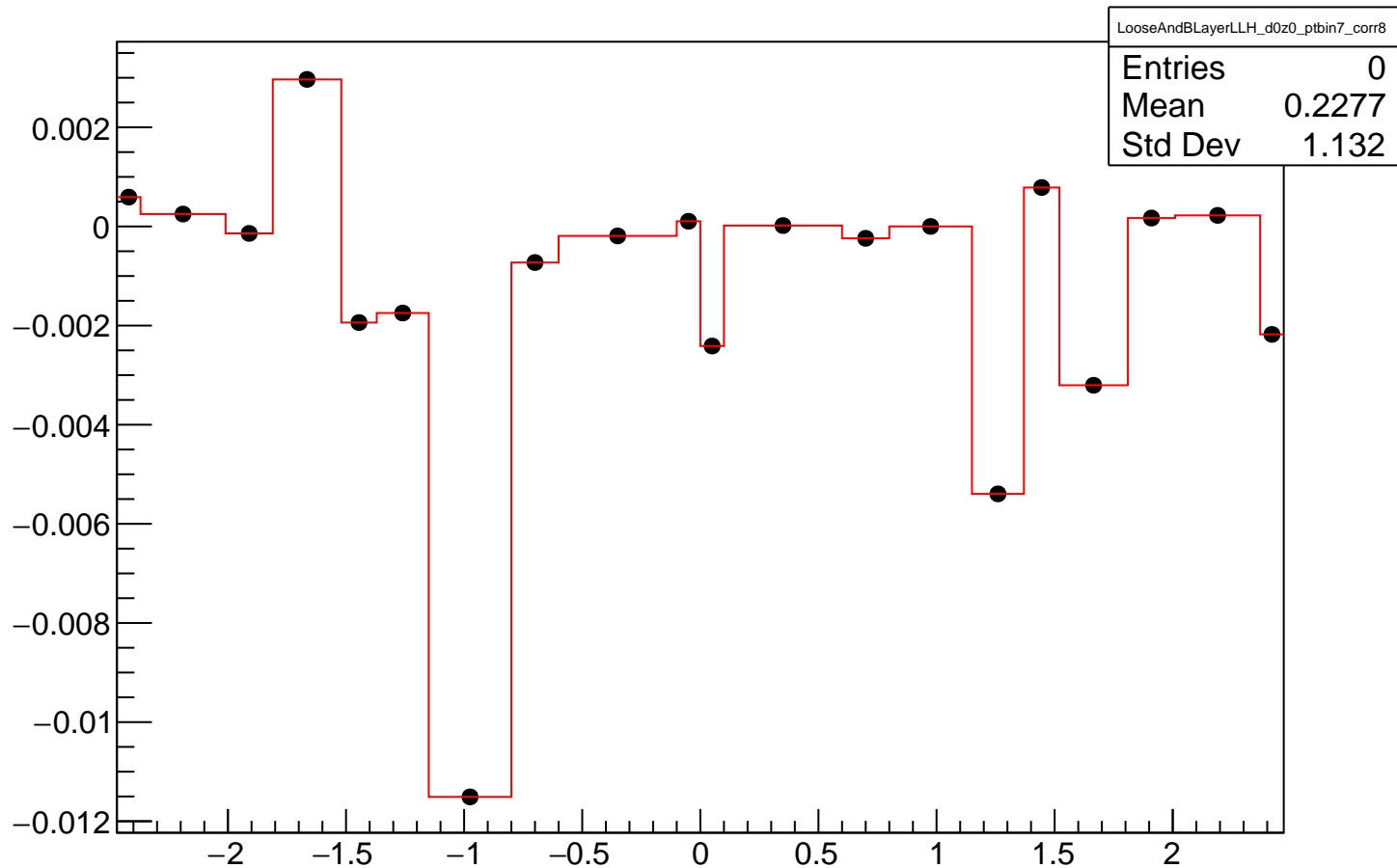
FullSim_LooseLLH_d0z0_corr8



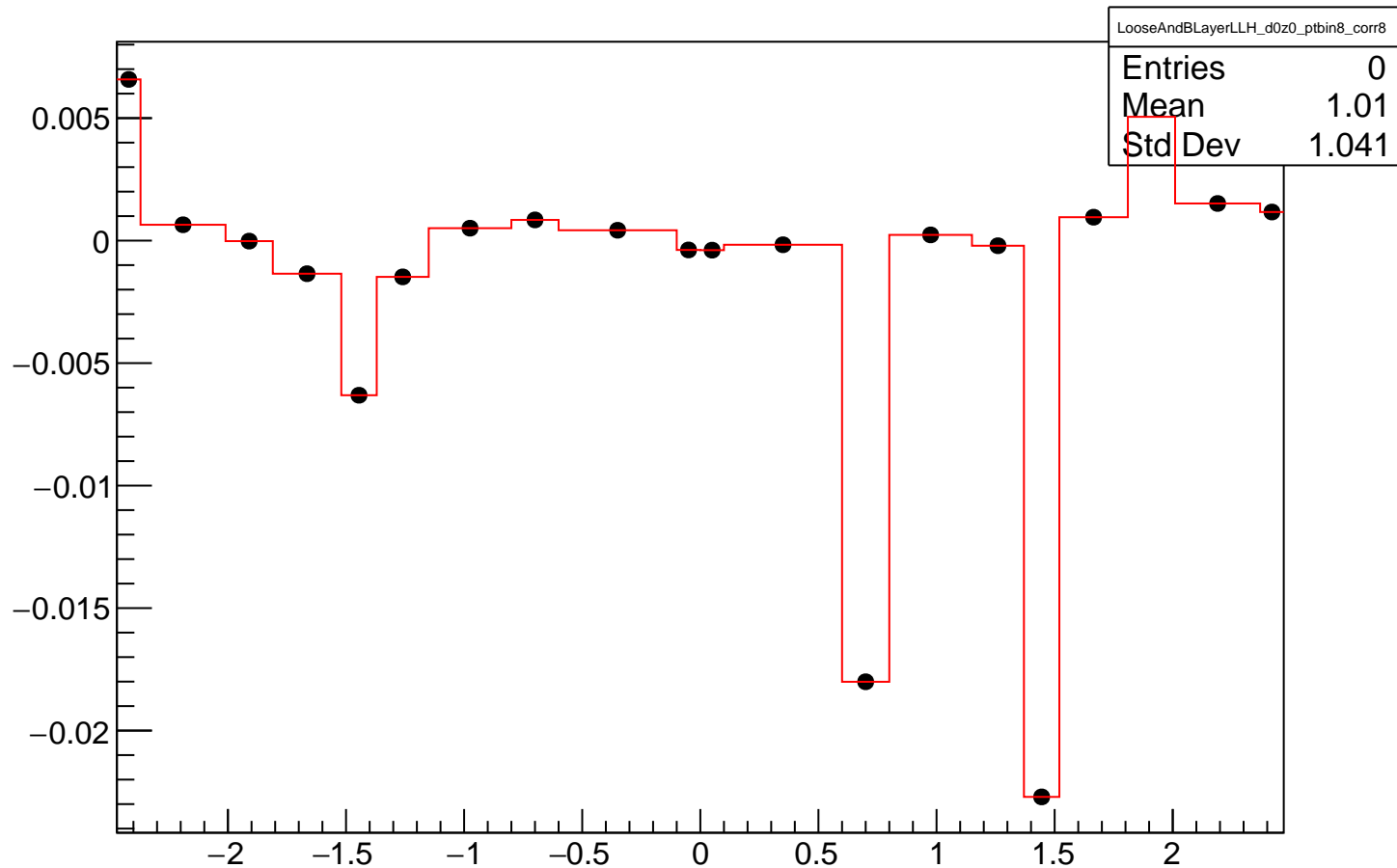
FullSim_LooseLLH_d0z0_corr8



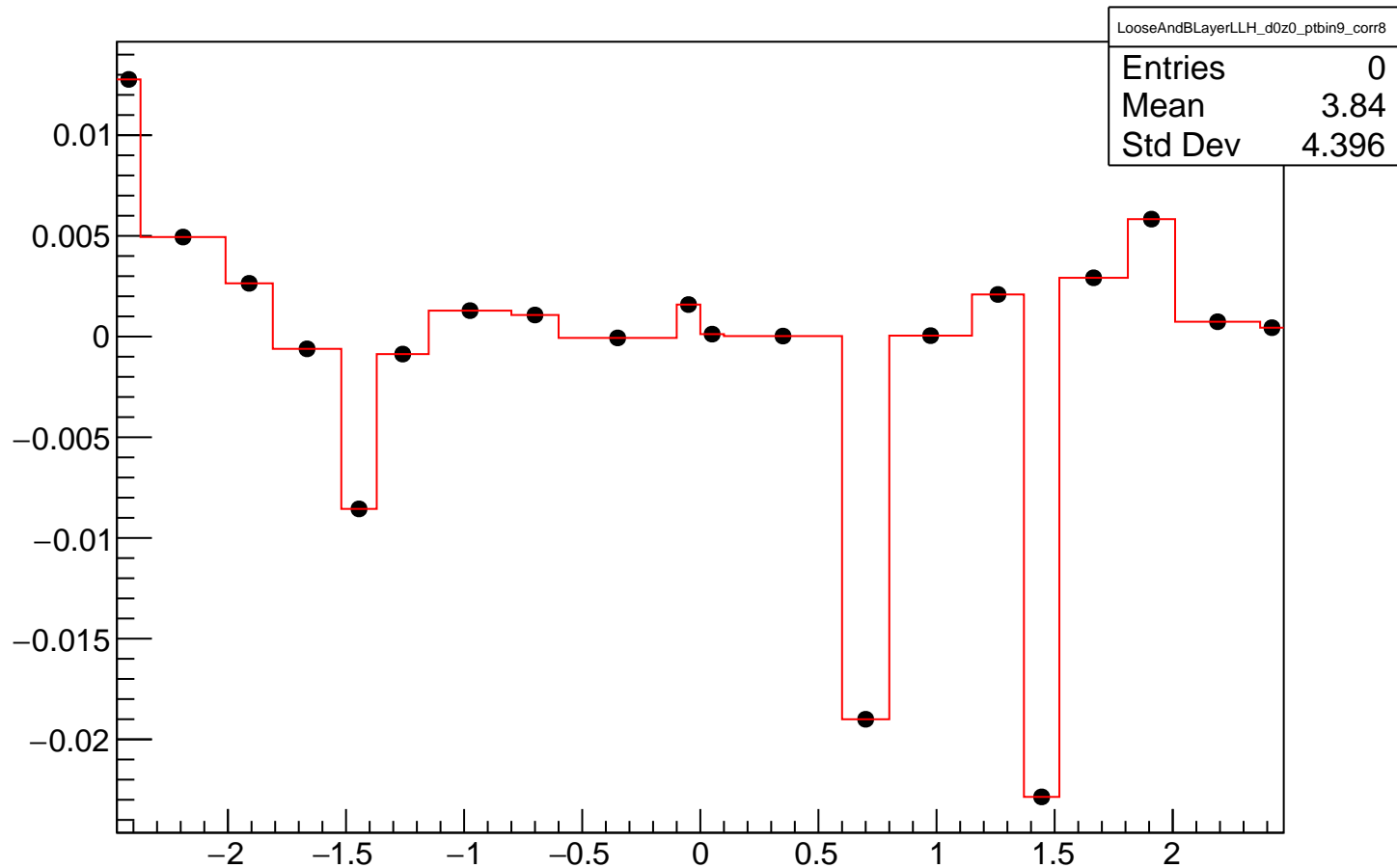
FullSim_LooseLLH_d0z0_corr8



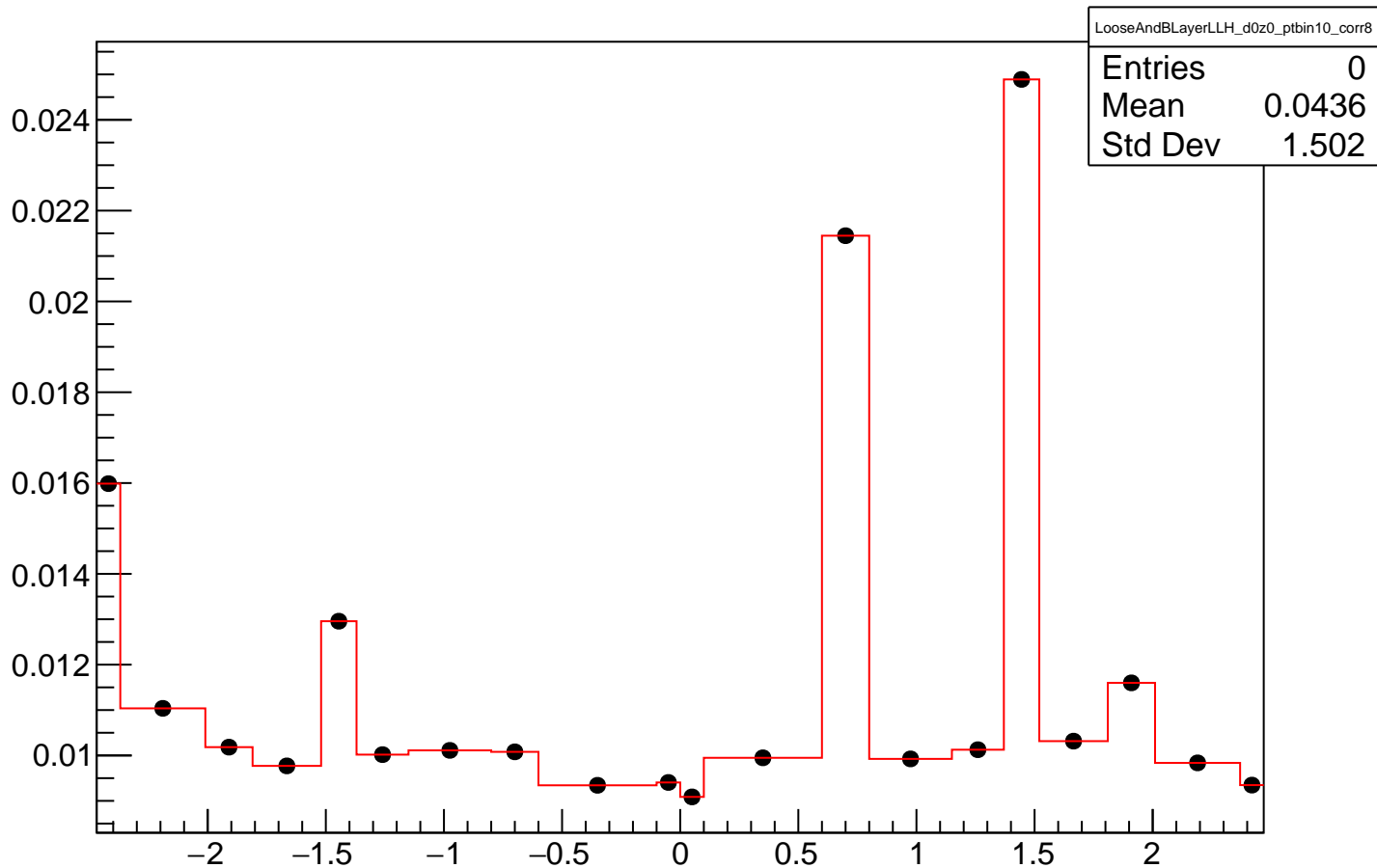
FullSim_LooseLLH_d0z0_corr8



FullSim_LooseLLH_d0z0_corr8



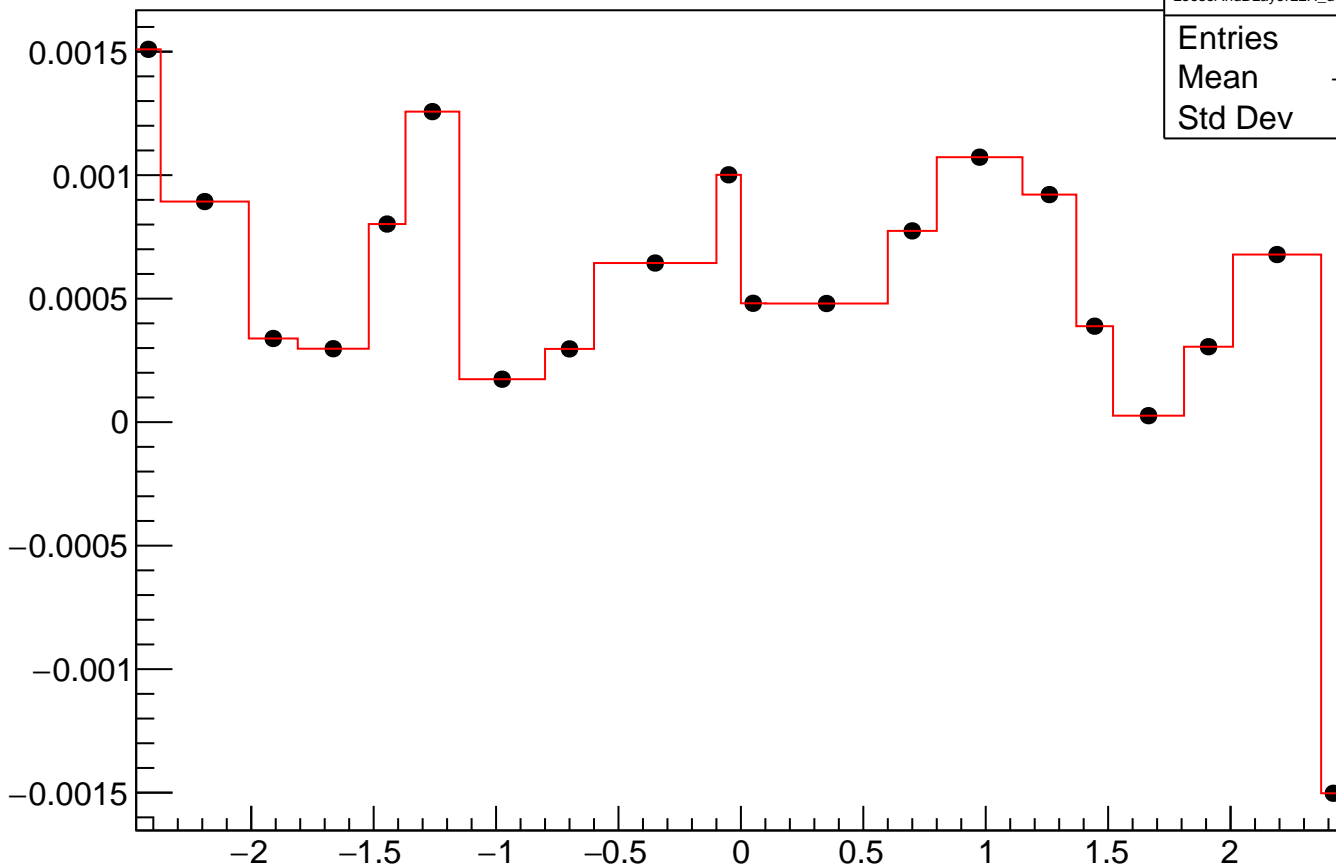
FullSim_LooseLLH_d0z0_corr8



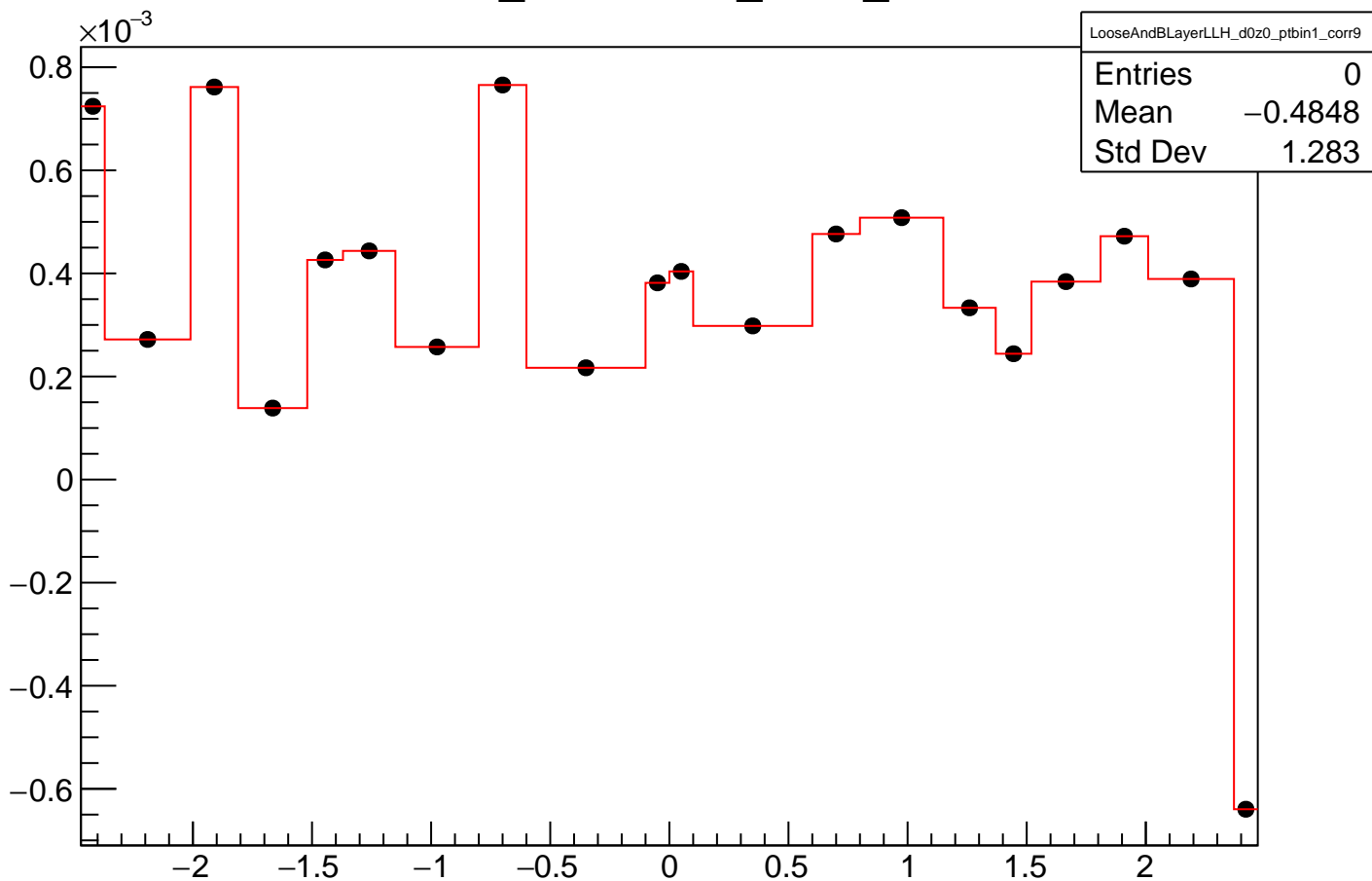
FullSim_LooseLLH_d0z0_corr9

LooseAndBLayerLLH_d0z0_ptbin0_corr9

Entries	0
Mean	-0.7534
Std Dev	1.072



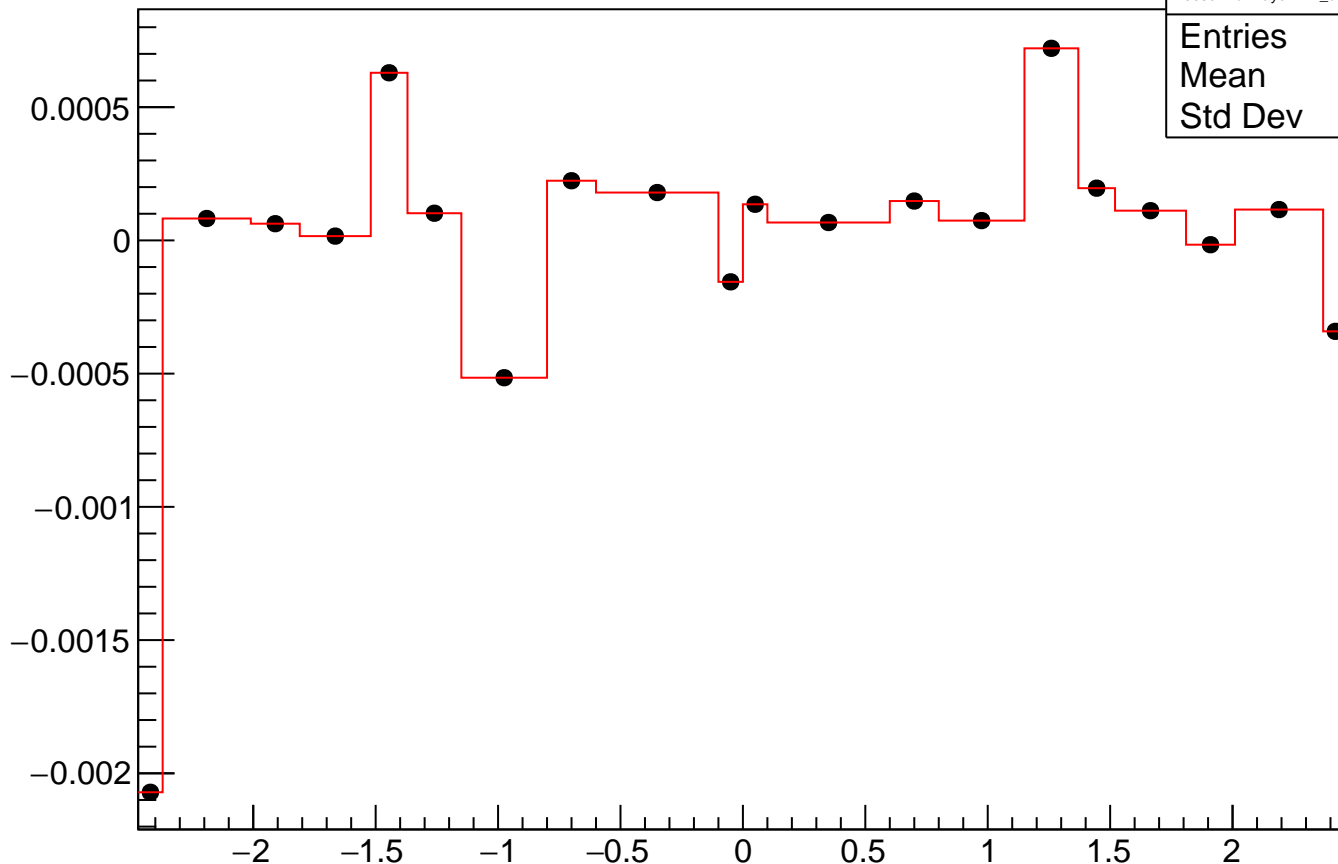
FullSim_LooseLLH_d0z0_corr9



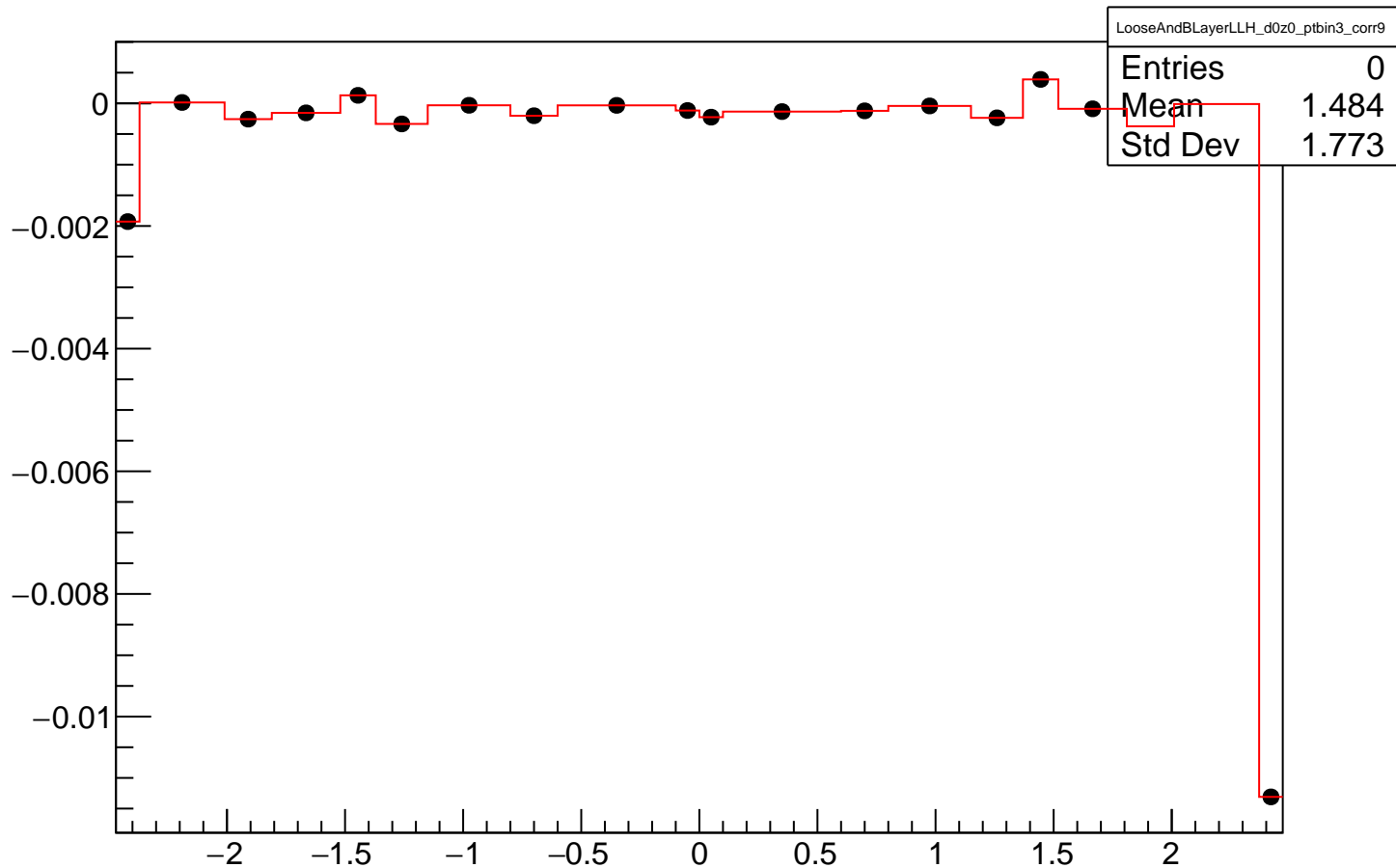
FullSim_LooseLLH_d0z0_corr9

LooseAndBLayerLLH_d0z0_ptbin2_corr9

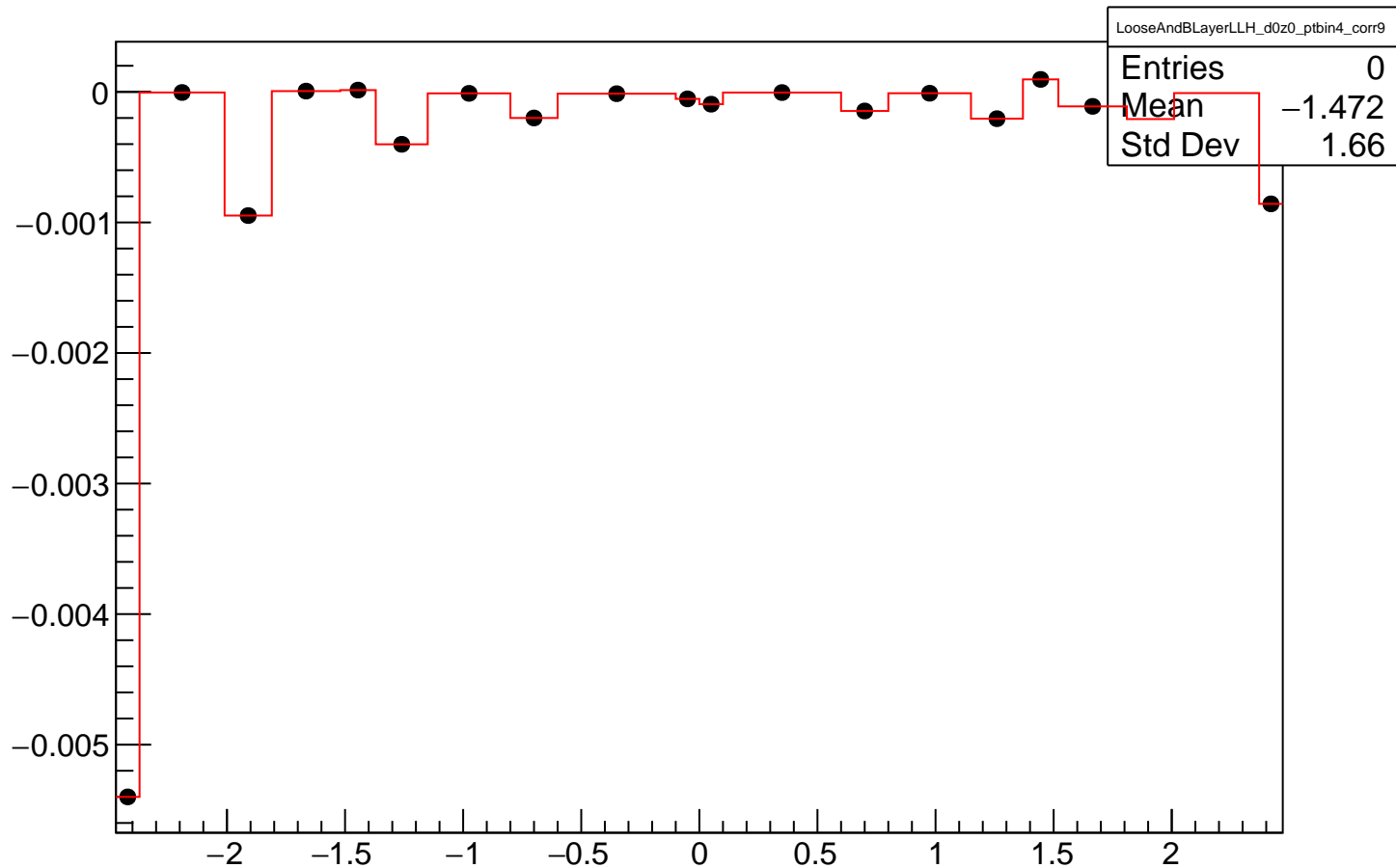
Entries	0
Mean	-20.89
Std Dev	19.86



FullSim_LooseLLH_d0z0_corr9

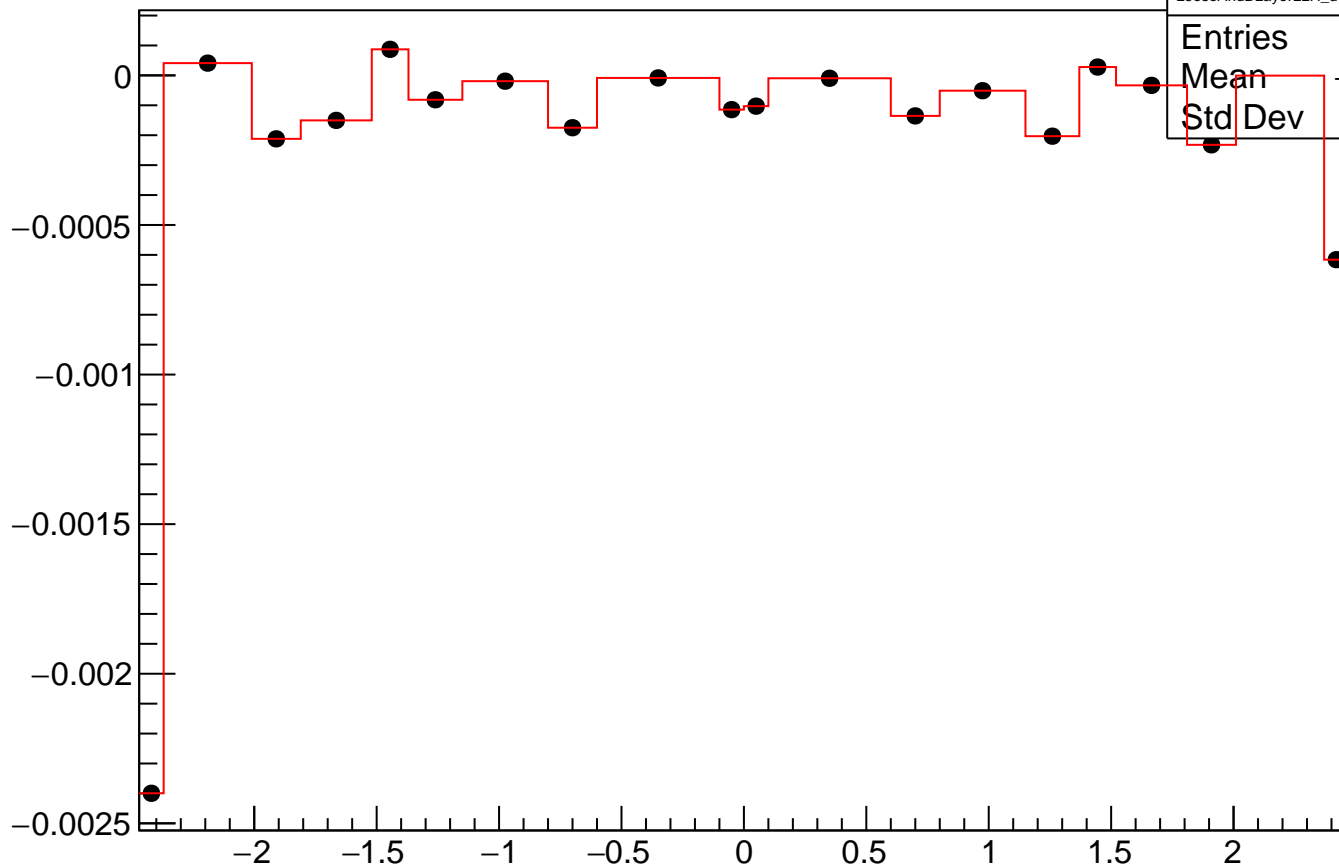


FullSim_LooseLLH_d0z0_corr9



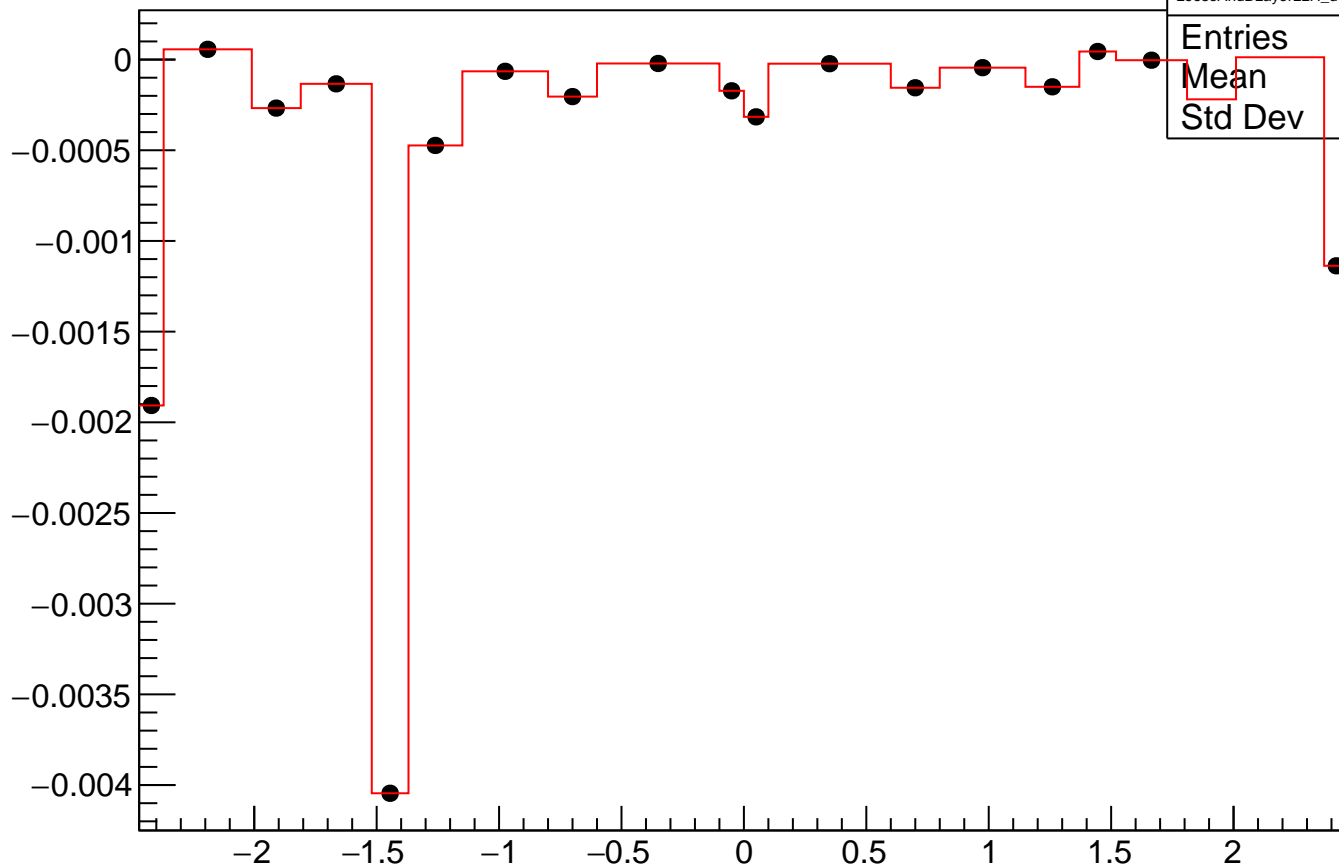
FullSim_LooseLLH_d0z0_corr9

LooseAndBLayerLLH_d0z0_ptbin5_corr9	
Entries	0
Mean	-0.9431
Std Dev	1.917

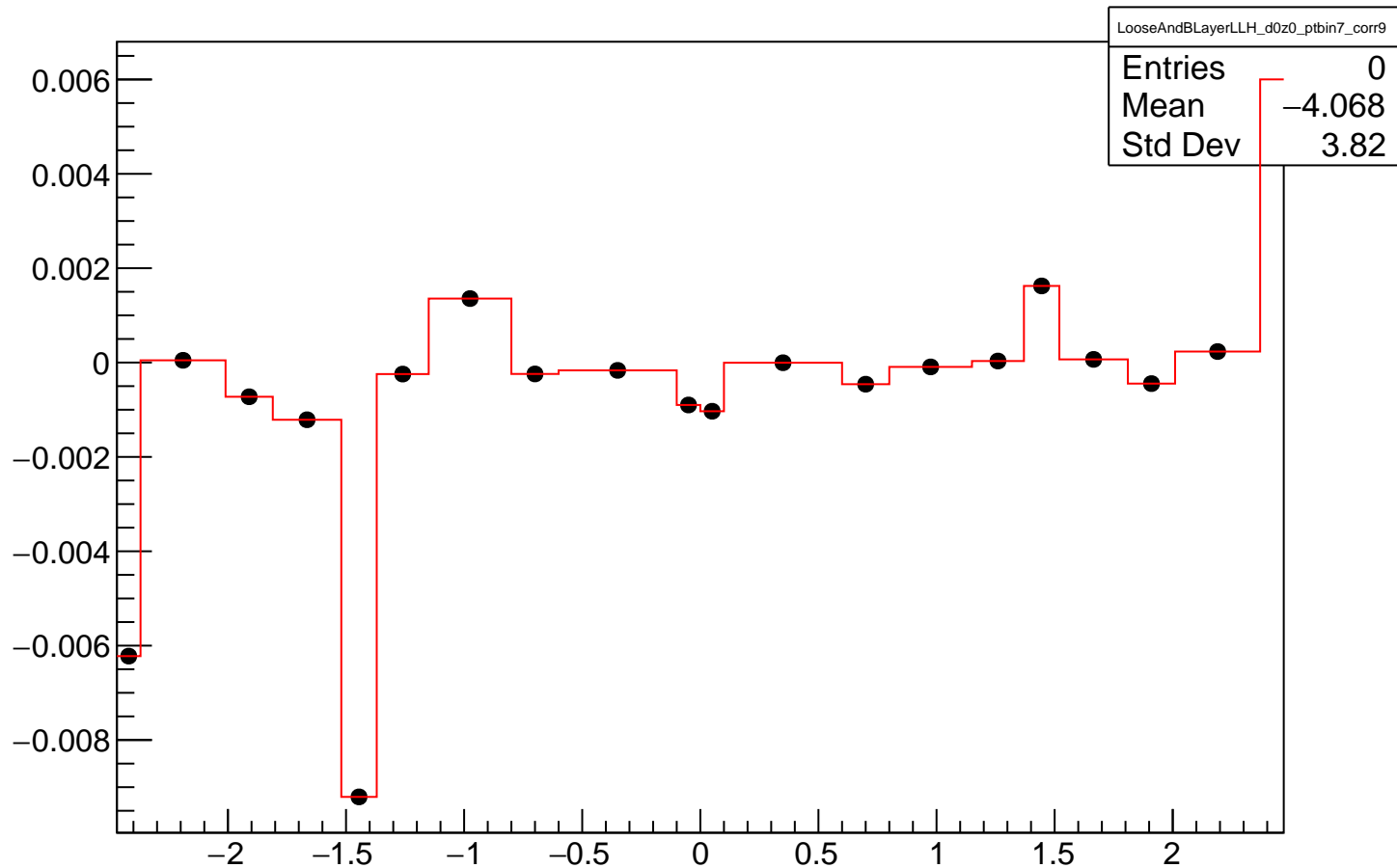


FullSim_LooseLLH_d0z0_corr9

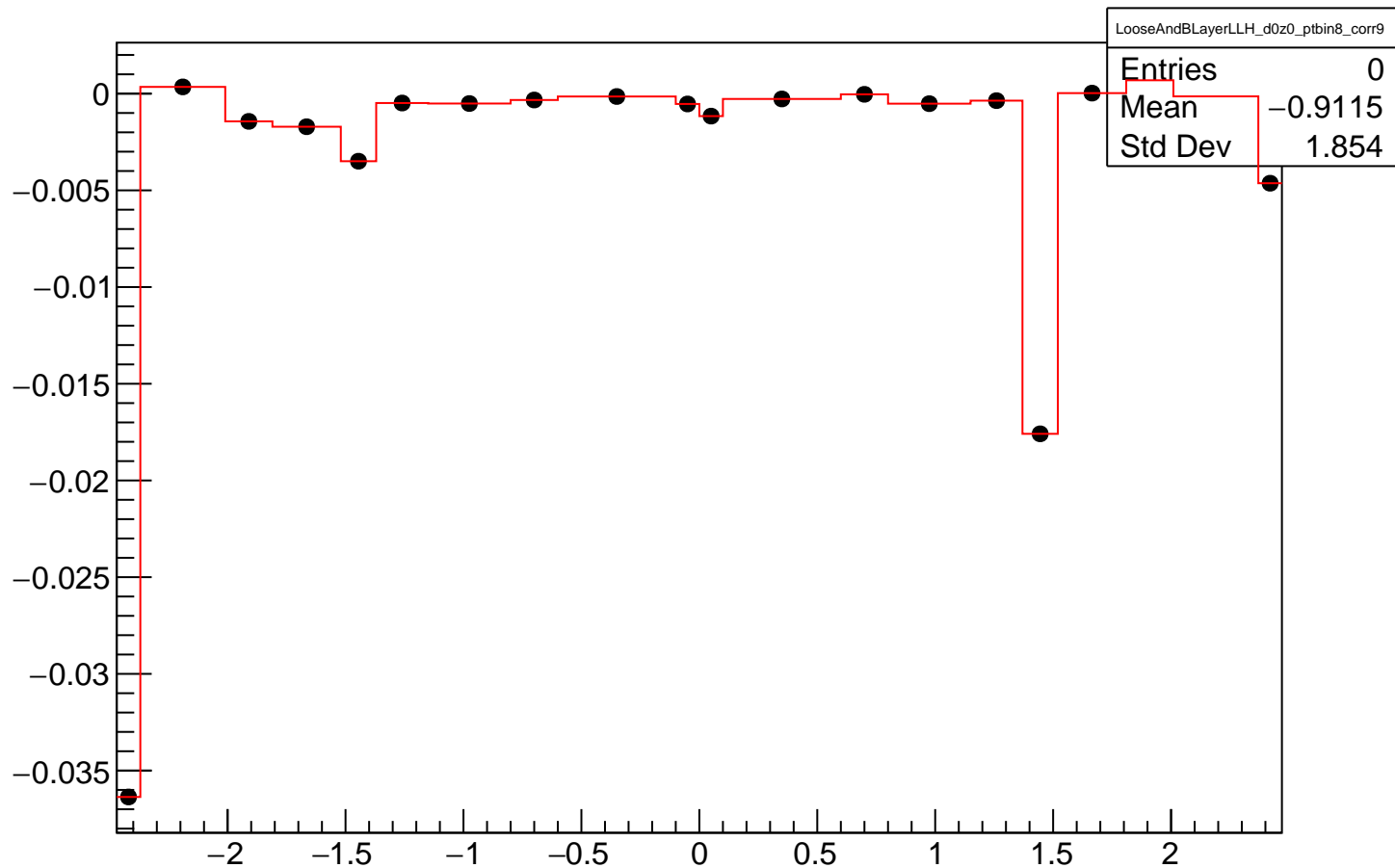
LooseAndBLayerLLH_d0z0_ptbin6_corr9	
Entries	0
Mean	-0.915
Std Dev	1.529



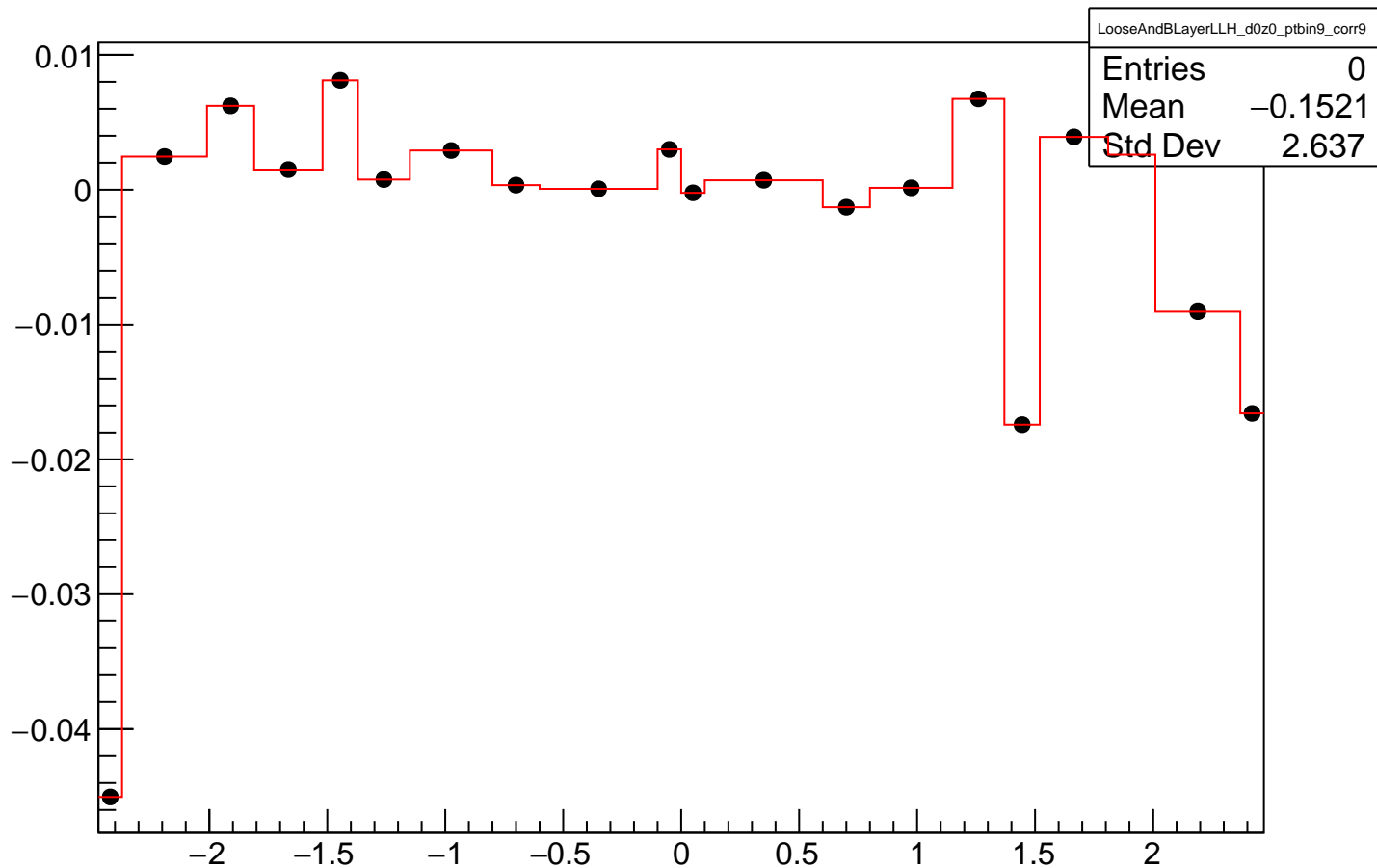
FullSim_LooseLLH_d0z0_corr9



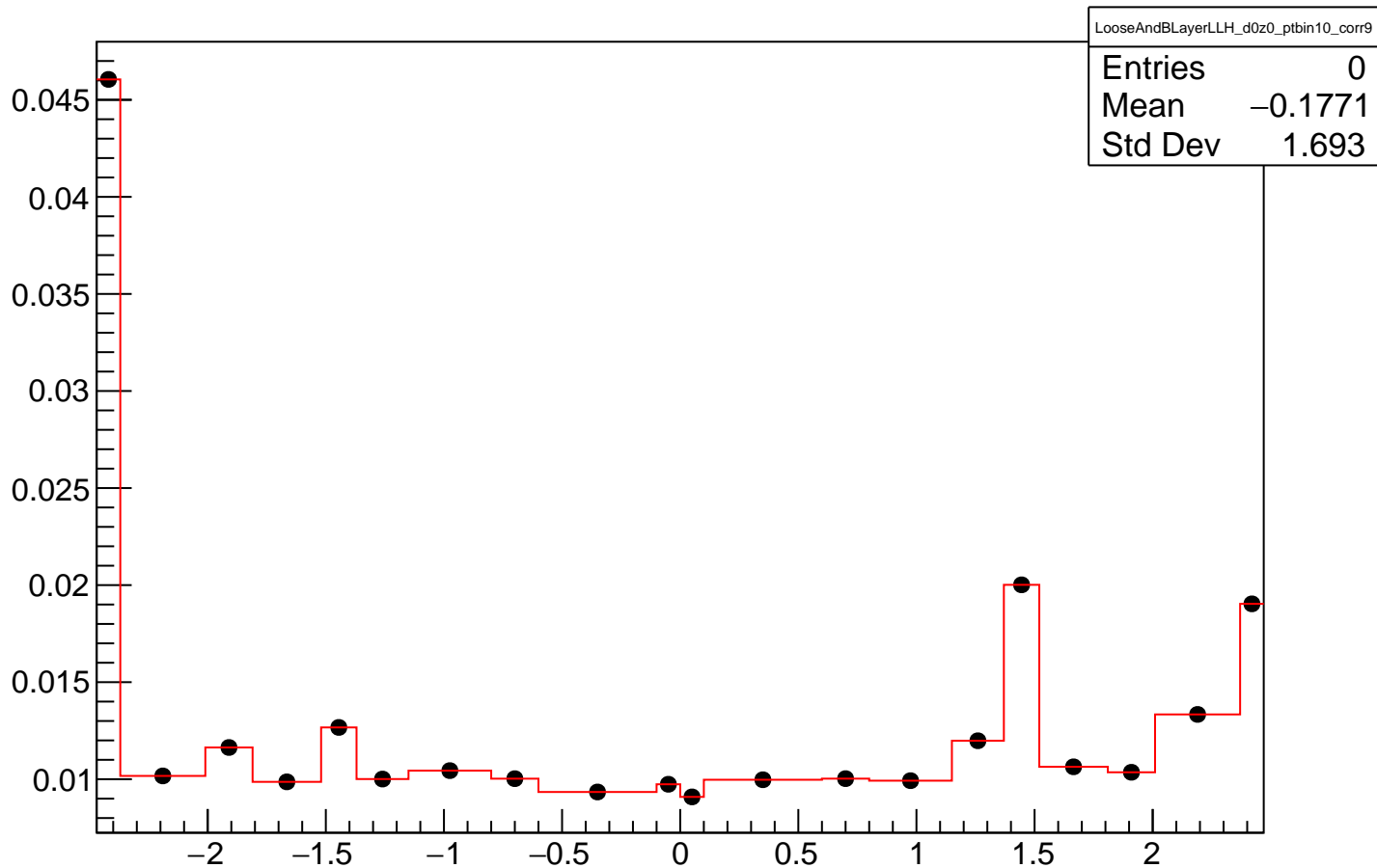
FullSim_LooseLLH_d0z0_corr9



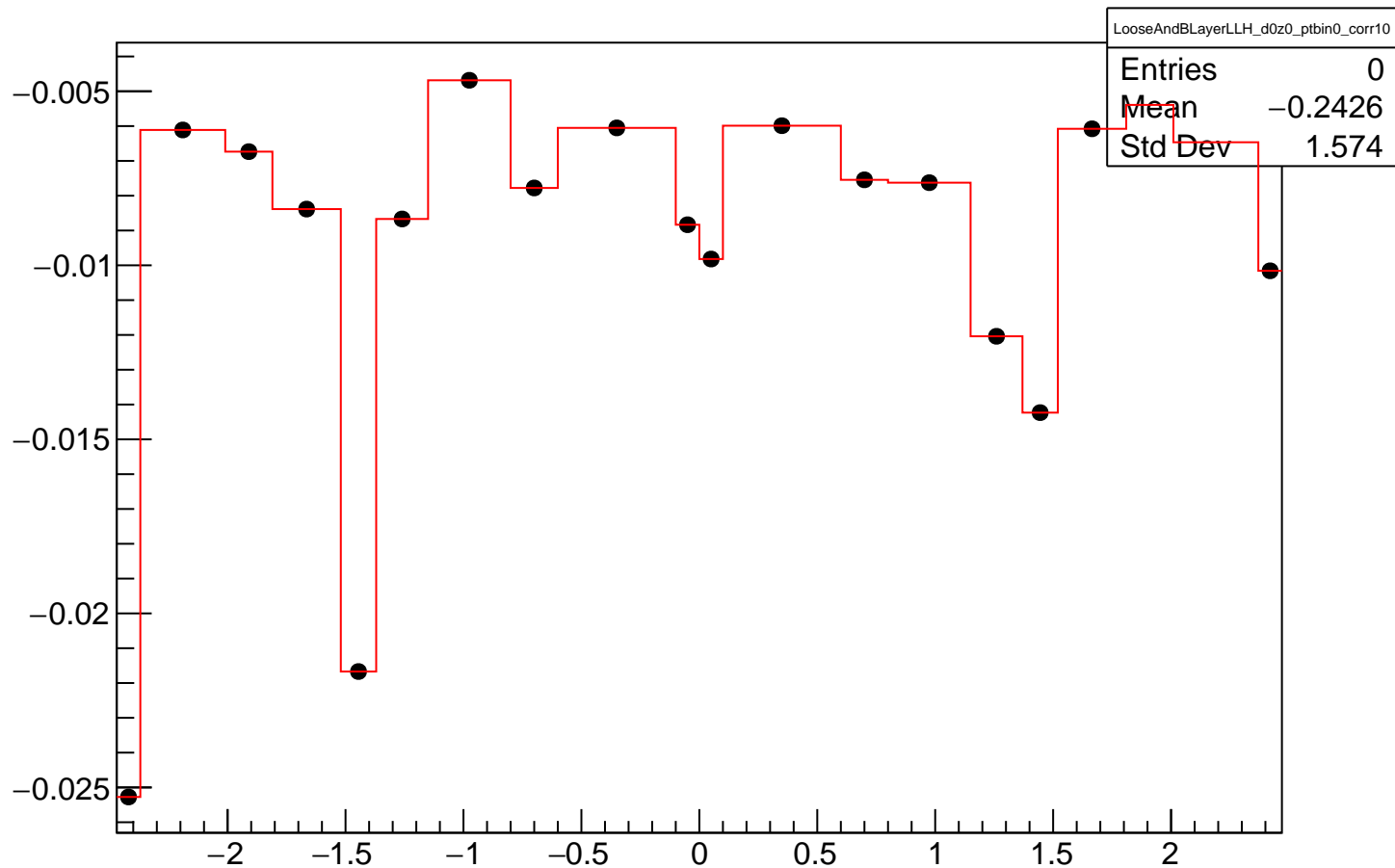
FullSim_LooseLLH_d0z0_corr9



FullSim_LooseLLH_d0z0_corr9



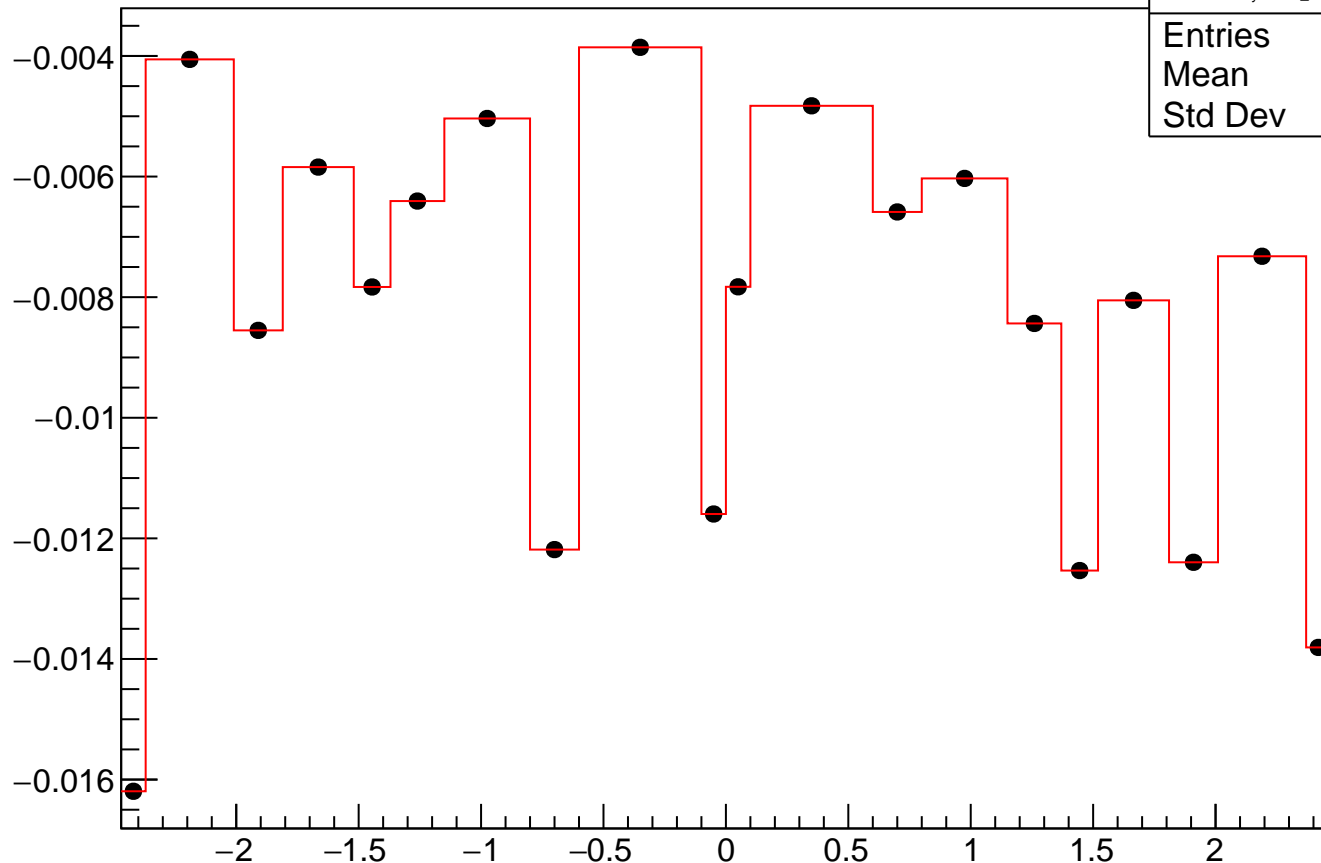
FullSim_LooseLLH_d0z0_corr10



FullSim_LooseLLH_d0z0_corr10

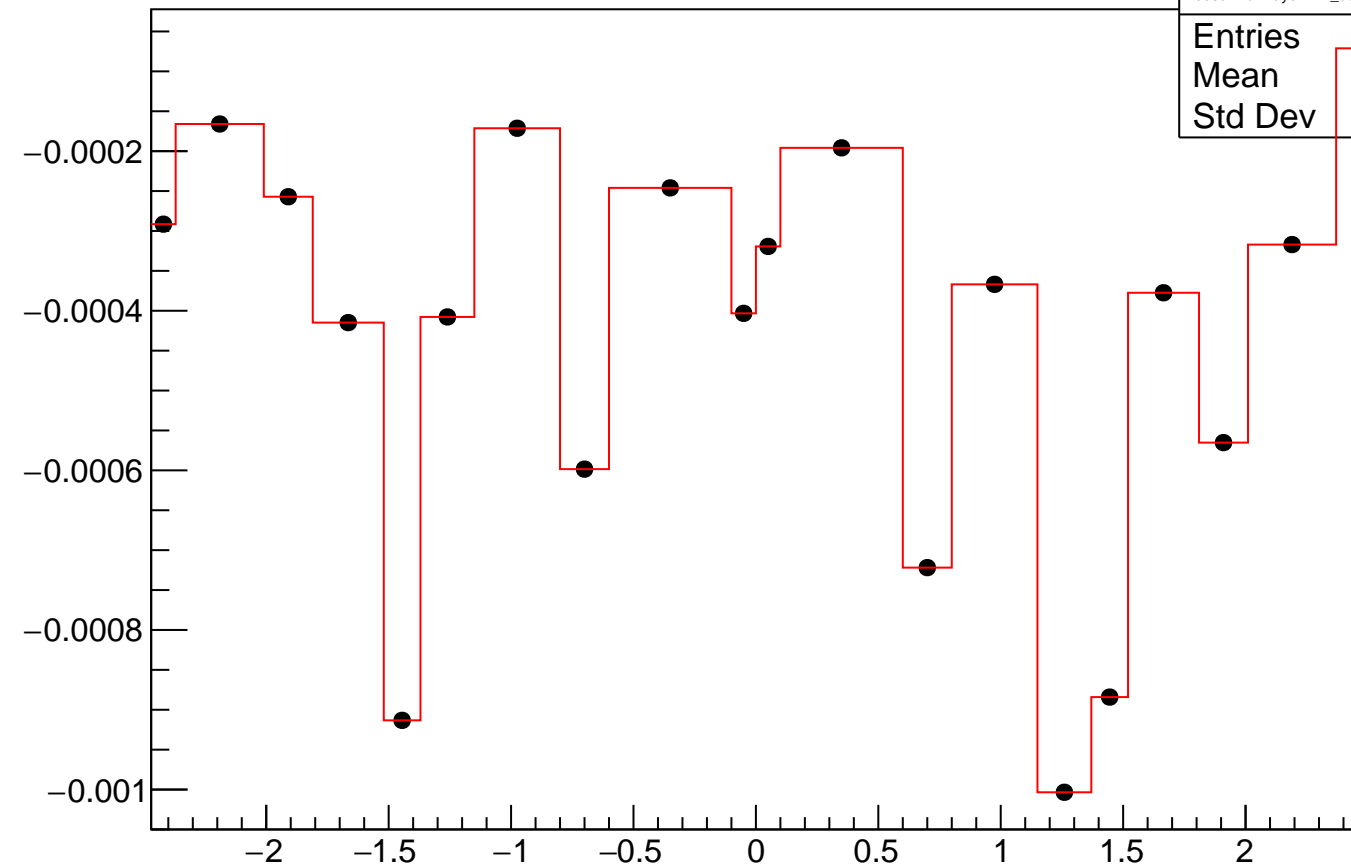
LooseAndBLayerLLH_d0z0_ptbin1_corr10

Entries	0
Mean	0.1119
Std Dev	1.593

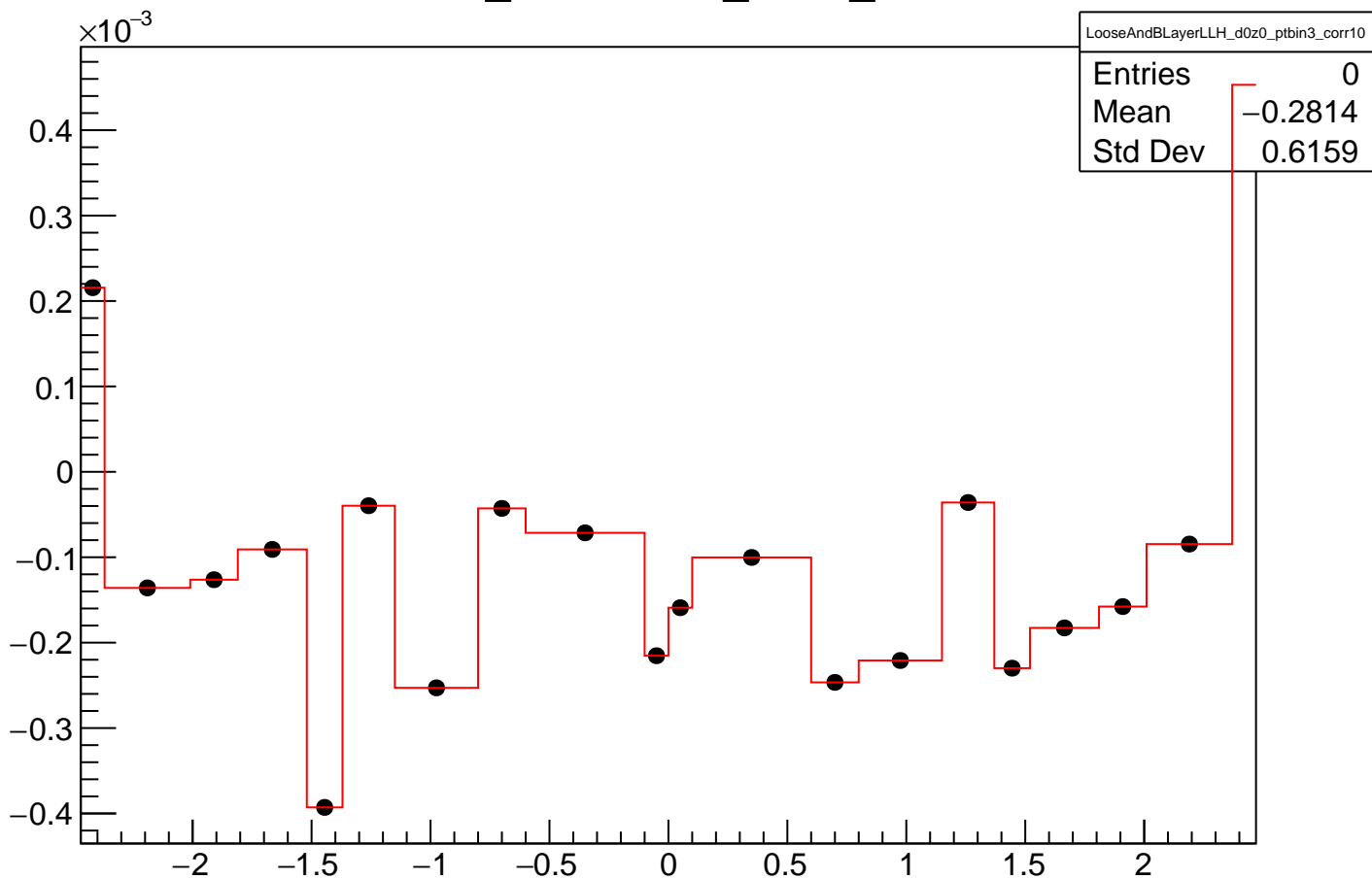


FullSim_LooseLLH_d0z0_corr10

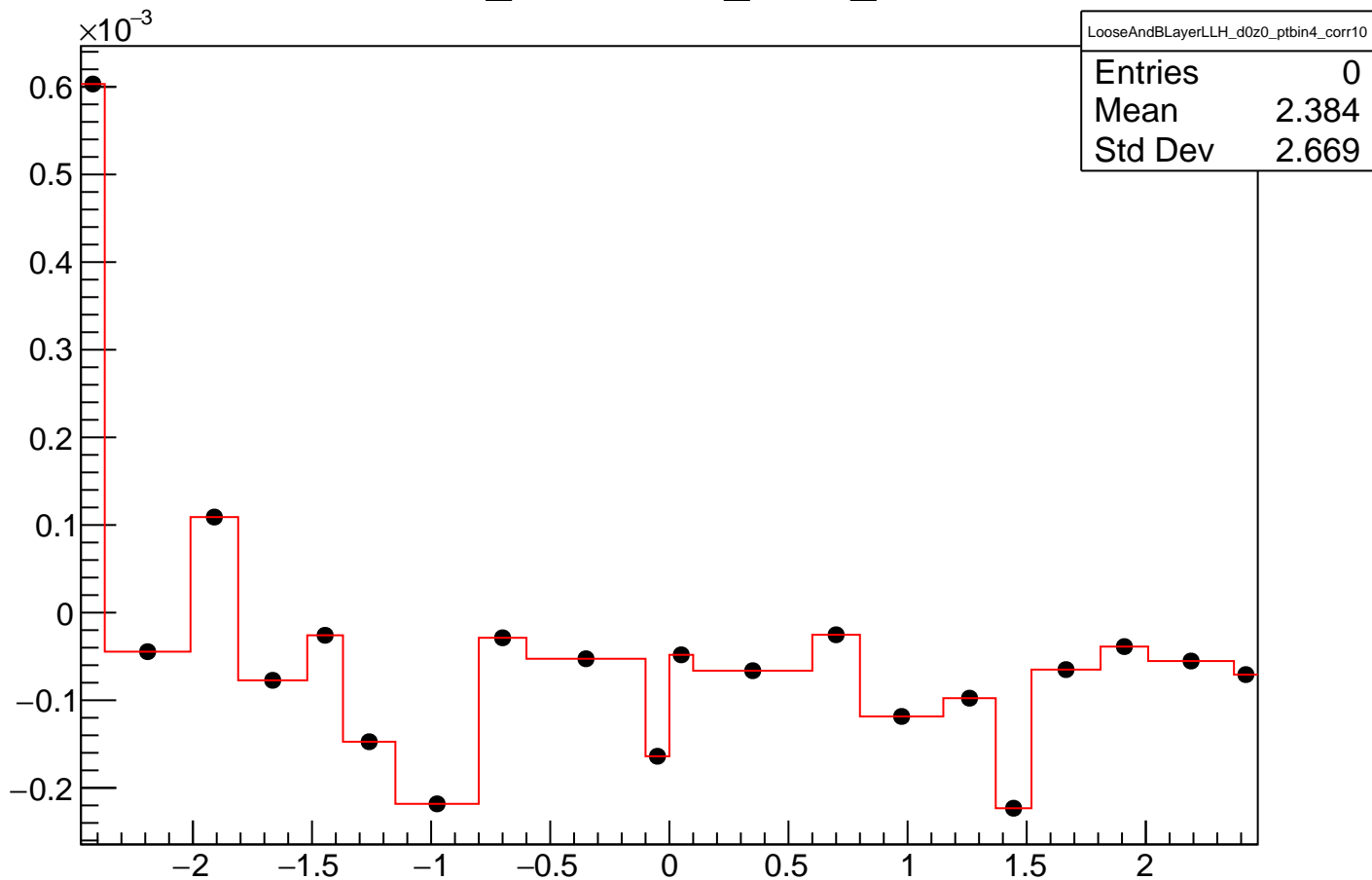
LooseAndBLayerLLH_d0z0_ptbin2_corr10	
Entries	0
Mean	0.1482
Std Dev	1.384



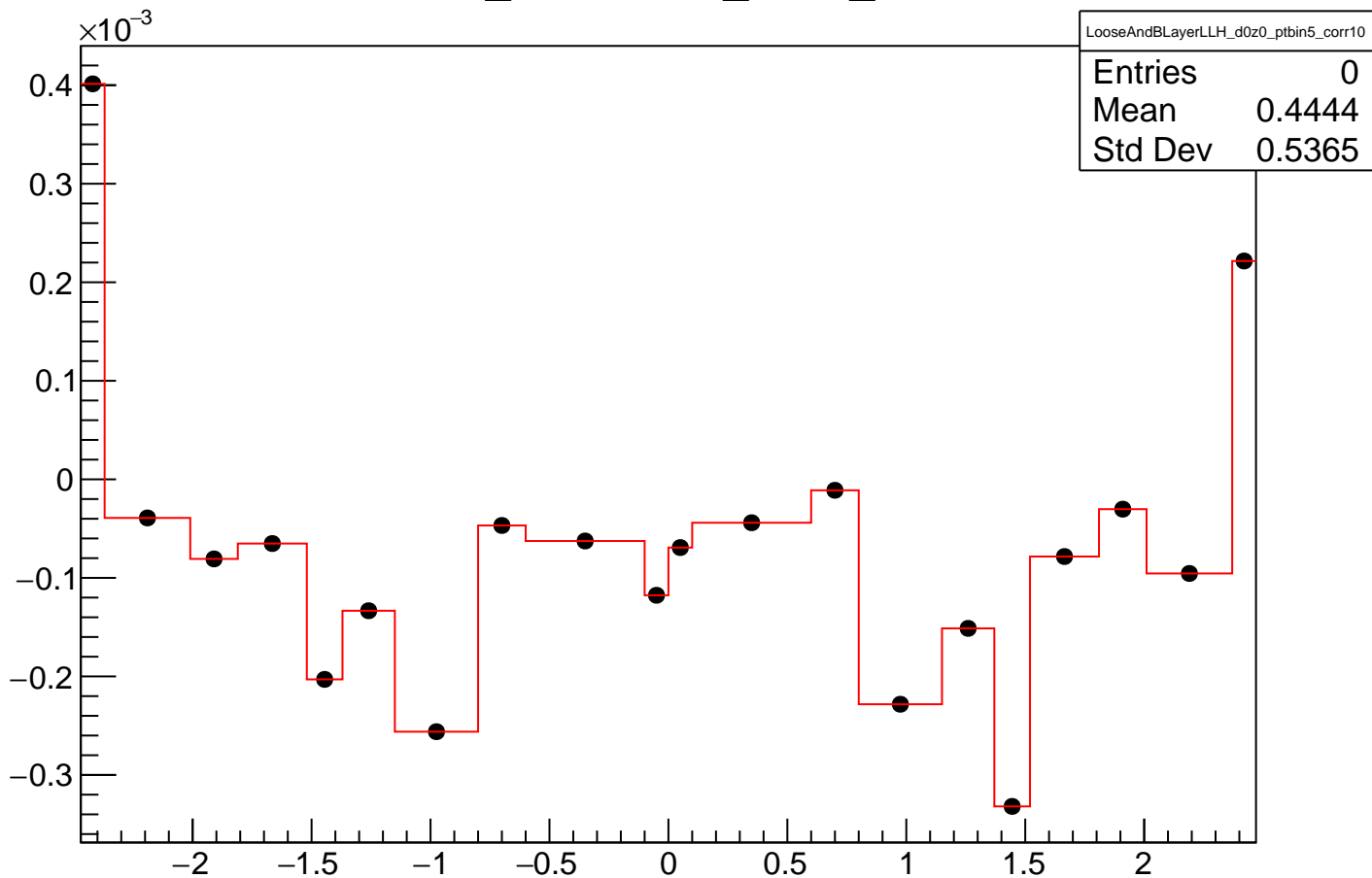
FullSim_LooseLLH_d0z0_corr10



FullSim_LooseLLH_d0z0_corr10



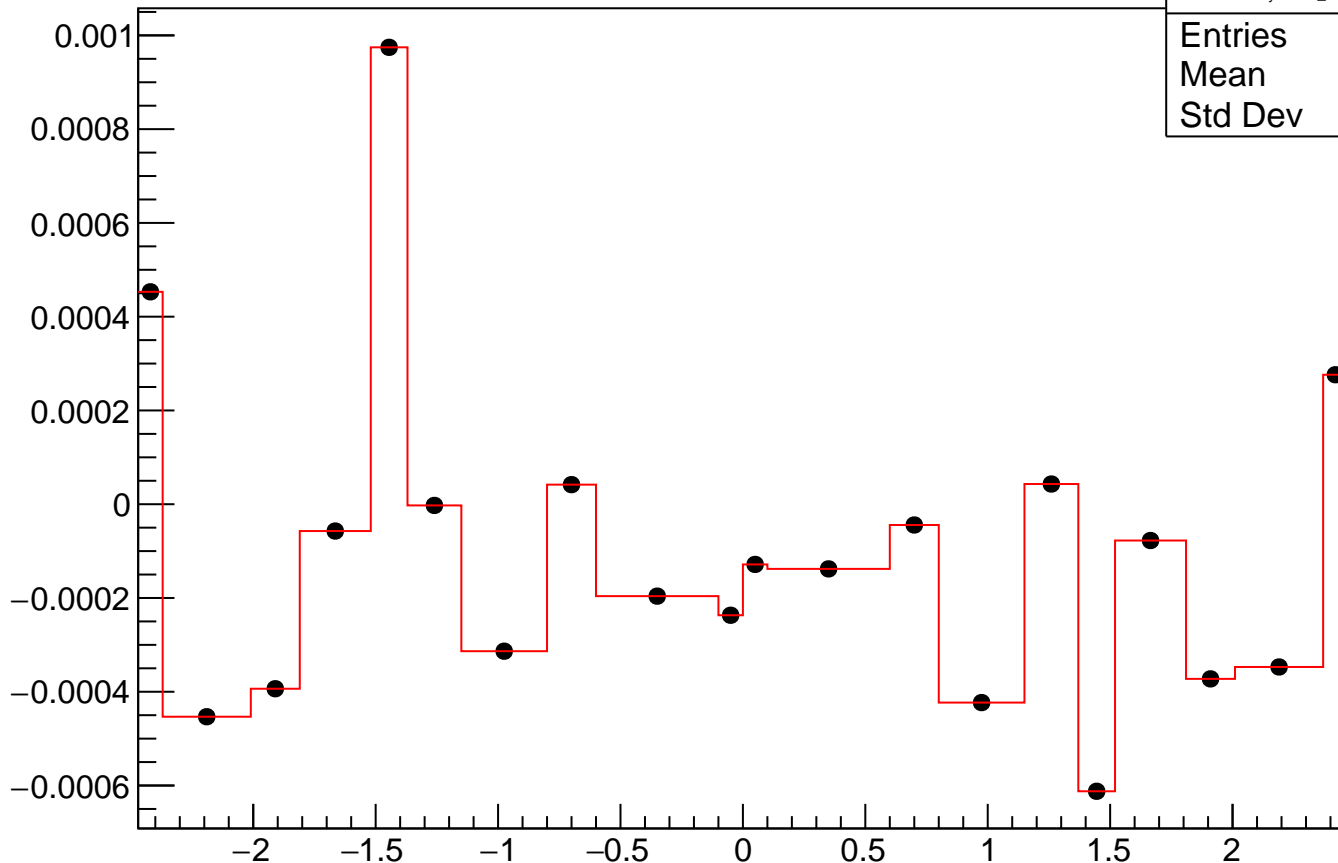
FullSim_LooseLLH_d0z0_corr10



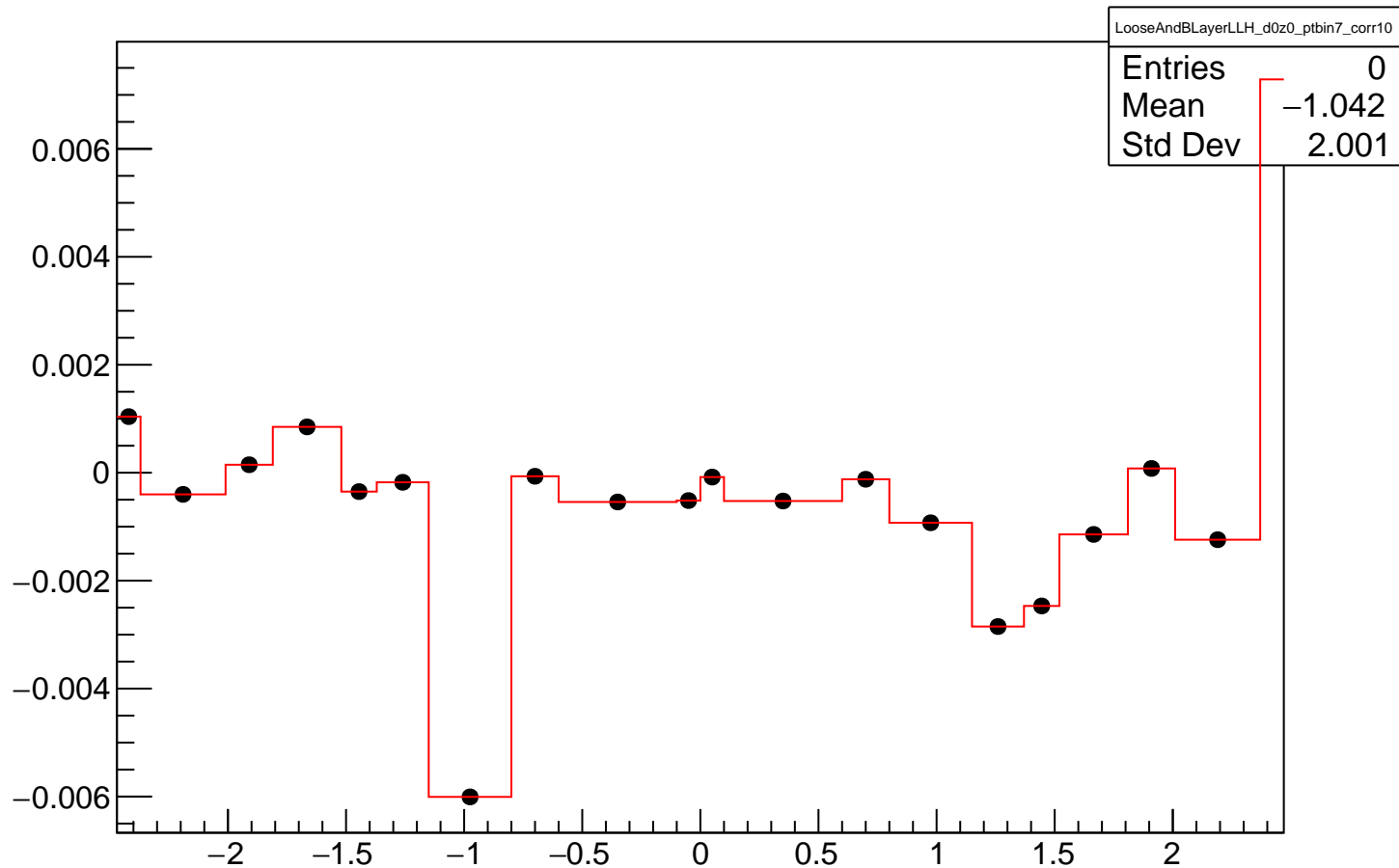
FullSim_LooseLLH_d0z0_corr10

LooseAndBLayerLLH_d0z0_ptbin6_corr10

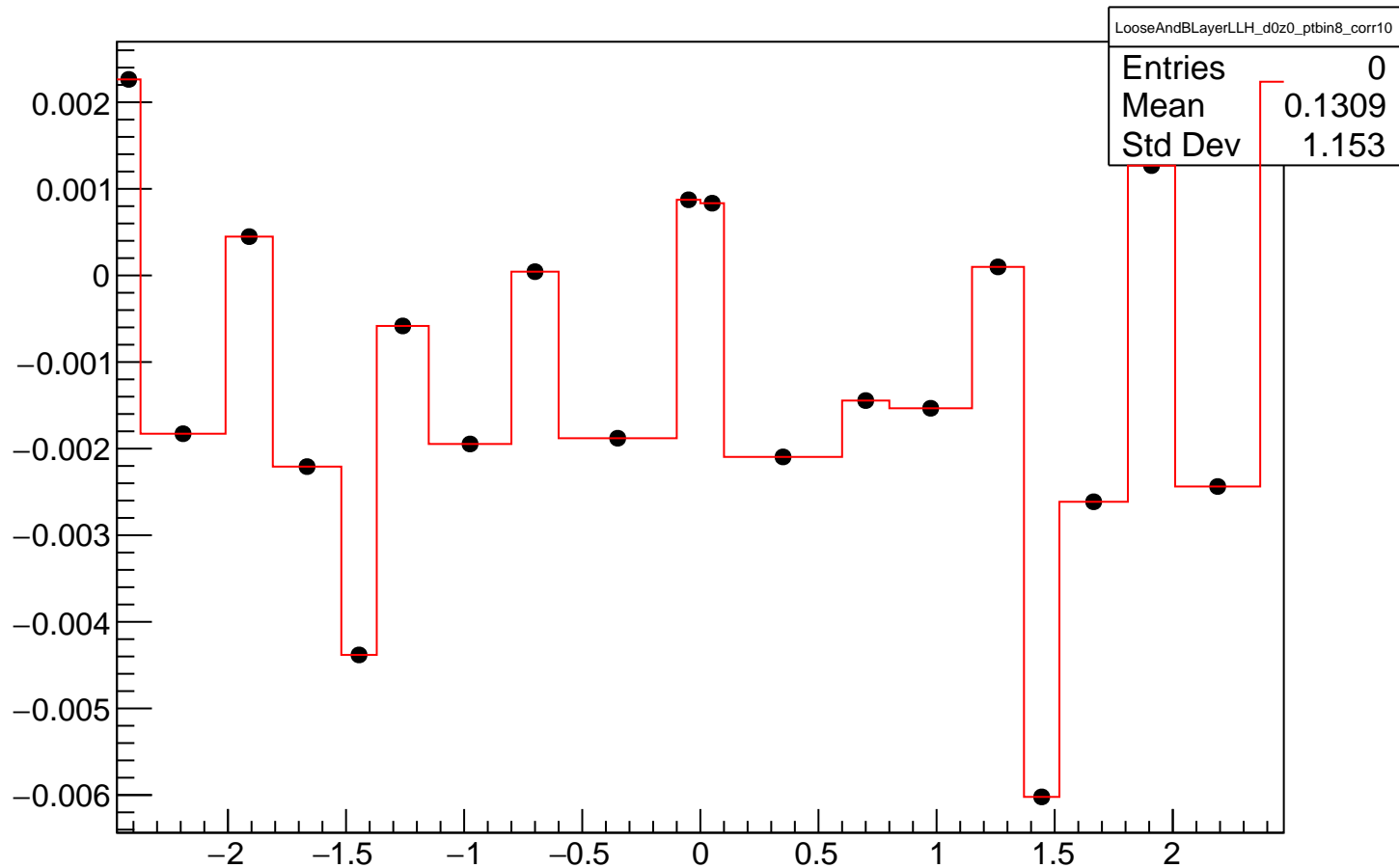
Entries	0
Mean	1.278
Std Dev	0.5558



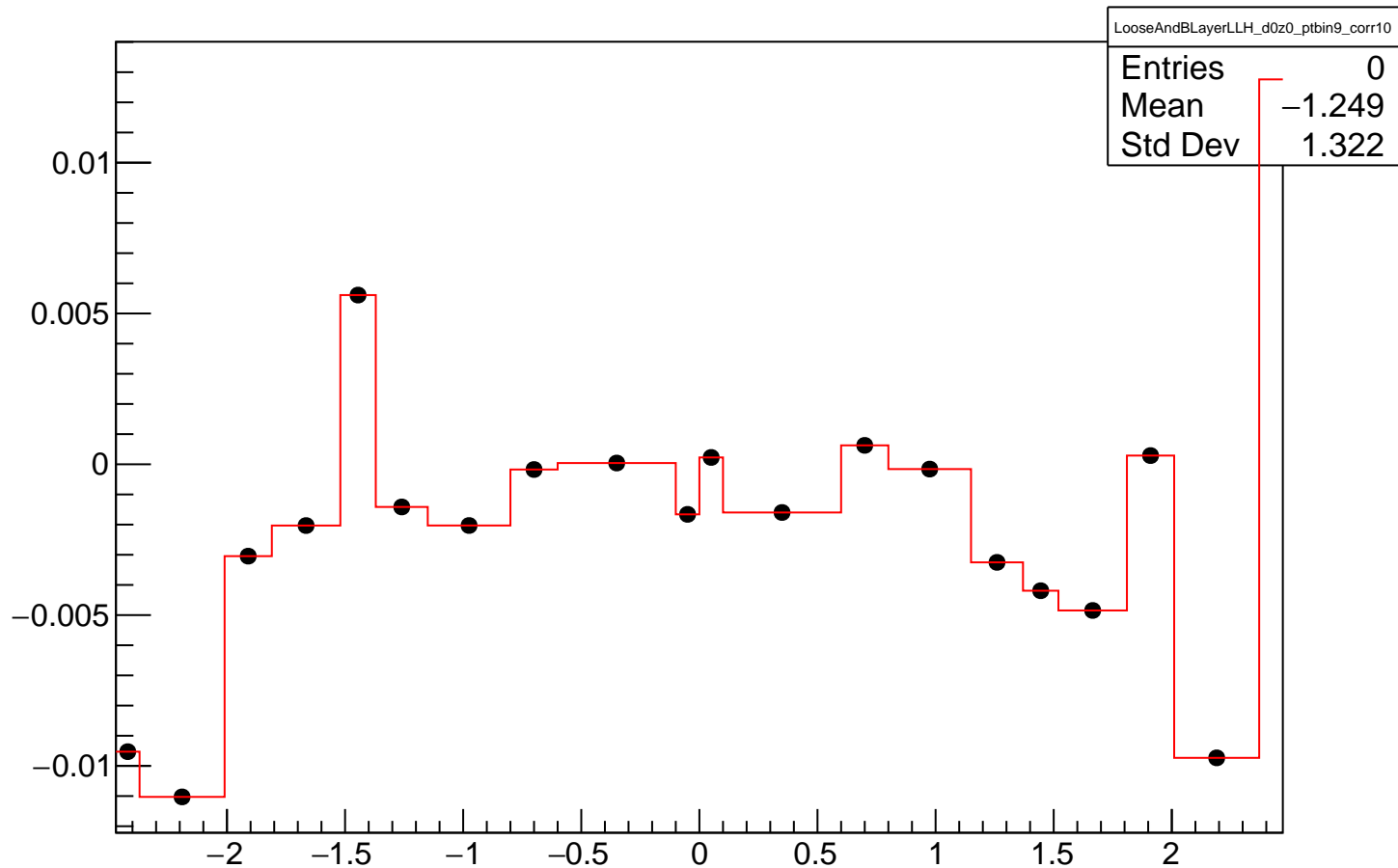
FullSim_LooseLLH_d0z0_corr10



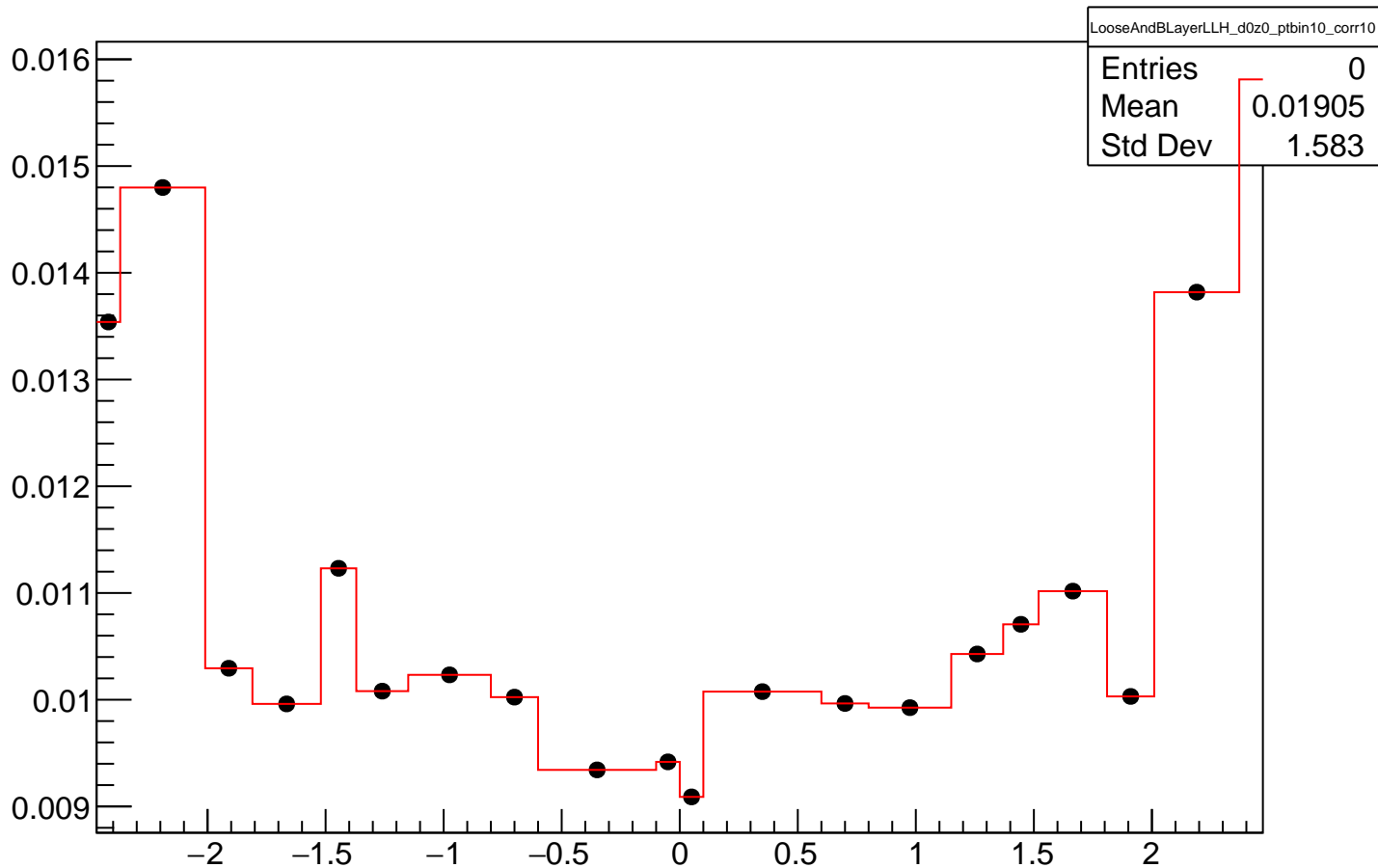
FullSim_LooseLLH_d0z0_corr10



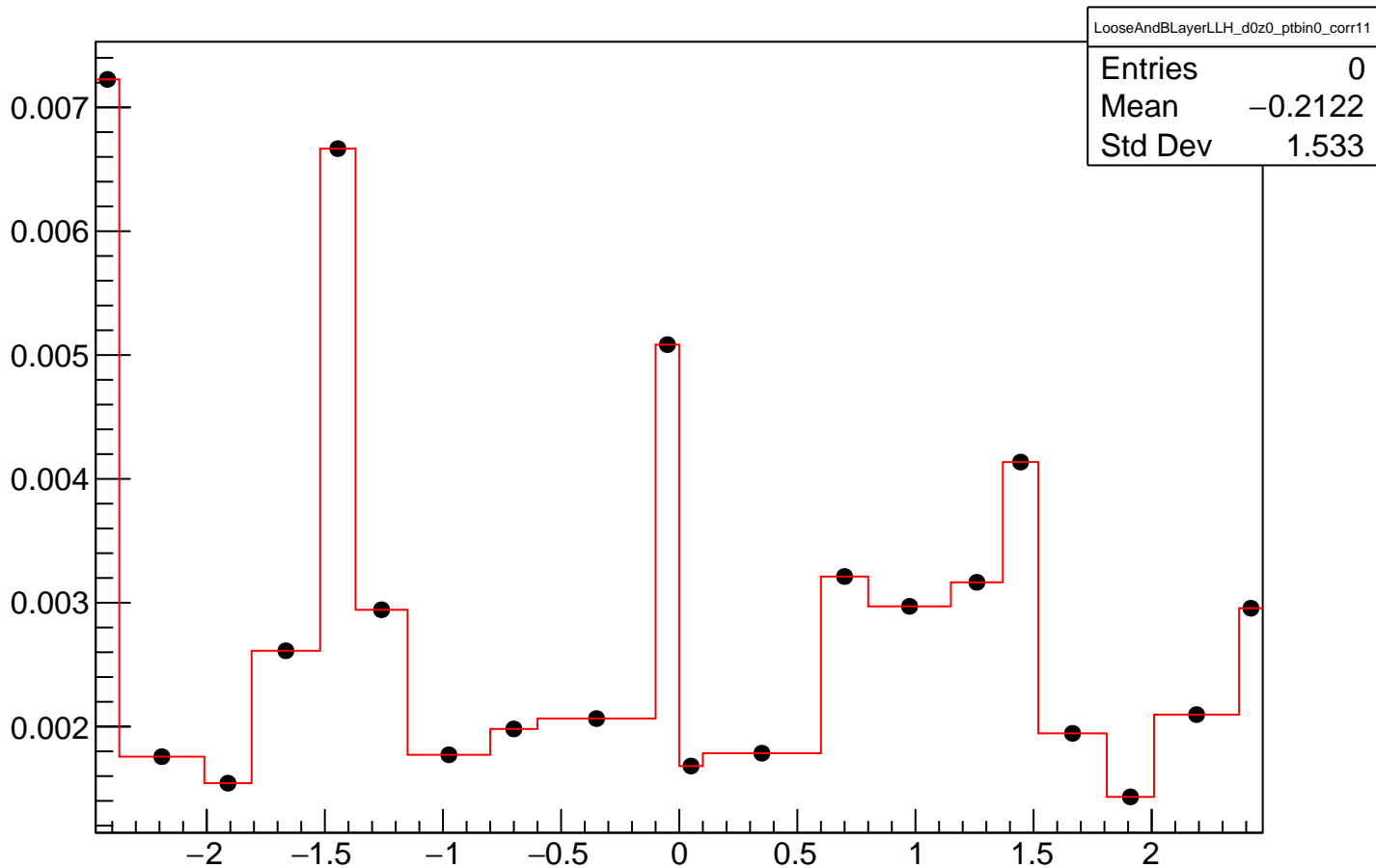
FullSim_LooseLLH_d0z0_corr10



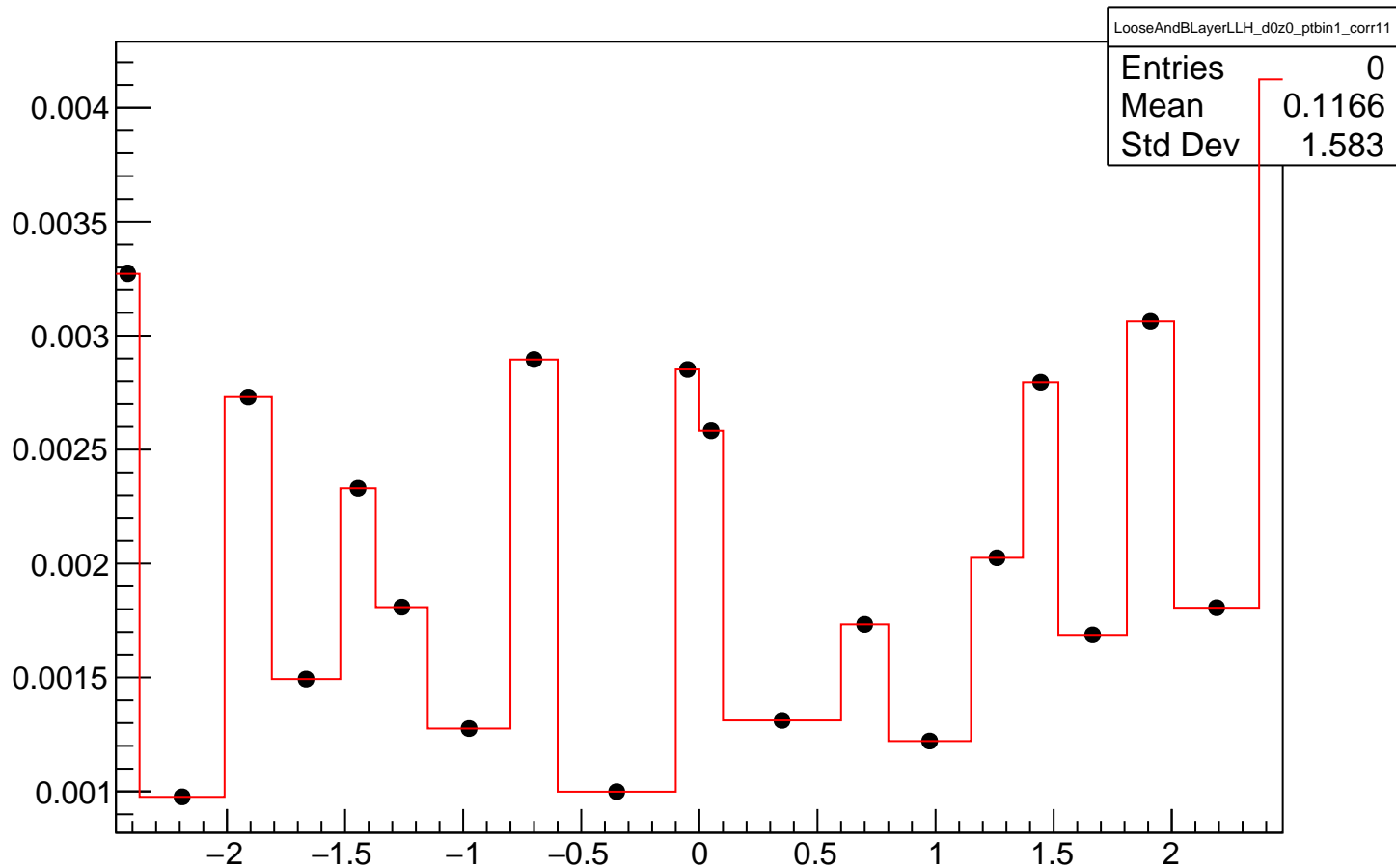
FullSim_LooseLLH_d0z0_corr10



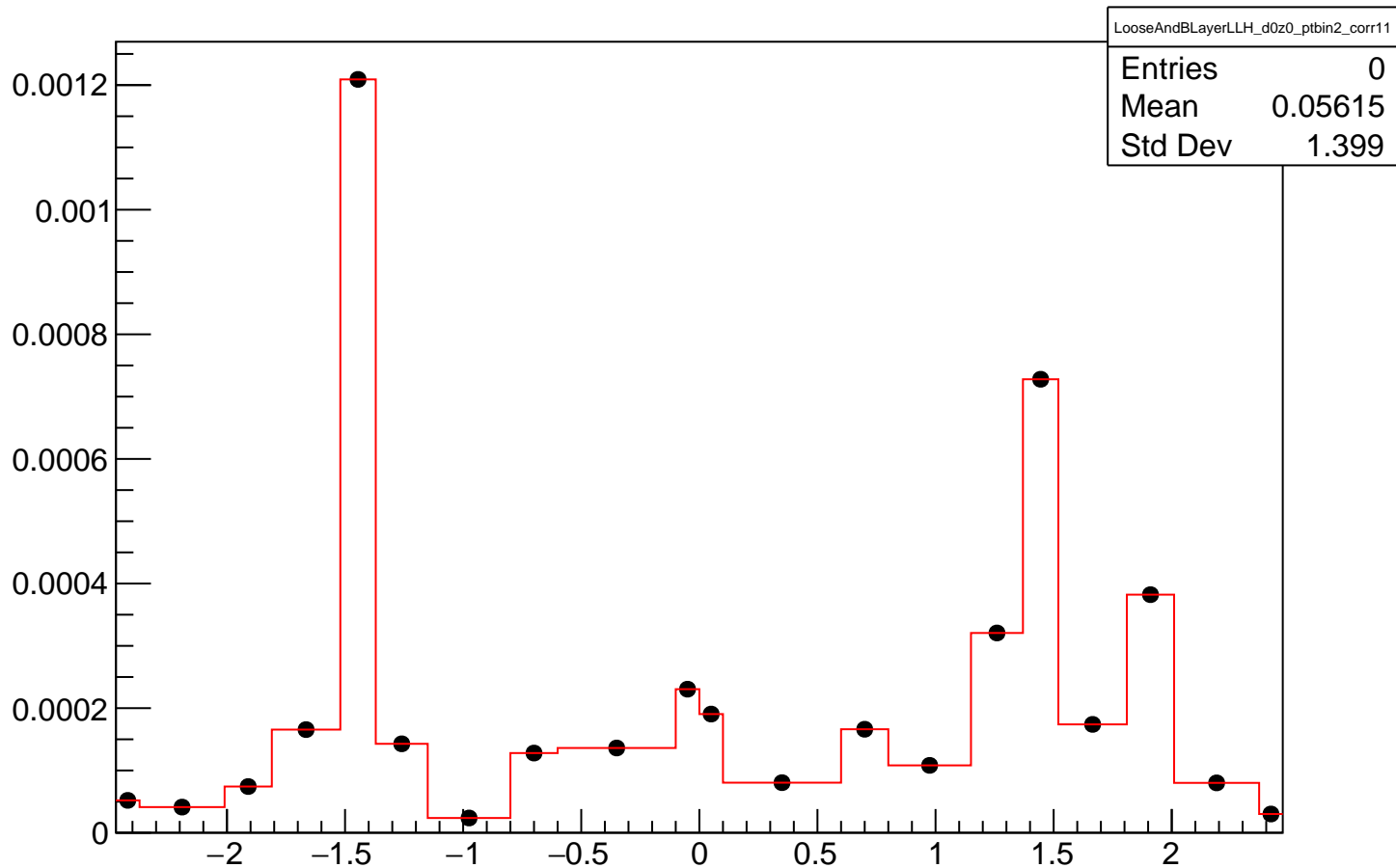
FullSim_LooseLLH_d0z0_corr11



FullSim_LooseLLH_d0z0_corr11



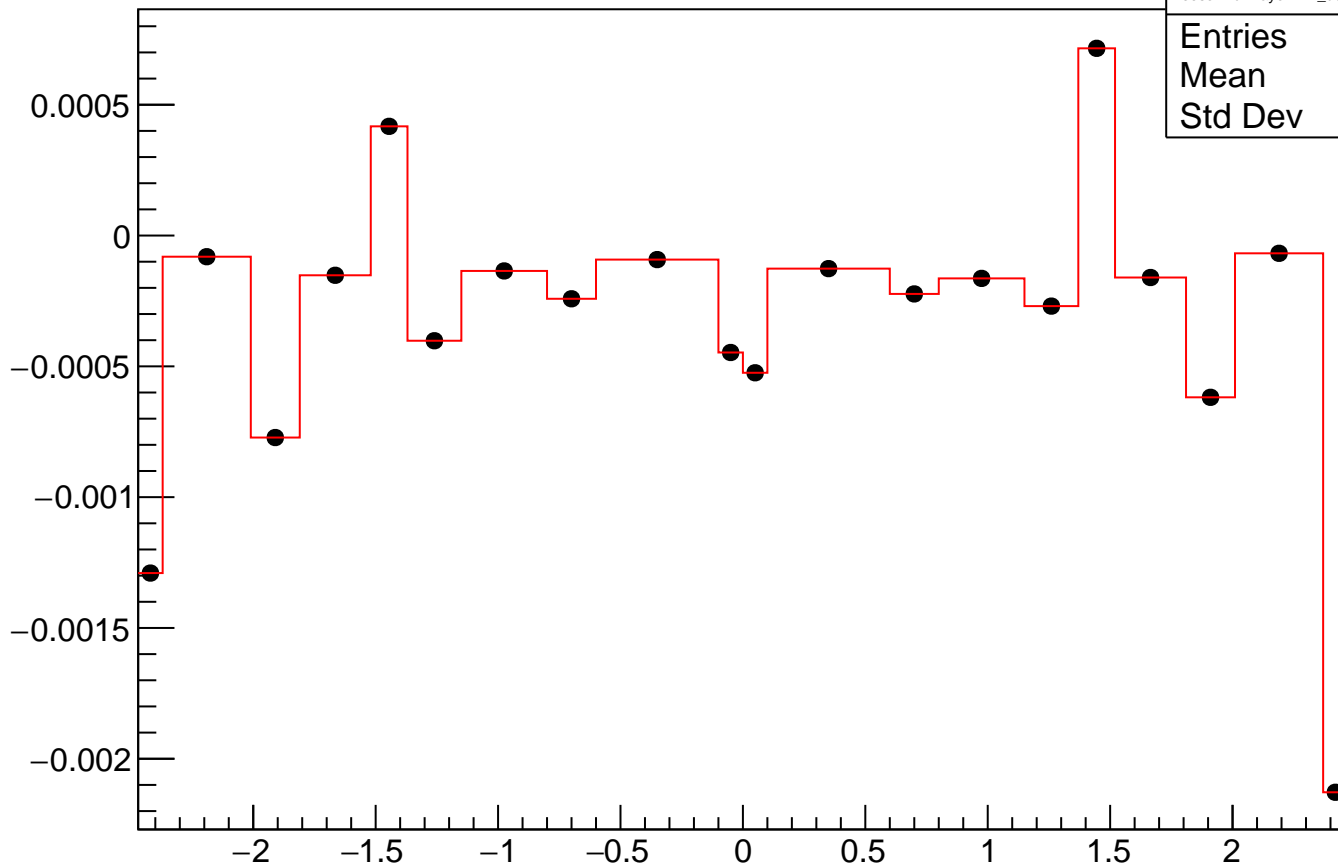
FullSim_LooseLLH_d0z0_corr11



FullSim_LooseLLH_d0z0_corr11

LooseAndBLayerLLH_d0z0_ptbin3_corr11

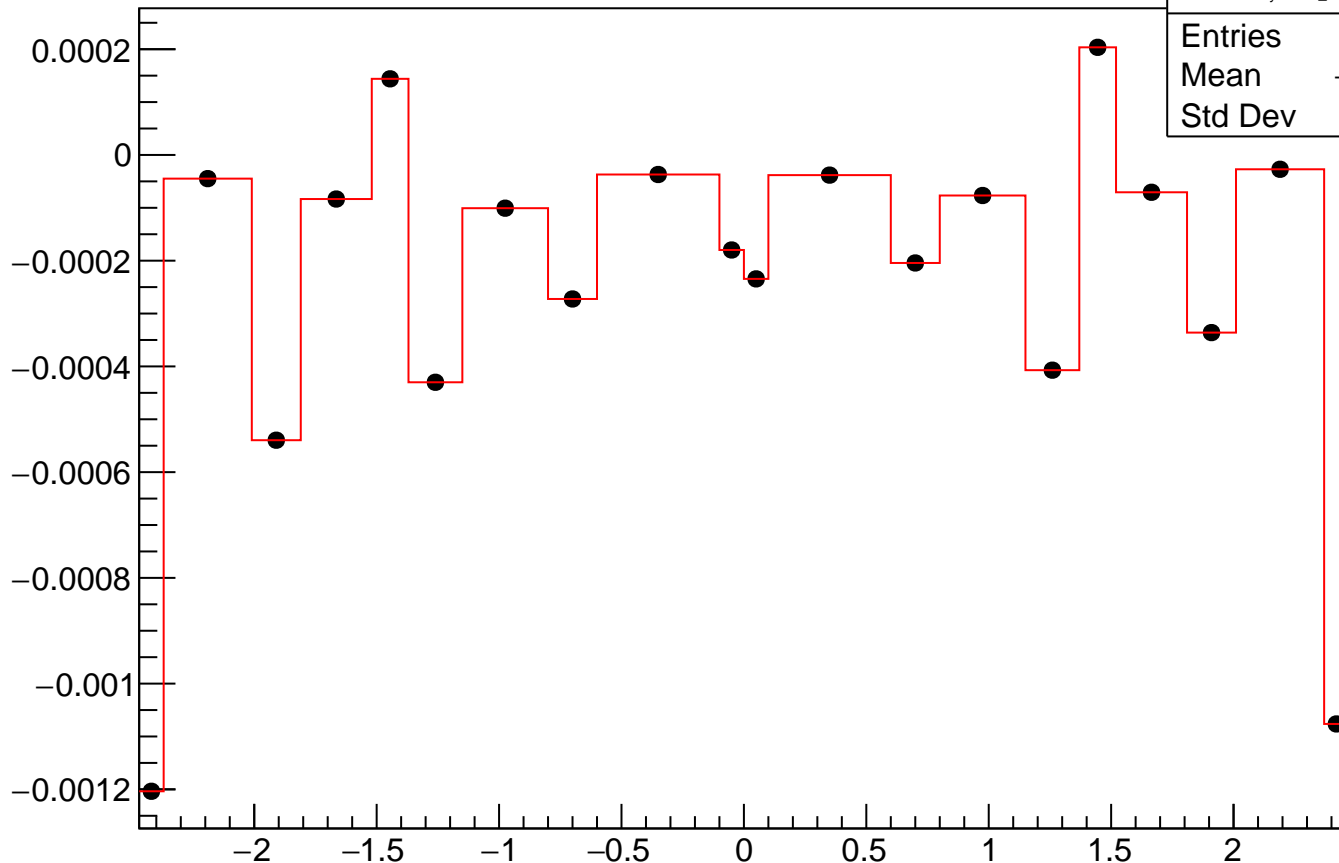
Entries	0
Mean	0.1705
Std Dev	1.95



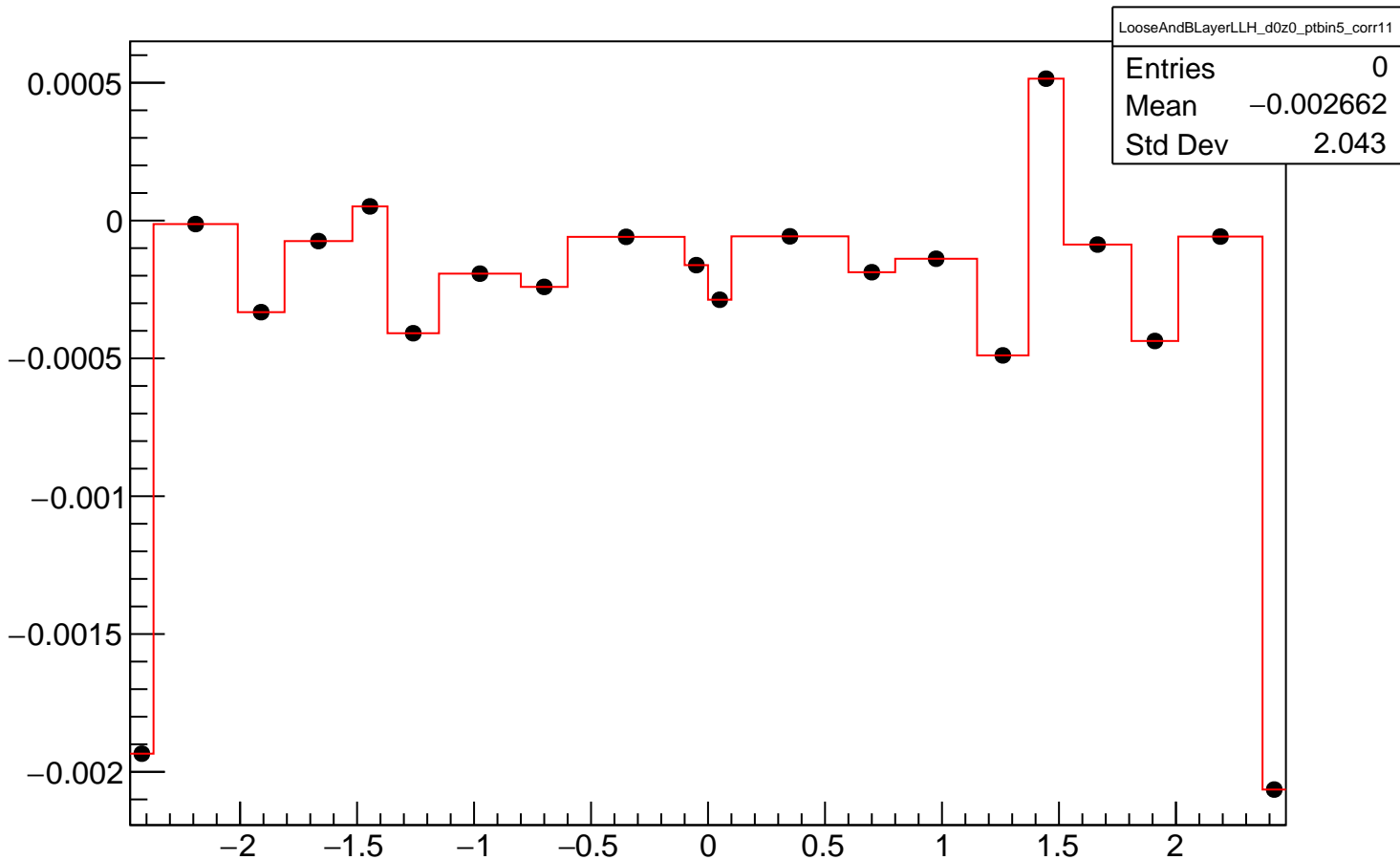
FullSim_LooseLLH_d0z0_corr11

LooseAndBLayerLLH_d0z0_ptbin4_corr11

Entries	0
Mean	-0.1874
Std Dev	1.903

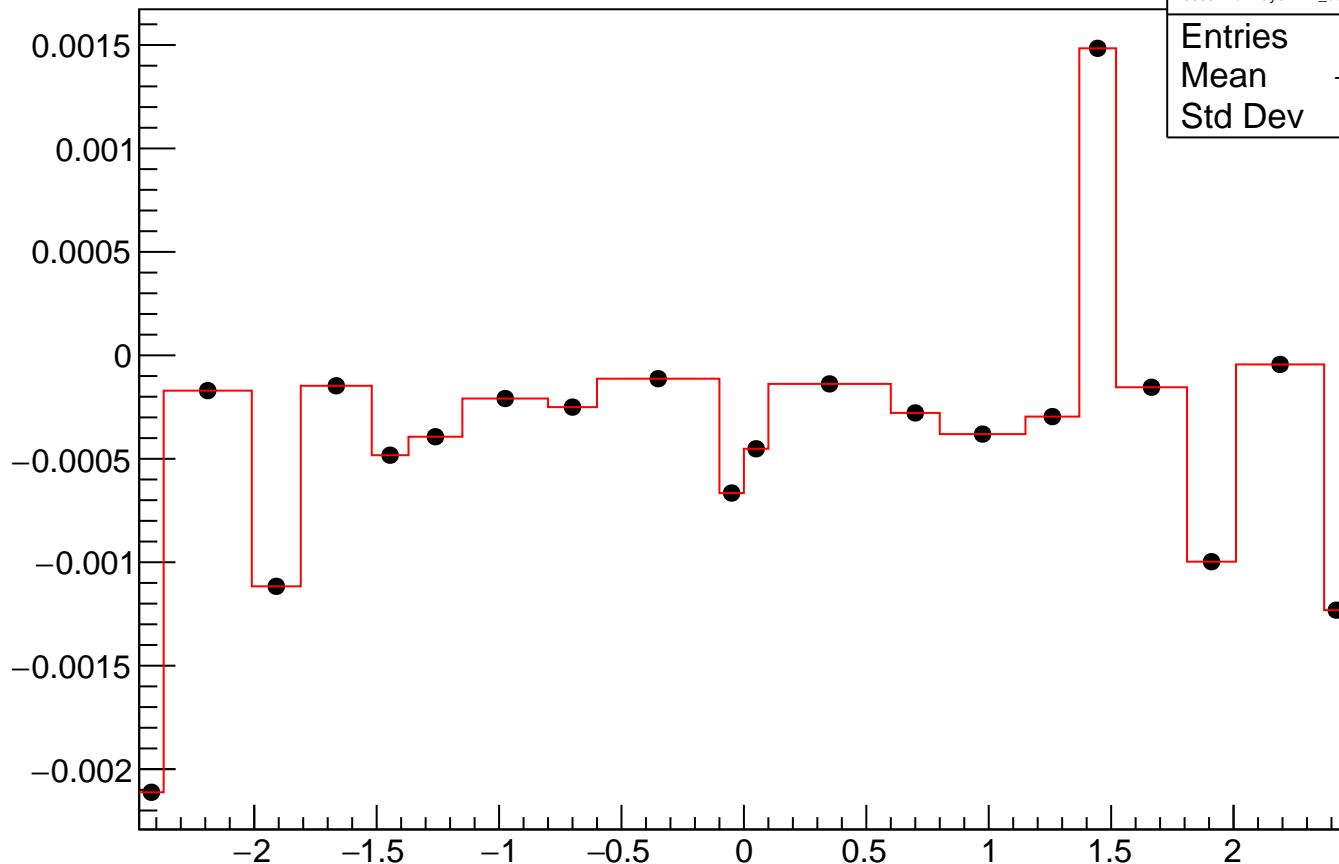


FullSim_LooseLLH_d0z0_corr11

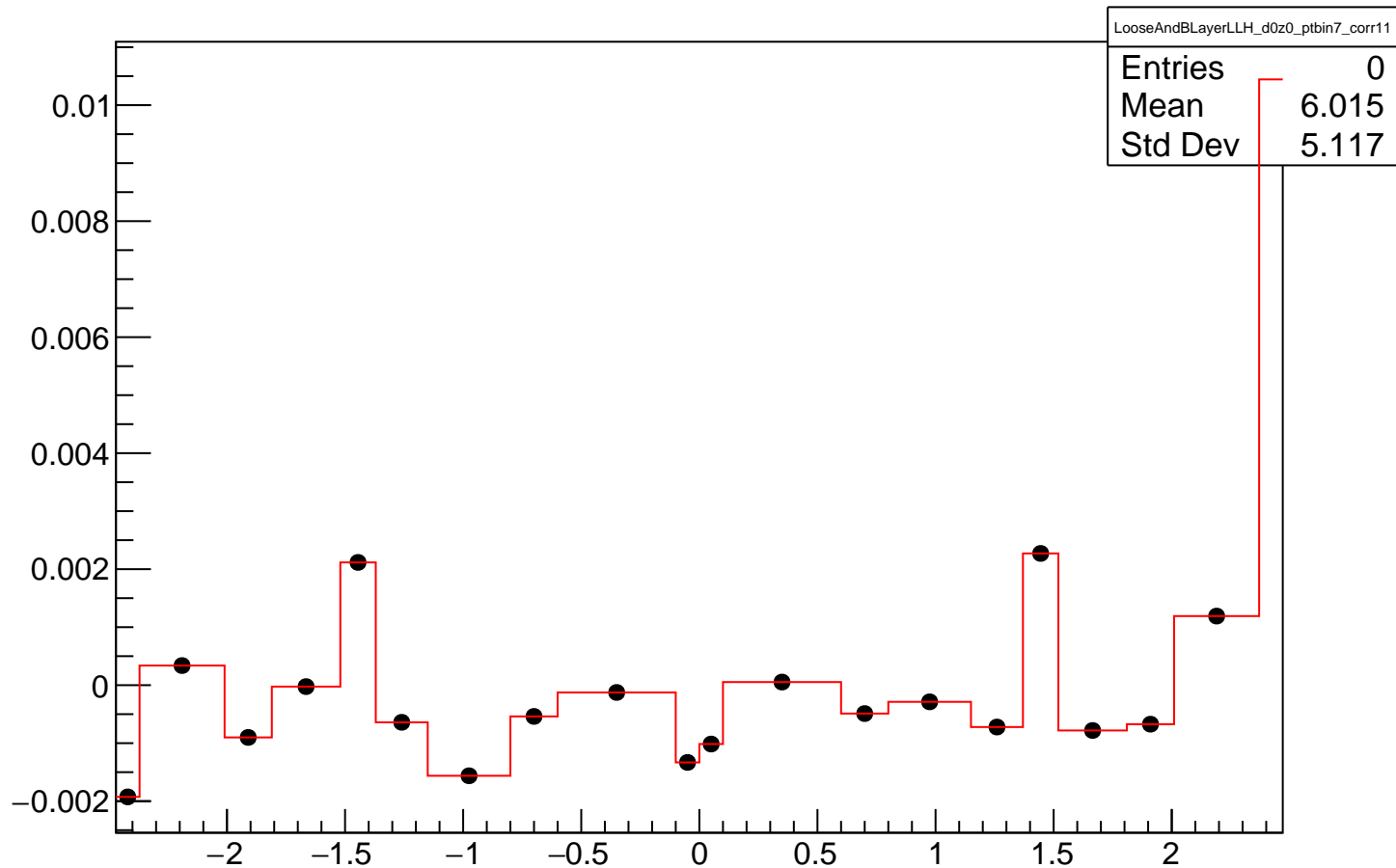


FullSim_LooseLLH_d0z0_corr11

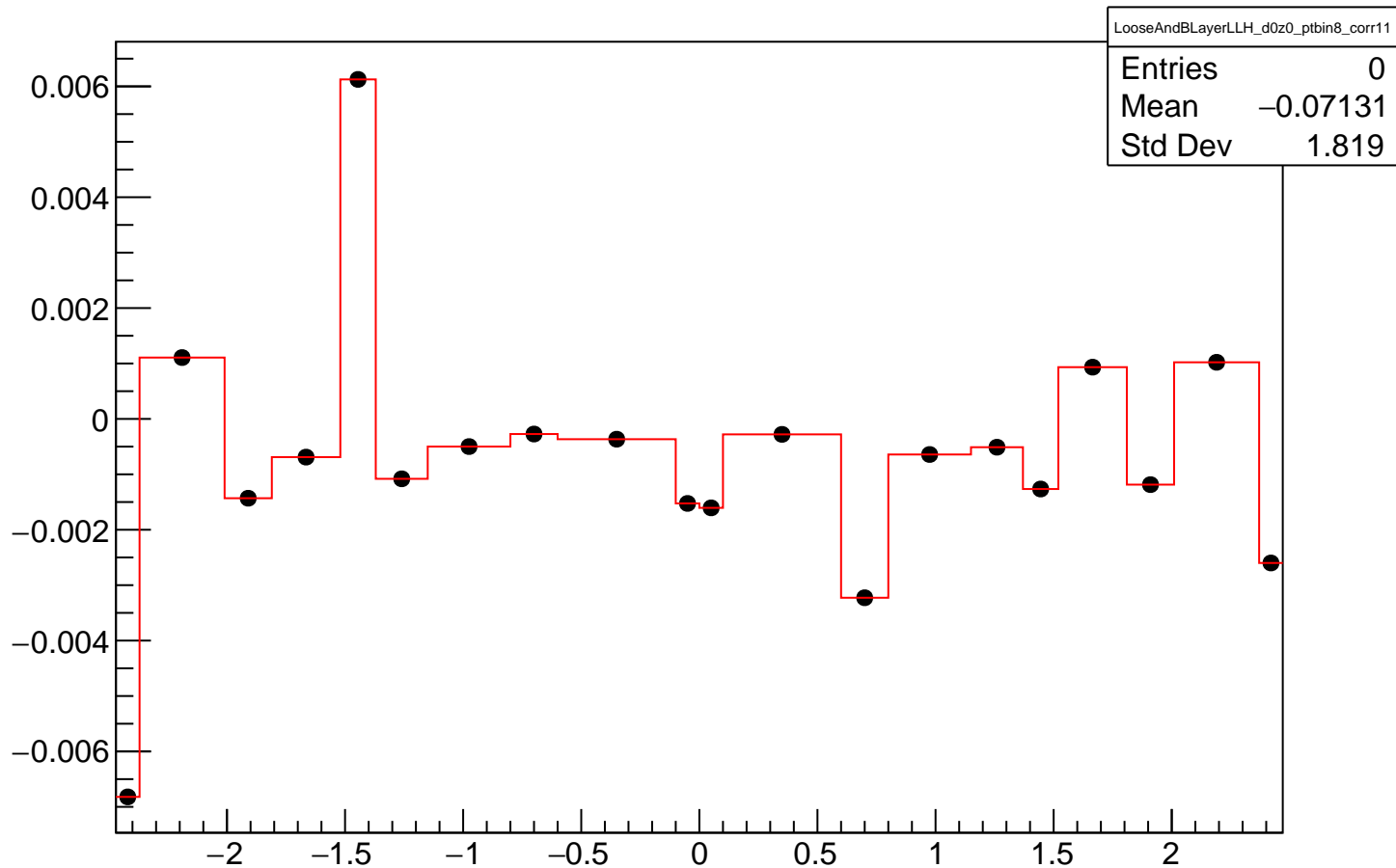
LooseAndBLayerLLH_d0z0_ptbin6_corr11	
Entries	0
Mean	-0.6631
Std Dev	1.767



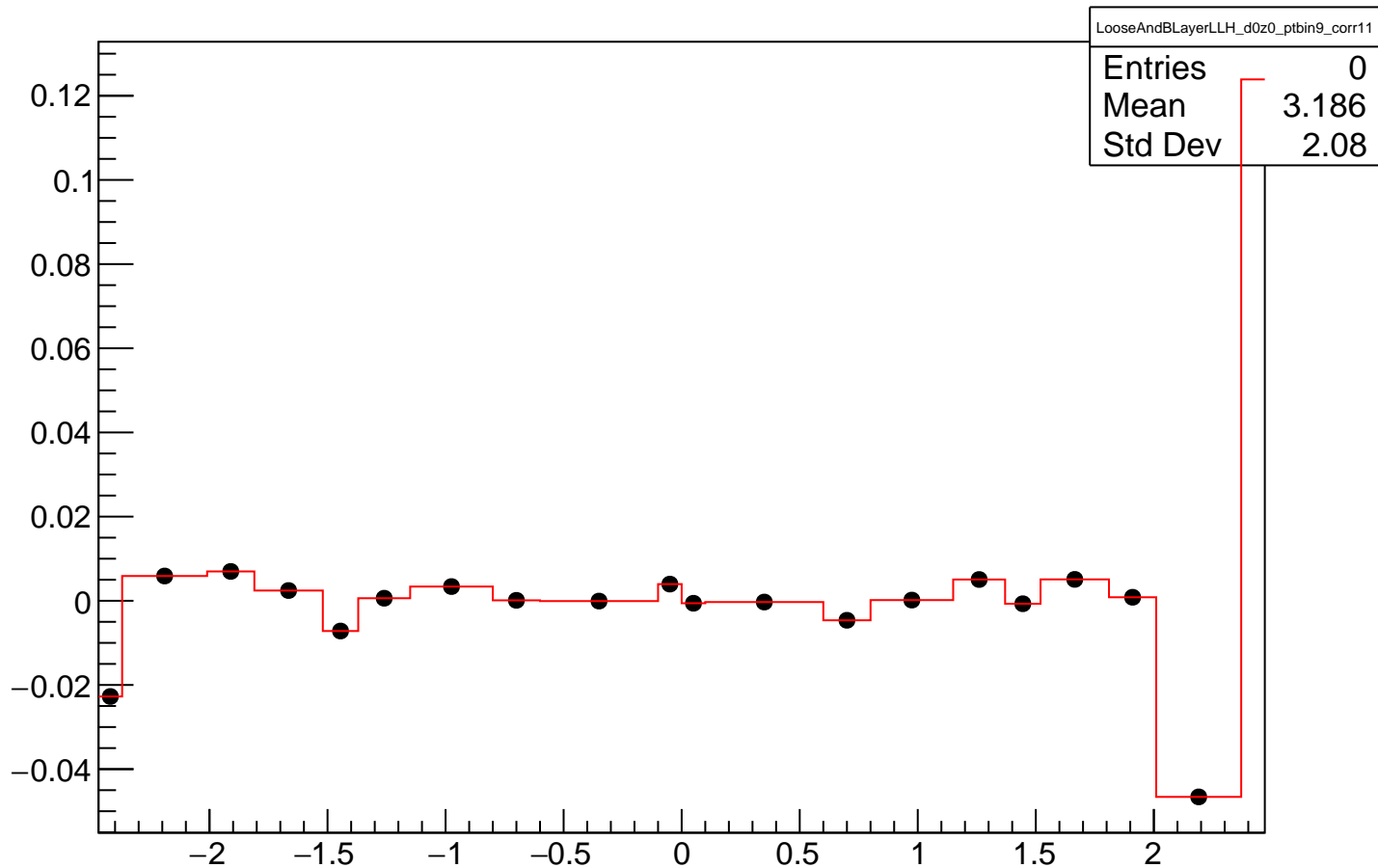
FullSim_LooseLLH_d0z0_corr11



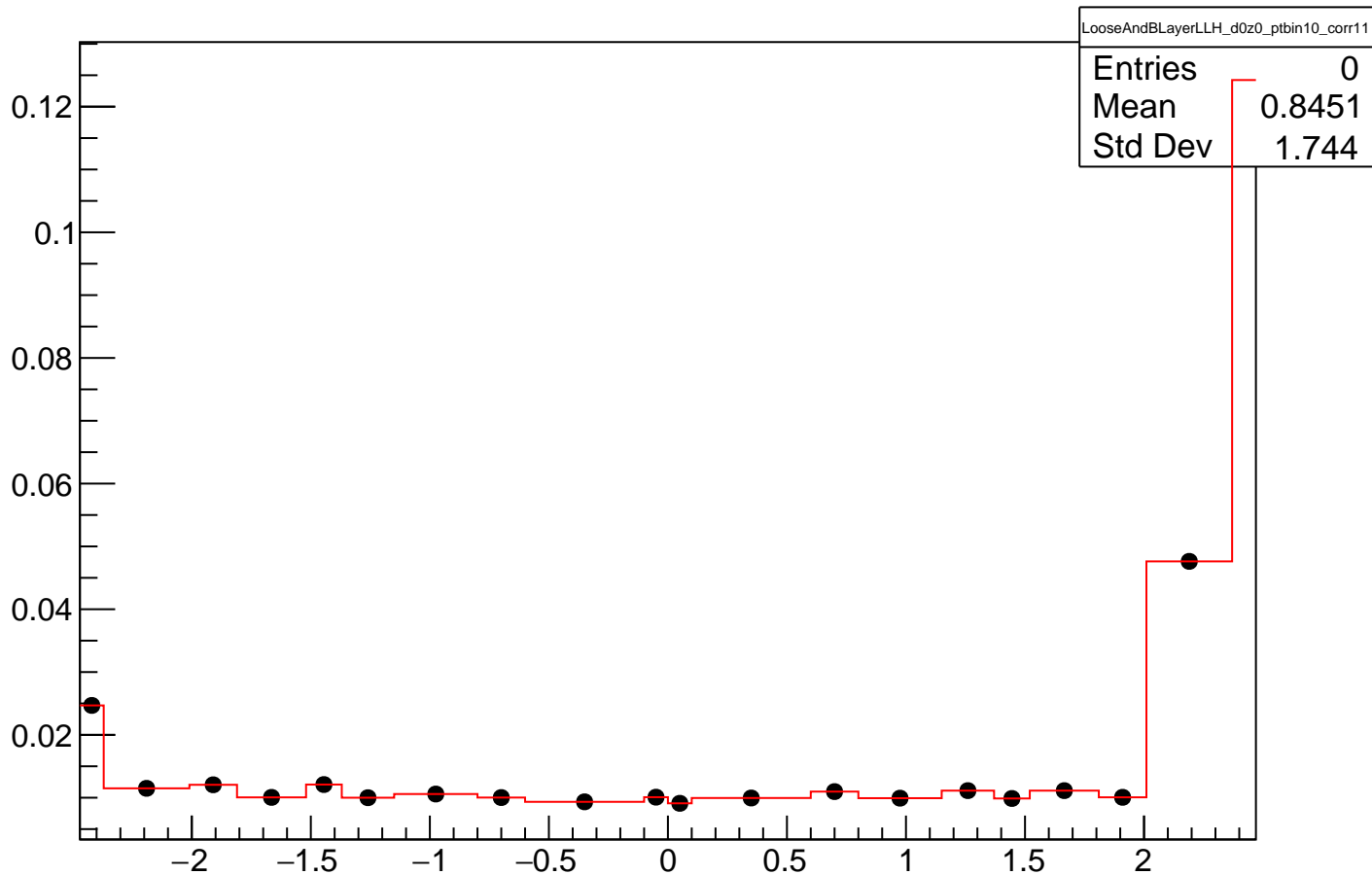
FullSim_LooseLLH_d0z0_corr11



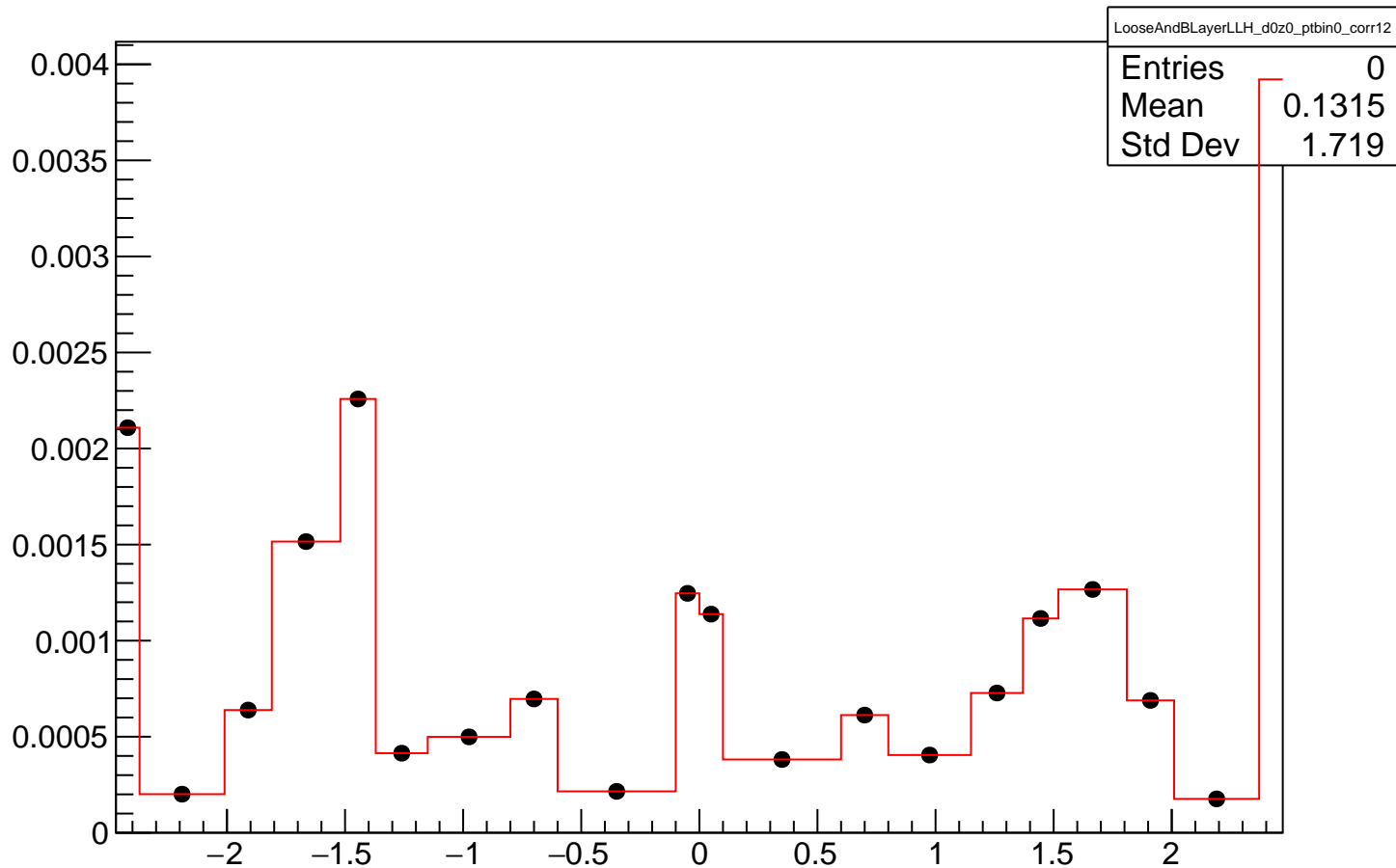
FullSim_LooseLLH_d0z0_corr11



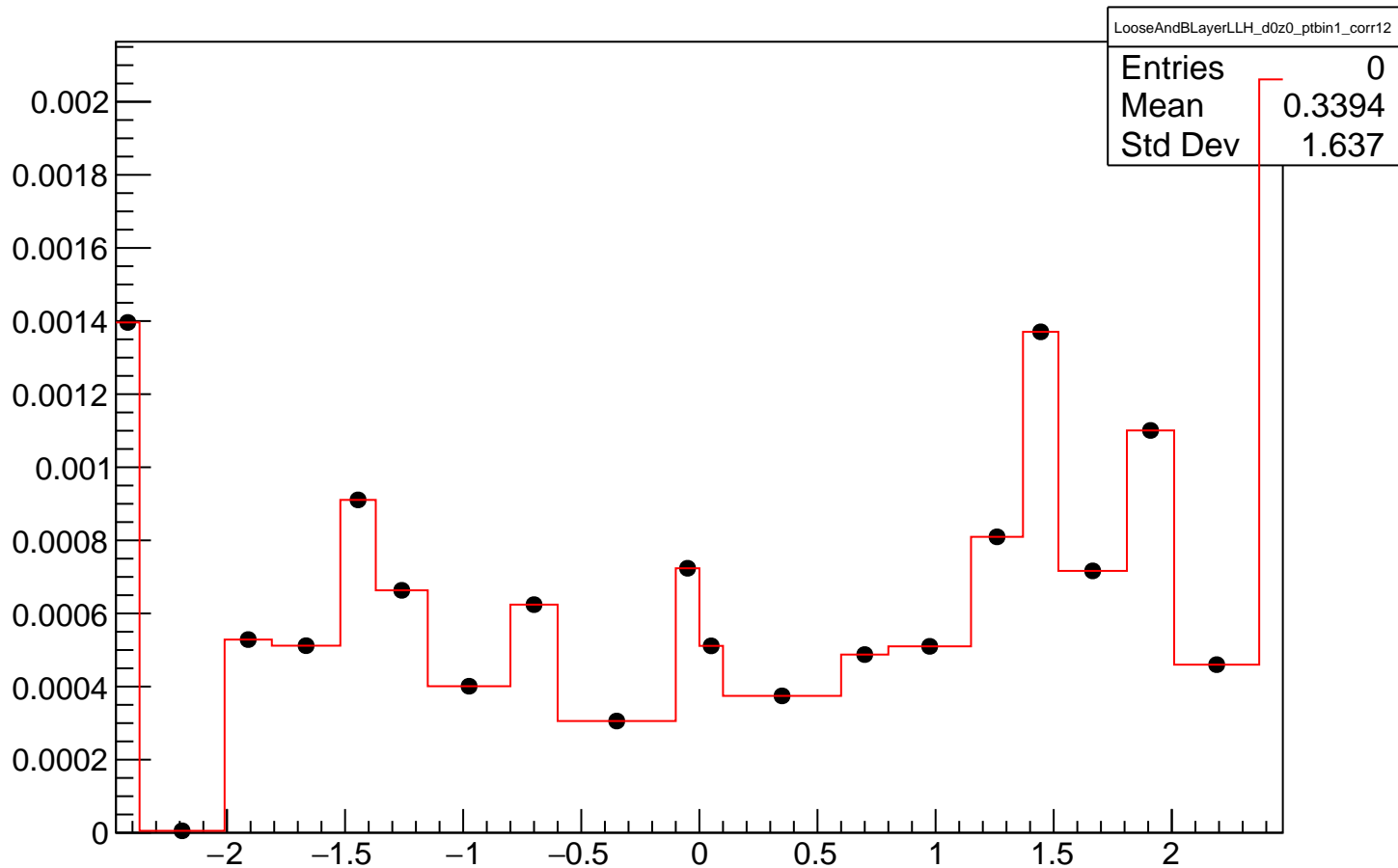
FullSim_LooseLLH_d0z0_corr11



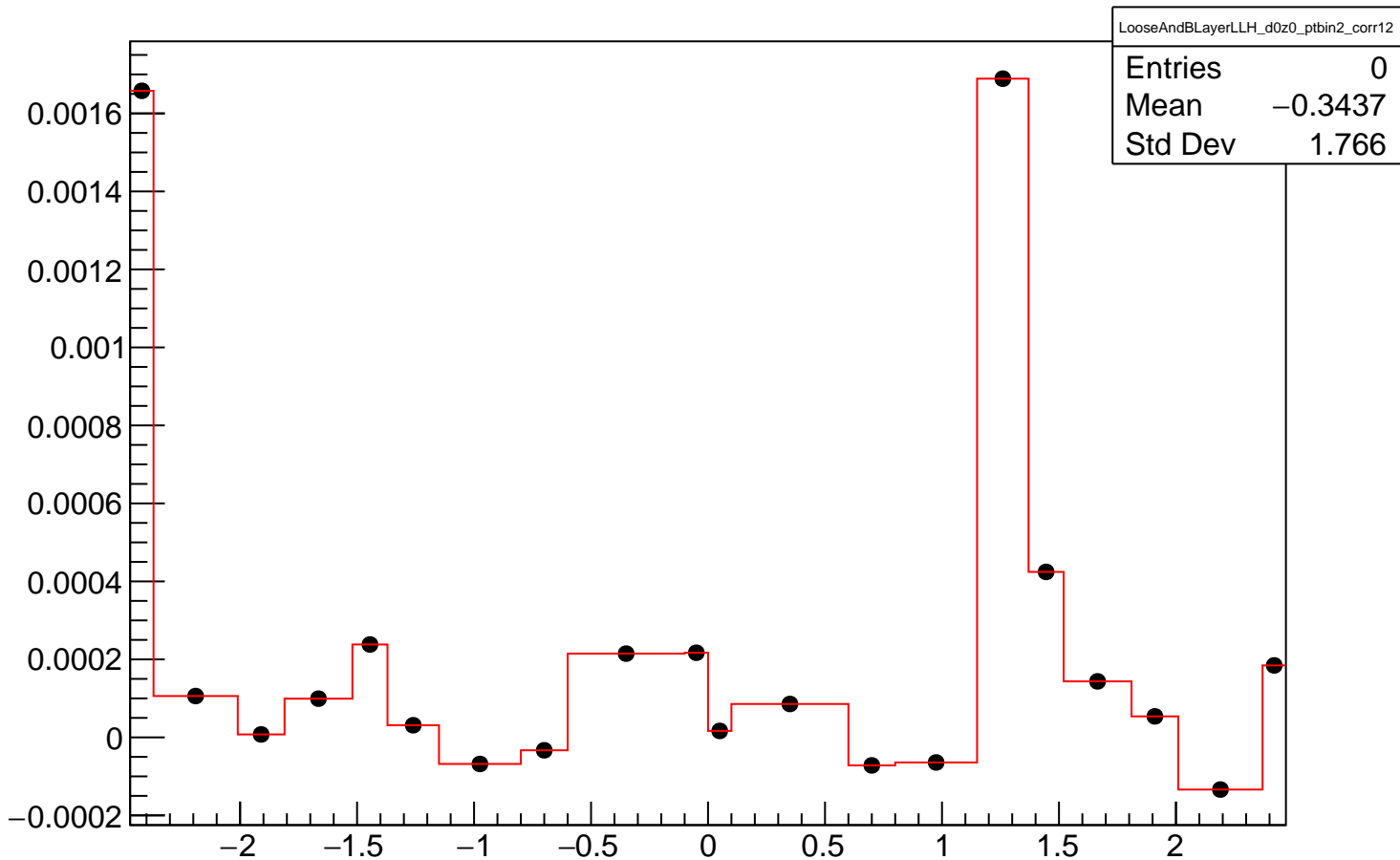
FullSim_LooseLLH_d0z0_corr12



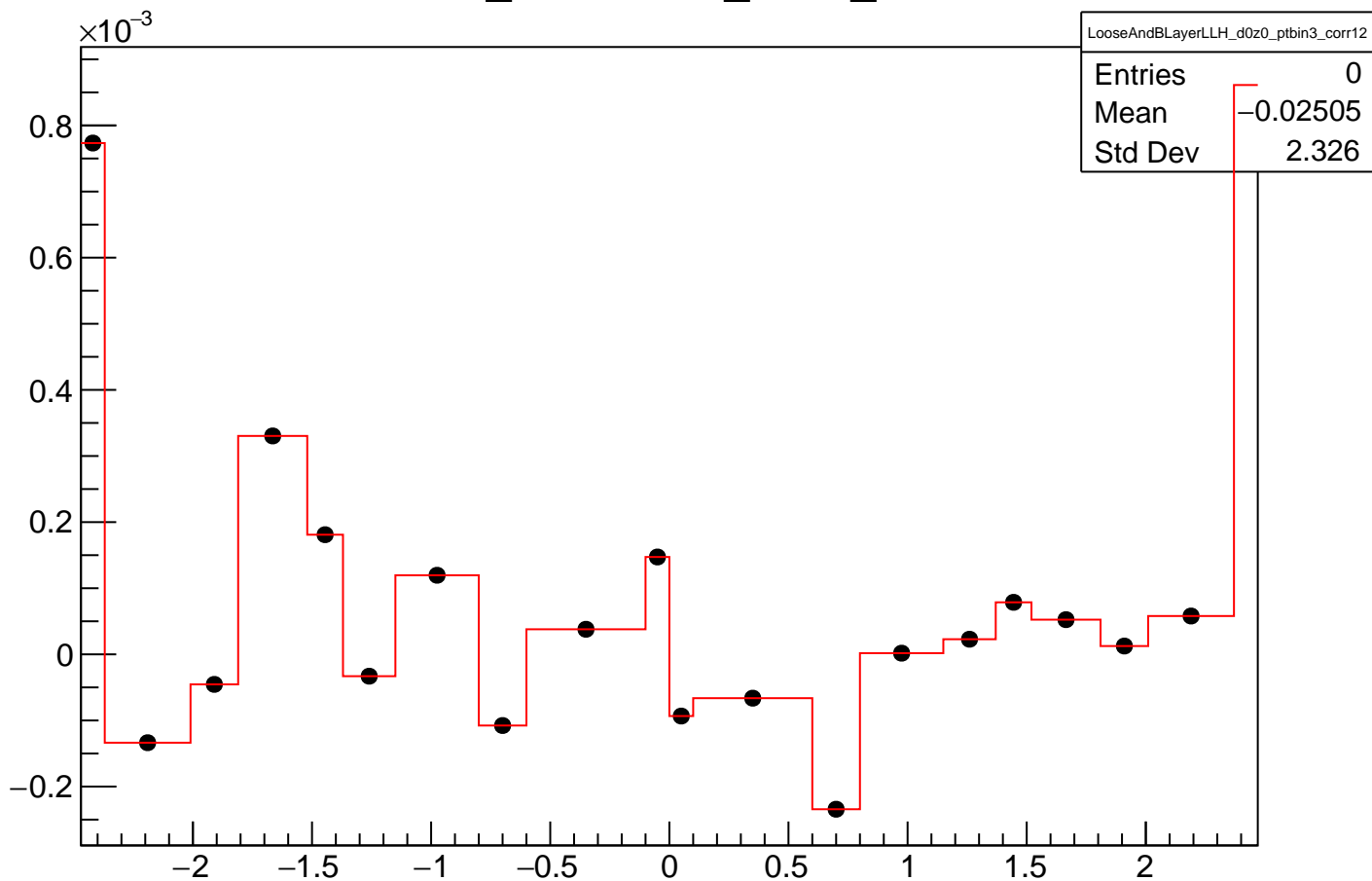
FullSim_LooseLLH_d0z0_corr12



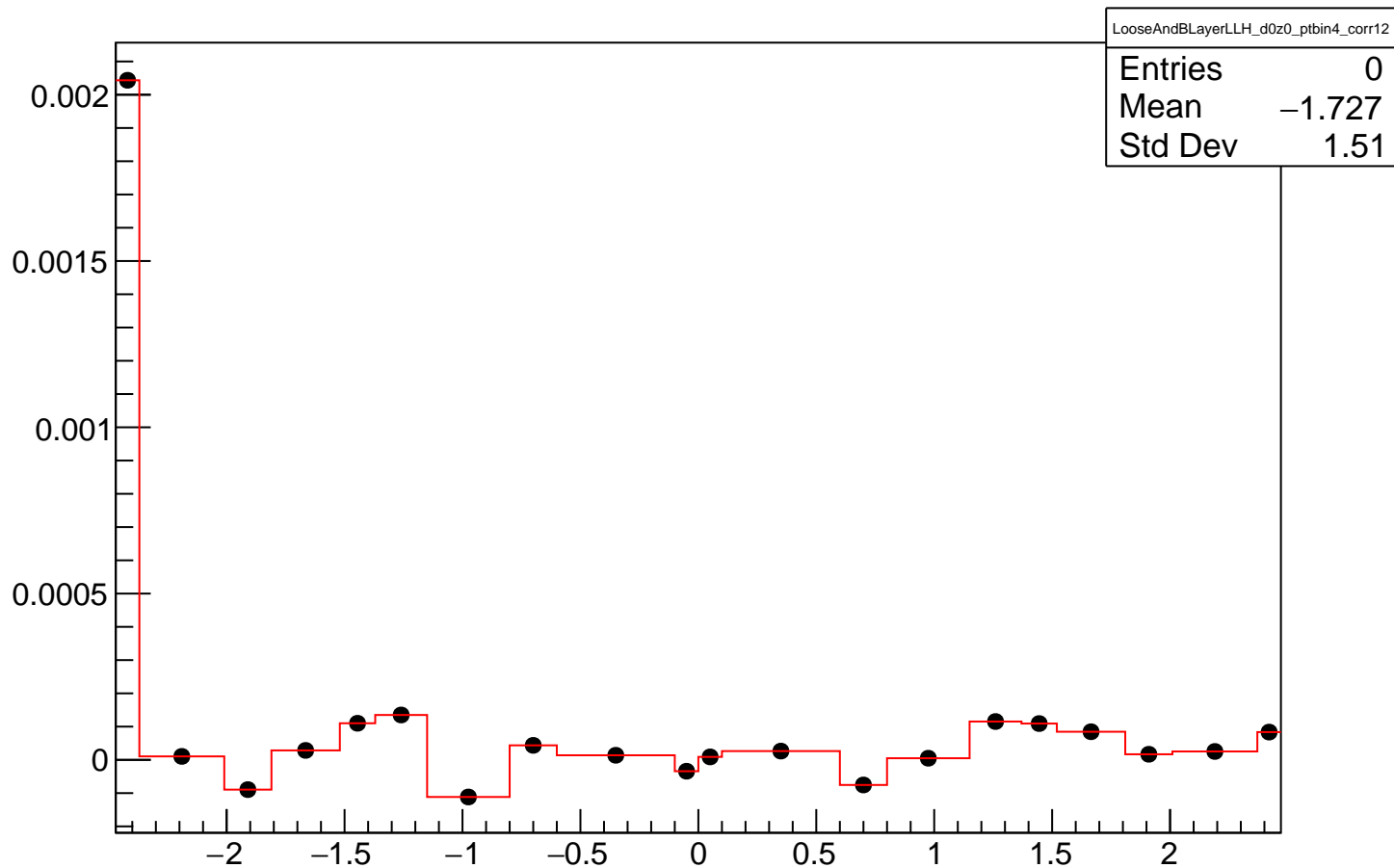
FullSim_LooseLLH_d0z0_corr12



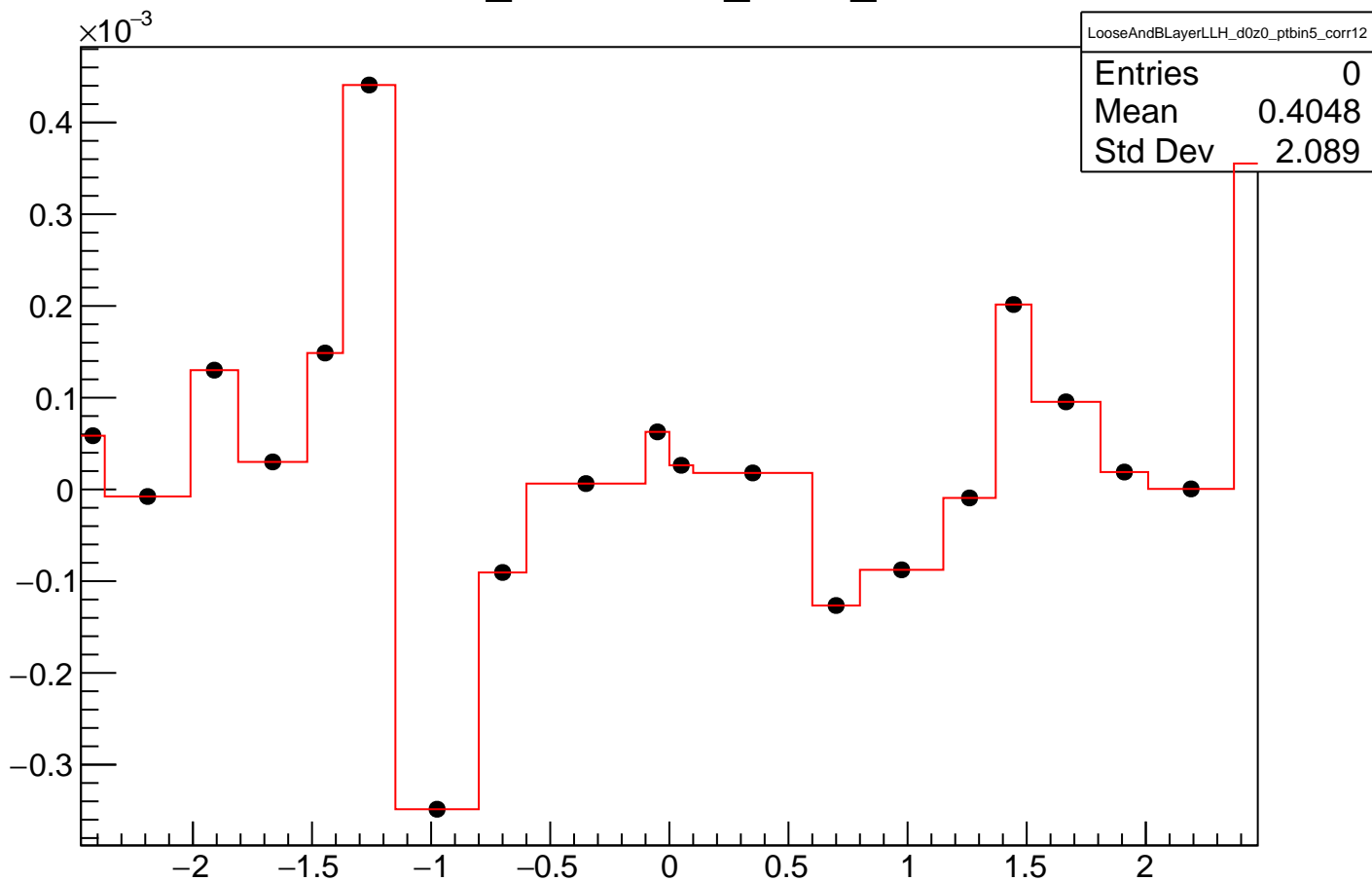
FullSim_LooseLLH_d0z0_corr12



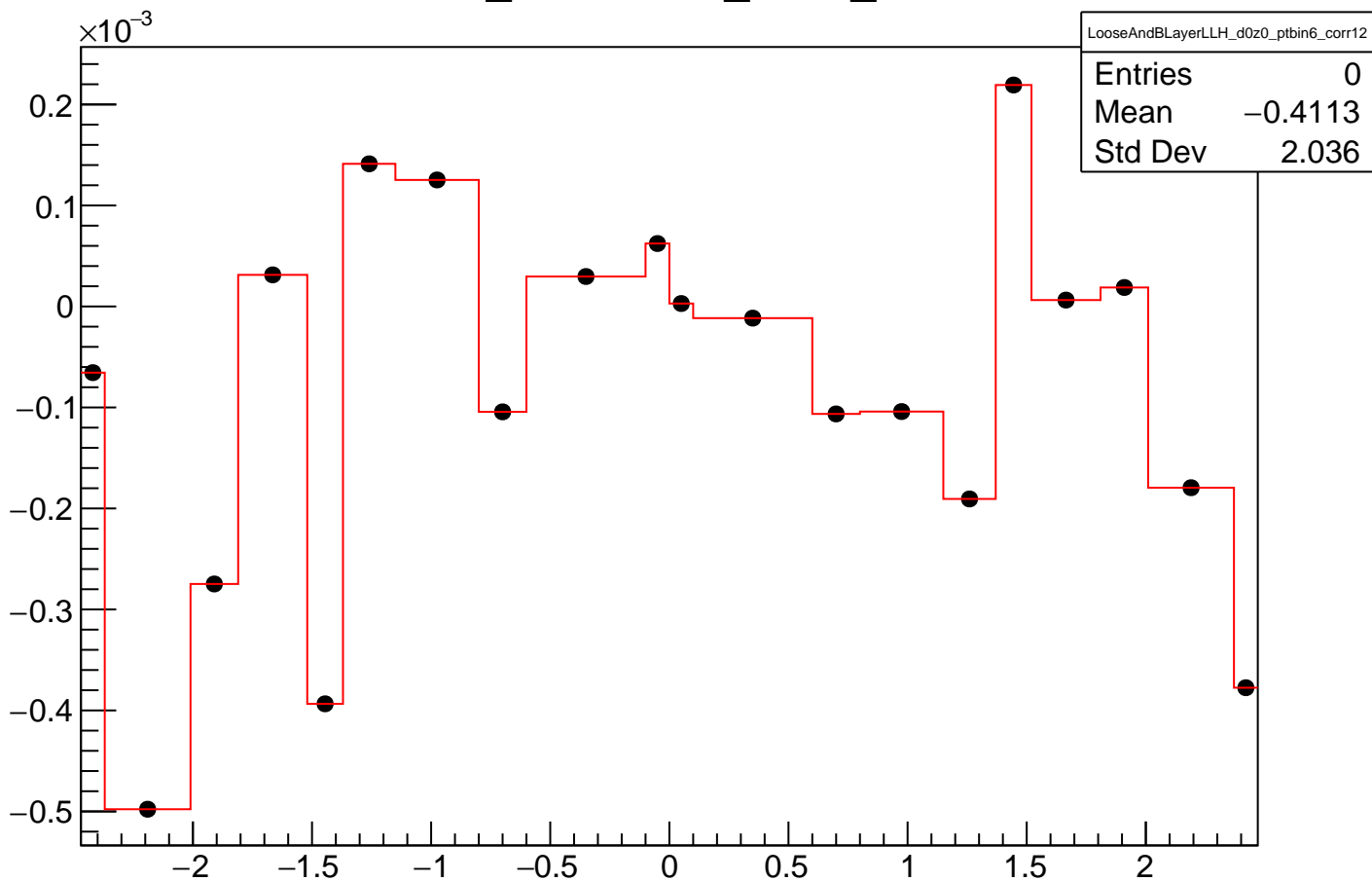
FullSim_LooseLLH_d0z0_corr12



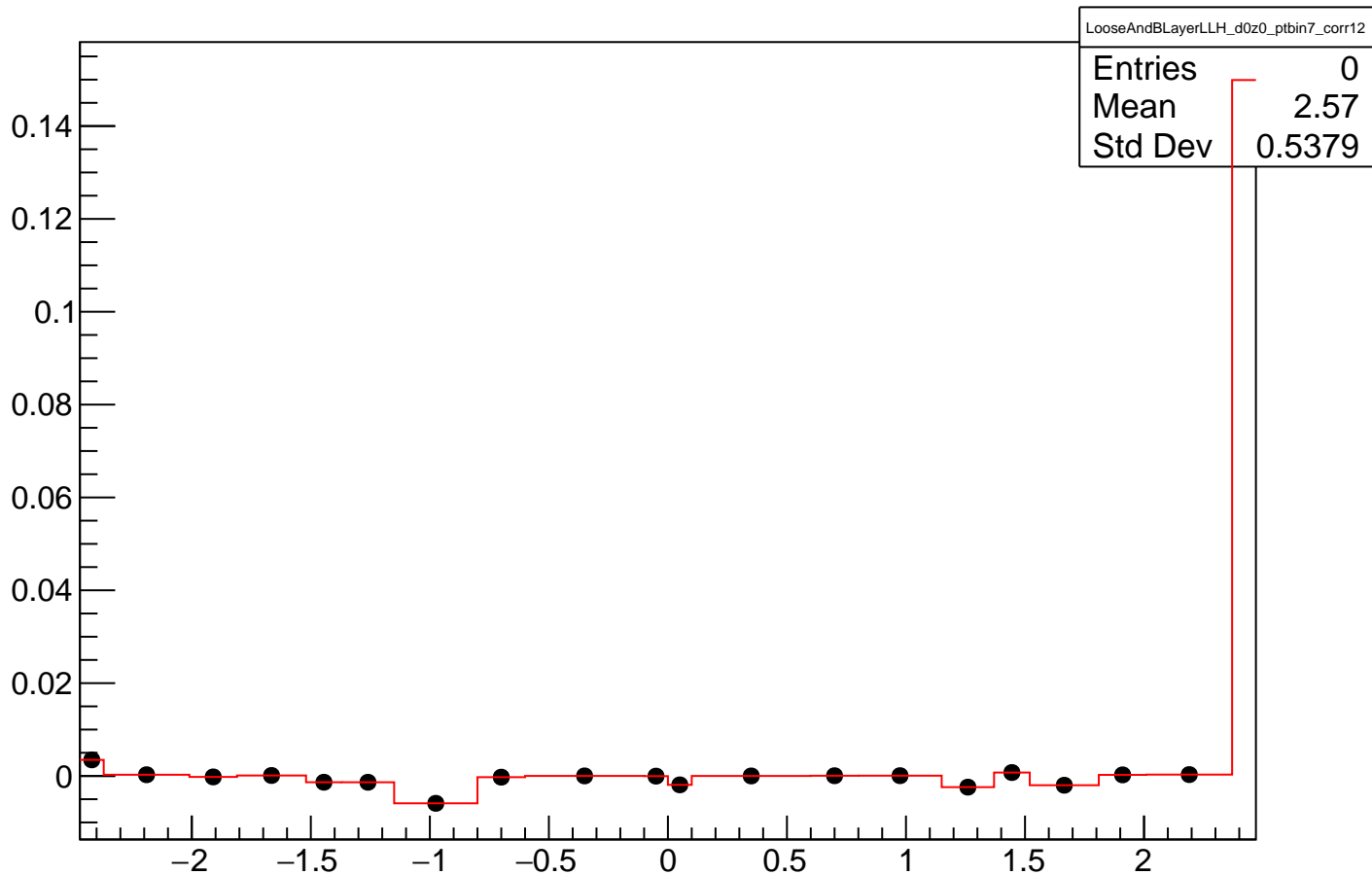
FullSim_LooseLLH_d0z0_corr12



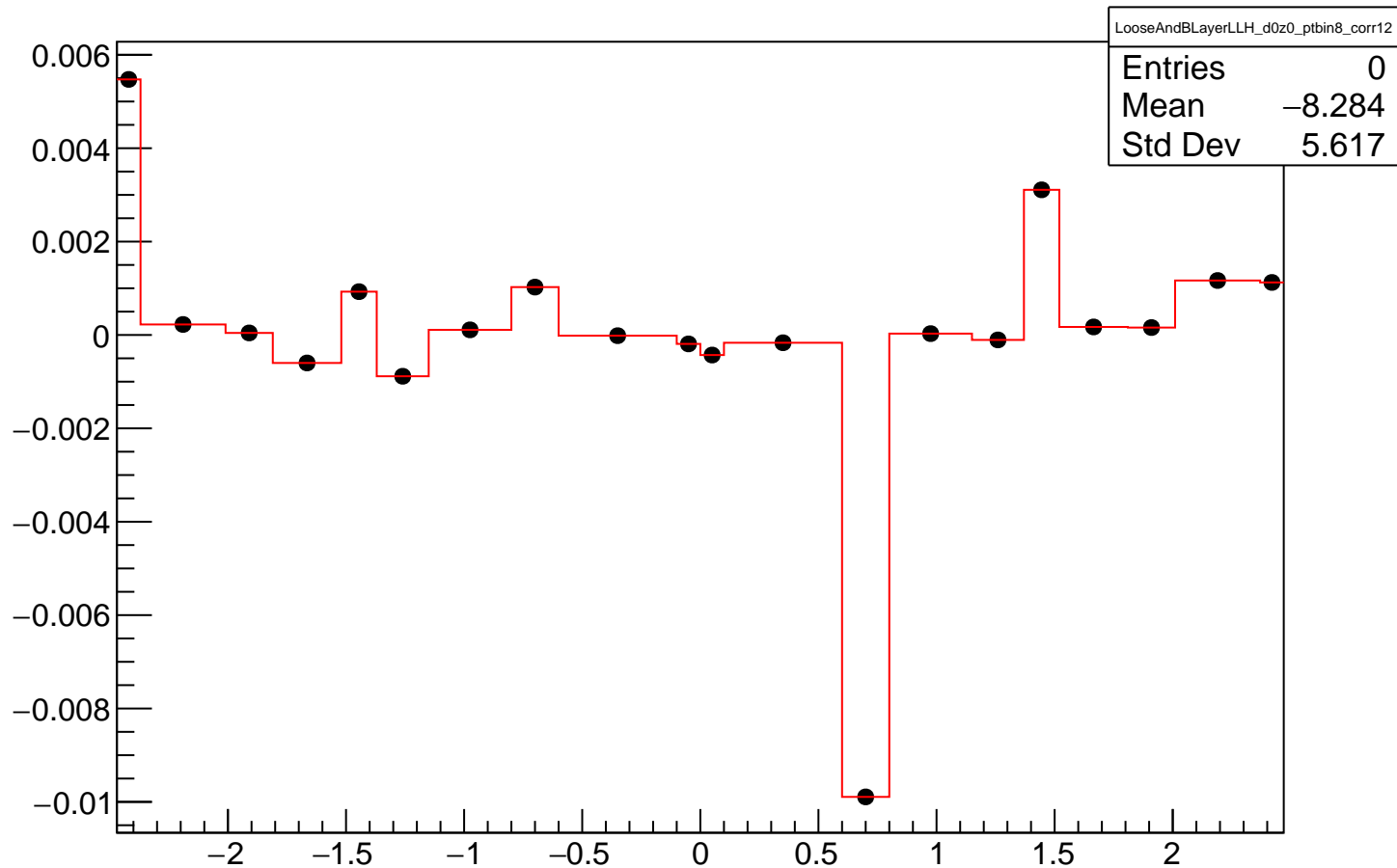
FullSim_LooseLLH_d0z0_corr12



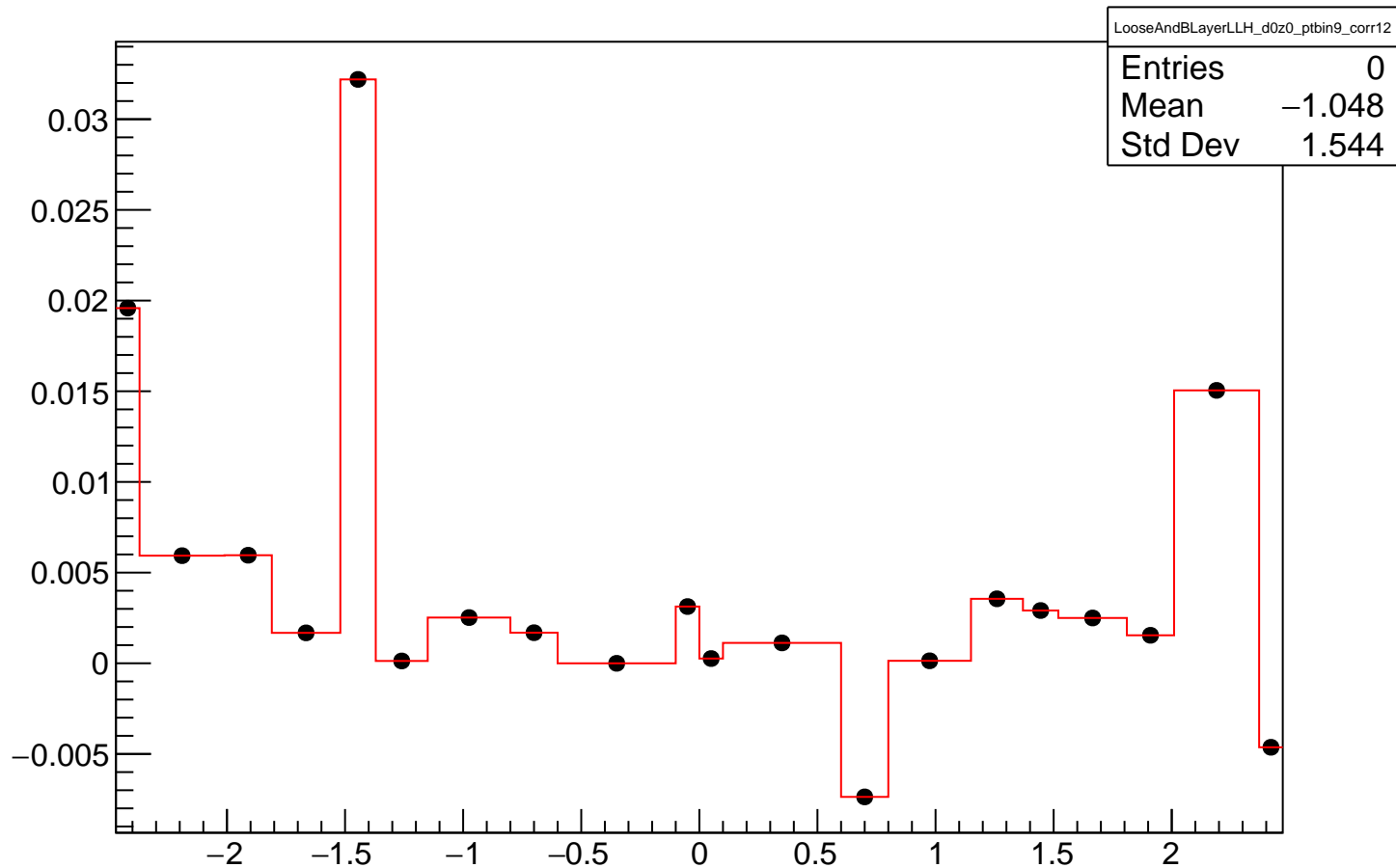
FullSim_LooseLLH_d0z0_corr12



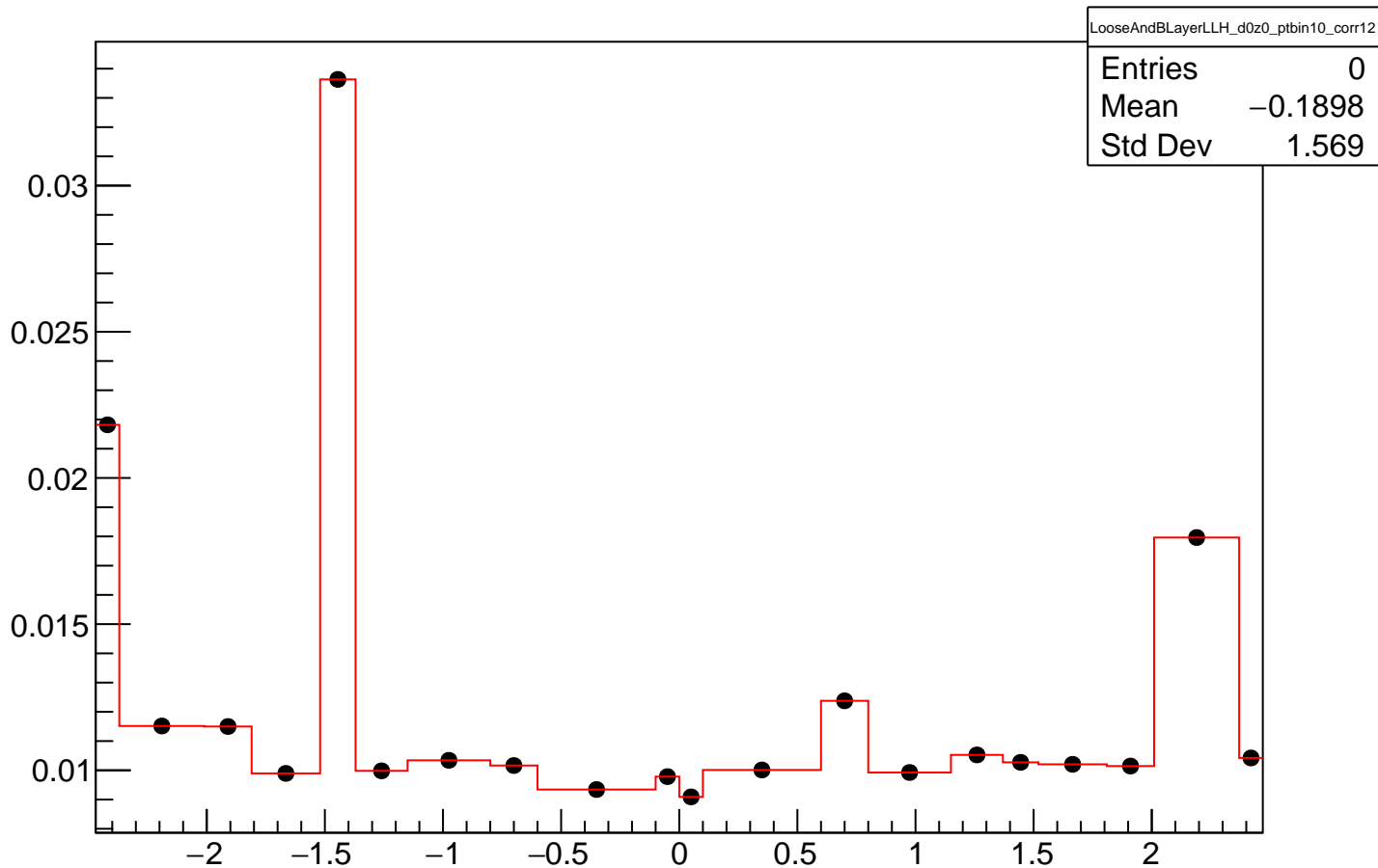
FullSim_LooseLLH_d0z0_corr12



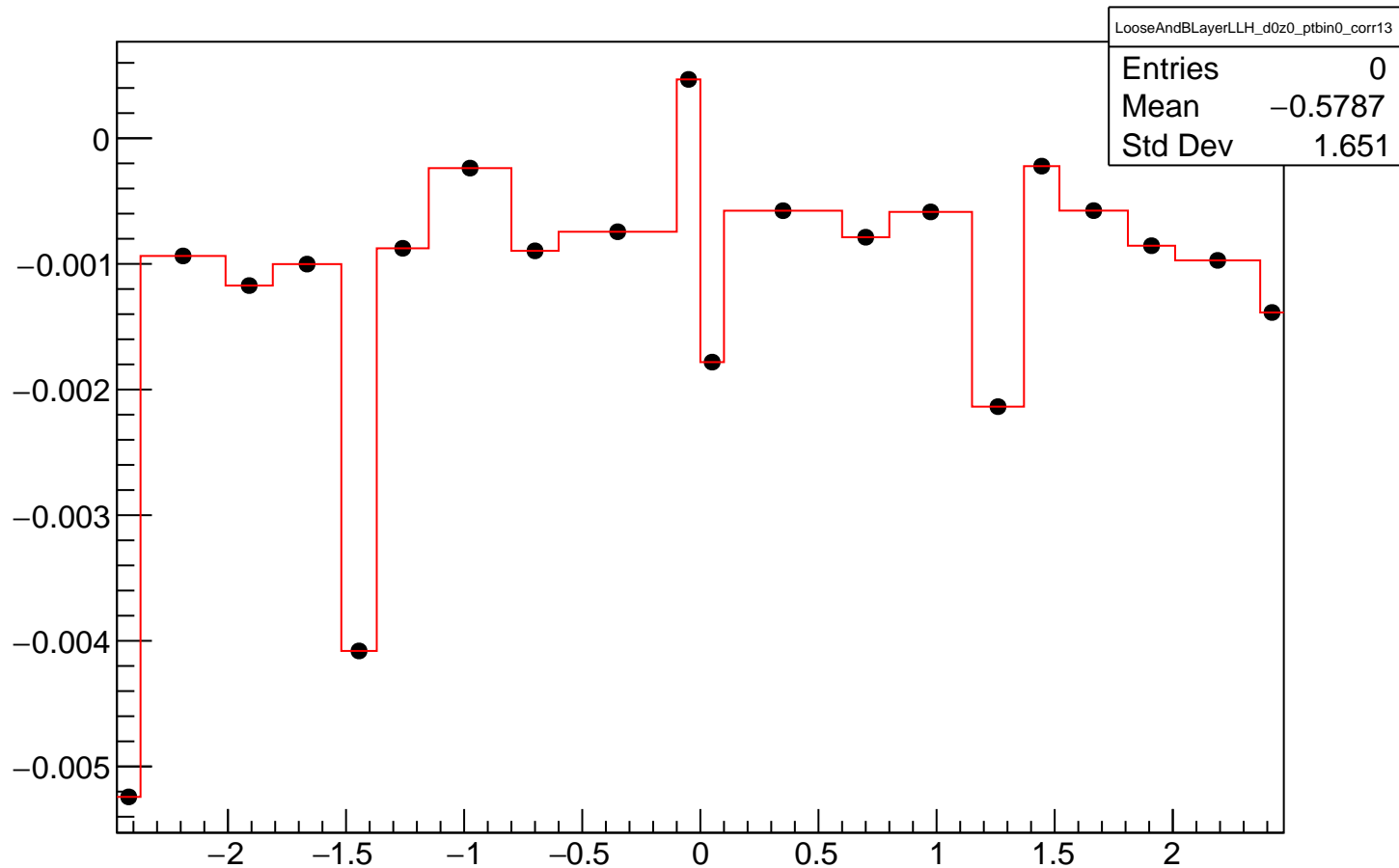
FullSim_LooseLLH_d0z0_corr12



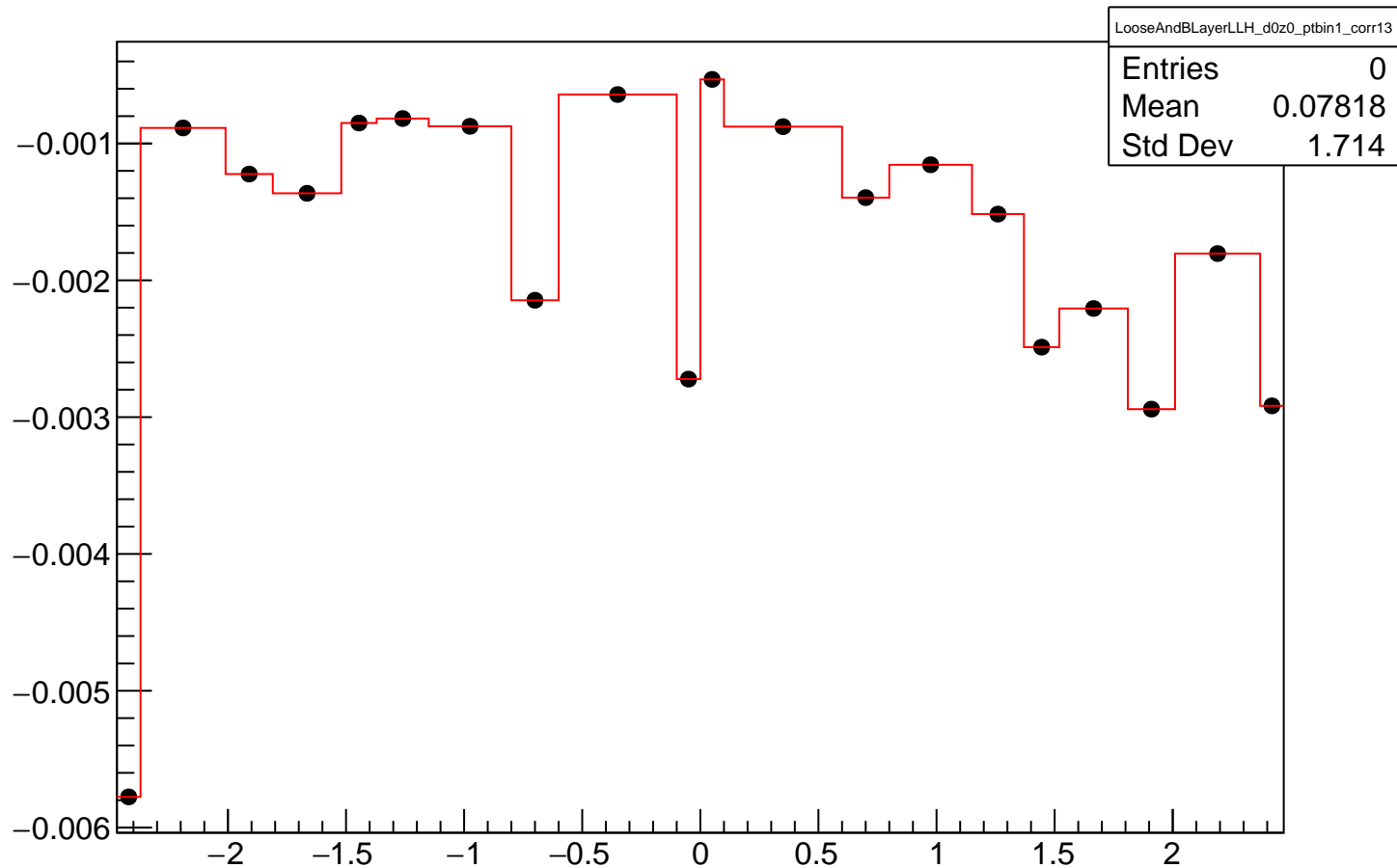
FullSim_LooseLLH_d0z0_corr12



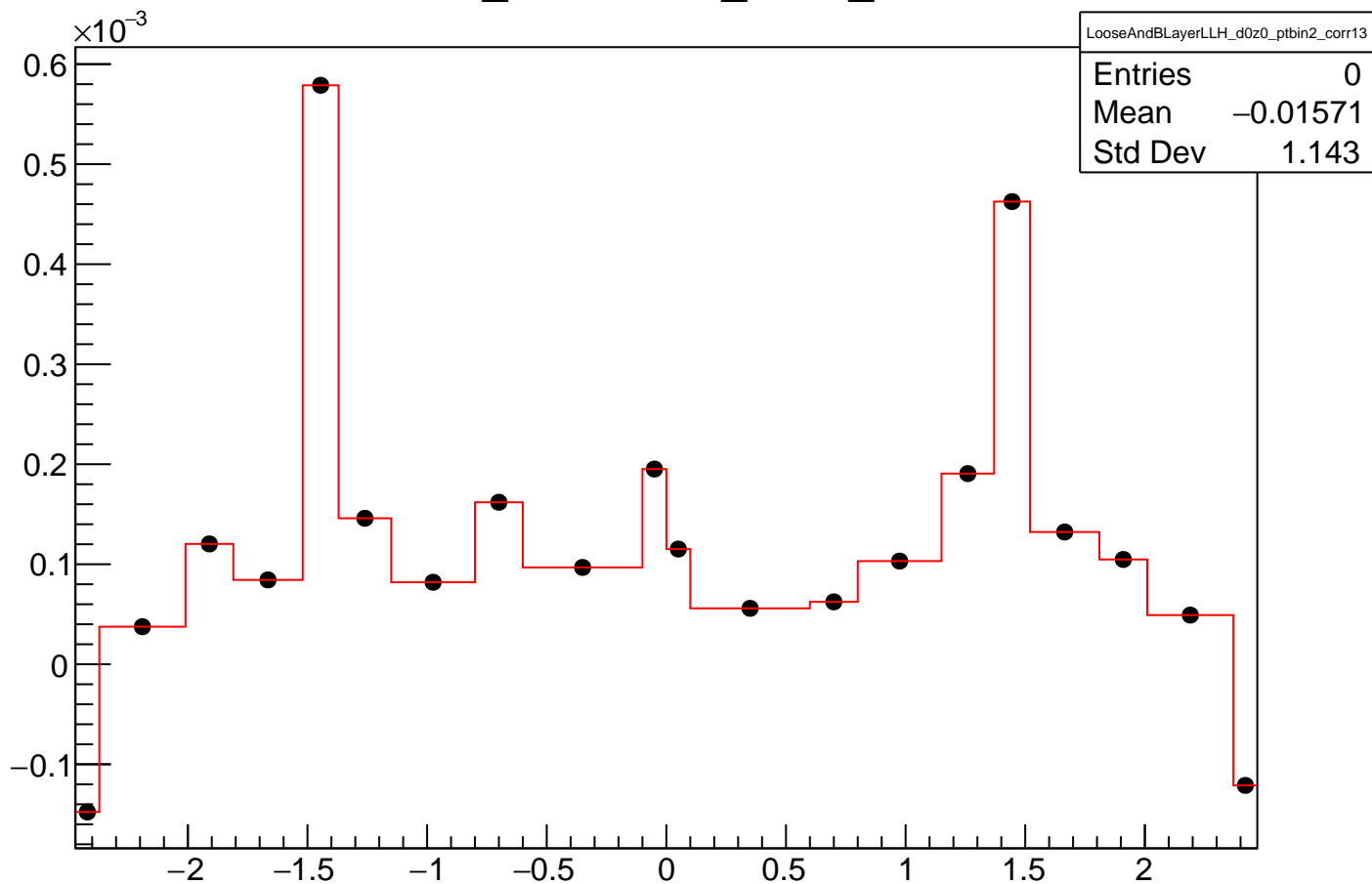
FullSim_LooseLLH_d0z0_corr13



FullSim_LooseLLH_d0z0_corr13



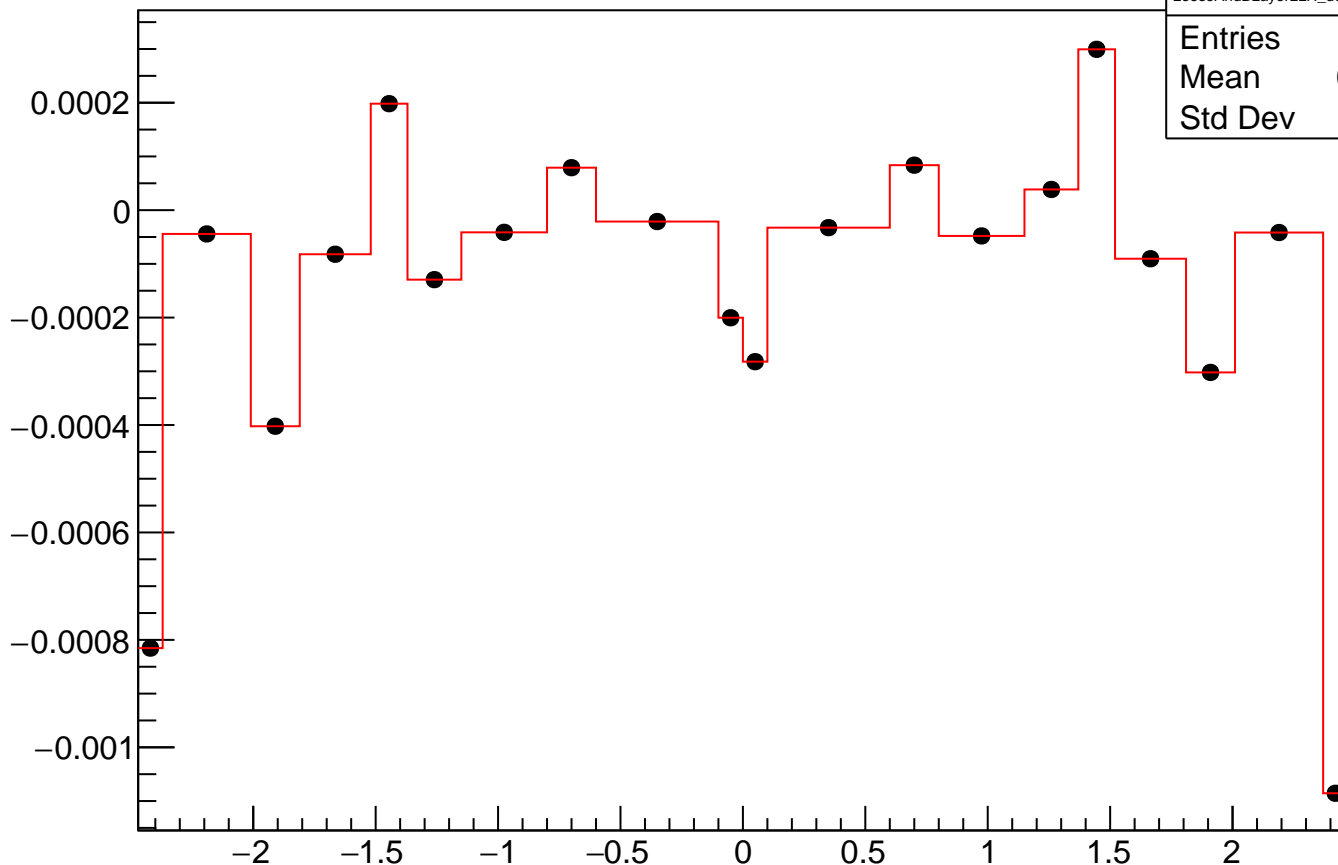
FullSim_LooseLLH_d0z0_corr13



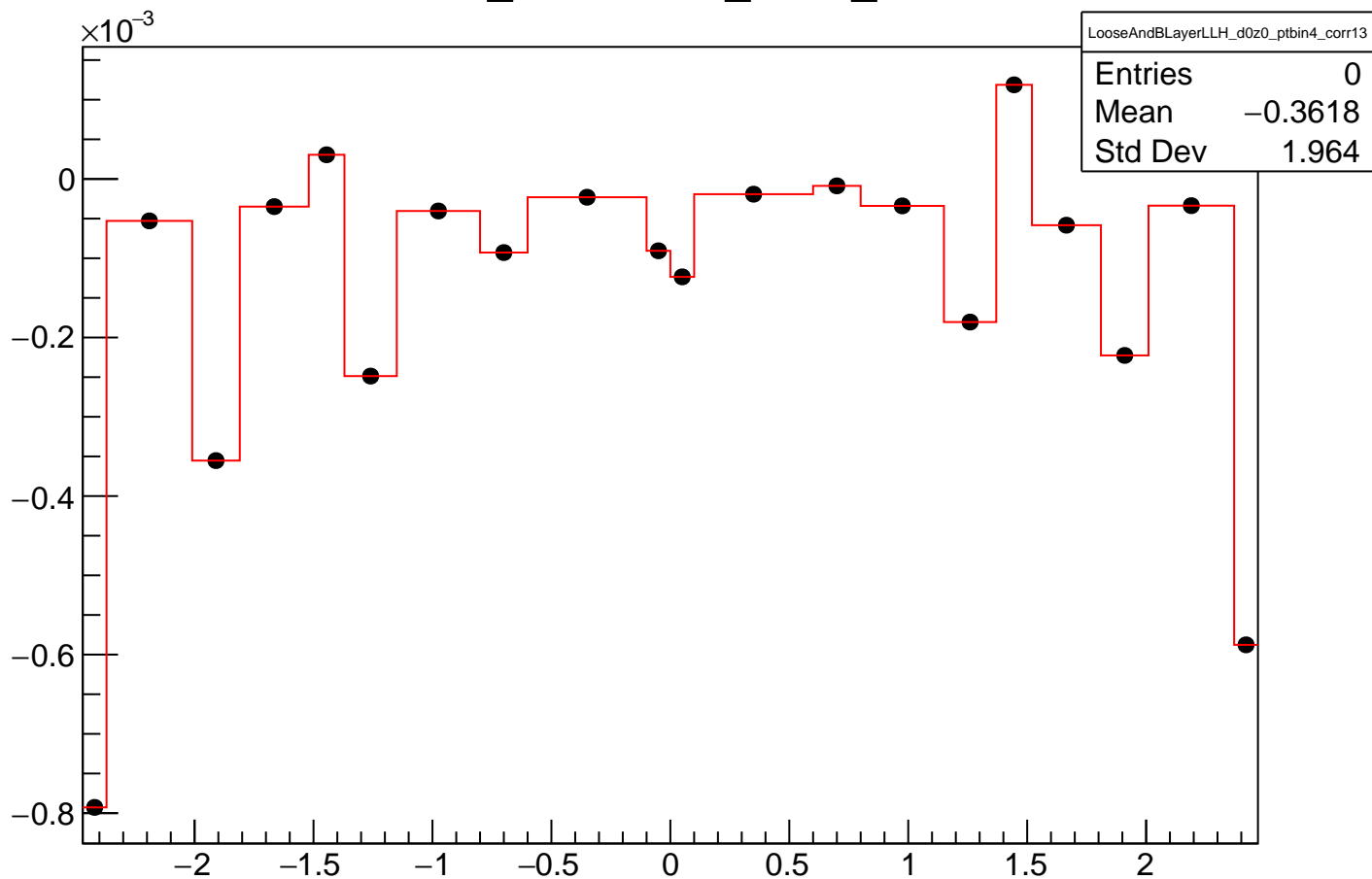
FullSim_LooseLLH_d0z0_corr13

LooseAndBLayerLLH_d0z0_ptbin3_corr13

Entries	0
Mean	0.04257
Std Dev	2.167



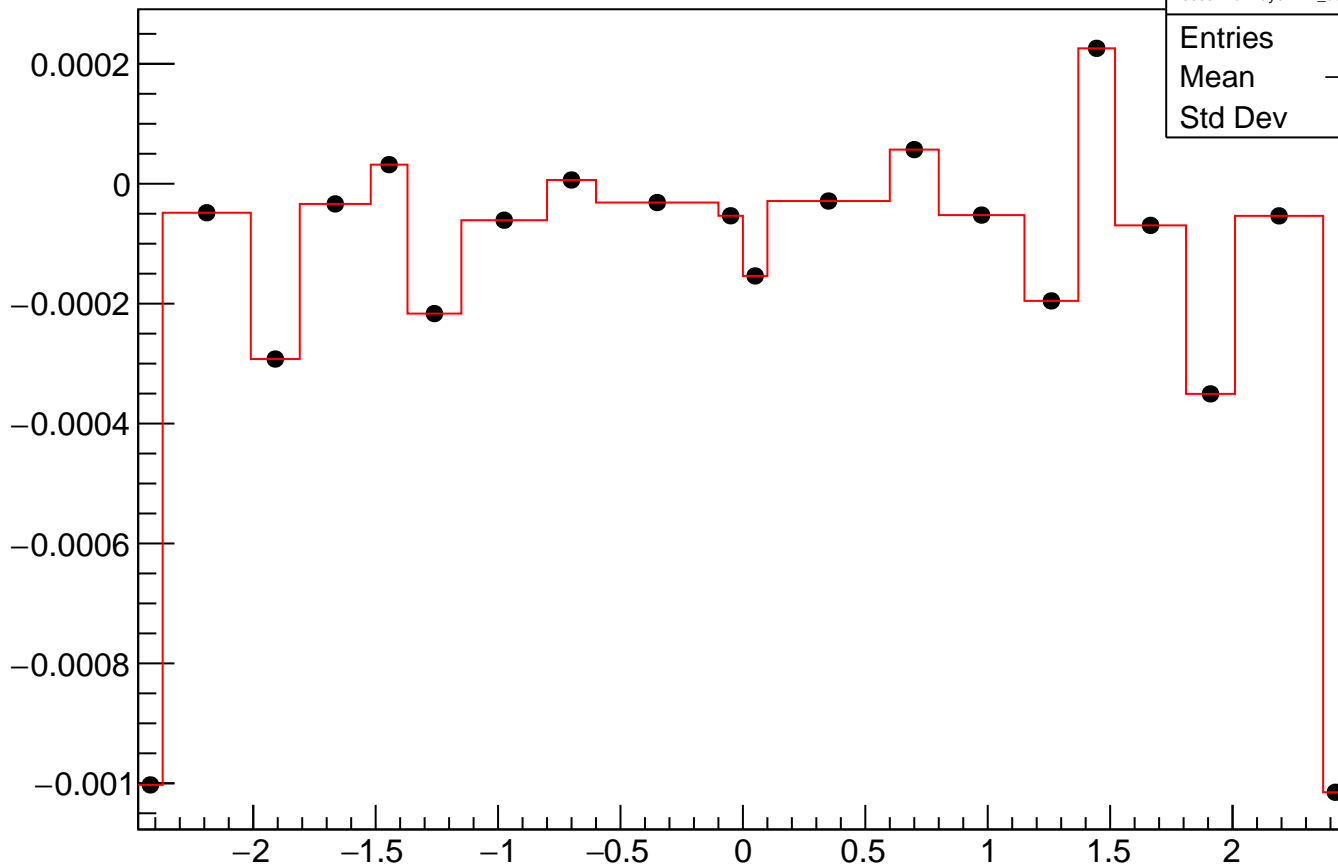
FullSim_LooseLLH_d0z0_corr13



FullSim_LooseLLH_d0z0_corr13

LooseAndBLayerLLH_d0z0_ptbin5_corr13

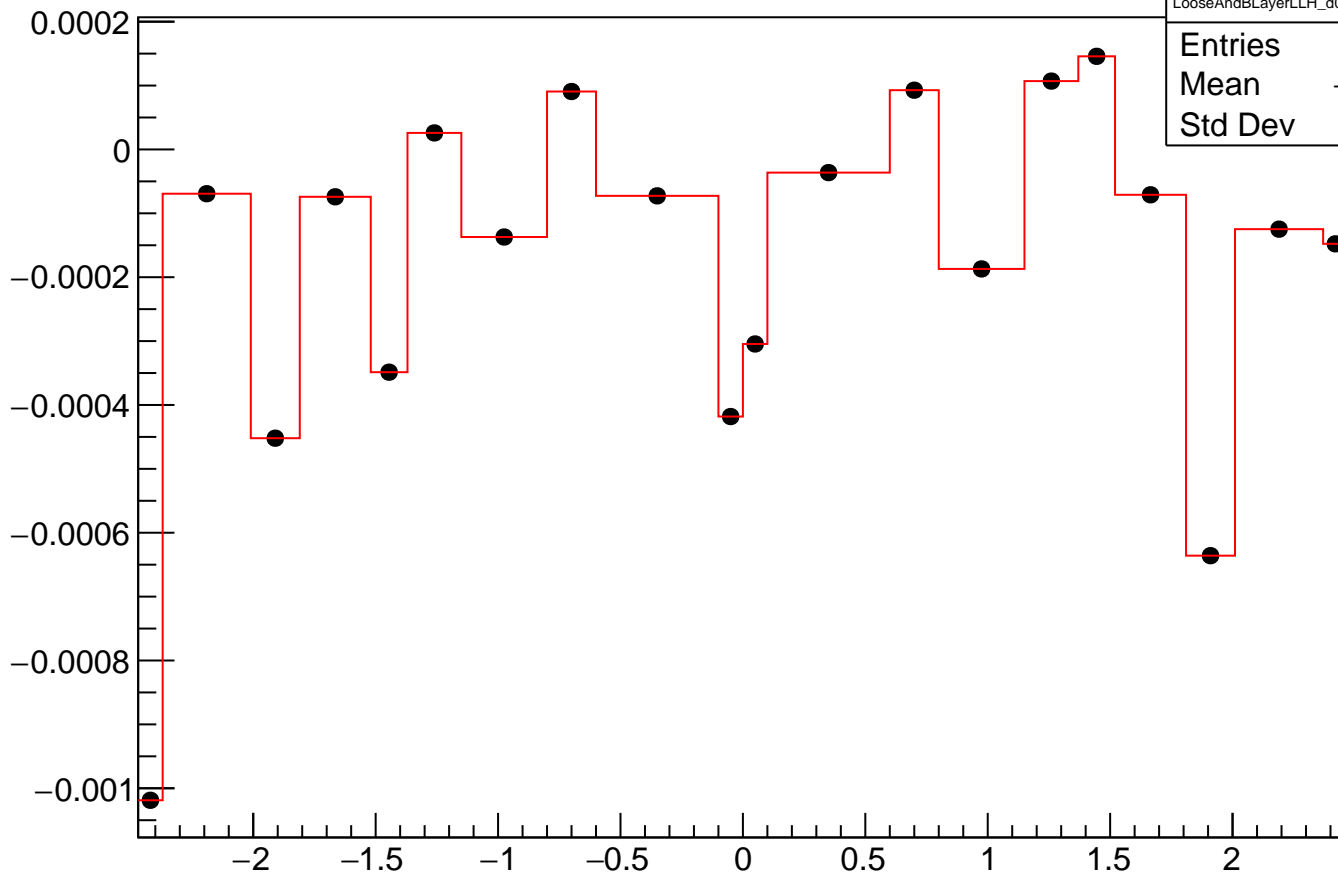
Entries	0
Mean	-0.04044
Std Dev	2.129



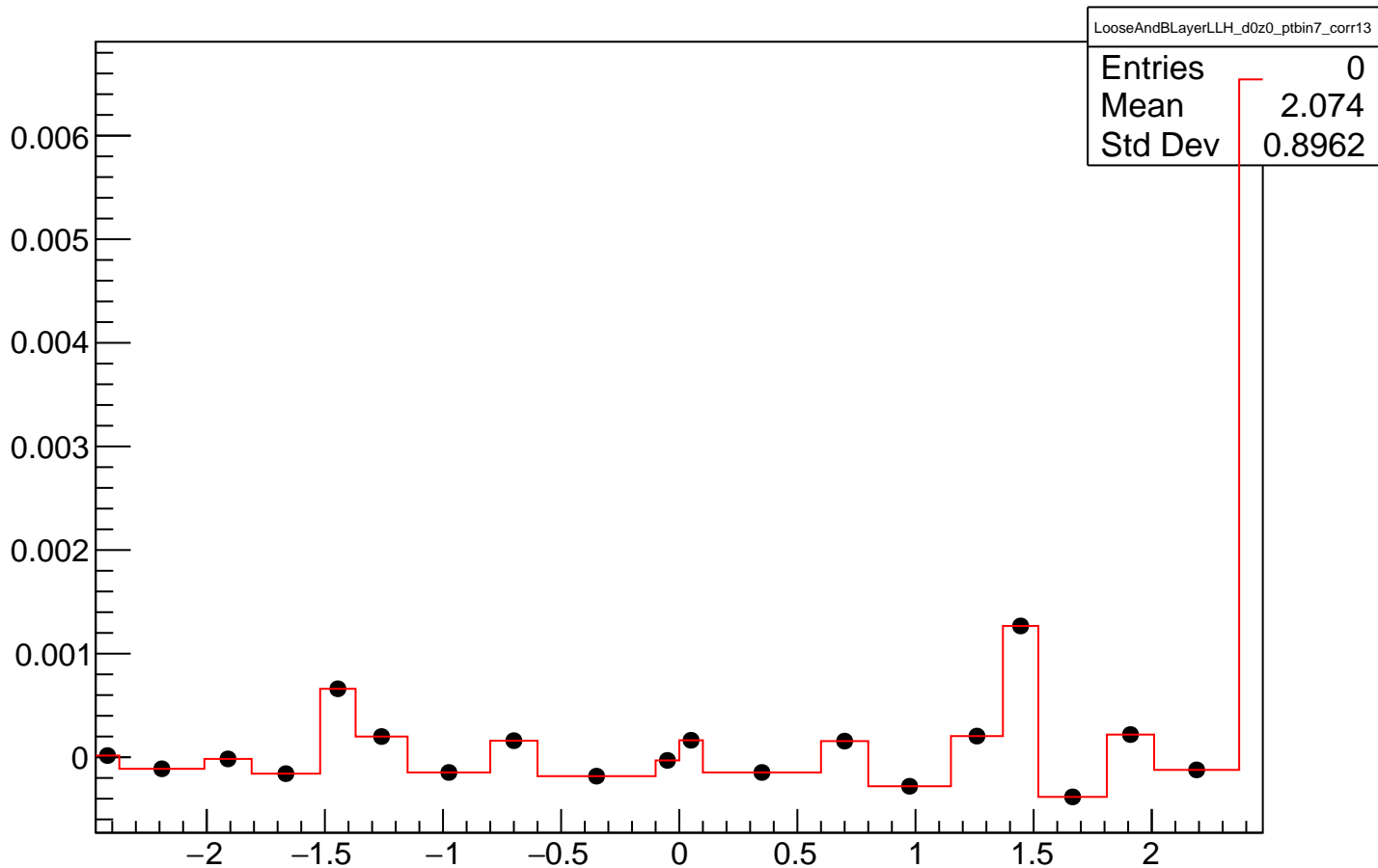
FullSim_LooseLLH_d0z0_corr13

LooseAndBLayerLLH_d0z0_ptbin6_corr13

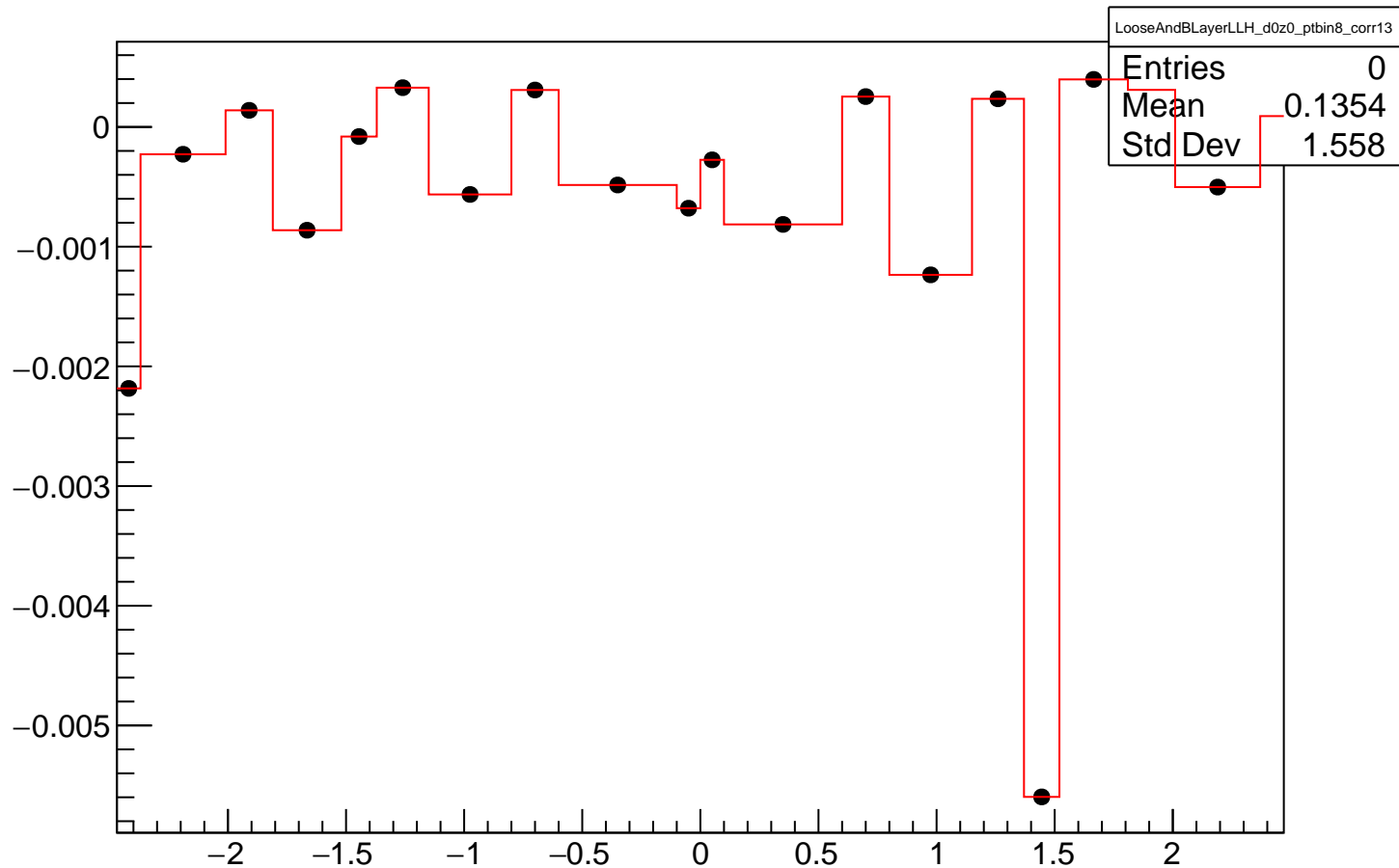
Entries	0
Mean	-0.6678
Std Dev	1.736



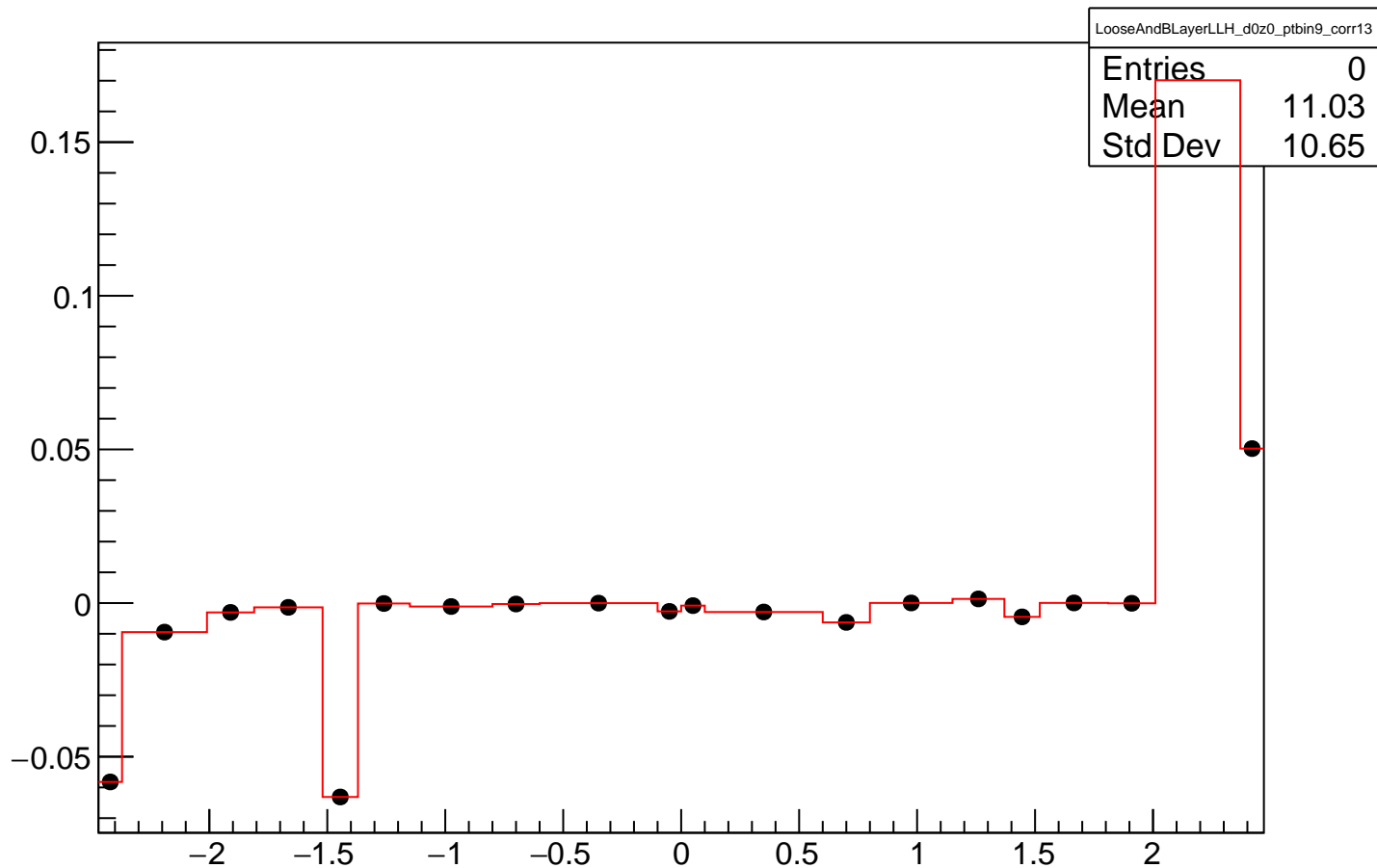
FullSim_LooseLLH_d0z0_corr13



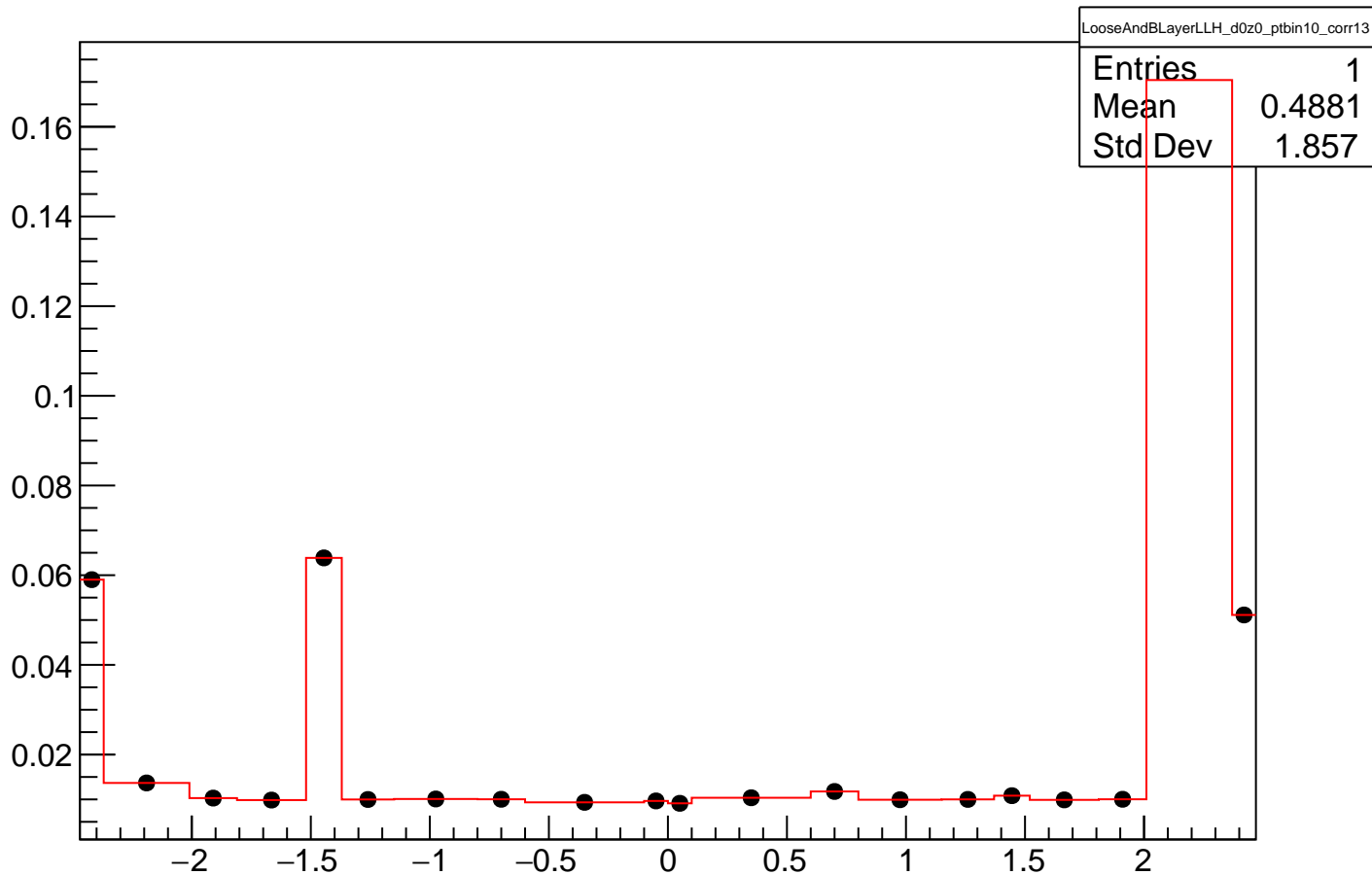
FullSim_LooseLLH_d0z0_corr13



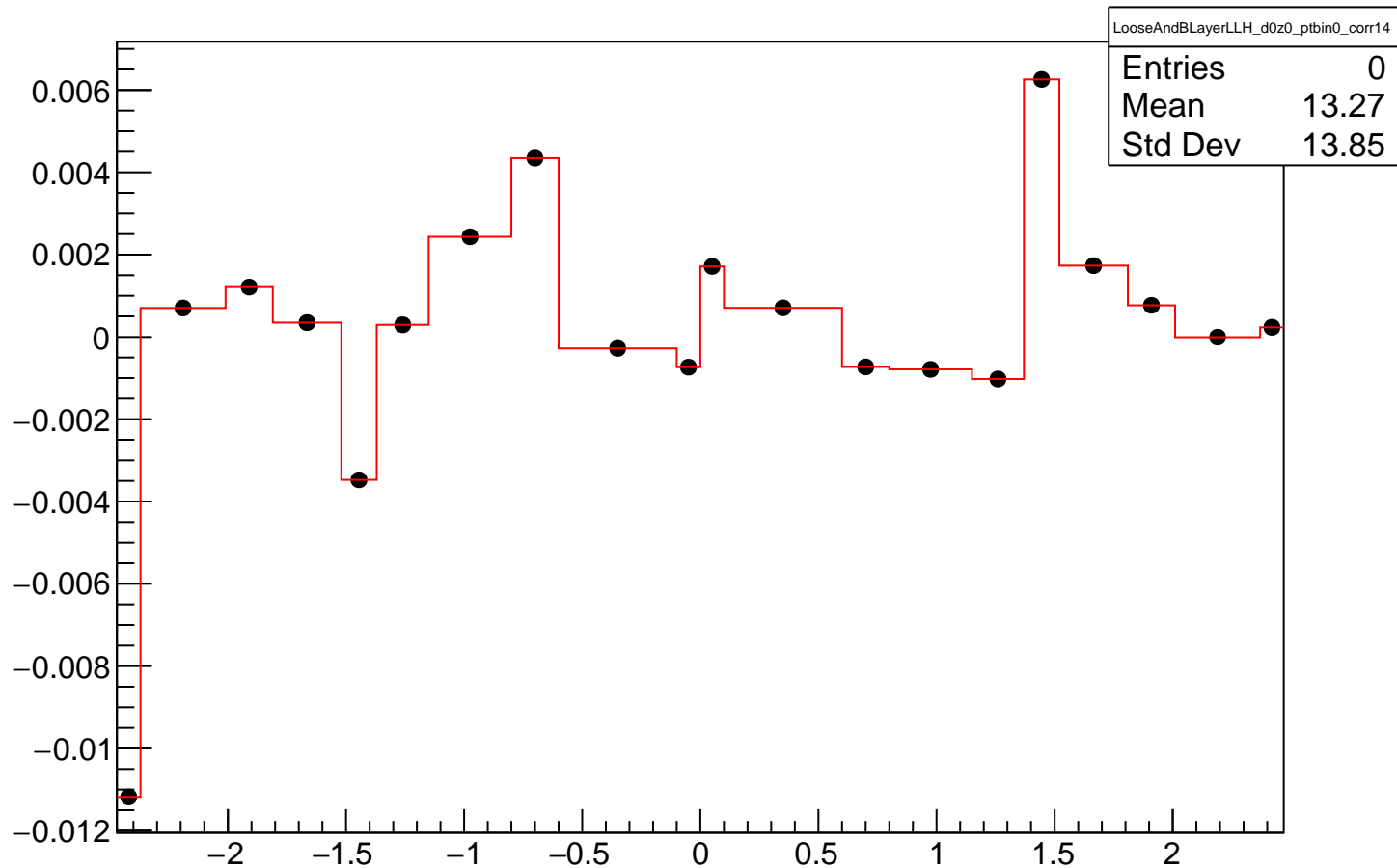
FullSim_LooseLLH_d0z0_corr13



FullSim_LooseLLH_d0z0_corr13



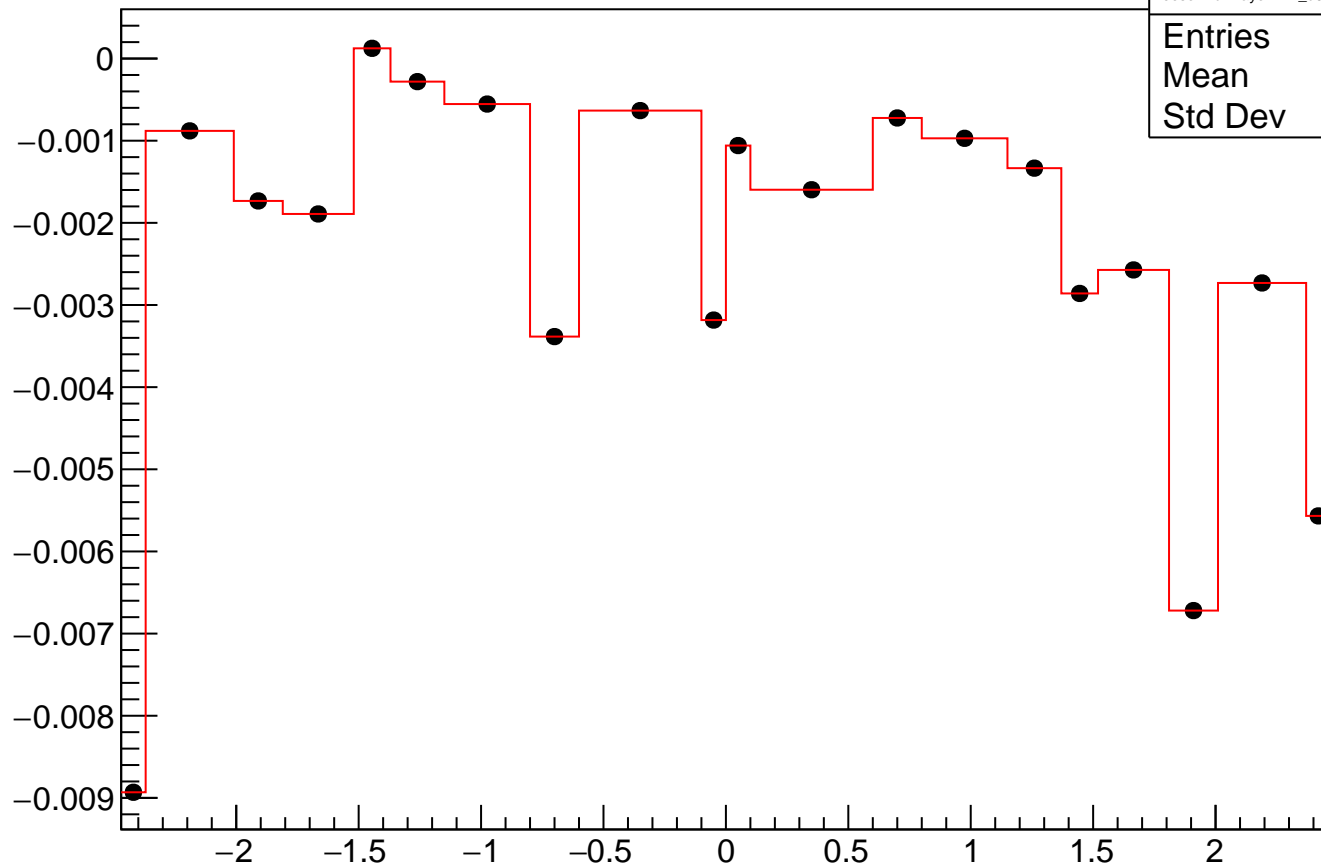
FullSim_LooseLLH_d0z0_corr14



FullSim_LooseLLH_d0z0_corr14

LooseAndBLayerLLH_d0z0_ptbin1_corr14

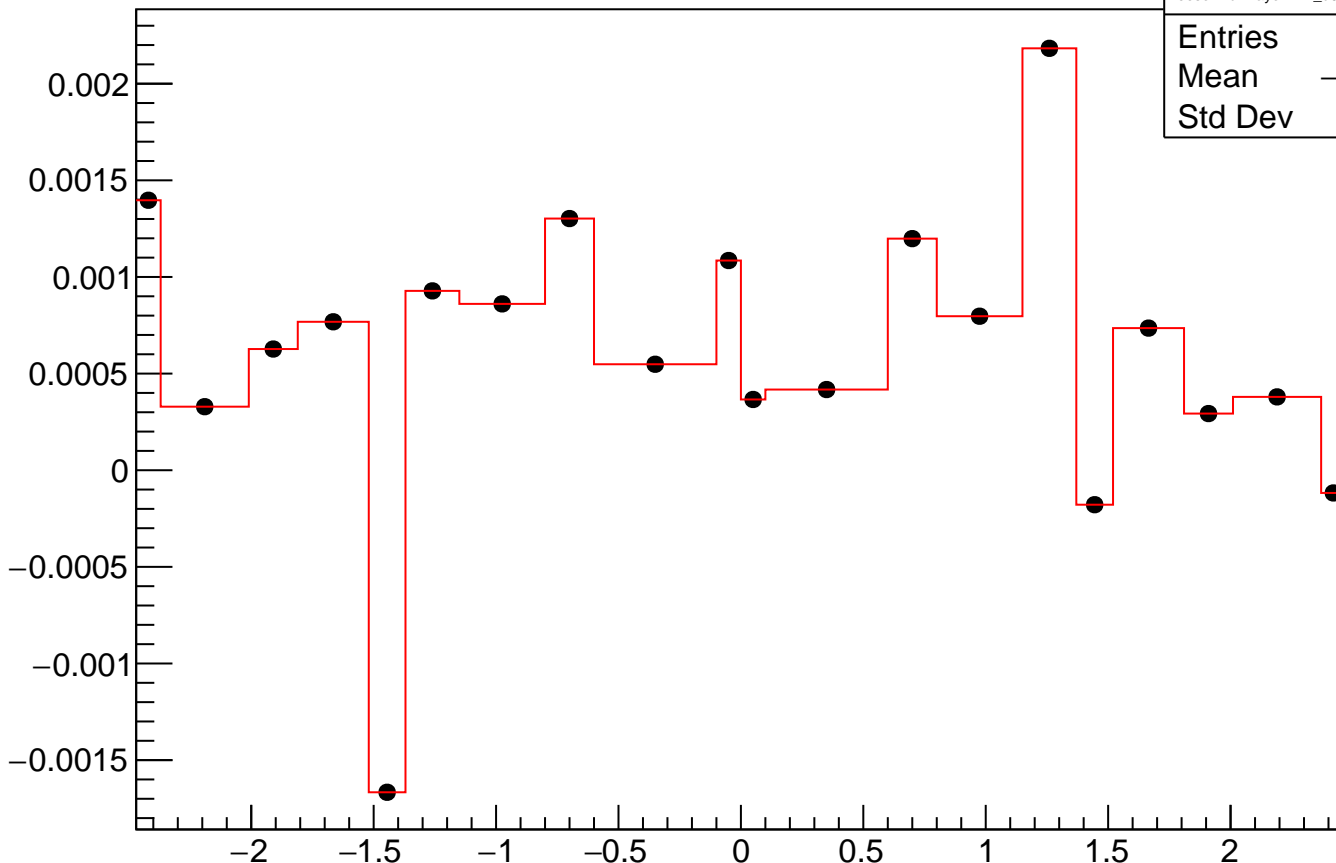
Entries	0
Mean	0.2313
Std Dev	1.806



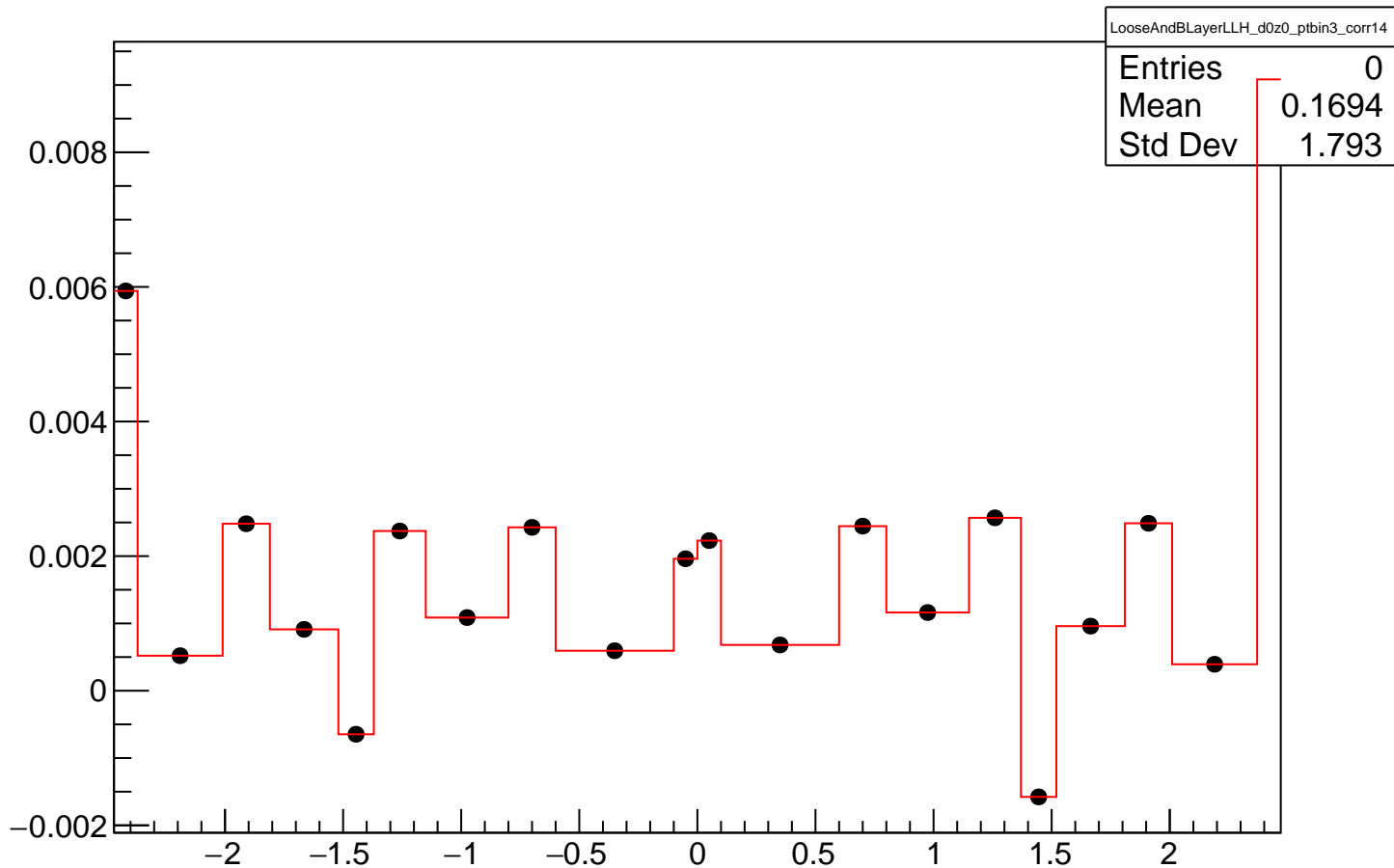
FullSim_LooseLLH_d0z0_corr14

LooseAndBLayerLLH_d0z0_ptbin2_corr14

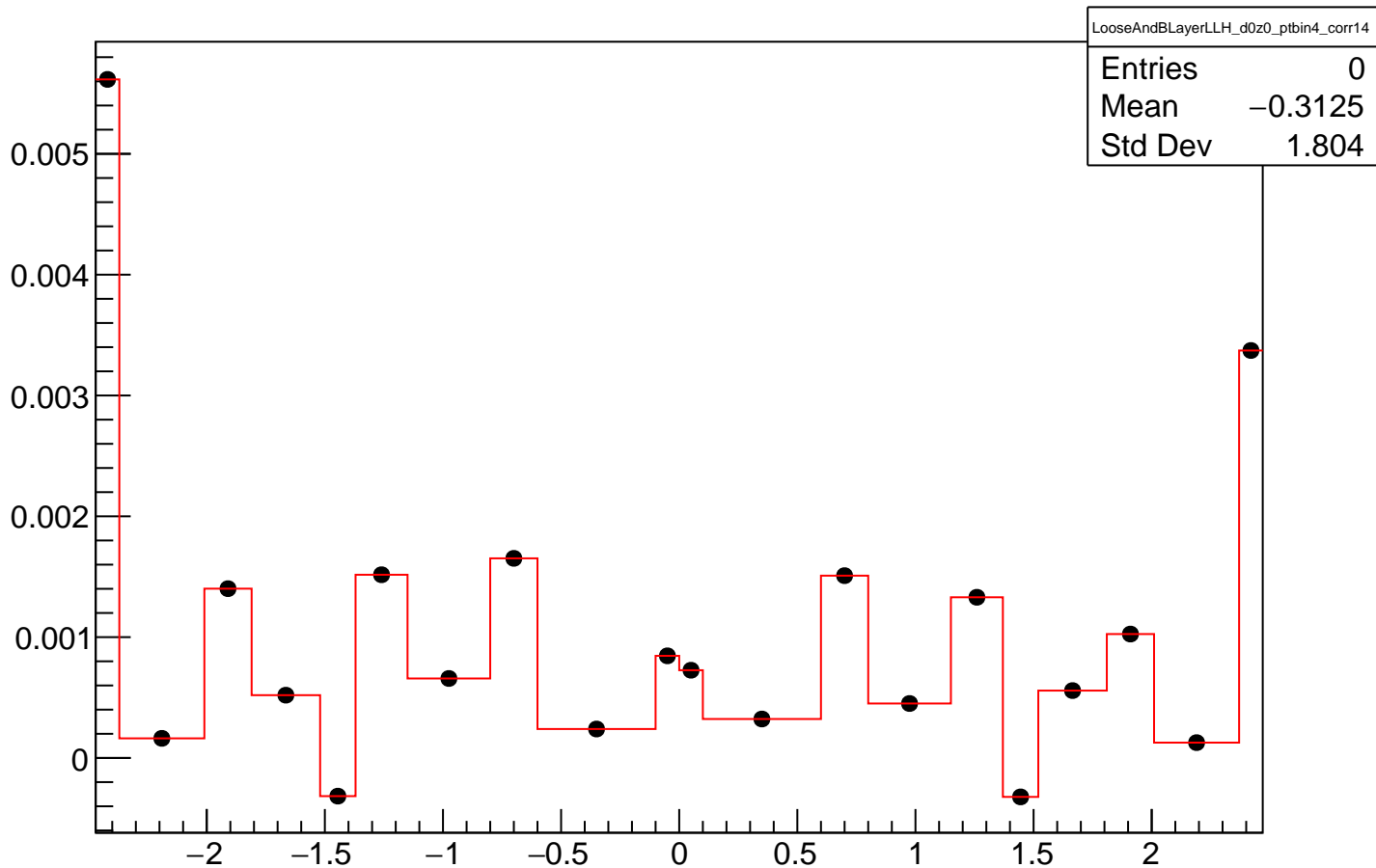
Entries	0
Mean	-0.05971
Std Dev	1.352



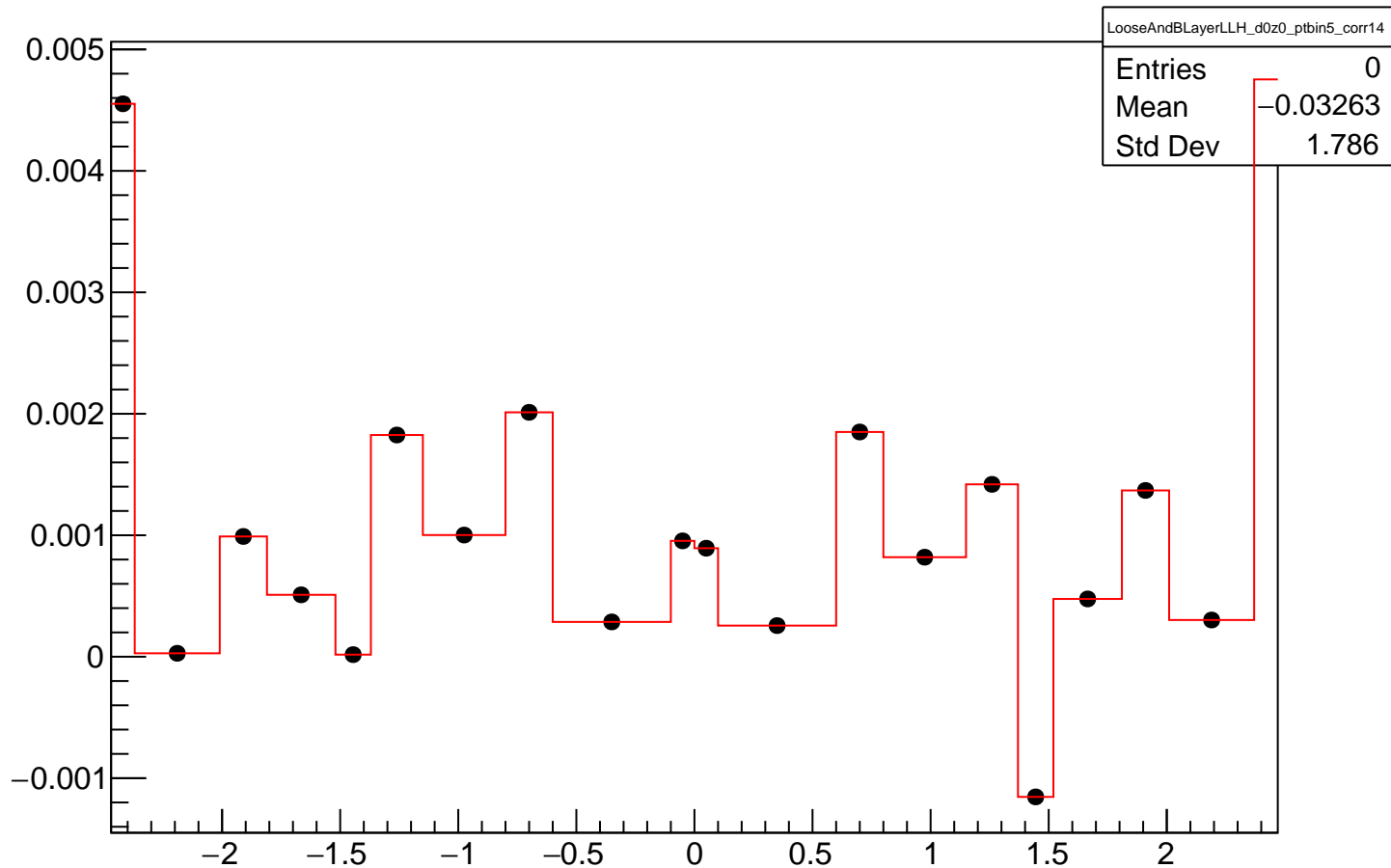
FullSim_LooseLLH_d0z0_corr14



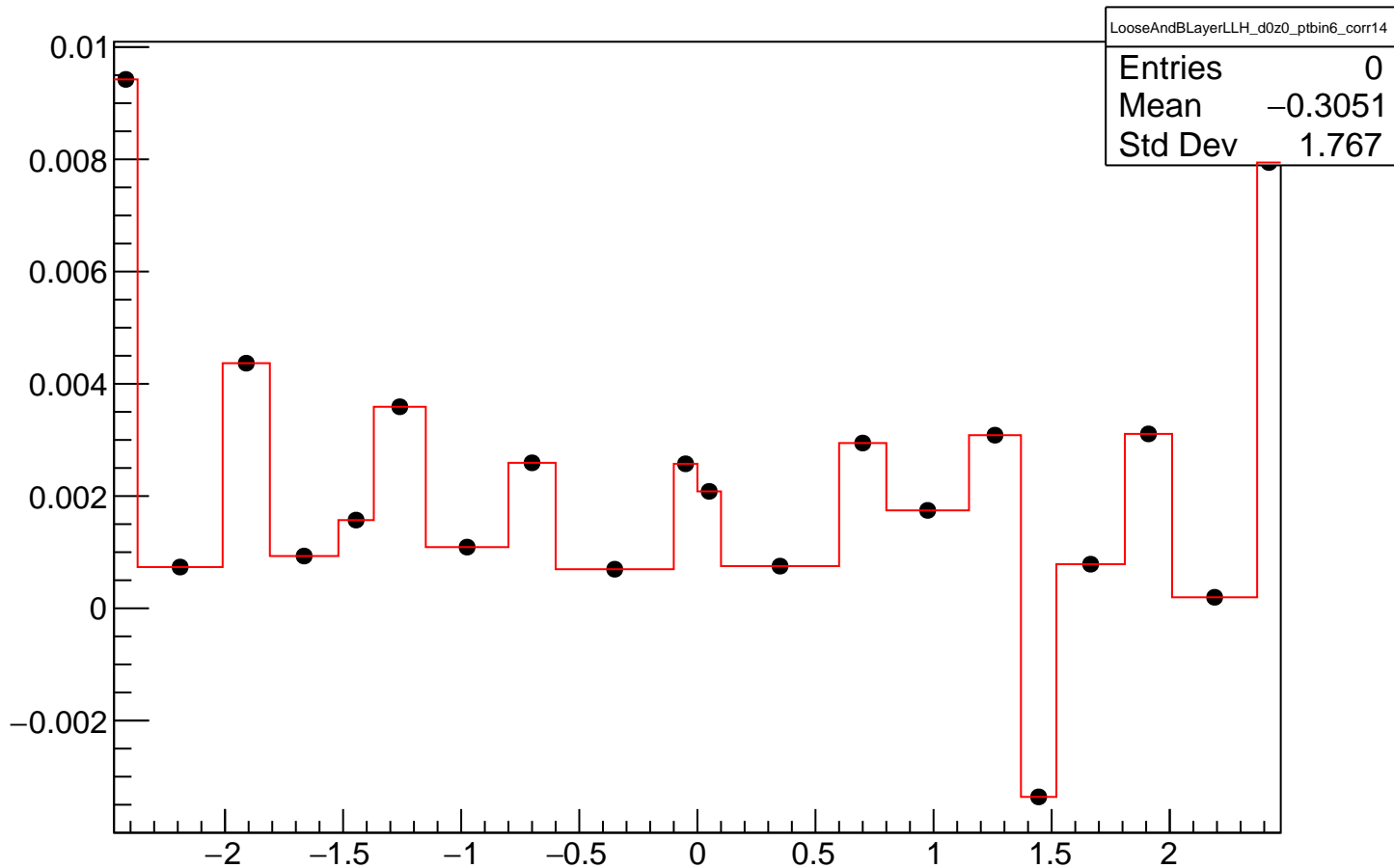
FullSim_LooseLLH_d0z0_corr14



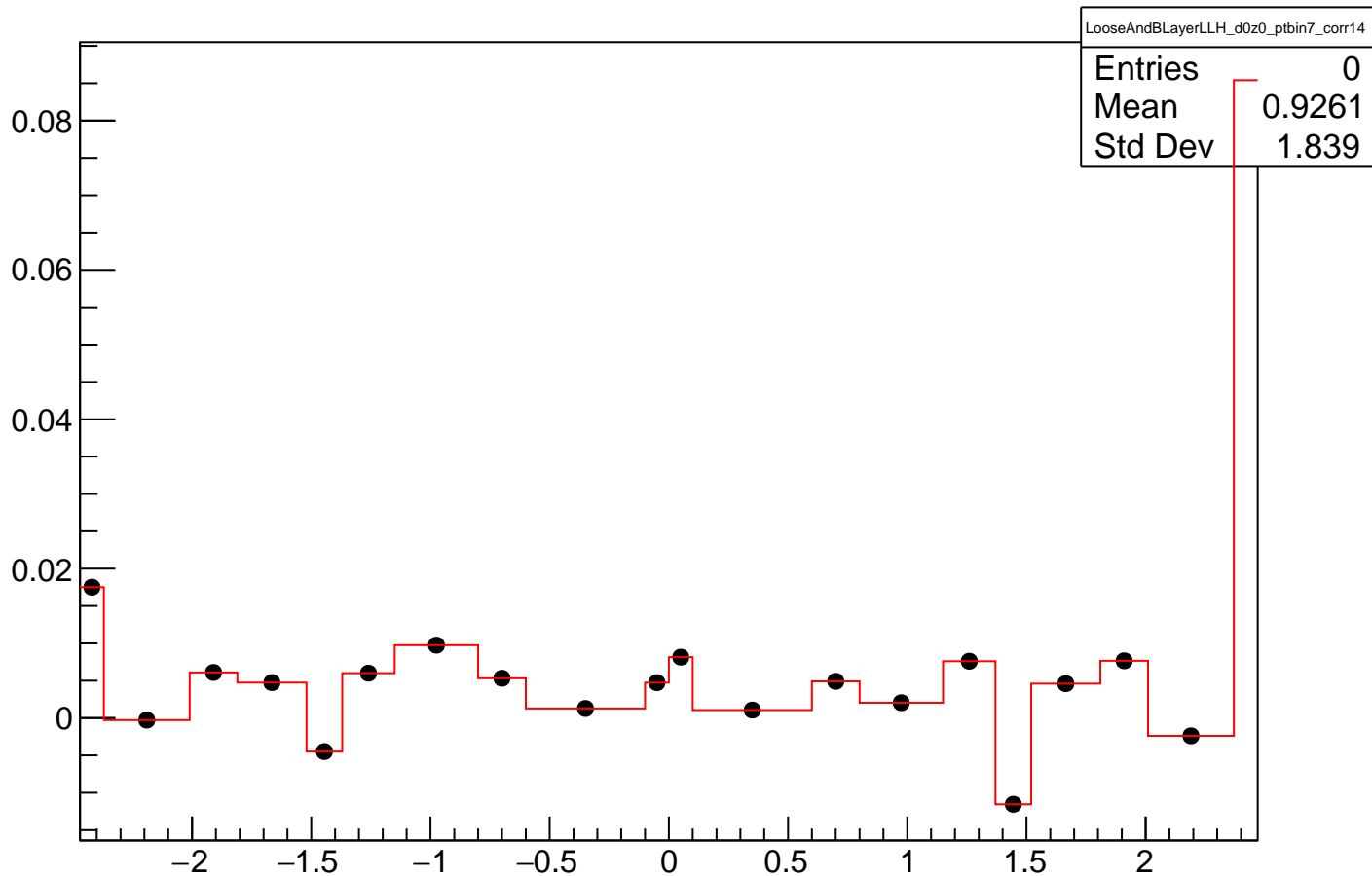
FullSim_LooseLLH_d0z0_corr14



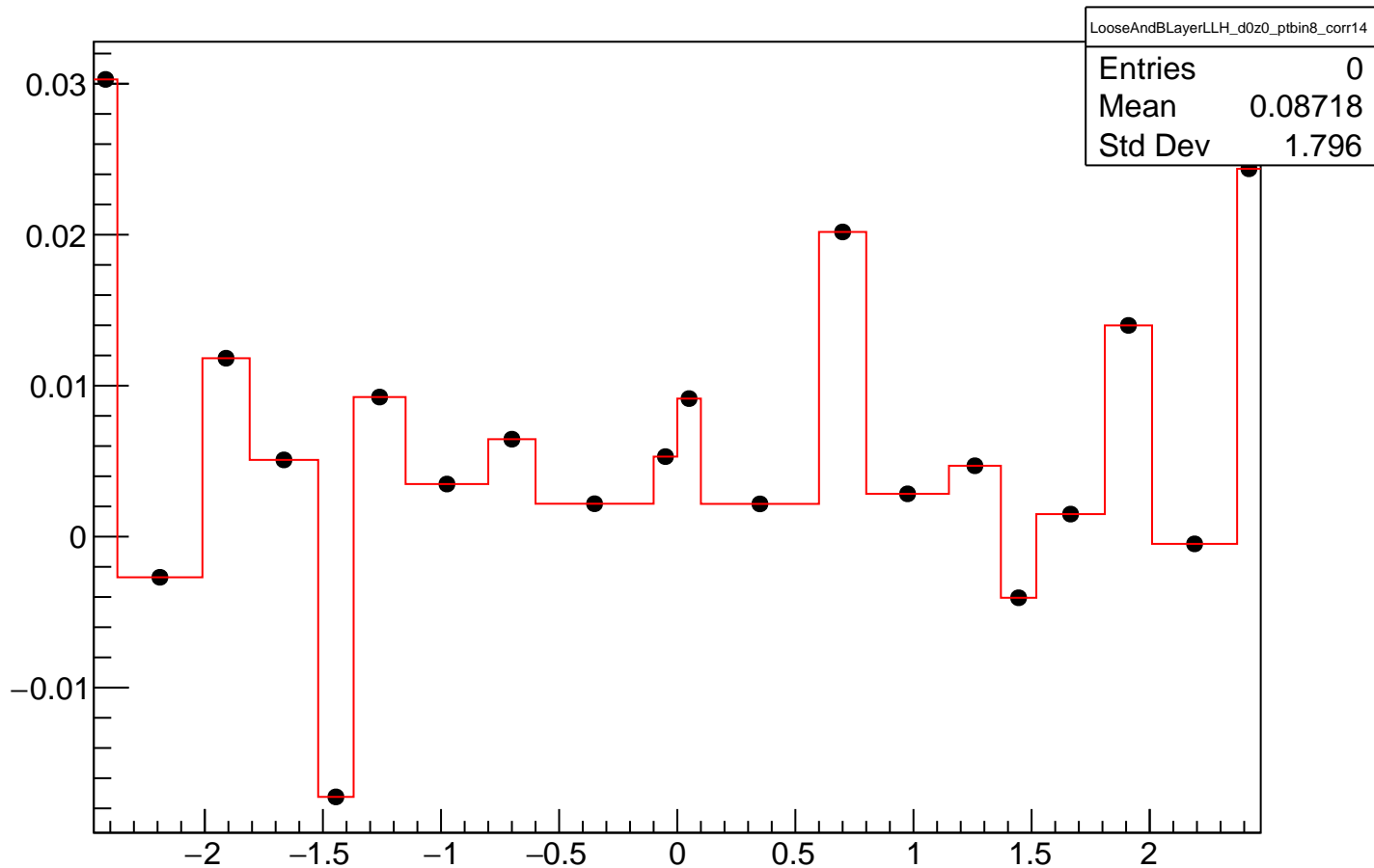
FullSim_LooseLLH_d0z0_corr14



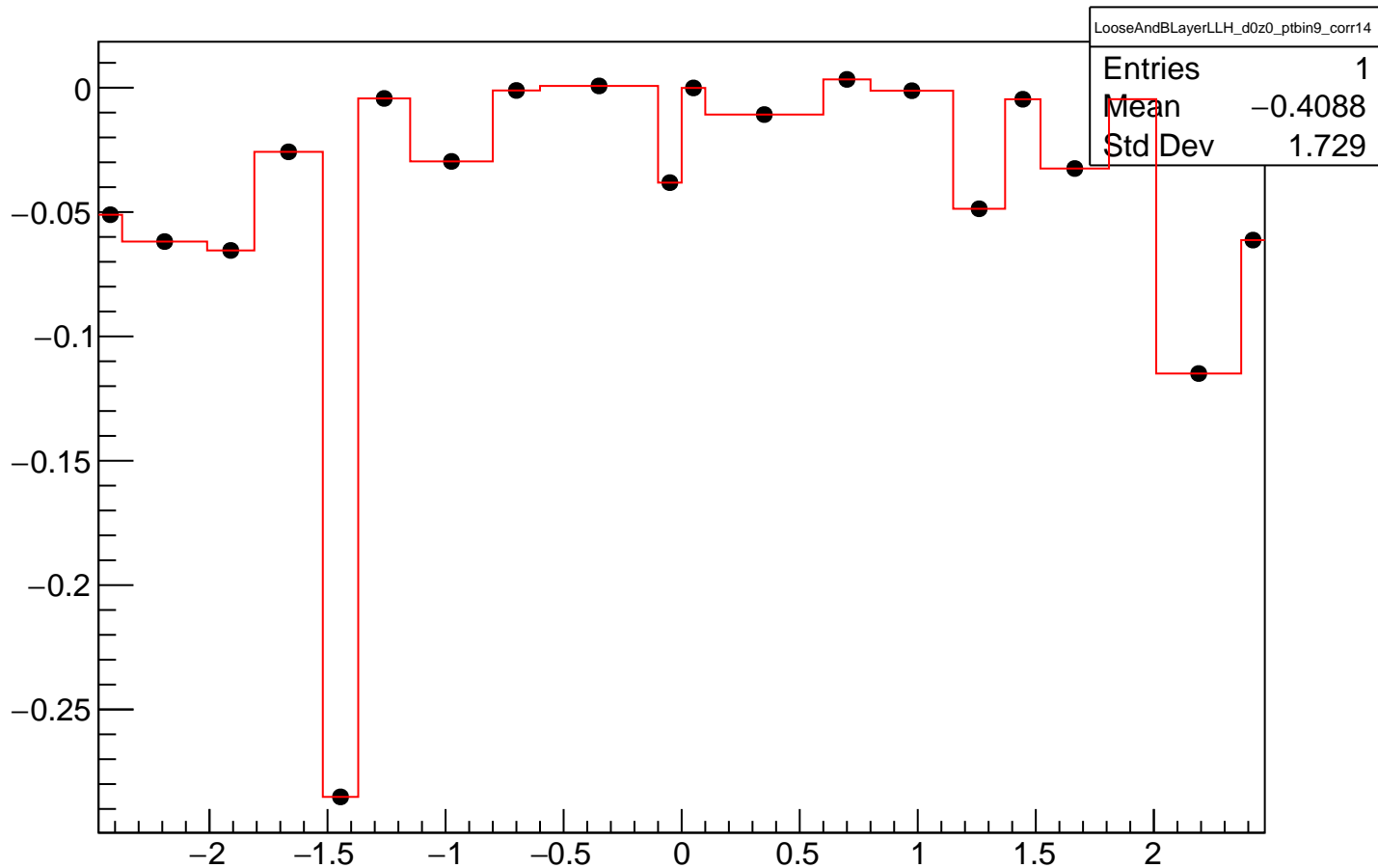
FullSim_LooseLLH_d0z0_corr14



FullSim_LooseLLH_d0z0_corr14



FullSim_LooseLLH_d0z0_corr14



FullSim_LooseLLH_d0z0_corr14

