

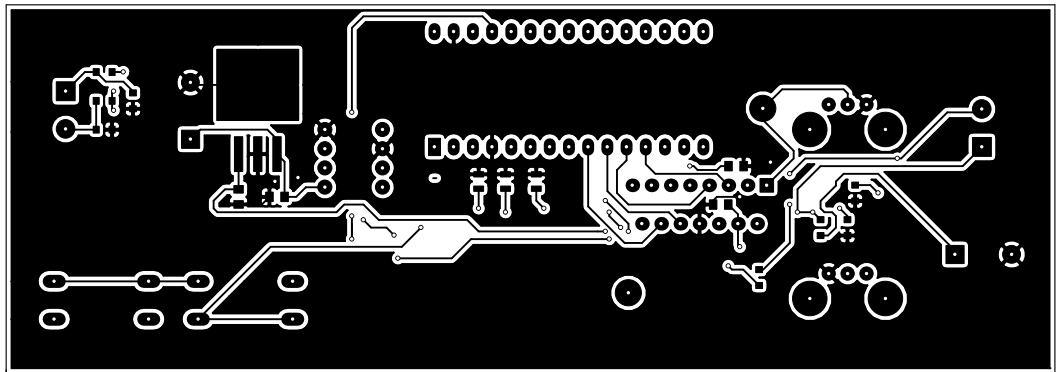
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File: hyperloop\_senior-design.kicad\_pcb

**Title:**

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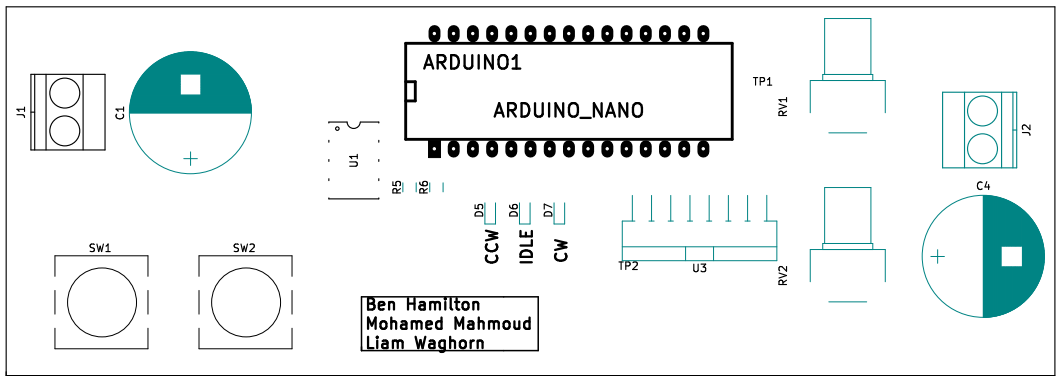
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