

A

B

C

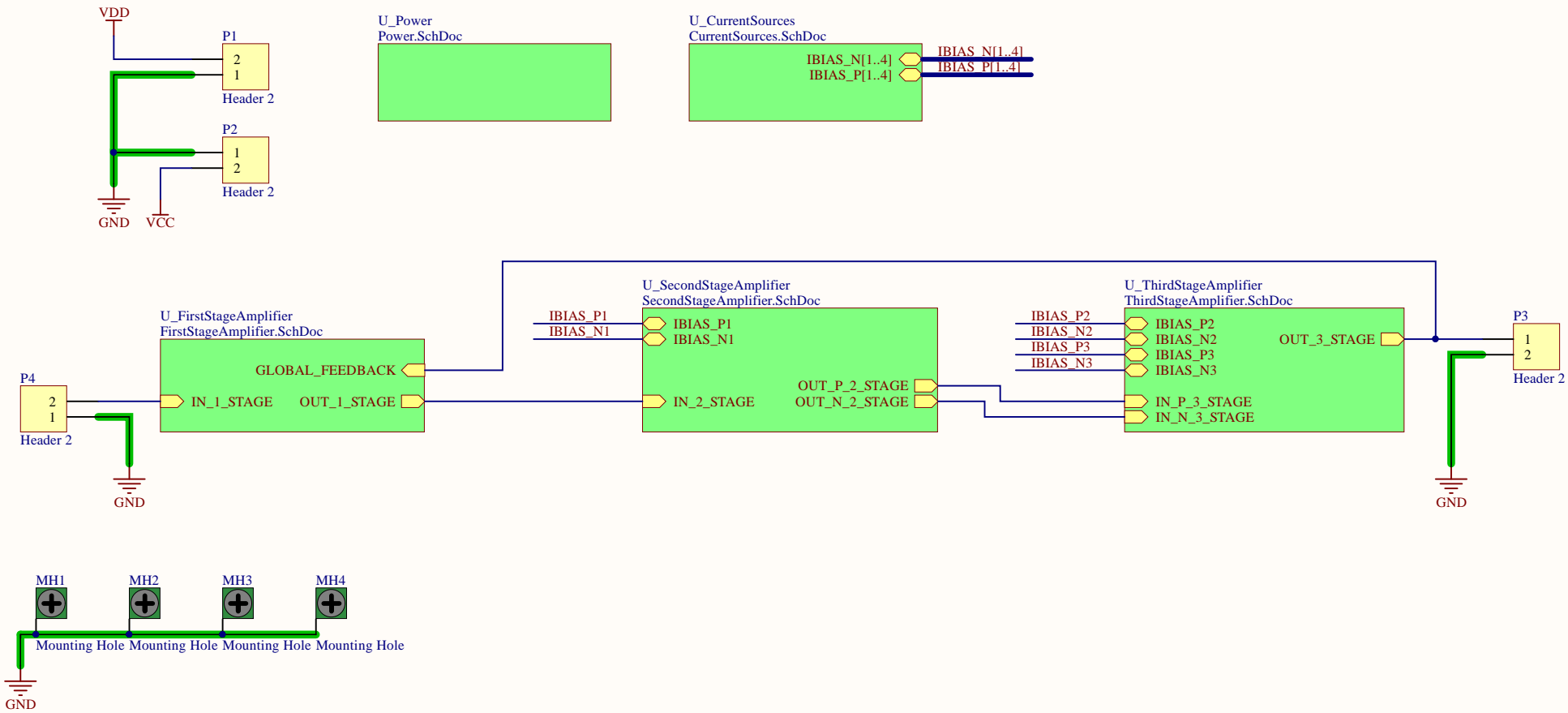
D

A

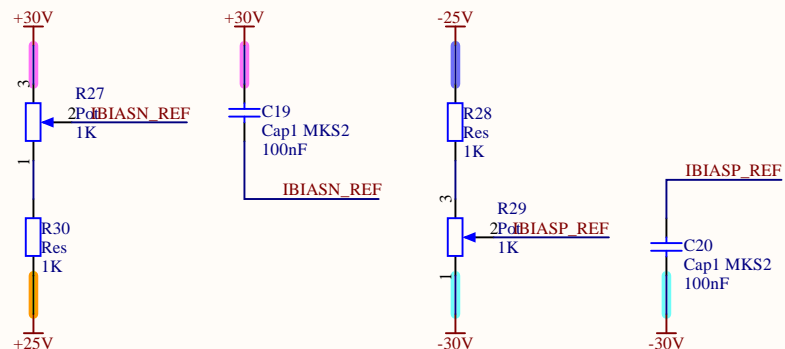
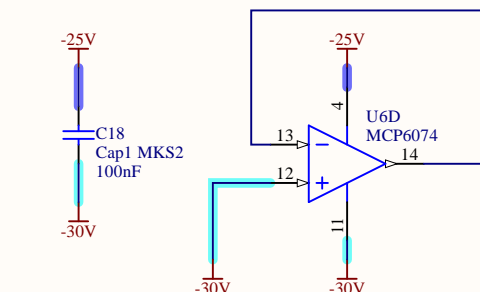
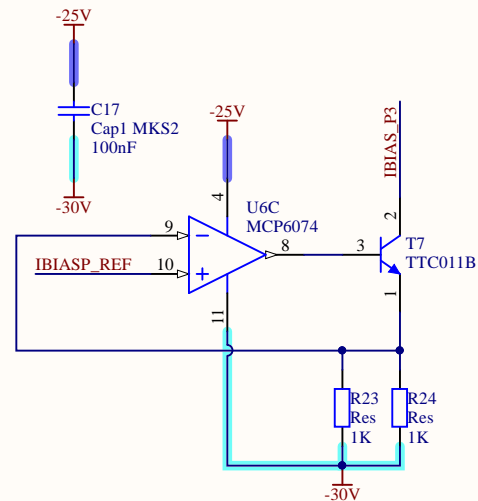
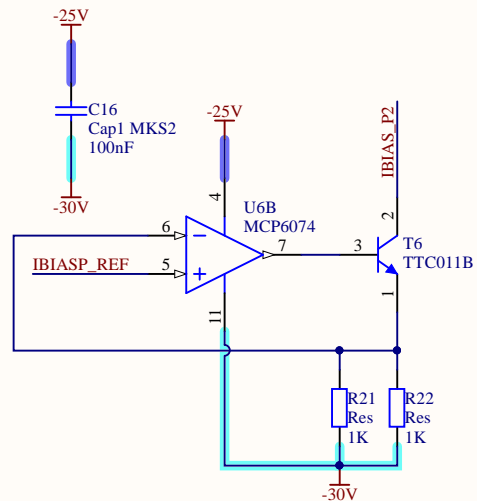
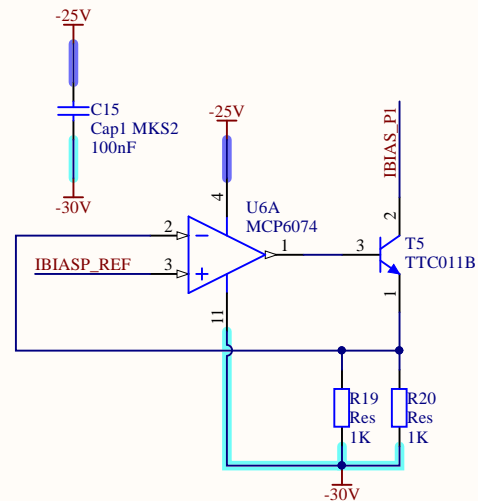
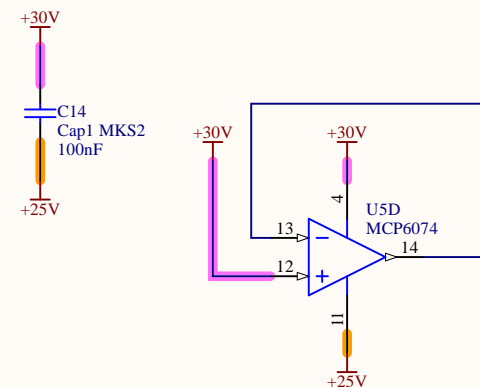
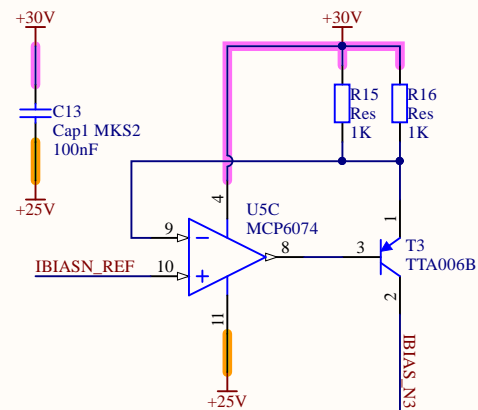
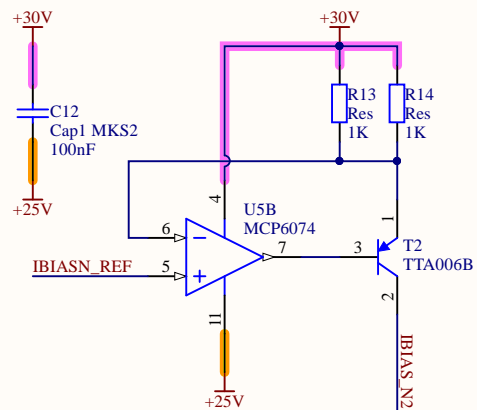
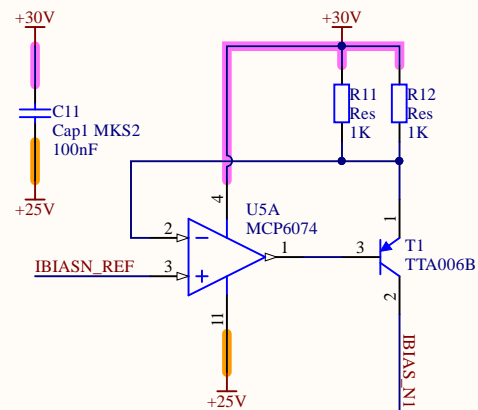
B

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Document name:	Main.SchDoc	Sheet:	1 of 6
Project name:	PowerAmplifierHighSlewRate.PrjPCB		
Author:	Michał Kowalik	Date/Time:	19.12.2018 22:06:39
Designer:	dr inż. Piotr Otfinowski	Revision:	0.2



<b>Document name:</b> CurrentSources.SchDoc		<b>Sheet:</b> 2 of 6	
<b>Project name:</b> PowerAmplifierHighSlewRate.PrjPCB			
<b>Author:</b> Michał Kowalik	<b>Date/Time:</b> 19.12.2018 22:06:39	<b>Revision:</b> 0.2	
<b>Designer:</b> dr inż. Piotr Otfinowski			

A

A

B

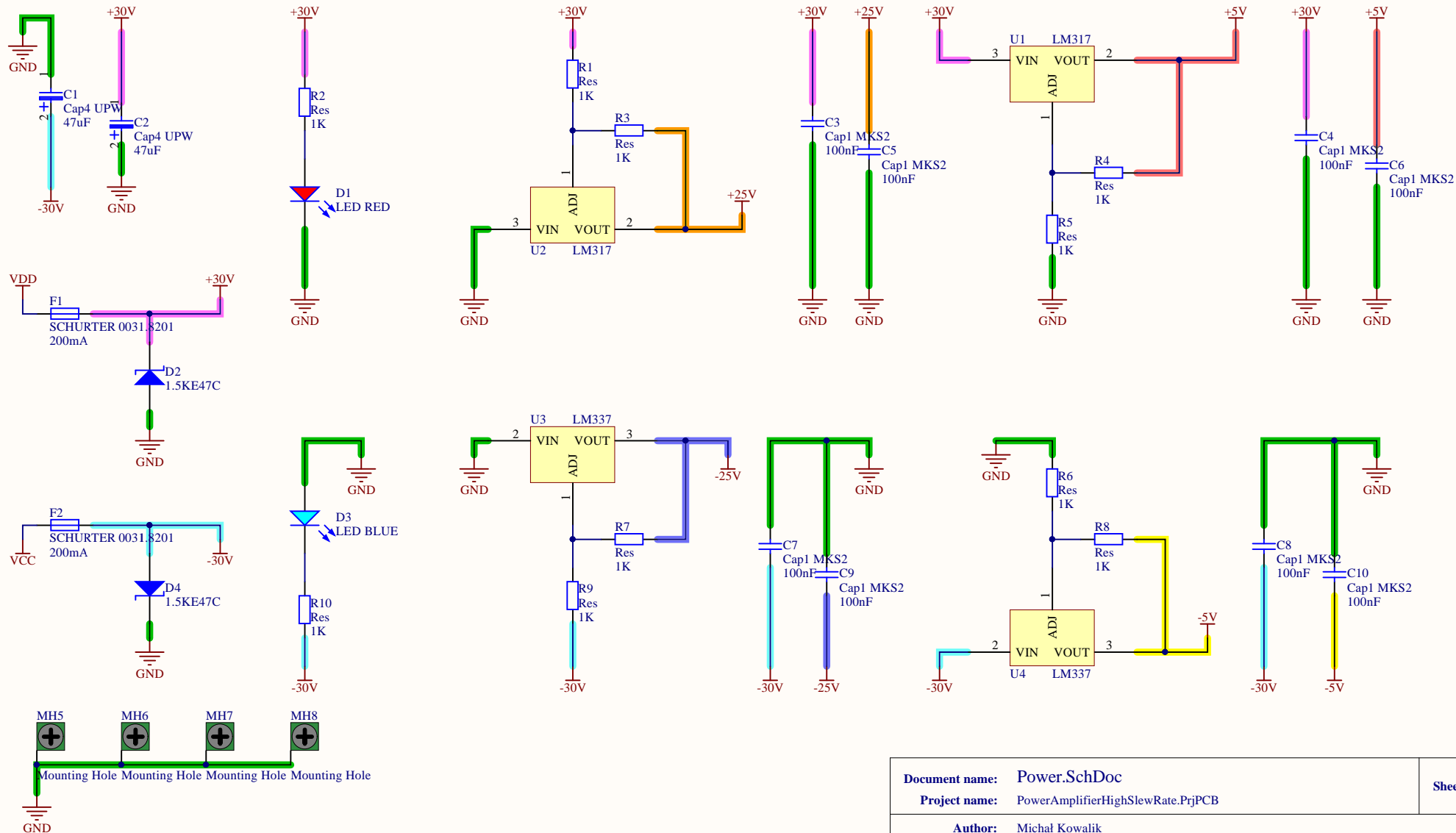
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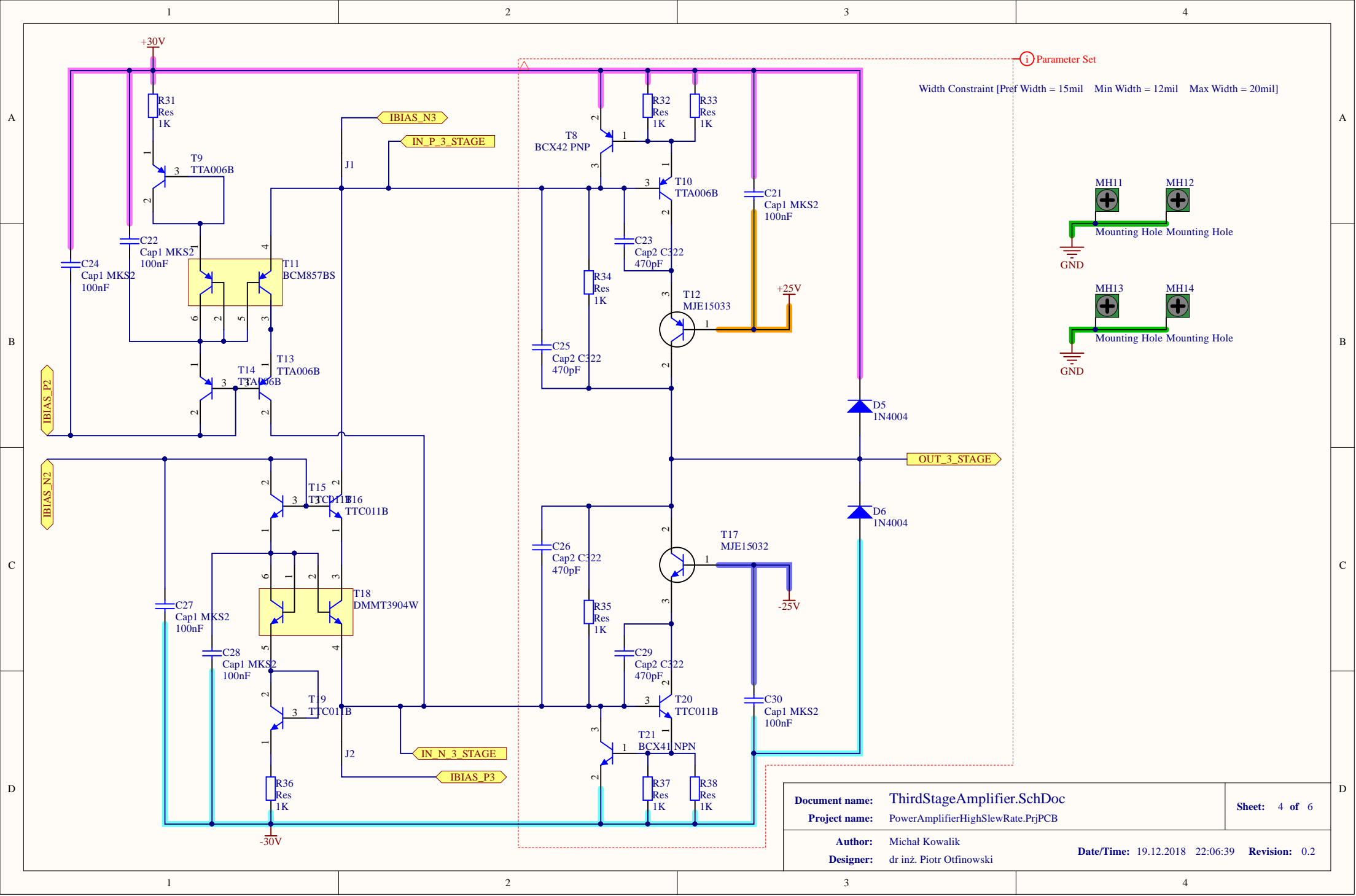
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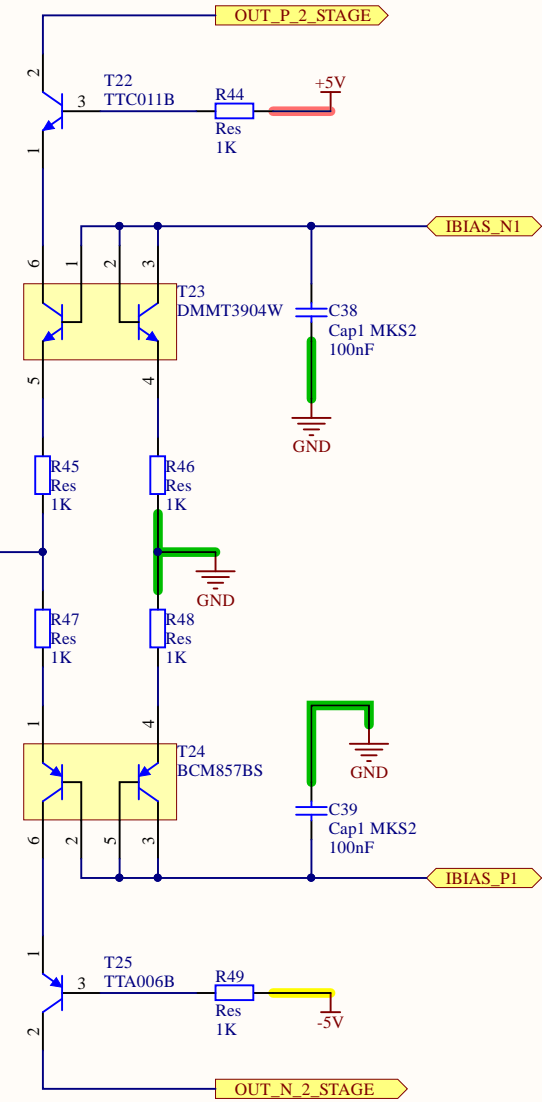
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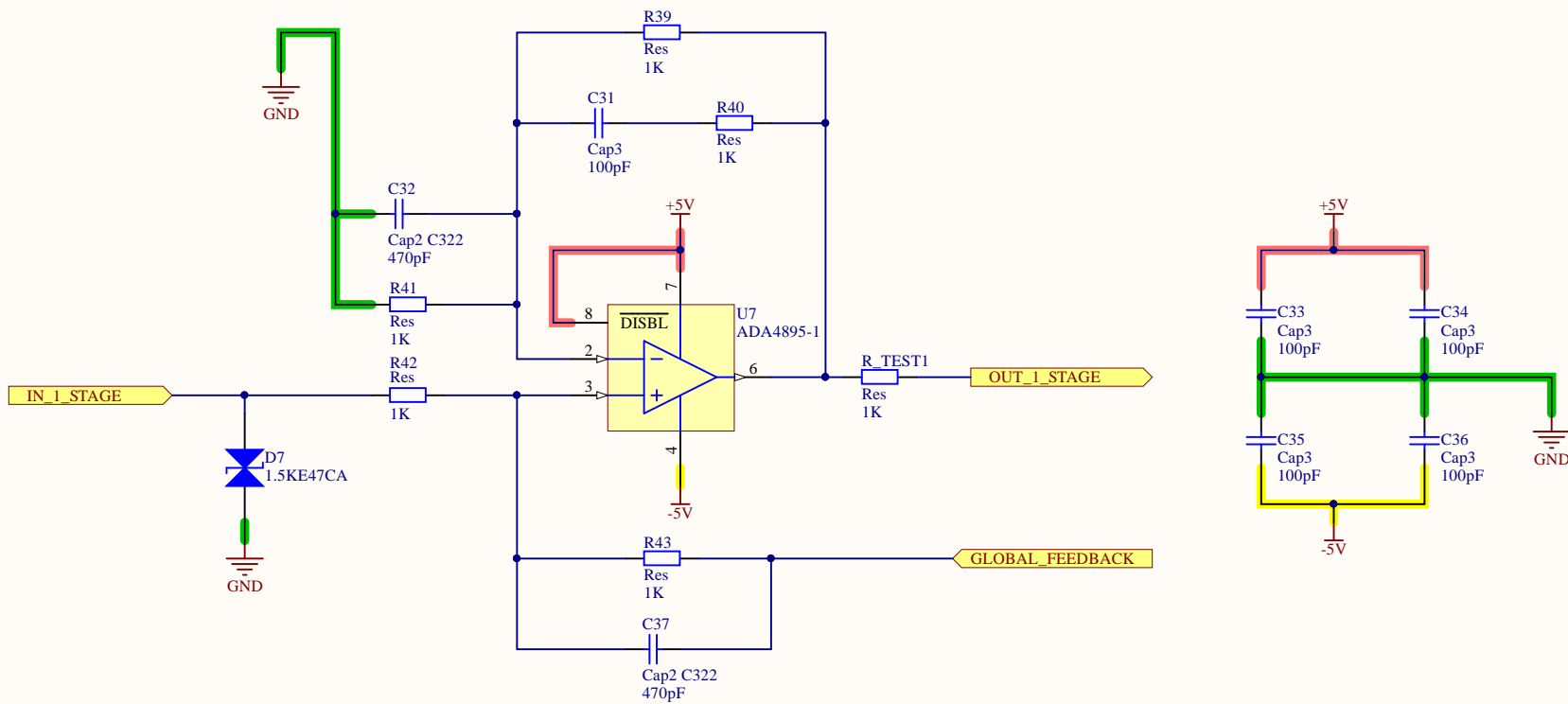
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Document name:	Power.SchDoc	Sheet:	3 of 6
Project name:	PowerAmplifierHighSlewRate.PrjPCB		
Author:	Michał Kowalik	Date/Time:	19.12.2018 22:06:39
Designer:	dr inż. Piotr Otfinowski	Revision:	0.2







Document name: FirstStageAmplifier.SchDoc

Project name: PowerAmplifierHighSlewRate.PrjPCB

Sheet: 6 of 6

Author: Michał Kowalik

Designer: dr inż. Piotr Otfinowski

Date/Time: 19.12.2018 22:06:40 Revision: 0.2