

# Marco Andorno

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## Experience

### SPEA

Mar 2017 - June 2017

#### INTERNSHIP

- Wrote the In-Circuit Test Application Handbook for 3030 Bed Of Nails Board Tester, a technical manual for Test Engineers.
- Acquired knowledge about test procedures for electronic boards.

## Education

### Master's degree in Electronic Engineering

Oct 2017 - present

#### POLITECNICO DI TORINO

- Specializing in electronic systems design.
- Member of *Mu Nu Chapter of IEEE- Eta Kappa Nu*.

#### Relevant projects:

- **RISC-V core:** RV32I in-order core, described in SystemVerilog.
- **Workshop on Innovative Systems:** Logic-in-Memory hardware implementation of the *k*-modes algorithm.
- **FFT unit:** Design of a hardware accelerator implementing the FFT, exploiting a microprogrammed control unit.
- **Logic analyzer:** Design of an 8-channel logic analyzer in VHDL, focusing on the UART interface.

### Bachelor's degree in Electronic Engineering - 110/110 cum laude

Sept 2014 - July 2017

#### POLITECNICO DI TORINO

- Acquired knowledge in: analog and digital electronics, electronic measurements, semiconductor devices, signal processing, telecommunications, automatic controls, computer architectures.
- Took part for all the three years in the Young Talent Project.
- Member of *Mu Nu Chapter of IEEE- Eta Kappa Nu*.

#### Relevant projects:

- **FIR digital filter:** Design of a FIR filter in VHDL, simulated in ModelSim
- **Logic simulator:** Design of a C++ program simulating the behavior of a logic circuit.

### High school diploma, Liceo Scientifico - 100/100 cum laude

Sept 2009 - July 2014

#### IIS 8 MARZO, SETTIMO TORINESE (TO), ITALY

## Extracurricular activities

### Mu Nu Chapter of IEEE-Eta Kappa Nu

Dec 2016 - present

#### TUTOR AND FUNDRAISING ASSISTANT

- Managed tutoring sessions for Electronic Engineering student.
- Helped finding companies willing to fund the association or sponsor its events.
- IEEE-Eta Kappa Nu is the honor society of IEEE recognizing excellence among the students in the fields of electronic and computer engineering.
- This chapter at Politecnico is the first in Europe.

### Alta Scuola Politecnica

Mar 2017

#### WINTER SCHOOL

- Took part in the fourth school of the XII cycle of ASP, contributing to the teamwork.
- One week of seminars and group work based on "Complex decision making under emergency conditions".
- Learned techniques which help during the decision making process.

### The Burlington School of English, London

Aug 2013

#### ENGLISH COURSE LEVEL C1

- Summer school offered to a selected group of students from *IIS 8 Marzo* high school.

### Oakdale College, London

Aug 2012

#### ENGLISH COURSE LEVEL B2

- Summer school offered to a selected group of students from *IIS 8 Marzo* high school.

## Honours

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### AWARDS

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| 2017 | <b>Young Talent Project scholarship,</b><br>Prize awarded to 200 students who scored highest in the admission test for the academic year 2014/15, renewed for the next two years as a result of reaching specific advancement goals. | <i>Politecnico di Torino<br/>and Fondazione<br/>CRT</i> |
| 2015 | <b>Scholarship for educational merit,</b><br>Award received from Presidents Mr. John Elkann and Dr. Sergio Marchionne and Mr. Federico Marchetti, founder and CEO of YOOX Group.   | <i>FCA and CNH<br/>Industrial</i>                       |
| 2014 | <b>Award for educational merit “Enrico Ugoccioni”,</b><br>Scholarship awarded to the best high school students.  | <i>Rotary Club</i>                                      |

### QUALIFICATIONS

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| 2014 | <b>Certificate of Advanced English (CAE),</b><br>Grade A, corresponding to Level C2 of the CEFR. | <i>University of<br/>Cambridge</i> |
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## Skills

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<b>Languages</b>	Italian (mother tongue) English (proficient)
<b>Software</b>	Windows and Unix operating systems Altera Quartus ModelSim Synopsys Design Compiler MATLAB and Simulink LTSpice Microsoft Office
<b>Computer languages</b>	VHDL, Verilog C, C++ Python C# (basic knowledge) Tcl (basic knowledge)