# Marco **Andorno**

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## **Exp**erience

**SPEA**                                                                                  *Mar 2017 - June 2017*
Internship
- Wrote the In-Circuit Test Application Handbook for 3030 Bed Of Nails Board Tester, a technical manual for Test Engineers.
- Acquired knowledge about test procedures for electronic boards.

## **Edu**cation

**Master's degree in Electronic Engineering**                                             *Oct 2017 - present*
Politecnico di Torino
- Specializing in electronic systems design.
- Member of *Mu Nu Chapter of IEEE- Eta Kappa Nu*.

Relevant projects:
- **RISC-V core**: RV32I in-order core, described in SystemVerilog.
- **Workshop on Innovative Systems**: Logic-in-Memory hardware implementation of the *k*-modes algorithm.
- **FFT unit**: Design of a hardware accelerator implementing the FFT, exploiting a microprogrammed control unit.
- **Logic analyzer**: Design of an 8-channel logic analyzer in VHDL, focusing on the UART interface.

**Bachelor's degree in Electronic Engineering - 110/110 cum laude**                       *Sept 2014 - July 2017*
Politecnico di Torino
- Acquired knowledge in: analog and digital electronics, electronic measurements, semiconductor devices, signal processing, telecommunications, automatic controls, computer architectures.
- Took part for all the three years in the Young Talent Project.
- Member of *Mu Nu Chapter of IEEE- Eta Kappa Nu*.

Relevant projects:
- **FIR digital filter**: Design of a FIR filter in VHDL, simulated in ModelSim
- **Logic simulator**: Design of a C++ program simulating the behavior of a logic circuit.

**High school diploma, Liceo Scientifico - 100/100 cum laude**                            *Sept 2009 - July 2014*
IIS 8 Marzo, Settimo Torinese (TO), Italy

## **Ext**racurricular activities

**Mu Nu Chapter of IEEE-Eta Kappa Nu**                                                    *Dec 2016 - present*
Tutor and fundraising assistant
- Managed tutoring sessions for Electronic Engineering student.
- Helped finding companies willing to fund the association or sponsor its events.
- IEEE-Eta Kappa Nu is the honor society of IEEE recognizing excellence among the students in the fields of electronic and computer engineering.
- This chapter at Politecnico is the first in Europe.

**Alta Scuola Politecnica**                                                               *Mar 2017*
Winter School
- Took part in the fourth school of the XII cycle of ASP, contributing to the teamwork.
- One week of seminars and group work based on "Complex decision making under emergency conditions".
- Learned techniques which help during the decision making process.

**The Burlington School of English, London**                                             *Aug 2013*
English course Level C1
- Summer school offered to a selected group of students from *IIS 8 Marzo* high school.

**Oakdale College, London**                                                              *Aug 2012*
English course Level B2
- Summer school offered to a selected group of students from *IIS 8 Marzo* high school.

# Honours

## AWARDS

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|---|---|---|
| 2017 | **Young Talent Project scholarship**, <br> Prize awarded to 200 students who scored highest in the admission test for the academic year 2014/15, renewed for the next two years as a result of reaching specific advancement goals. | *Politecnico di Torino and Fondazione CRT* |
| 2015 | **Scholarship for educational merit**, <br> Award received from Presidents Mr. John Elkann and Dr. Sergio Marchionne and Mr. Federico Marchetti, founder and CEO of YOOX Group. | *FCA and CNH Industrial* |
| 2014 | **Award for educational merit "Enrico Ugoccioni"**, <br> Scholarship awarded to the best high school students. | *Rotary Club* |

## QUALIFICATIONS

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|---|---|---|
| 2014 | **Certificate of Advanced English (CAE)**, <br> Grade A, corresponding to Level C2 of the CEFR. | *University of Cambridge* |

# Skills

**Languages**
Italian (mother tongue)
English (proficient)

**Software**
Windows and Unix operating systems
Altera Quartus
ModelSim
Synopsys Design Compiler
MATLAB and Simulink
LTSpice
Microsoft Office

**Computer languages**
VHDL, Verilog
C, C++
Python
C# (basic knowledge)
Tcl (basic knowledge)