

	1	2	3	4	5	6
A						A
B	<div>Power_management</div> <div>File: Power_management.kicad_sch</div>	<div>Microcontroller</div> <div>File: Microcontroller.kicad_sch</div>	<div>Peripherals</div> <div>File: Peripherals.kicad_sch</div>	<div>Connectors</div> <div>File: Connectors.kicad_sch</div>		
C						C
D						D
	1	2	3	4	5	6

Title: DATALOGGER01A

Universal battery powered SDcard datalogger.

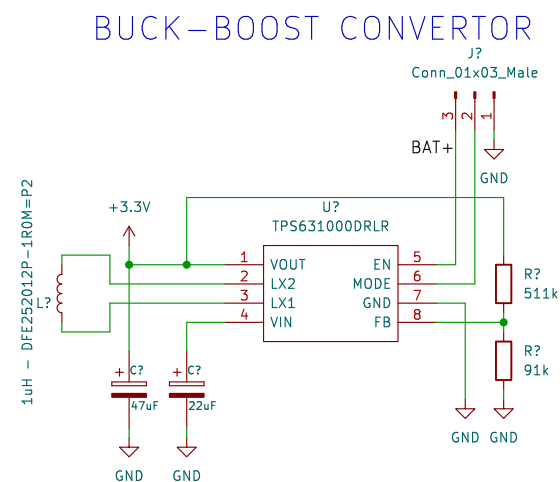
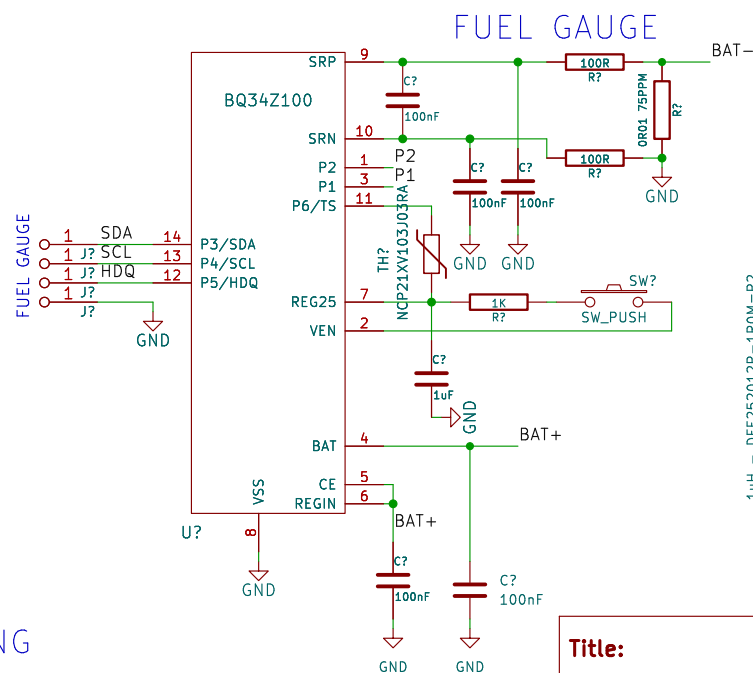
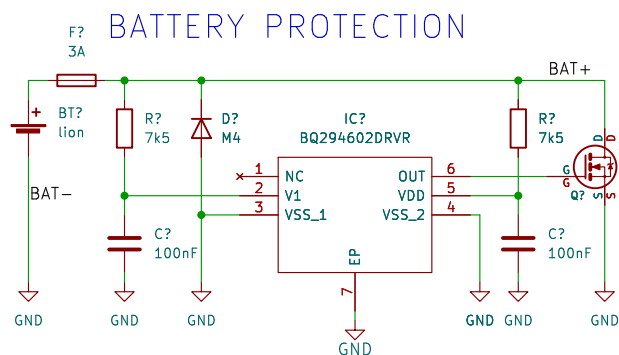
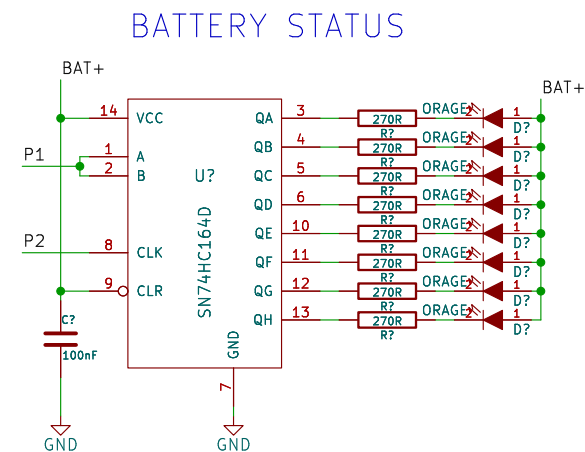
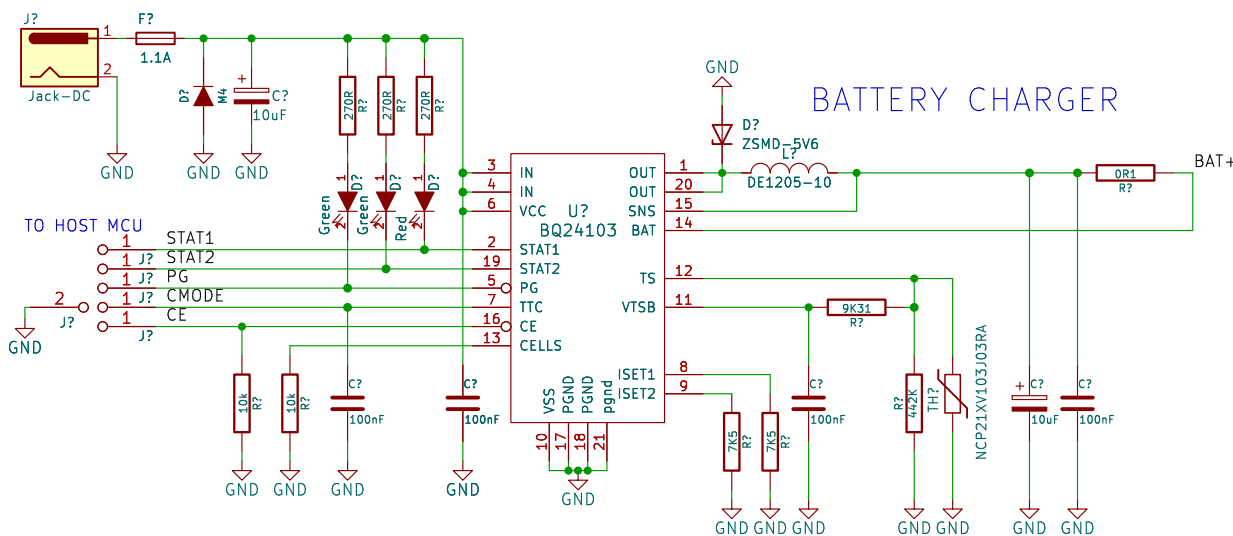
Author: Mlab www.mlab.cz

Size: A4

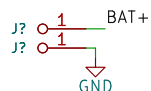
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POWER OUTPUT MECHANICAL FIXING



Title:

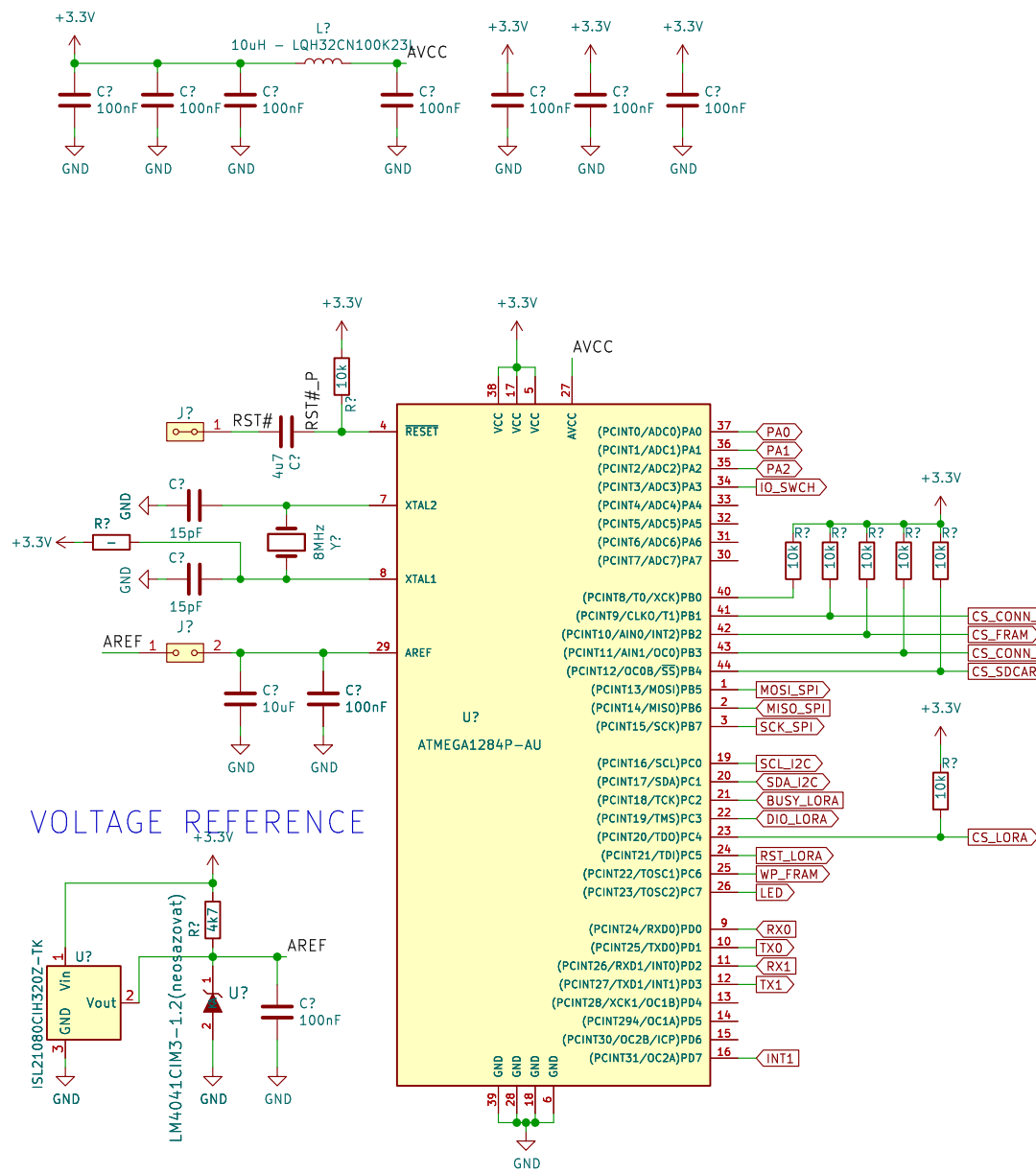
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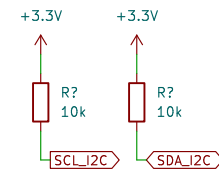
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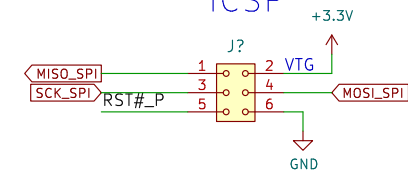


VOLTAGE REFERENCE

I2C PULL UPS



ICSP



- PB0 -
- PB1 - CS_CONN_1
- PB2 - CS_FRAM
- PB3 - CS_CONN_2
- PB4 - CS_SDCARD
- PB5 - MOSI_SPI
- PB6 - MISO_SPI
- PB7 - SCK_SPI

Title:

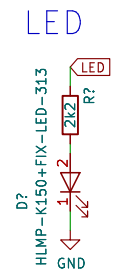
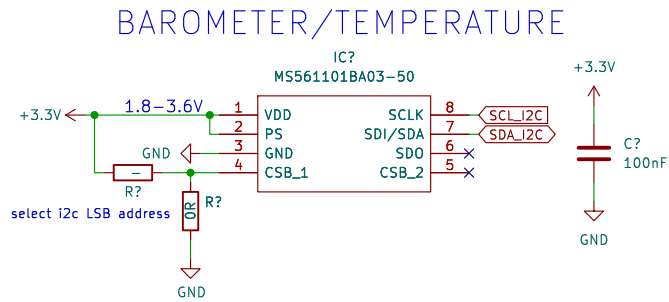
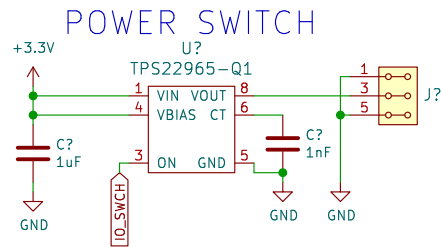
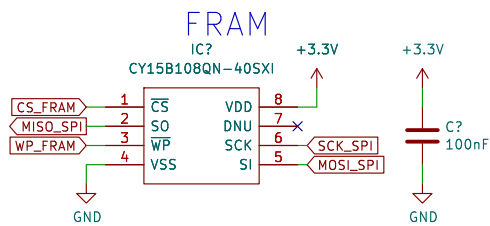
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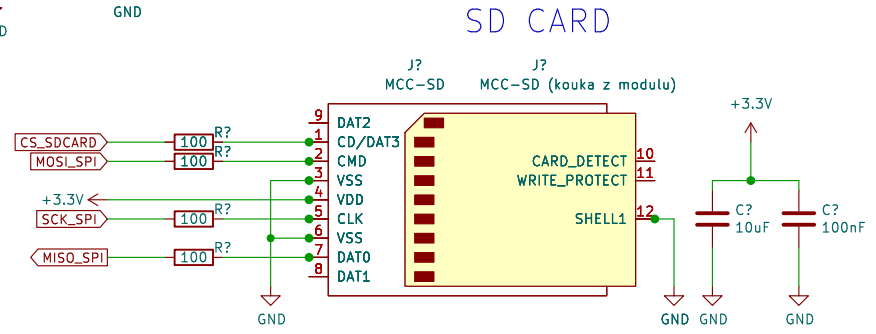
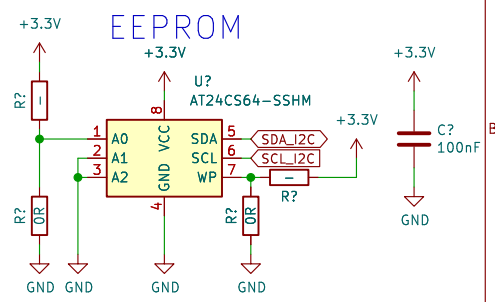
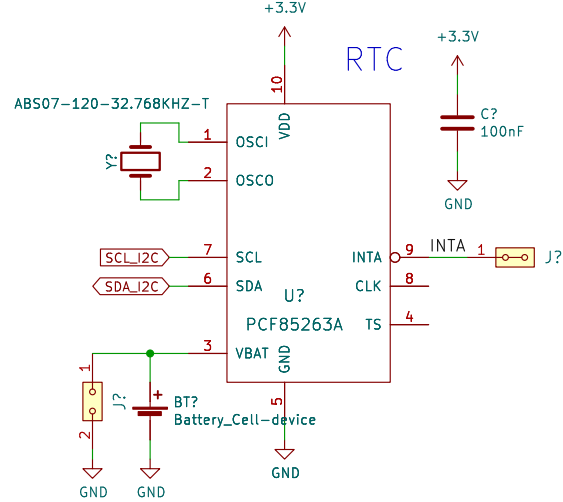
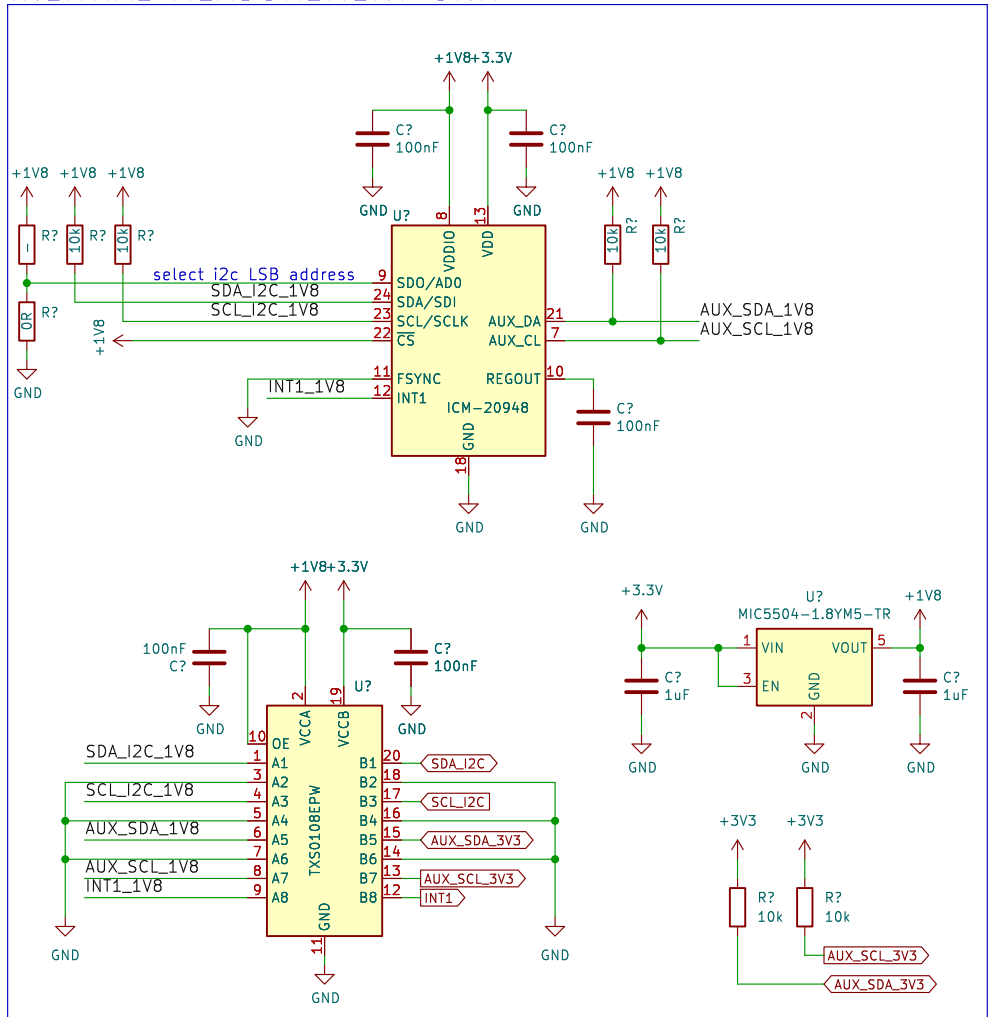
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INERTIAL MEASUREMENT UNIT



Title:

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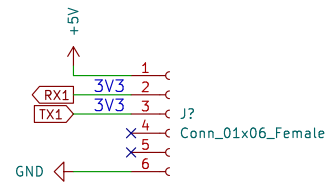
Page: 4/6

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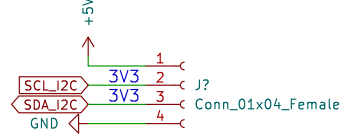


PIXHAWK STANDARD HEADERS

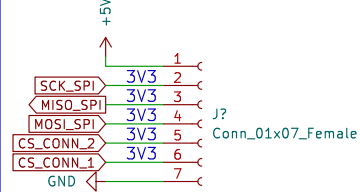
UART CONN.



I2C CONN.

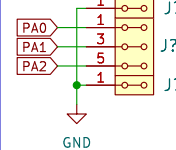


SPI CONN.

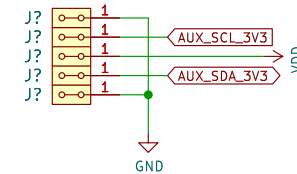


PIN HEADERS

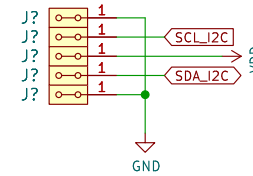
ANALOG CONN



AUX_I2C CONN



I2C CONN



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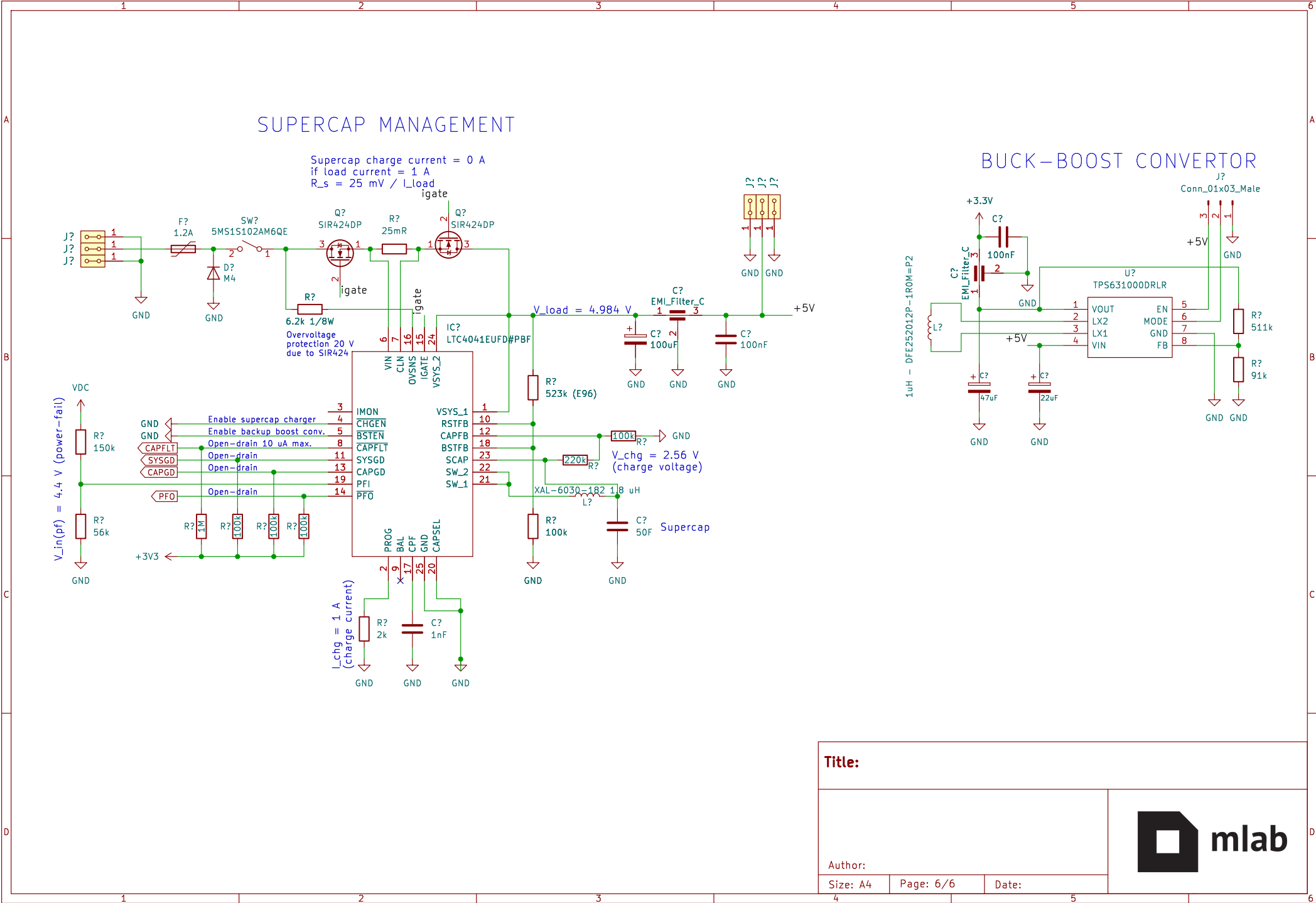
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[illegible]

The image contains two circuit diagrams. The first, titled 'SUPERCAP MANAGEMENT', is a detailed schematic for a system involving a supercapacitor and a backup boost converter. It features an LTC4041EUFD#PBF IC, a TPS63100DRLR IC, and various passive components like resistors, capacitors, and inductors. Key annotations include 'Supercap charge current = 0 A if load current = 1 A', 'V_load = 4.984 V', and 'V_chg = 2.56 V (charge voltage)'. The second diagram, titled 'BUCK-BOOST CONVERTOR', shows a simpler circuit using a TPS63100DRLR IC to convert a 5V input to a 3.3V output. It includes an inductor L1 and several capacitors for filtering and decoupling.



			
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