

	1	2	3	4	5	6
A						A
B	<div>Power_management</div> <div>File: Power_management.kicad_sch</div>	<div>Microcontroller</div> <div>File: Microcontroller.kicad_sch</div>	<div>Peripherals</div> <div>File: Peripherals.kicad_sch</div>	<div>Connectors</div> <div>File: Connectors.kicad_sch</div>		
C						C
D						D
	1	2	3	4	5	6

Title: DATALOGGER01A

Universal battery powered SDcard datalogger.

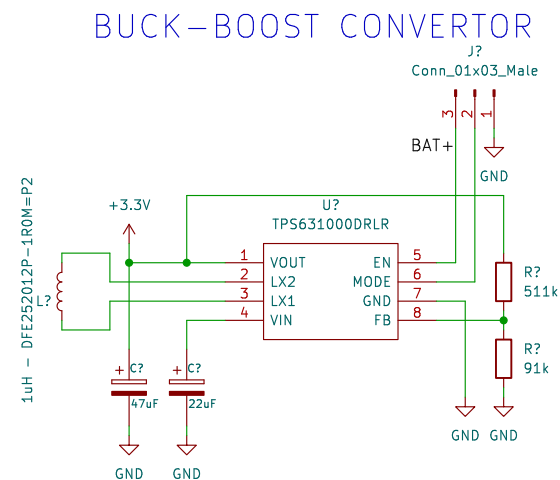
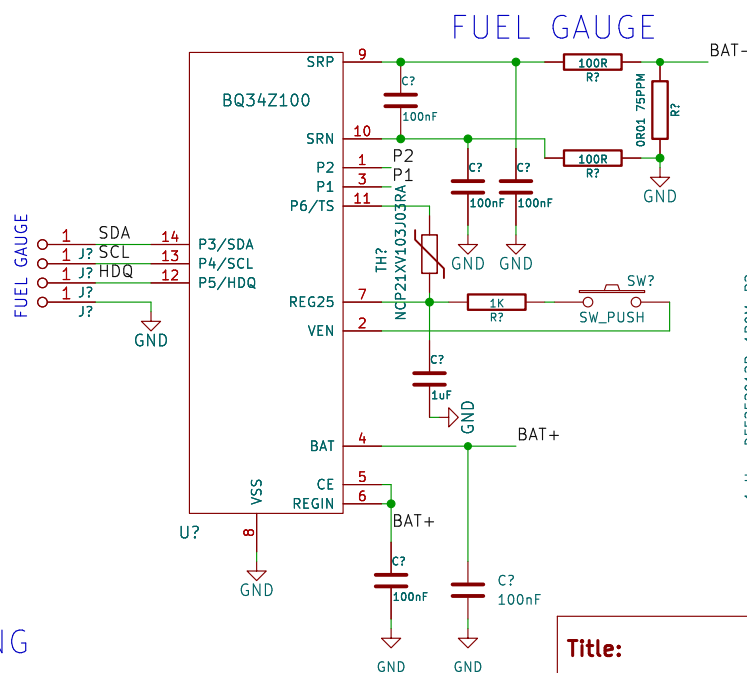
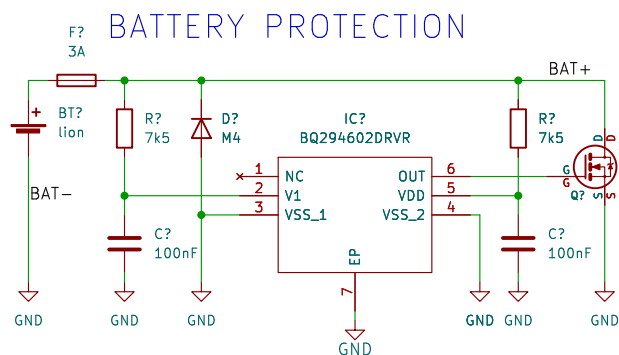
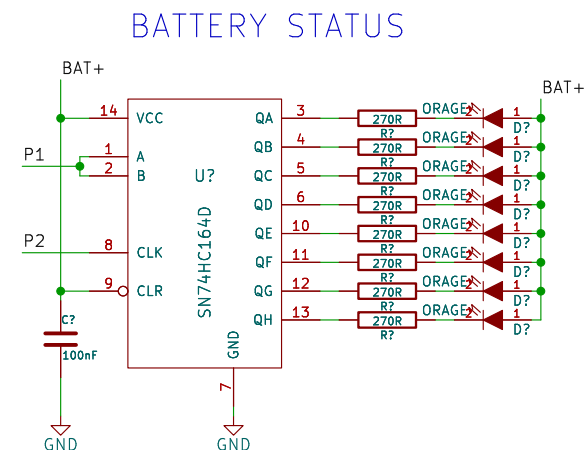
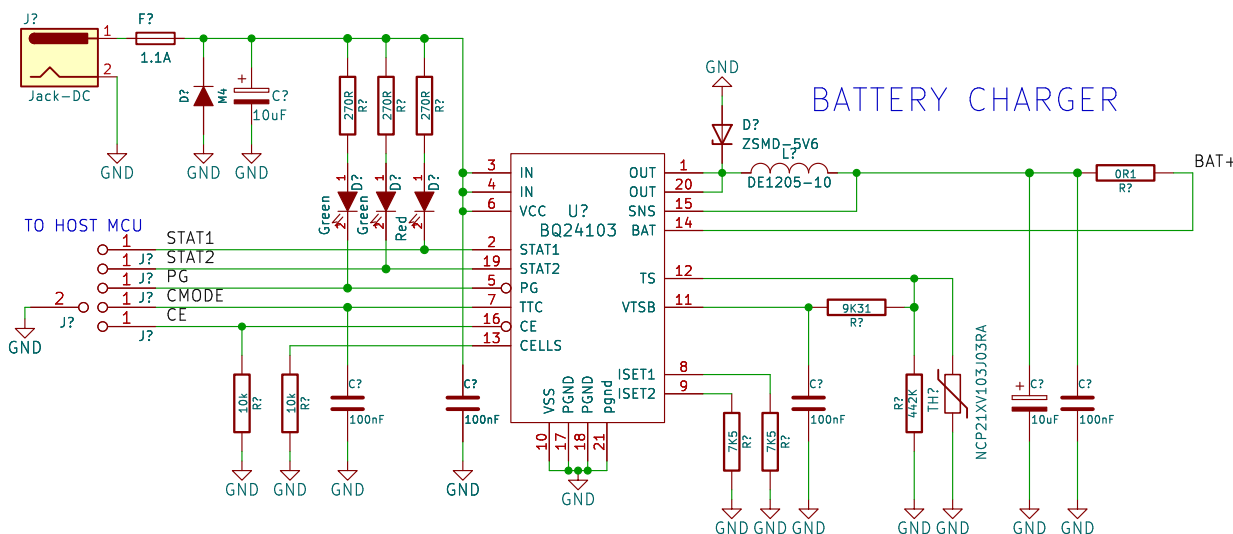
Author: Mlab www.mlab.cz

Size: A4

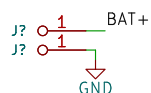
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Date: 2022-08-14





POWER OUTPUT MECHANICAL FIXING



Title:

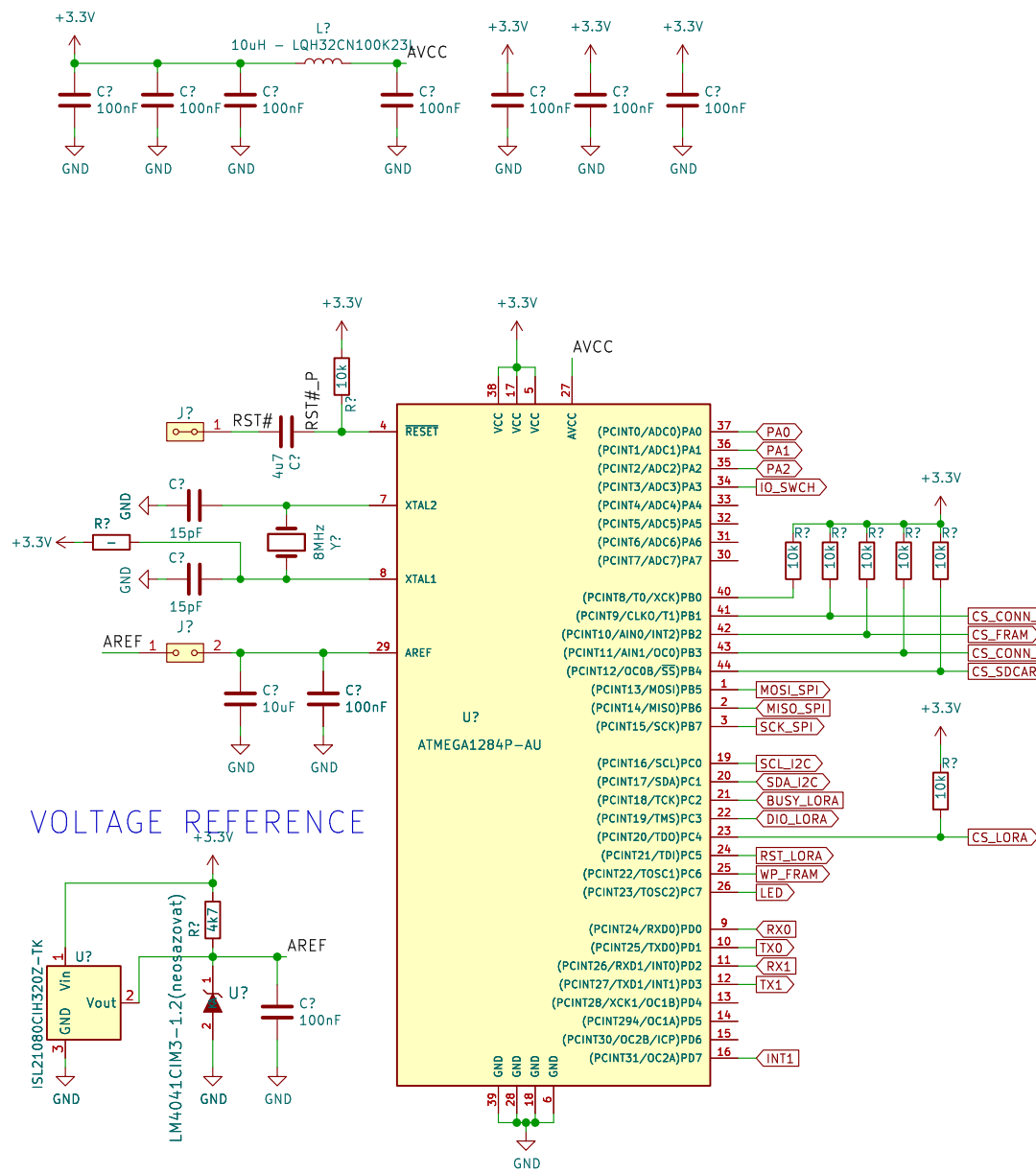
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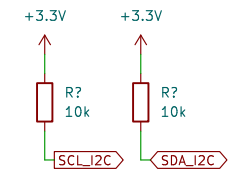
Page: 2/6

Date:

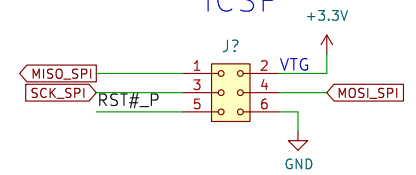




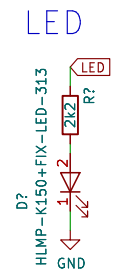
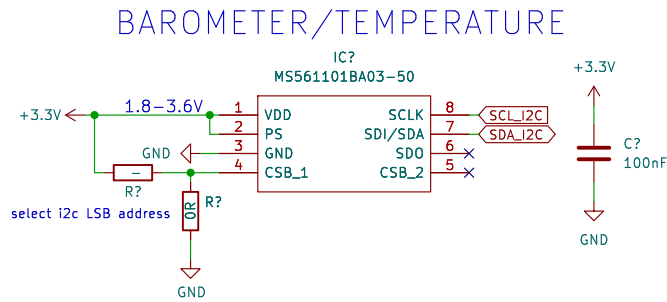
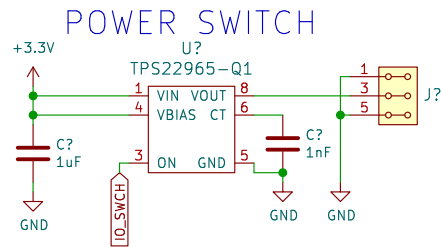
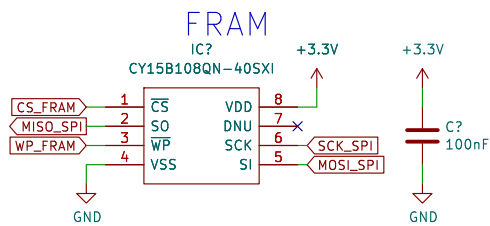
I2C PULL UPS



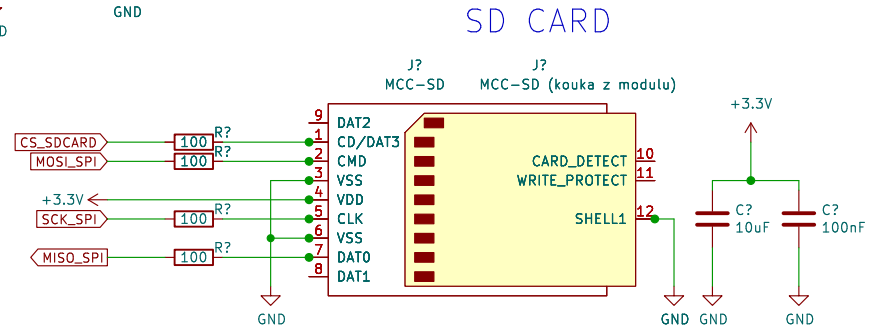
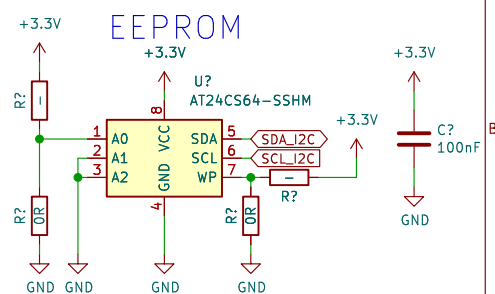
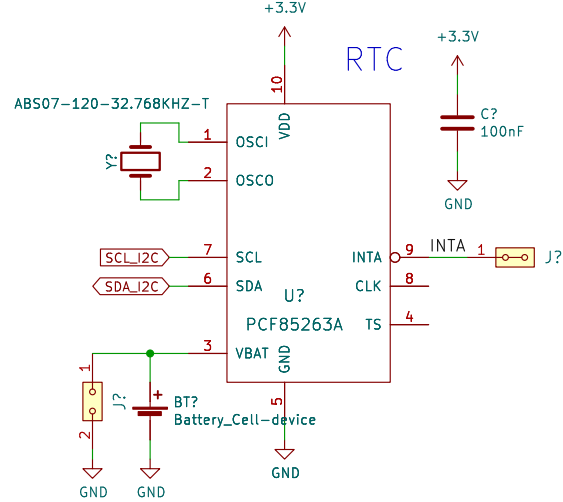
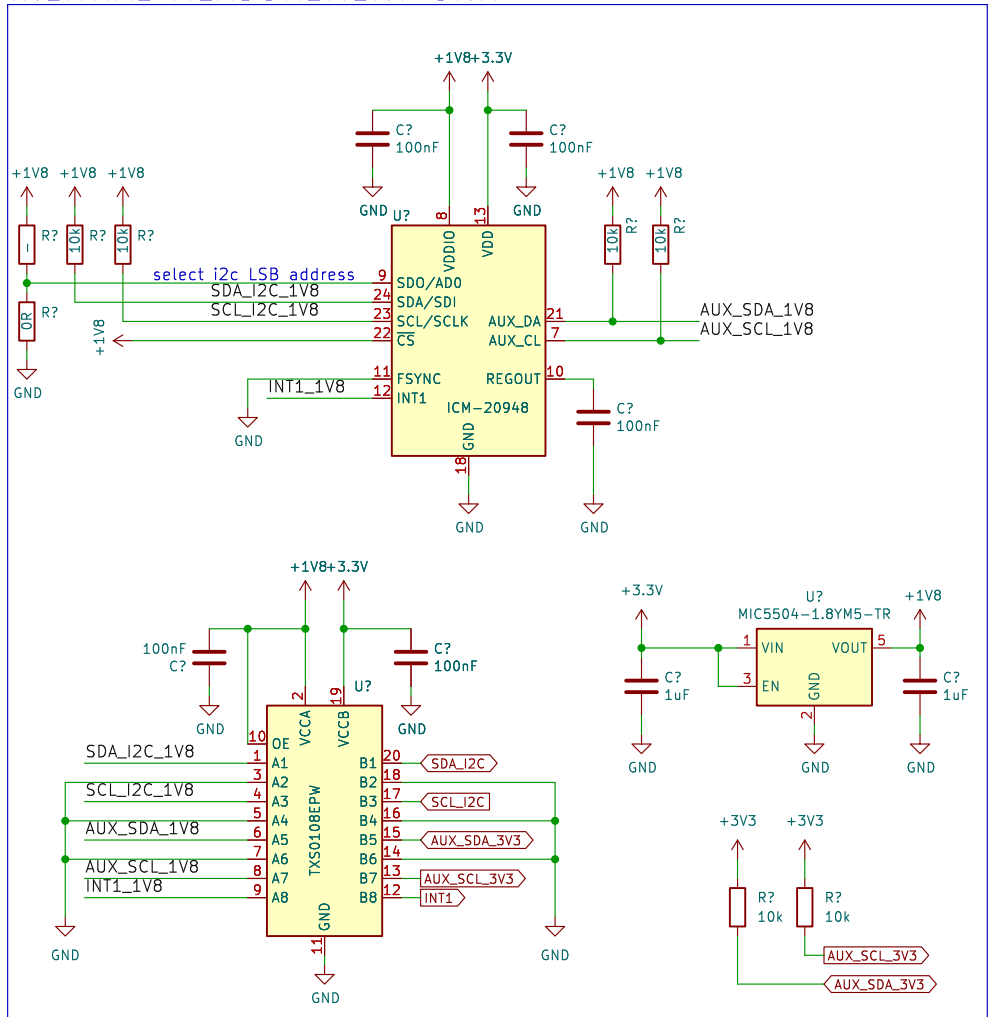
ICSP



- PB0 -
- PB1 - CS_CONN_1
- PB2 - CS_FRAM
- PB3 - CS_CONN_2
- PB4 - CS_CONN_1
- PB5 - MOSI_SPI
- PB6 - MISO_SPI
- PB7 - SCK_SPI

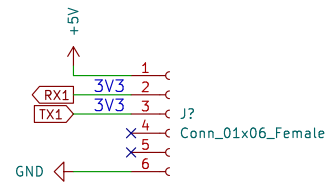


INERTIAL MEASUREMENT UNIT

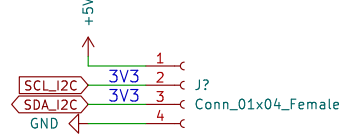


PIXHAWK STANDARD HEADERS

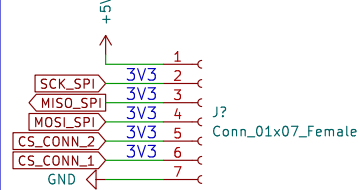
UART CONN.



I2C CONN.

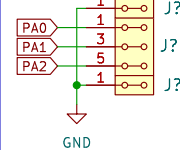


SPI CONN.

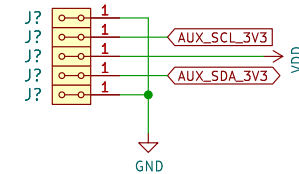


PIN HEADERS

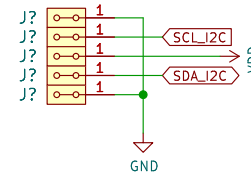
ANALOG CONN



AUX_I2C CONN



I2C CONN



Title:

Author:

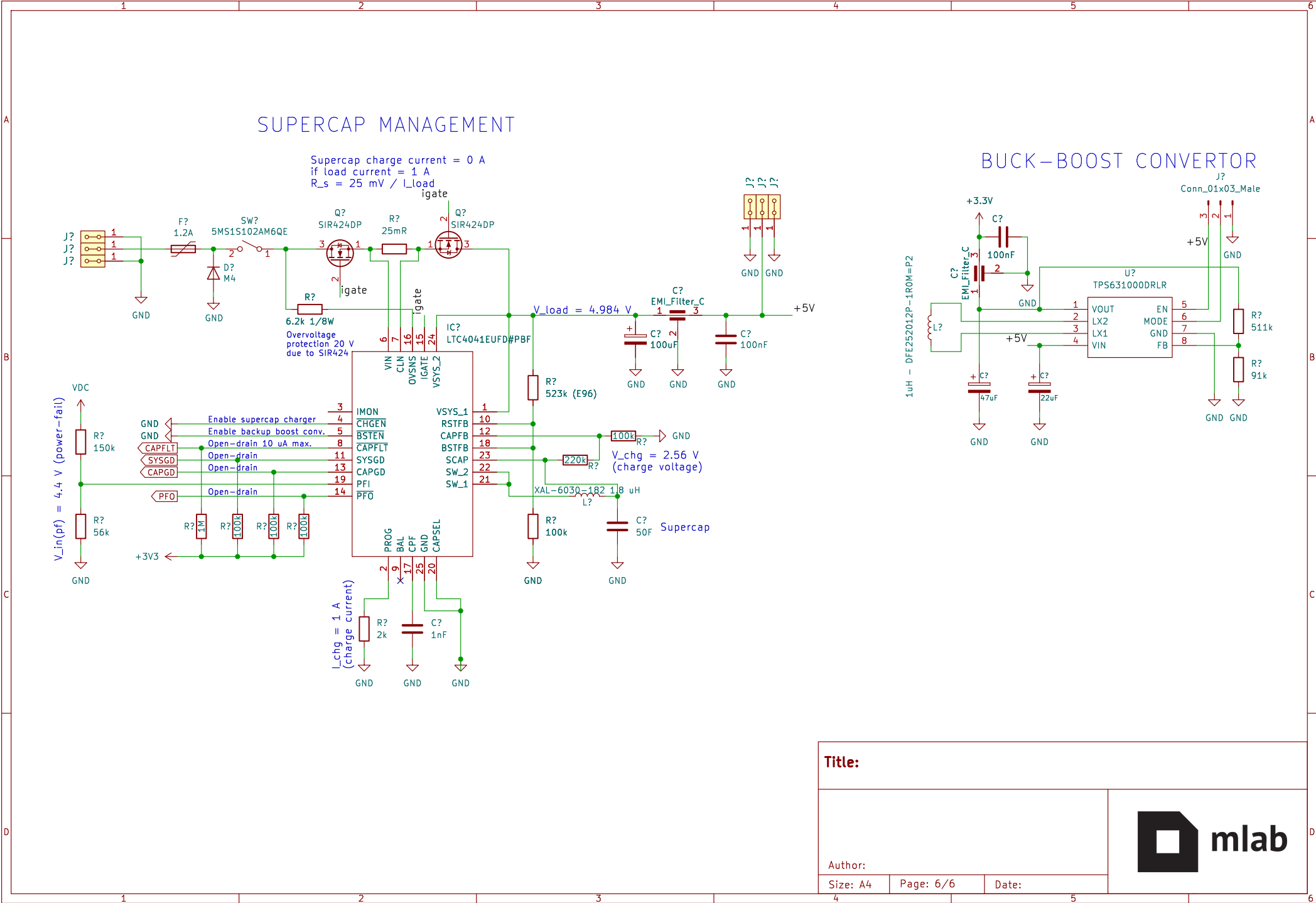
Size: A4

Page: 5/6

Date:



The image contains two circuit diagrams. The first, titled 'SUPERCAP MANAGEMENT', is a detailed schematic for a supercapacitor management system. It features a MOSFET (SIR424DP) driven by a gate driver (LTC4041EUFD#PBF) and a diode (5MS1S102AM6QE). The circuit includes a supercapacitor (50F) and a backup boost converter (TPS63100DRLR). Key parameters include a supercapacitor charge current of 0 A, a load current of 1 A, and a supercapacitor voltage of 2.56 V. The second diagram, titled 'BUCK-BOOST CONVERTOR', shows a simpler circuit using a TPS63100DRLR IC to convert a 5V input to a 3.3V output. It includes an inductor (1uH) and several capacitors (100nF, 47uF, 22uF).

[illegible]

				
Author:				
Size: A4	Page: 6/6	Date:		