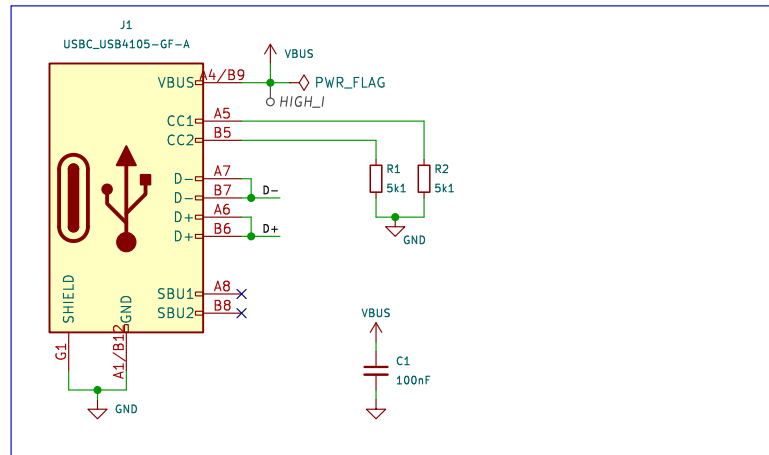
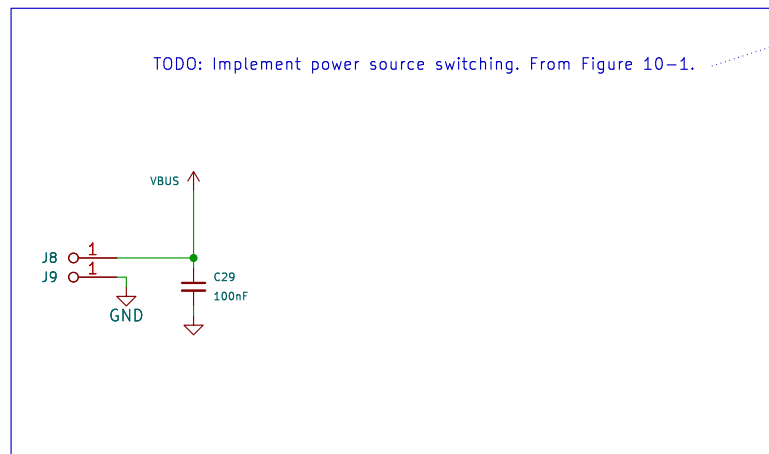


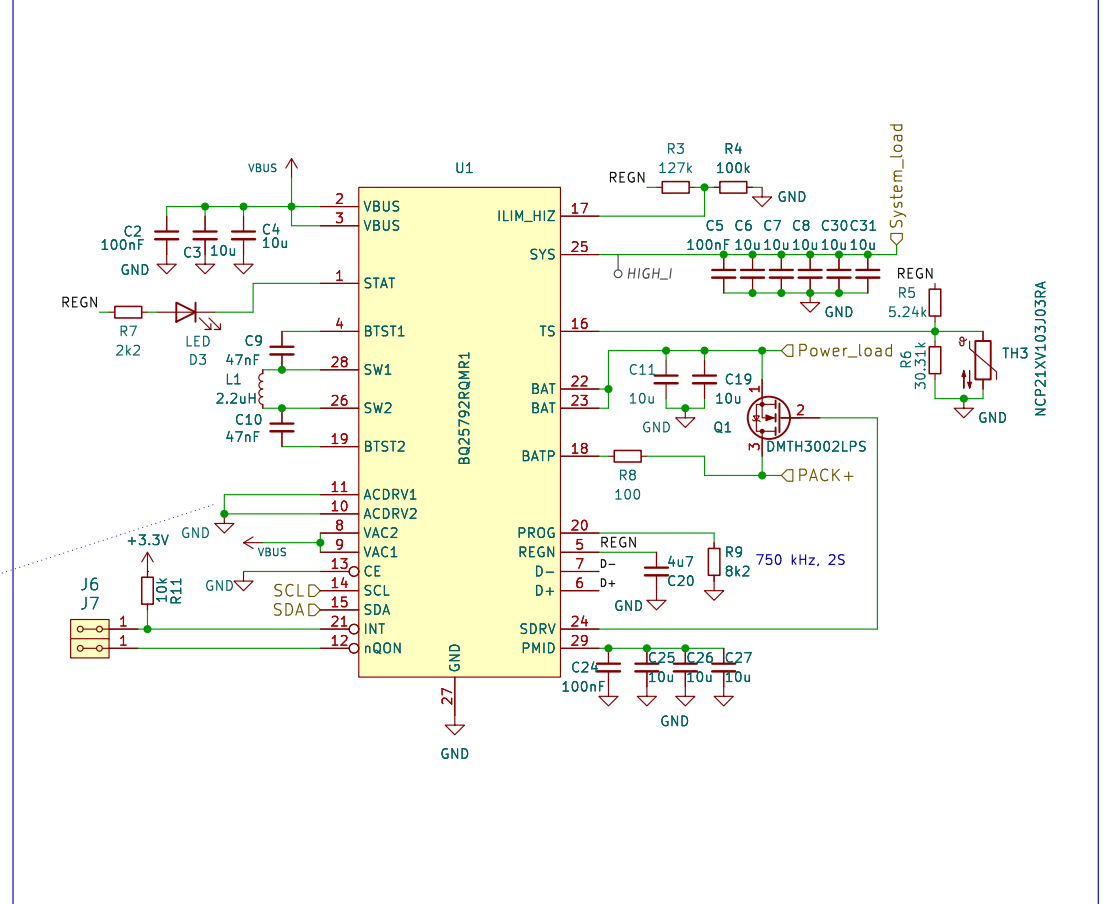
USB-C interface



High-power DC input



Battery Charger



Title:

Author:

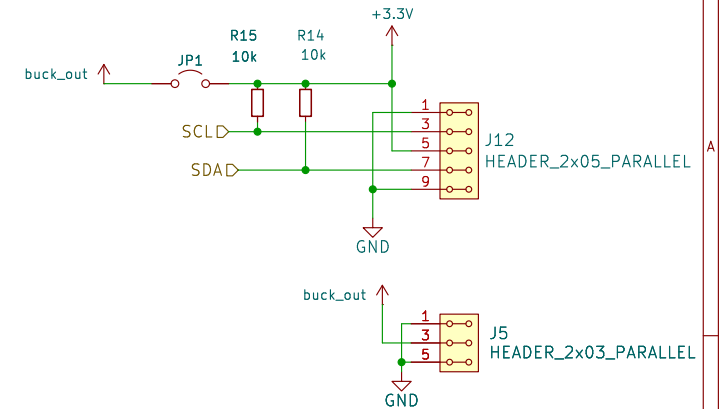
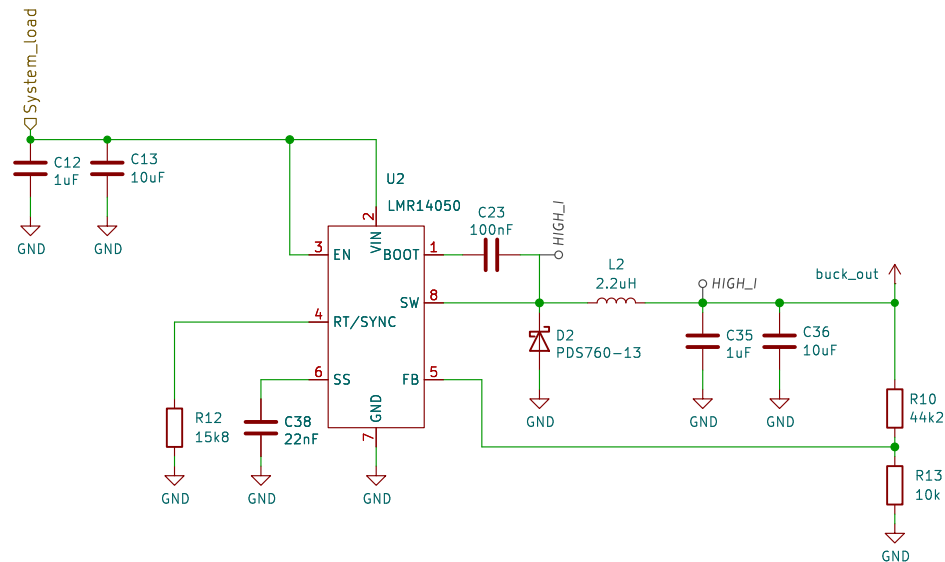
Size: A4

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Date:



- Designed parameters:
- Source voltage: 4 - 8.4V (Two liion cells)
- Output voltage: 5V
- Output current: 2A (IC maximal continuous current is 5A)
- Output inductance: 2.2 uH, K_{ind} : 33%.
- Switching frequency: 1.5 Mhz (15k8)
- OutC (Vripp): 50mV; 2uF



The schematic diagram shows the power distribution network. The **Power_load** is connected to **J2** (pin 1) and **J3** (pin 1), which are both connected to **GND**. The **System_load** is connected to **J4** (pins 7, 5, 3, 1), which are all connected to **GND**.

```

RFBT (feedback top resistor values)
=====
Bottom value fixed 10k Ohm

68 kOhm - 5.1V
44 kOhm - 3.3V

```

Author:

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