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TODO: Implement power source switching. From Figure 10-1.

The diagram shows a circuit for power source switching. A central node is connected to two input terminals, J8 and J9, both labeled with a red '1'. Terminal J9 is connected to ground (GND). Terminal J8 is connected to a node that branches to the VBUS supply and a 100nF capacitor (C29) connected to ground.

[illegible]

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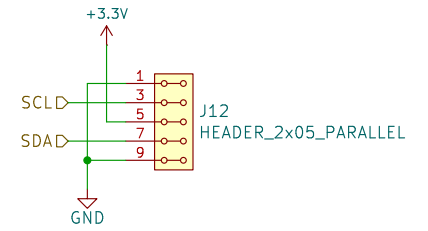
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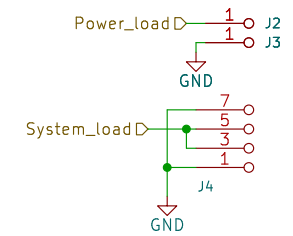
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Switch buck regulator

TODO: Implement +5V/3.3V regulated output.



POWER OUTPUT



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