



TODO: Implement power source switching. From Figure 10-1.

The diagram shows a circuit for power source switching. A central green dot is connected to a green line labeled 'VBUS' with an upward arrow. To the left, two red lines labeled 'J8' and 'J9' with upward arrows connect to the same central dot. The red line for J8 has a '1' above it, and the red line for J9 has a '1' above it. Below the central dot, a capacitor labeled 'C29' with '100nF' below it is connected to ground. The ground is represented by a triangle symbol labeled 'GND'.

[illegible]

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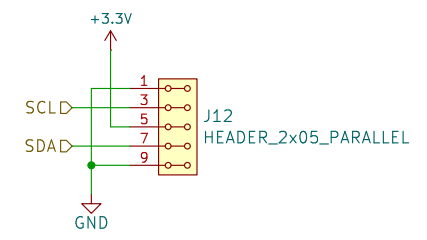
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Switch buck regulator

TODO: Implement +5V/3.3V regulated output.



## POWER OUTPUT

