





TODO: Implement power source switching. From Figure 10-1.

The diagram shows a power source switching circuit. A green line represents the VBUS signal. This line branches to connect to two red circular terminals labeled J8 and J9. Each terminal has a red '1' next to it. The J8 terminal is connected to a green line that goes up to a green arrow labeled VBUS. The J9 terminal is connected to a green line that goes down to a green triangle labeled GND. A capacitor labeled C29 with a value of 100nF is connected between the VBUS line and GND.

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Designed parameters:

- Source voltage: 4 - 8.4V (Two liion cells)
- Output voltage: 5V
- Output current: 2A (IC maximal continuous current is 5A)
- Output inductance: 2.2 uH, K_{ind} : 33%,
- Switching frequency: 1.5 Mhz (15k8)
- OutC (Vripp): 50mV; 2uF

