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TODO: Implement power source switching. From Figure 10-1.

The diagram shows a circuit for power source switching. A green line labeled **VBUS** with an upward arrow connects to a central node. Two red lines, labeled **J8** and **J9**, branch off to the left. Each has a red circle with the number **1** next to it. The **J9** line goes down to a ground symbol labeled **GND**. The central node also connects to a capacitor labeled **C29** with a value of **100nF**, which is connected to another ground symbol.

[illegible]

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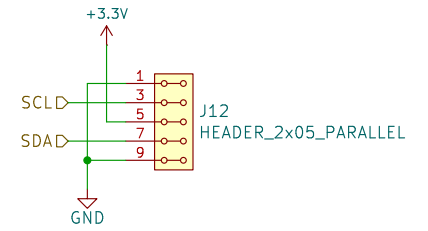
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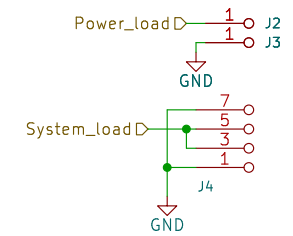
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Switch buck regulator

TODO: Implement +5V/3.3V regulated output.



## POWER OUTPUT



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