

A

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B

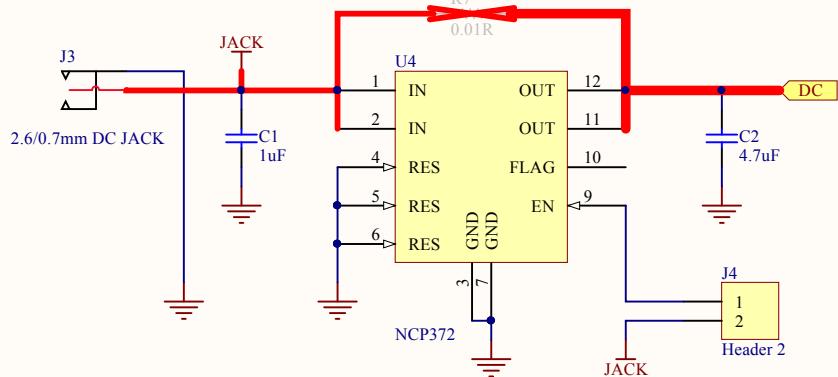
B

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C

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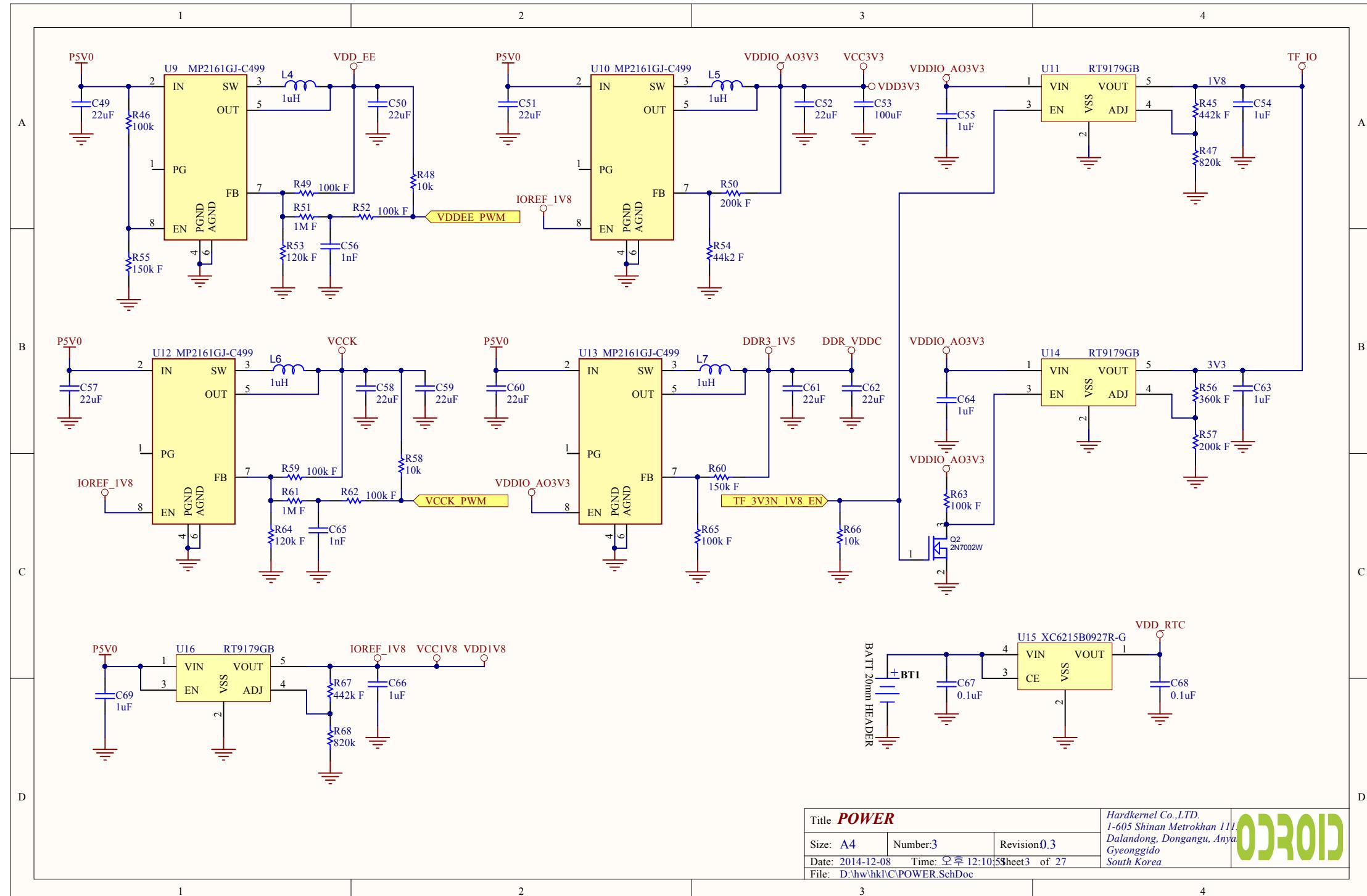
D

Title **DC Jack**

Size: A4	Number: 2	Revision: 0.3
Date: 2014-12-08	Time: 오후 12:10	Sheet 2 of 27
File: D:\hw\hkl\C\CON_DCJACK.SchDoc		

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Title **POWER**

Size: A4 Number: 3 Revision: 0.3
Date: 2014-12-08 Time: 오후 12:10 | \$sheet3 of 27
File: D:\hw\hkl\C\POWER.SchDoc

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Gyeonggi-do
South Korea



A

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B

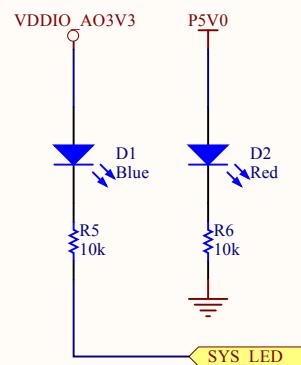
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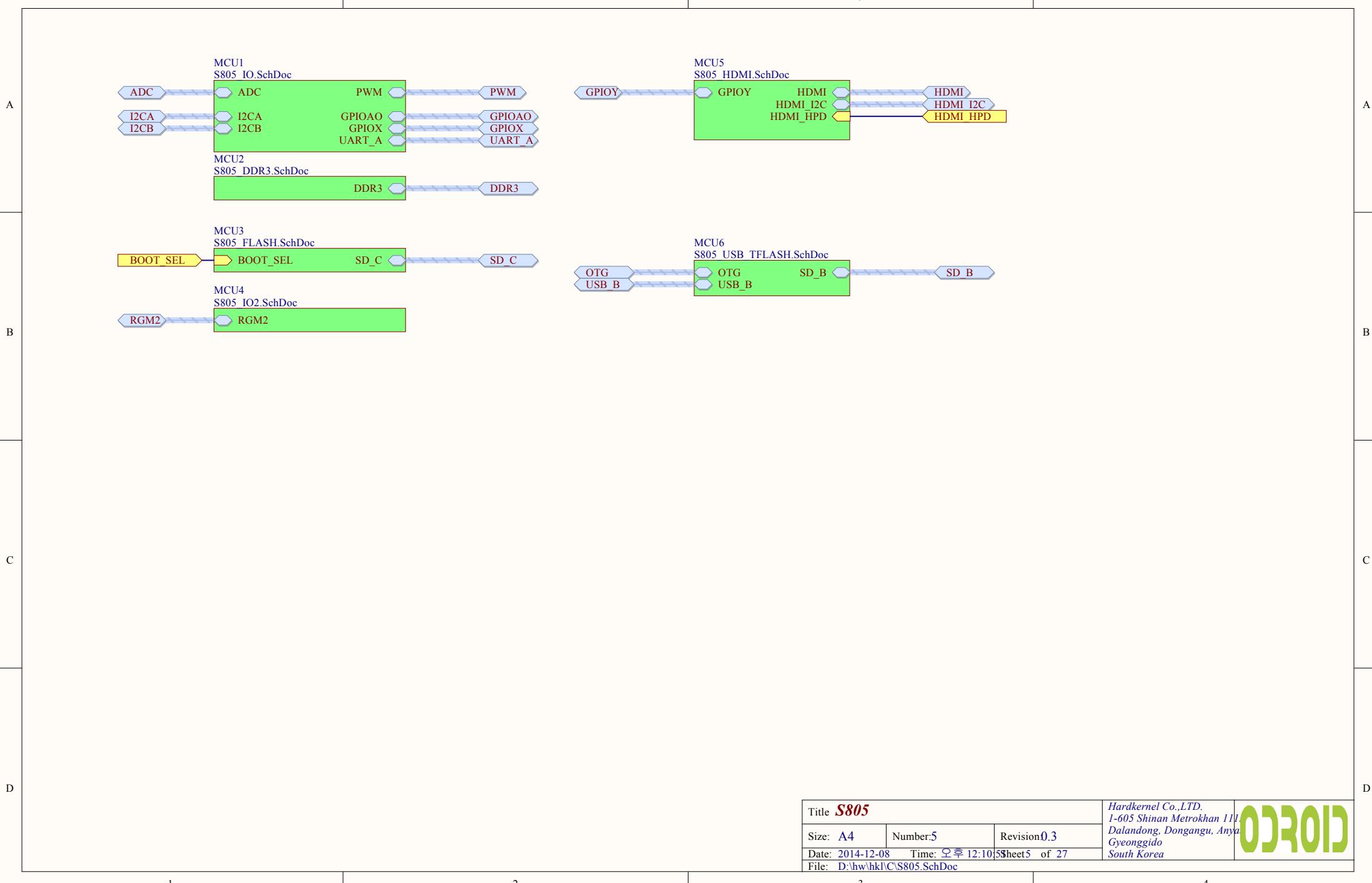
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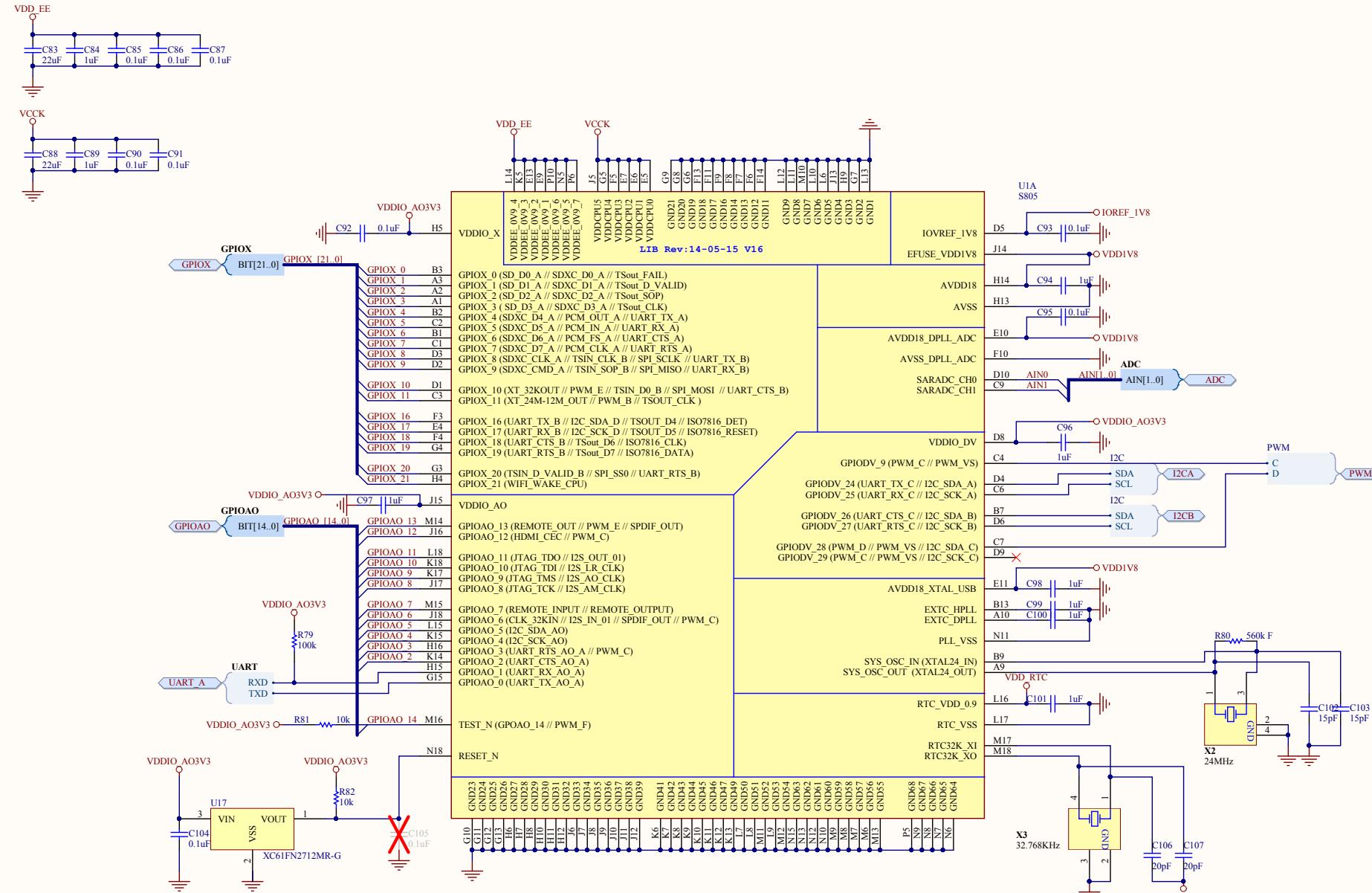
D



Title LEDs			<i>Hardkernel Co.,LTD. 704 Anyang K-center 1591-9 Burim Dongan Gu Gyeonggido South Korea</i>
Size: A4	Number: 4	Revision: 0.3	
Date: 2014-12-08	Time: 오후 12:10	\$sheet 4 of 27	
File: D:\hw\hkl\C\C_LEDs.SchDoc			ODROID

1 2 3 4





A

A

B

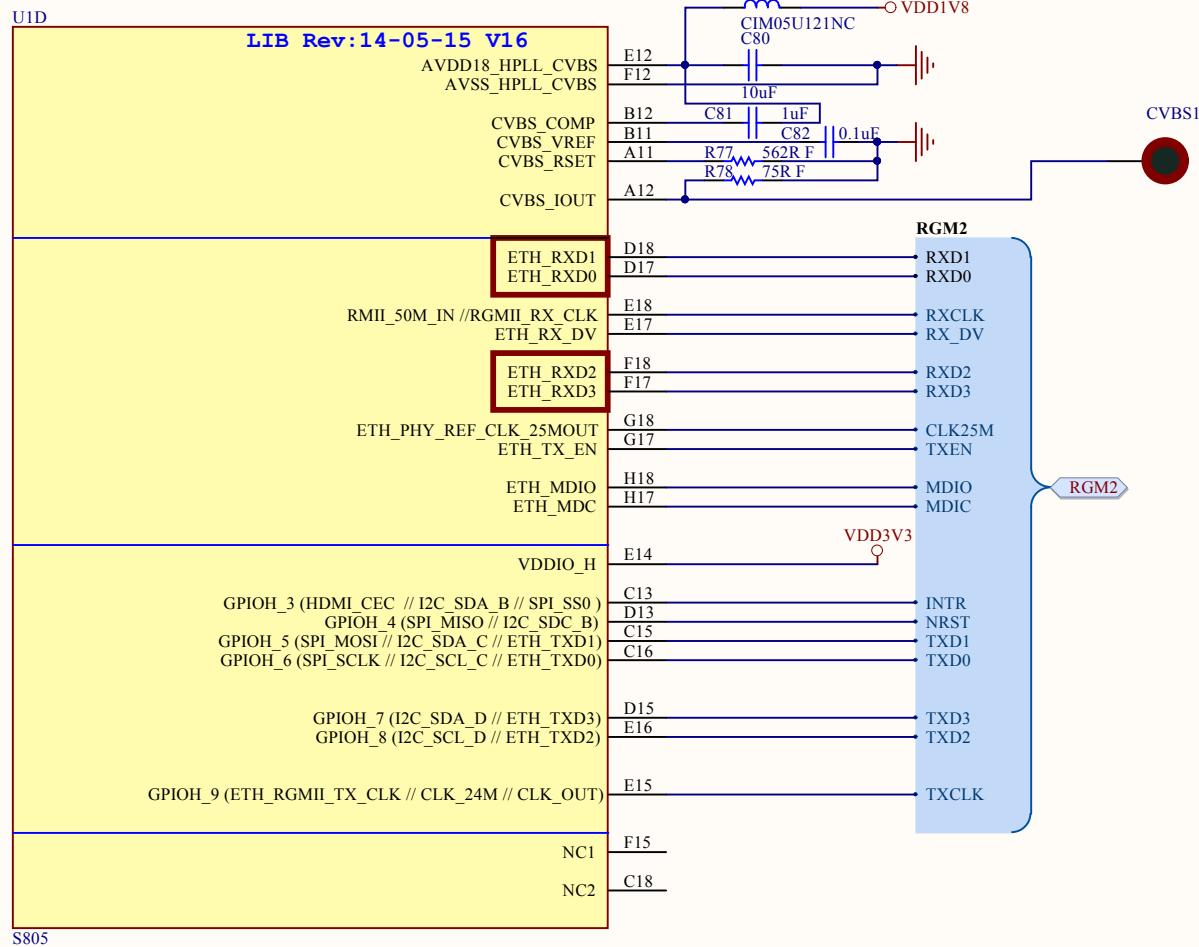
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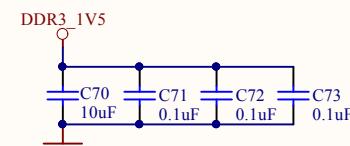
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**Title *S805 IO***

Size: A4	Number: 7	Revision 0.3
Date: 2014-12-08	Time: 오후 12:10	\$sheet7 of 27
File: D:\hw\hkl\C\S805_IO2.SchDoc		

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LIB Rev:14-05-15 V16

DDR3_1V5

LIB Rev:14-05-15 V16

A[15..0] A0 U

The diagram illustrates the connection of a three-bit bus segment BA[2..0] to three individual lines: BA0, BA1, and BA2. These three lines are then connected to three output pins: U10, V13, and U12. The connections are as follows:

- BA[2..0] connects to BA0
- BA[2..0] connects to BA1
- BA[2..0] connects to BA2
- BA0 connects to U10
- BA1 connects to V13
- BA2 connects to U12

DQ0	V18	D0
DQ1	U18	D1
DQ2	R18	D2
DQ3	P17	D3
DQ4	P18	D4
DQ5	R17	D5
DQ6	U17	D6
DQ7	V17	D7
DQM0	N17	DQM0
DQS0_N	T18	DQS_N0
	T17	DQS_P0

DQ8	P16	D8
DQ9	U16	D9
DQ10	P13	D10
DQ11	R13	D11
DQ12	T14	D12
DQ13	R14	D13
DQ14	P15	D14
DQ15	R16	D15
DQM1	V16	DQM1
DQS1_N	R15	DQS N1
DQS1_S	T15	DQS P1

DQ16	R3	D16
DQ17	T6	D17
DQ18	V5	D18
DQ19	R6	D19
DQ20	R7	D20
DQ21	P4	D21
DQ22	T3	D22
DQ23	R4	D23
DQM2	U5	DQM2
DQS2_N	T5	DQS N2
DQS2_S	R5	DQS P2

DQ24	V3	D24
DQ25	U3	D25
DQ26	T1	D26
DQ27	T2	D27
DQ28	R2	D28
DQ29	U1	D29
DQ30	U4	D30
DQ31	V4	D31
DQM3	V1	DQM3
DQS3_N	V2	DQS N3
DOS3	U2	DQS P3

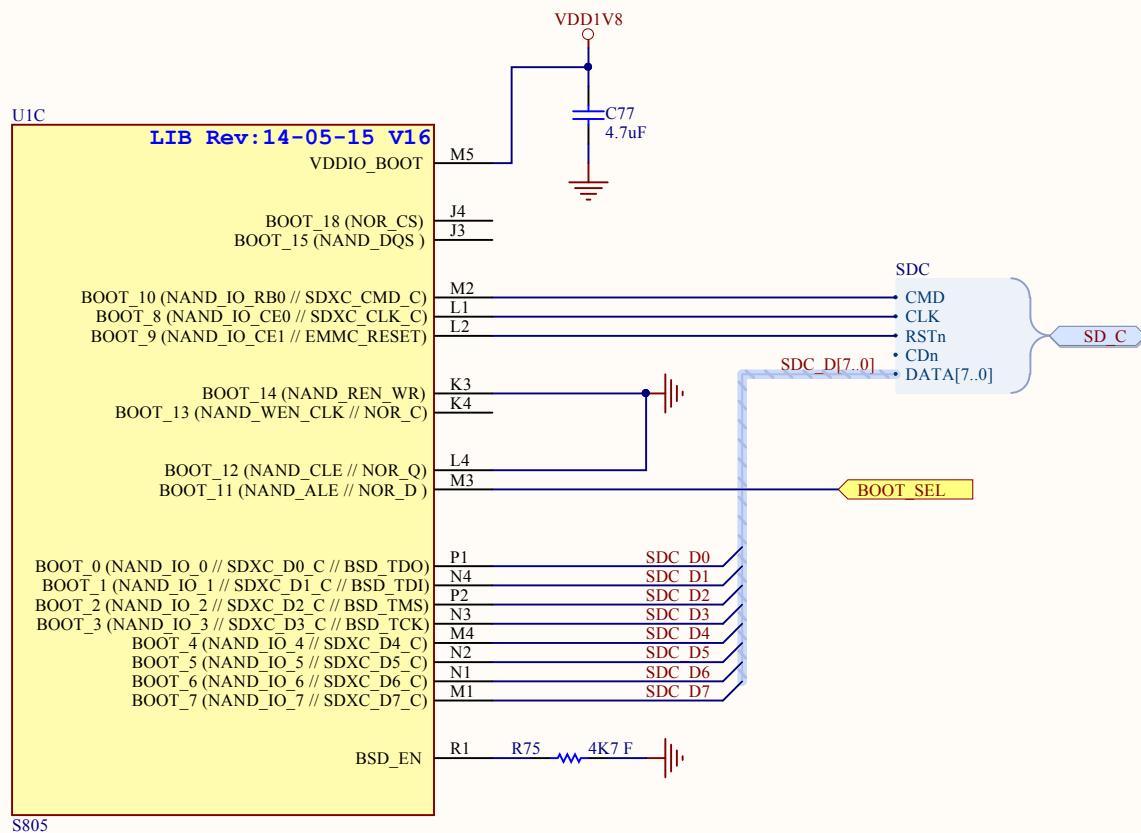
CK_N
CK

DOS N[3..0]  DIFF100
DOS P[3..0]  DIFF100

Title ***S805 DDR3***
Size: A4 Number:8
Date: 2014-12-08 Time: 오 12:10:55
File: D:\hw\hwk1\C\S805 DDR3.SchDoc

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Gyeonggido
South Korea*

Drop



Title S805 IO			R&D PART 704 Anyang K-center 1591-9	
Size: A4	Number:9	Revision 0.3	Burim Dongan Gu Gyeonggi-do South Korea	
Date: 2014-12-08	Time: 오후 12:10	5Sheet 9 of 27		
File: D:\hw\hkl\CS805 FLASH.SchDoc				

A

A

B

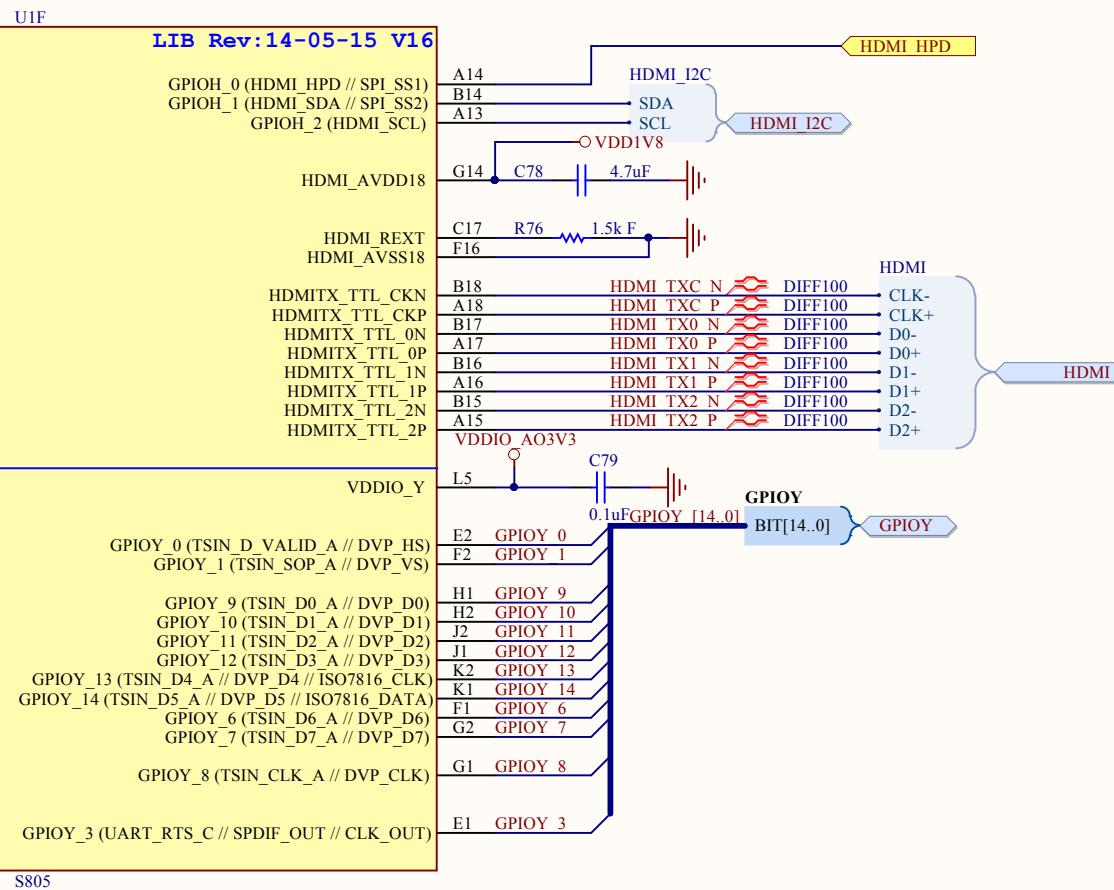
B

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Title **S805 IO**

Size: A4	Number: 10	Revision: 0.3
Date: 2014-12-08	Time: 오후 12:10	Sheet 10 of 27
File: D:\hw\hkl\C\S805_HDMI.SchDoc		

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Gyeonggi-do
South Korea

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A

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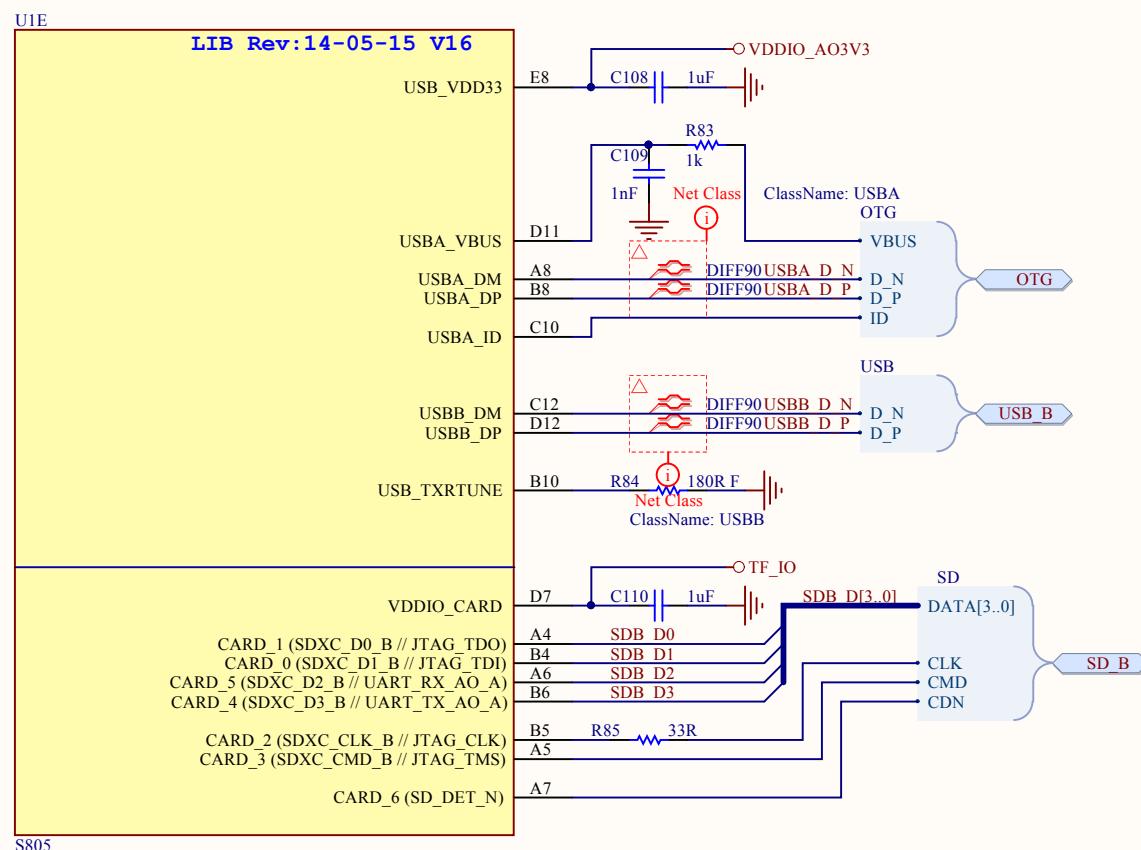
B

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D

**Title *S805 USB2 SDB***

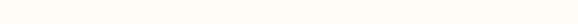
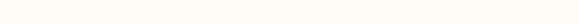
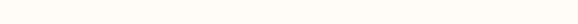
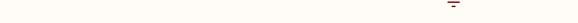
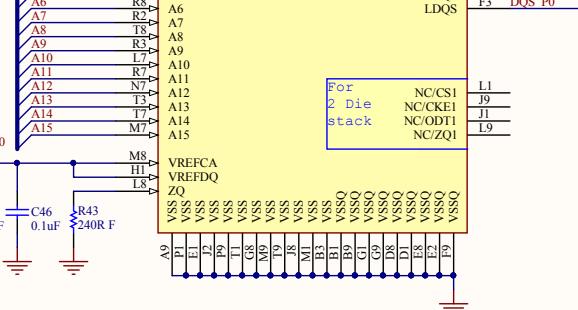
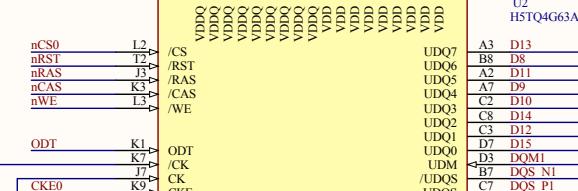
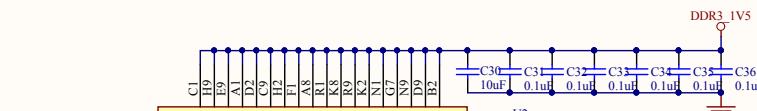
Size: A4	Number: 11	Revision: 0.3
Date: 2014-12-08	Time: 오후 12:10	Sheet 11 of 27
File: D:\hw\hkl\C\S805_USB_TFLASH.SchDoc		

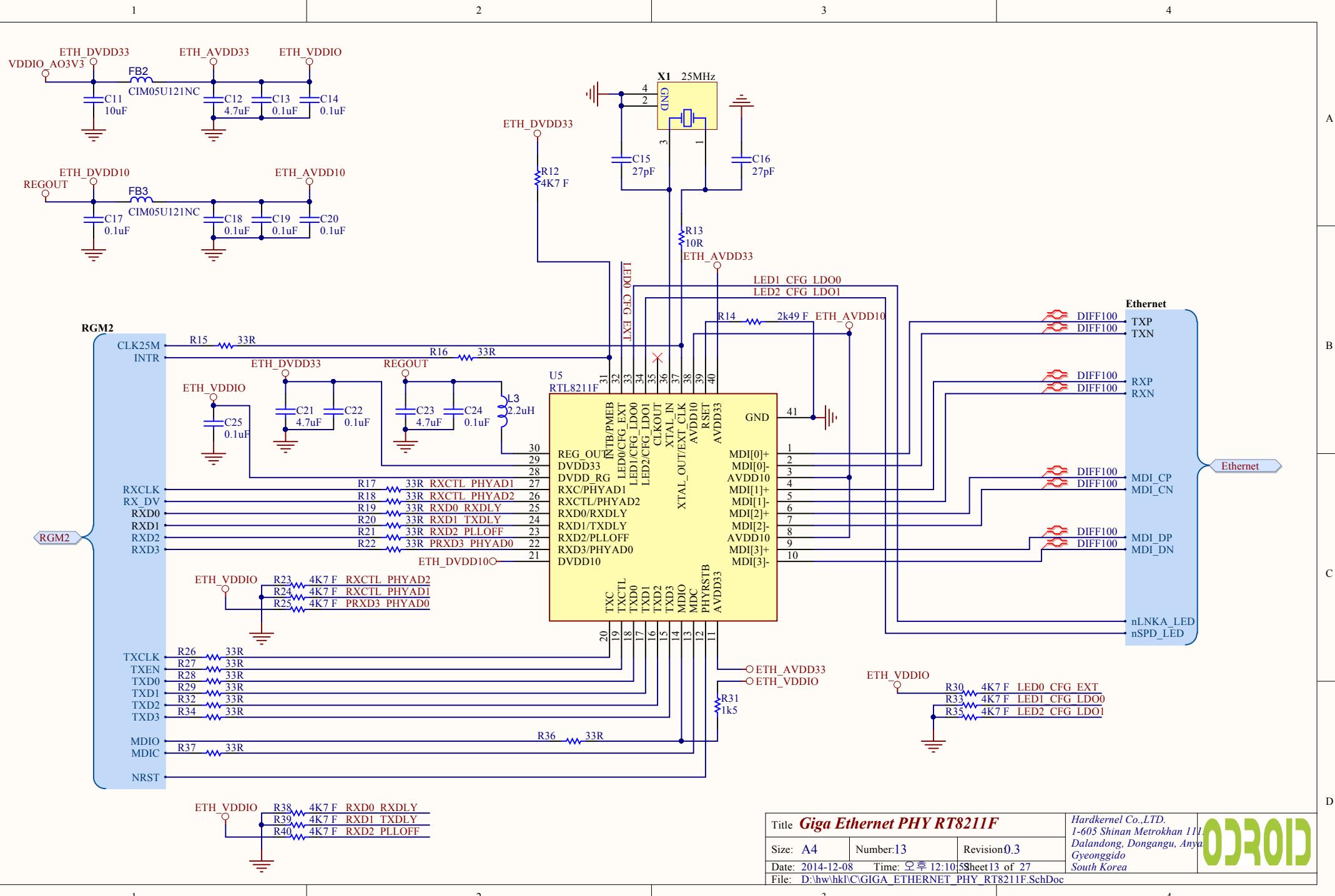
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South Korea

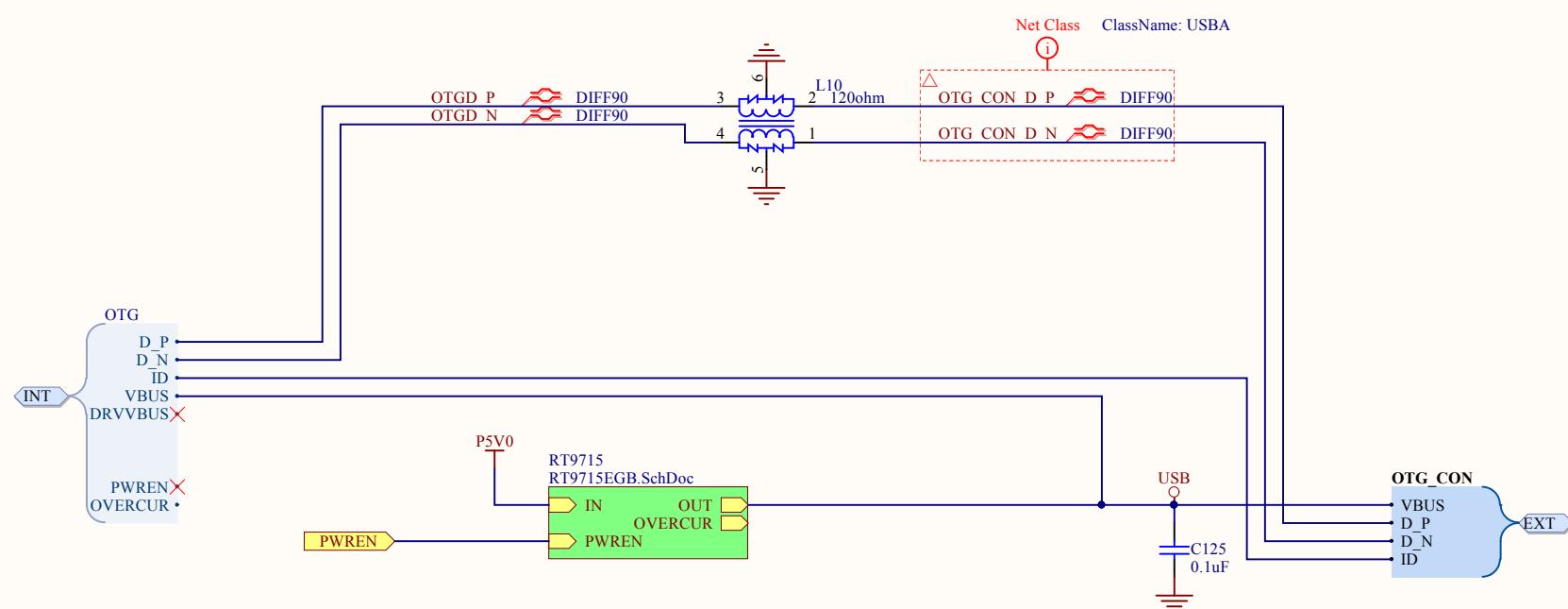
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DDR3 1V5







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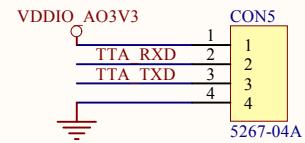
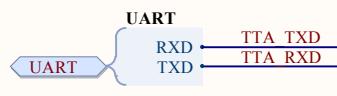
B

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Title *CON_UART*

Size: A4	Number: 15	Revision: 0.3
Date: 2014-12-08	Time: 오후 12:10	Sheet 15 of 27
File: D:\hw\hkl\C\CON_UART.SchDoc		

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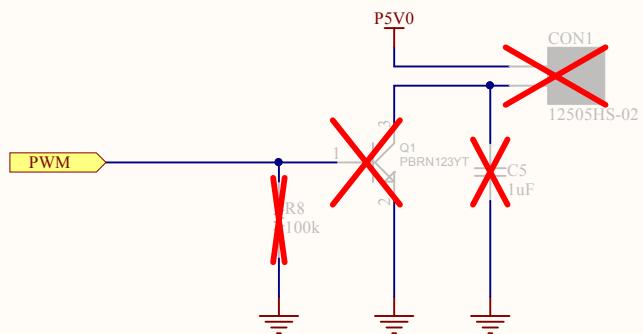
B

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D

Title **FAN**

Size: A4	Number: 16	Revision: 0.3
Date: 2014-12-08	Time: 오후 12:10	Sheet 16 of 27
File: D:\hw\hkl\C\CON_FAN.SchDoc		

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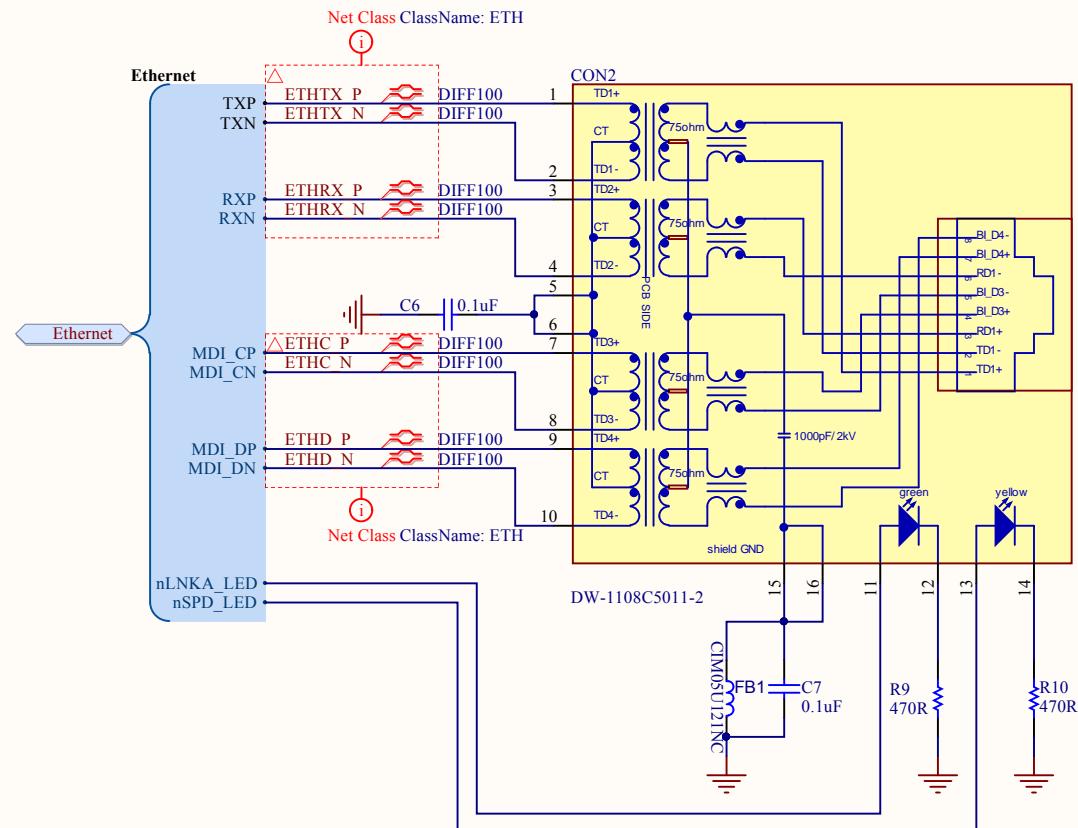
B

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Title **Giga Ethernet**

Size: A4 Number: 17 Revision: 0.3

Date: 2014-12-08 Time: 오후 12:10 | Sheet 17 of 27

File: D:\hw\hkl\C\CON_GIGA_ETH.SchDoc

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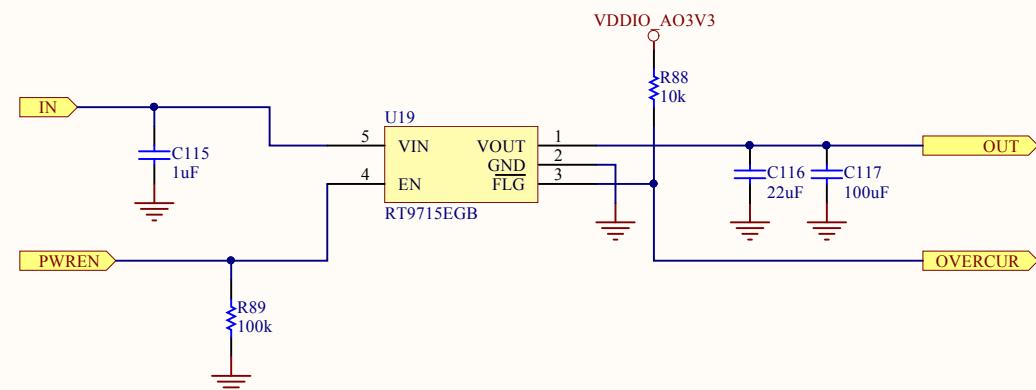
B

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C

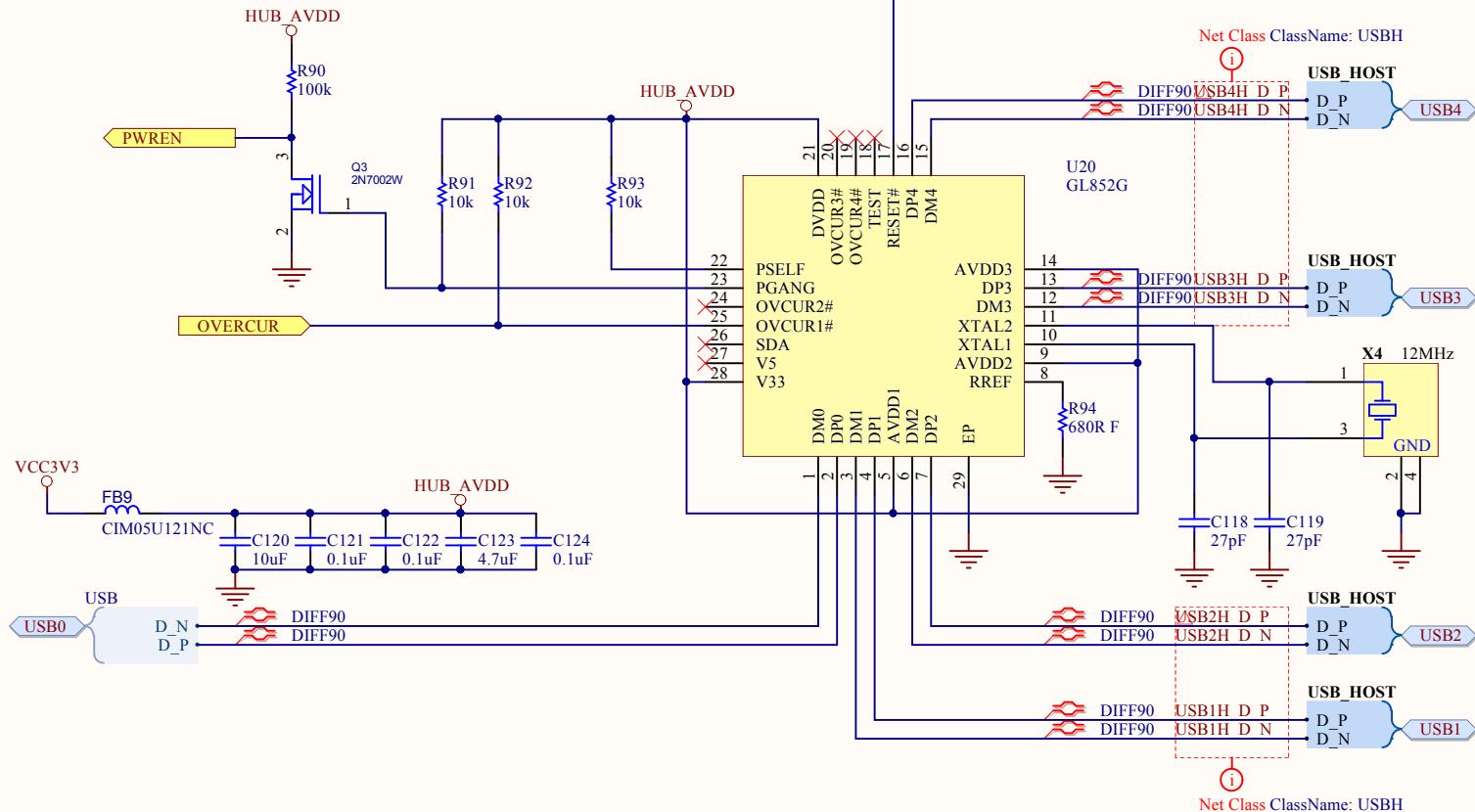
D

D

Title **USB HOST Power Switch**Size: **A4** Number:**18** Revision**0.3**Date: **2014-12-08** Time: **오후 12:10** Sheet**18** of **27**File: **D:\hw\hkl\C\RT9715EGB.SchDoc**

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Title **USB HUB GL852G-0HG**

Size: A4	Number: 19	Revision 0.3
Date: 2014-12-08	Time: 오후 12:10	Sheet 19 of 27
File: D:\hw\hkl\C\USB_HUB_GL852G-0HG.SchDoc		

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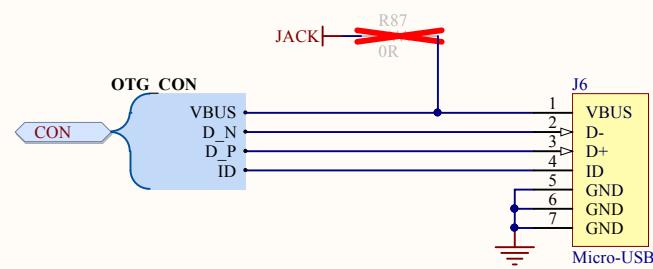
B

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Title **Micro USB Connector**

Size: A4	Number: 20	Revision: 0.3
Date: 2014-12-08	Time: 오후 12:10	Sheet 20 of 27
File: D:\hw\hkl\C\CON_MICROUSB.SchDoc		Hardkernel Co.,LTD. 704 Anyang K-center 1591-9 Burim, Dongangu, Anyang Kyeonggi-do South Korea

ODROID

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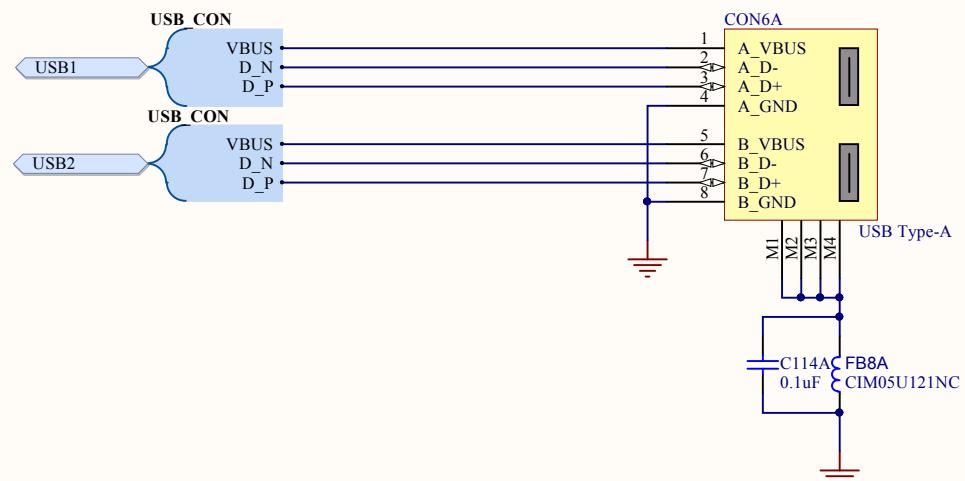
B

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Title **USB 2.0 Type A Connector**

Size: A4 Number:21 Revision:0.3

Date: 2014-12-08 Time: 오후 12:10 Sheet 21 of 27

File: D:\hw\hkl\C\CON_USB_RA.SchDoc

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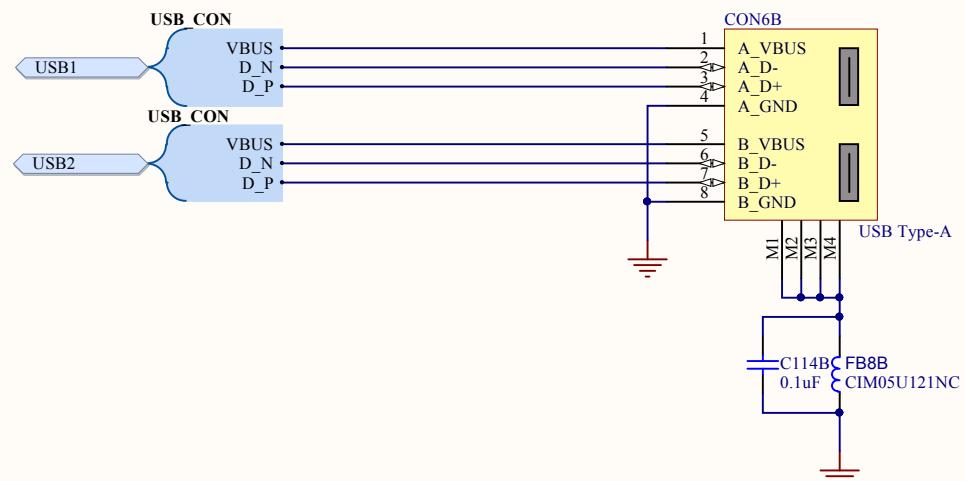
B

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Title **USB 2.0 Type A Connector**

Size: **A4** Number:**21** Revision:**0.3**

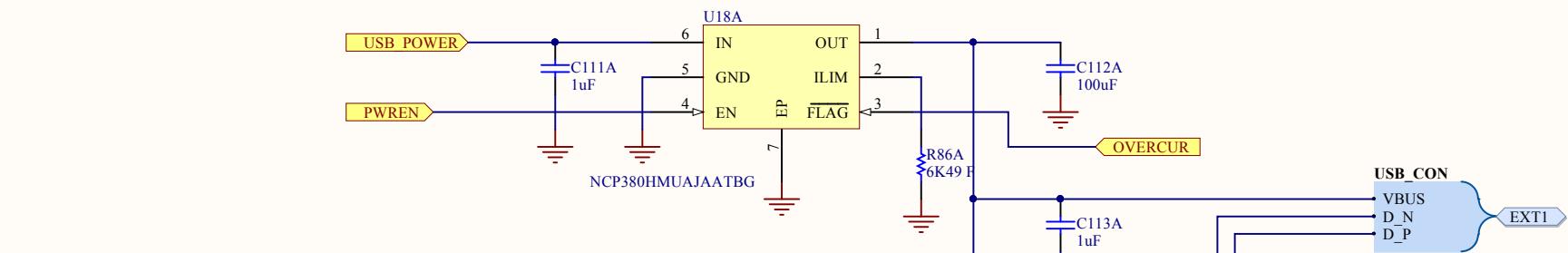
Date: **2014-12-08** Time: **오후 12:10** Sheet**21** of **27**

File: **D:\hw\hkl\C\CON_USB_RA.SchDoc**

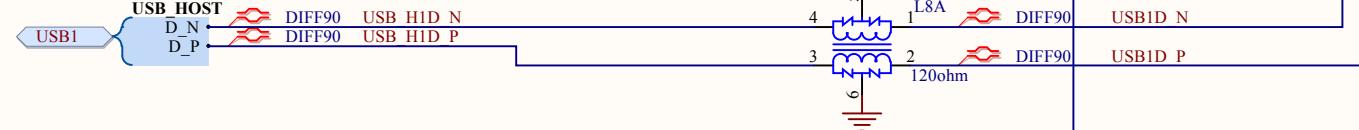
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South Korea

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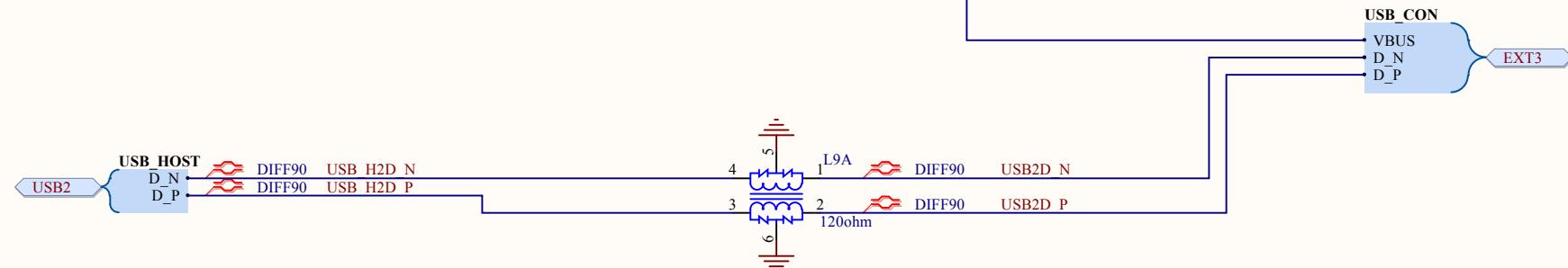
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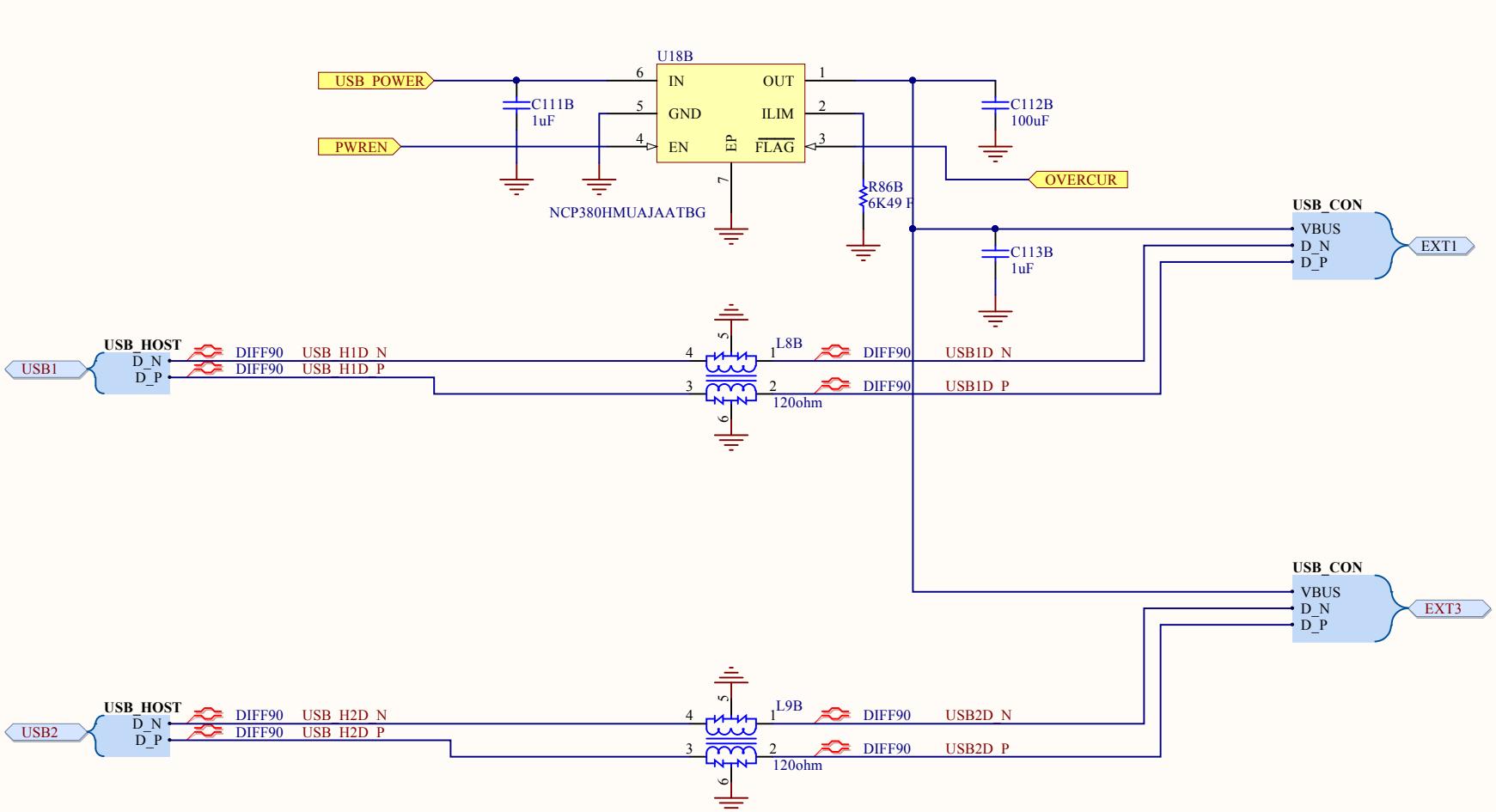
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Title **USB HOST PWR SW**

Size: A4	Number: 22	Revision: 0.3
Date: 2014-12-08	Time: 오후 12:10	Sheet 22 of 27
File: D:\hw\hkl\C\USB_HOST_PWR_SW.SchDoc		

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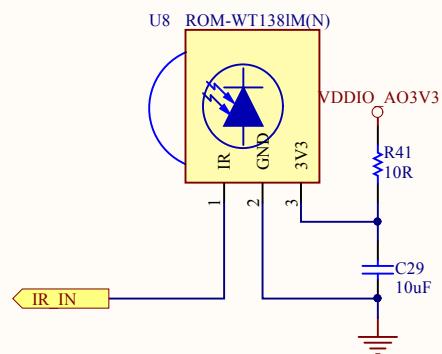
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Title **USB HOST PWR SW**

Size: A4	Number: 22	Revision: 0.3
Date: 2014-12-08	Time: 오후 12:10	Sheet 22 of 27
File: D:\hw\hkl\C\USB_HOST_PWR_SW.SchDoc		Hardkernel Co., LTD.

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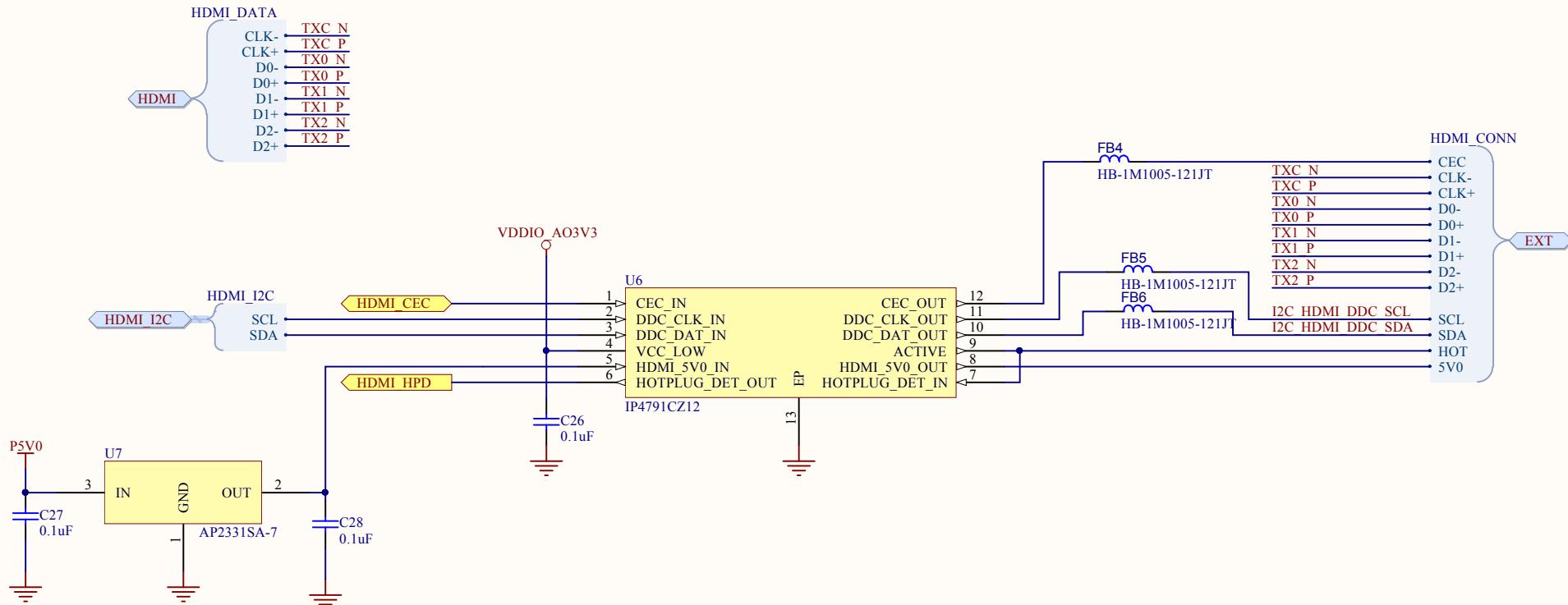


Title ***Remote Control Receiver***

Size: A4	Number: 23	Revision: 0.3
Date: 2014-12-08	Time: 오후 12:10	Sheet 23 of 27
File: D:\hw\hkl\C\IR.SchDoc		

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Title **HDMI 2C**

Size: A4	Number: 24	Revision: 0.3
Date: 2014-12-08	Time: 오후 12:10	Sheet 24 of 27
File: D:\hw\hkl\C\HDMI_I2C.SchDoc		Kyeongido South Korea

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South Korea

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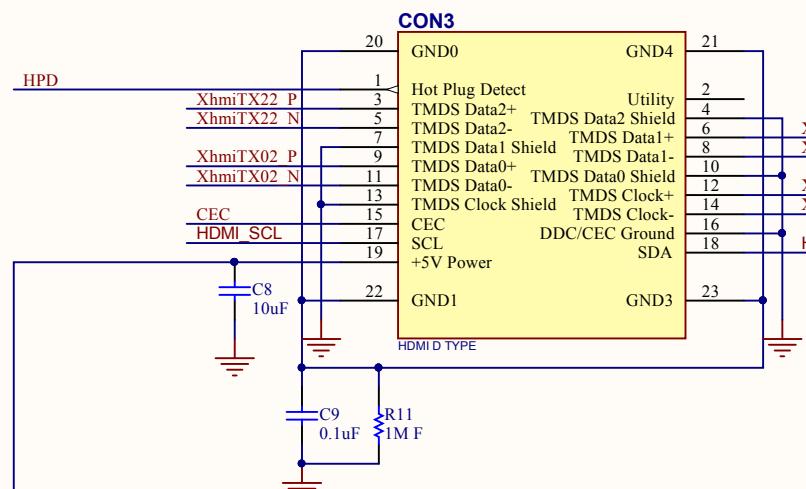
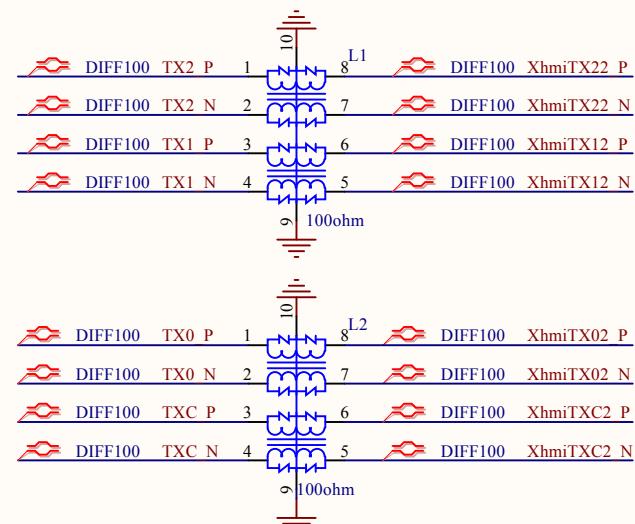
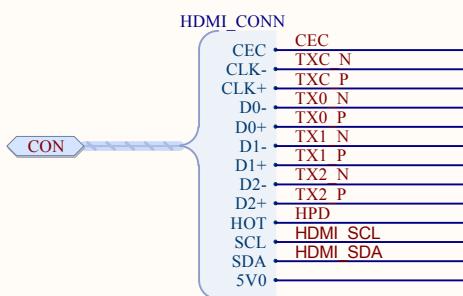
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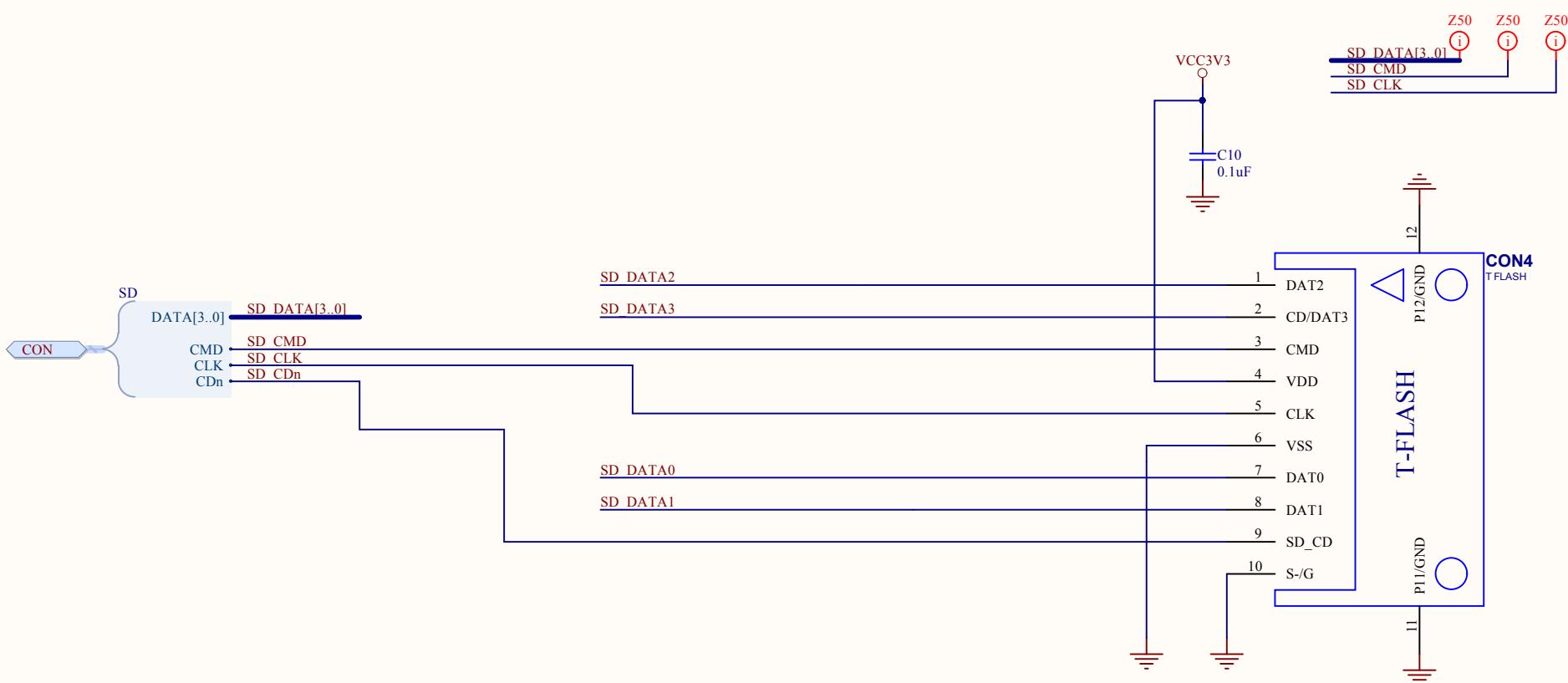
D

D

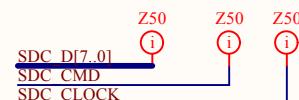
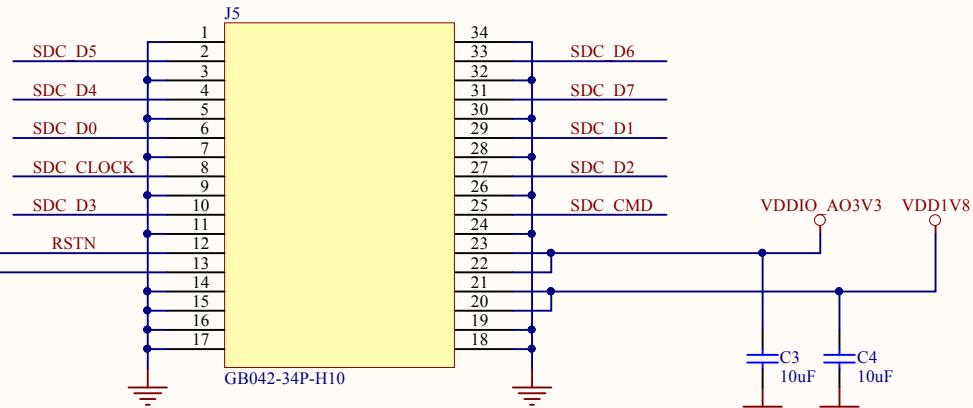
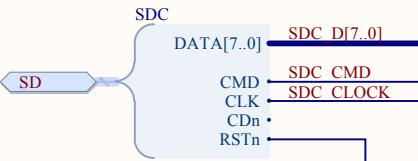
Title **Micro HDMI Connector**Size: **A4** Number:**25** Revision:**0.3**Date: **2014-12-08** Time: **오후 12:10** Sheet**25** of **27**File: **D:\hw\hkl\C\CON_HDMI.SchDoc**

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South Korea





Title Micro SD Socket			Hardkernel Co.,LTD. 704 Anyang K-center 1591-9 Burim, Dongangu, Anyang Gyeonggido South Korea
Size: A4	Number: 26	Revision: 0.3	
Date: 2014-12-08	Time: 오후 12:10	Sheet: 26 of 27	
File: D:\hw\hkl\C\CON_MICROSD_HD08-AL00335.SchDoc			ODROID



Title **CON_EMMC**

Size: A4	Number: 27	Revision: 0.3
Date: 2014-12-08	Time: 오후 12:10	Sheet 27 of 27
File: D:\hw\hkl\C\CON_EMMC.SchDoc		

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