

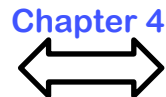
Heart

Pacemaker

Verification

Chapter 2

Non-
deterministic



UPPAAL
Model

Chapter 5

Interpolation

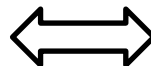
Chapter 6

**UPP2SF
Model Translation**

Simulation

Chapter 3

Deterministic



Stateflow
Chart

HDL Coder

Chapter 7

Simulink Coder

Implementation

FPGA
implementation



C Code
implementation