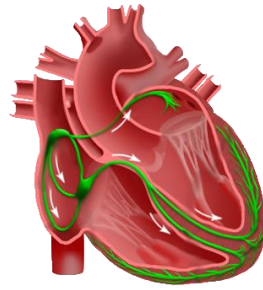


Heart



Pacemaker



Verification

Non-deterministic

UPPAAL Model

Interpolation

Deterministic

Stateflow Chart

HDL Coder

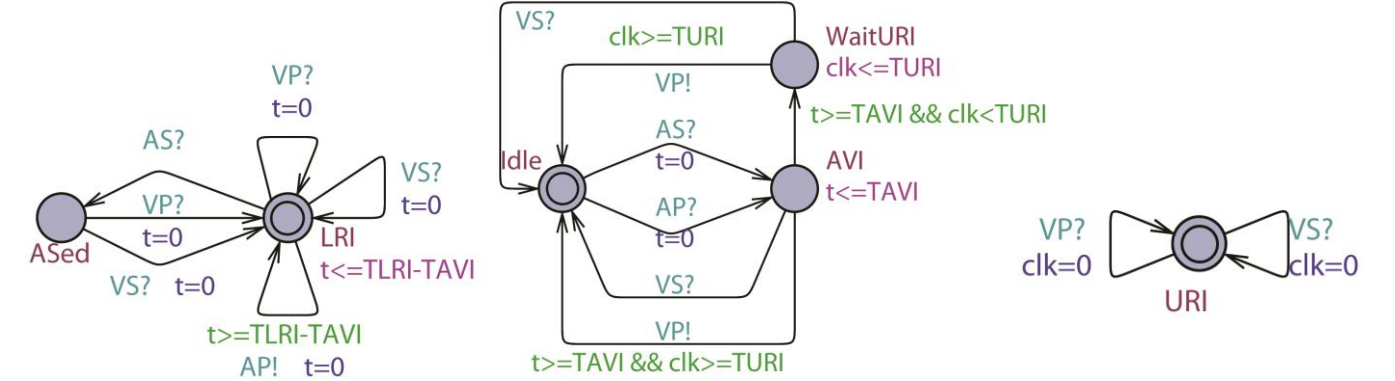
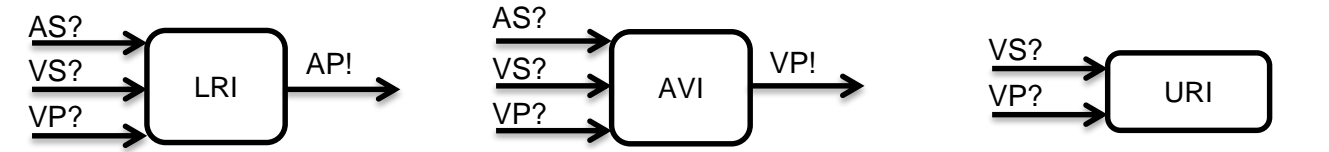
FPGA implementation

Implementation

UPP2SF Model Translation

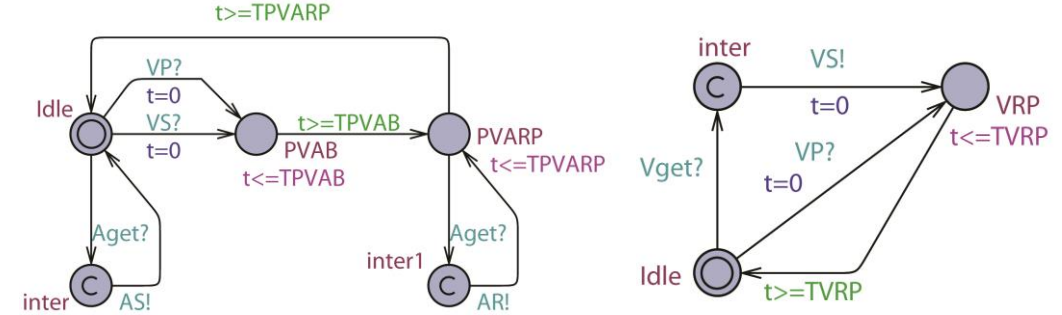
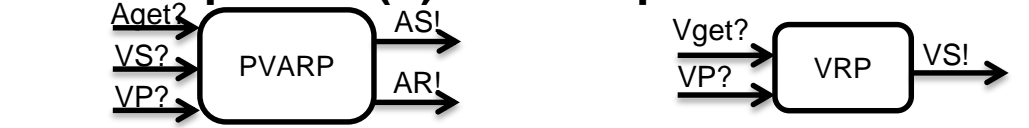
Simulink Coder

C Code implementation



(a) LRI component (b) AVI component

(c) URI component



(d) PVARP component (e) VRP component