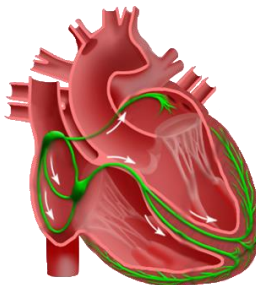


Heart



Pacemaker



Verification

Non-
deterministic



UPPAAL
Model

Interpolation



UPP2SF
Model Translation



Simulation

Deterministic



Stateflow
Chart

HDL Coder



Simulink Coder



Implementation

FPGA
implementation



C Code
implementation