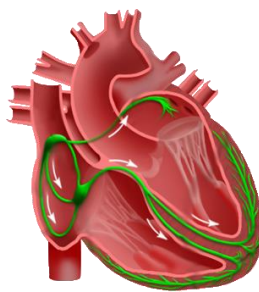


Heart



Pacemaker



Verification

Non-deterministic

UPPAAL Model

Interpolation

Deterministic

Stateflow Chart

UPP2SF Model Translation

HDL Coder

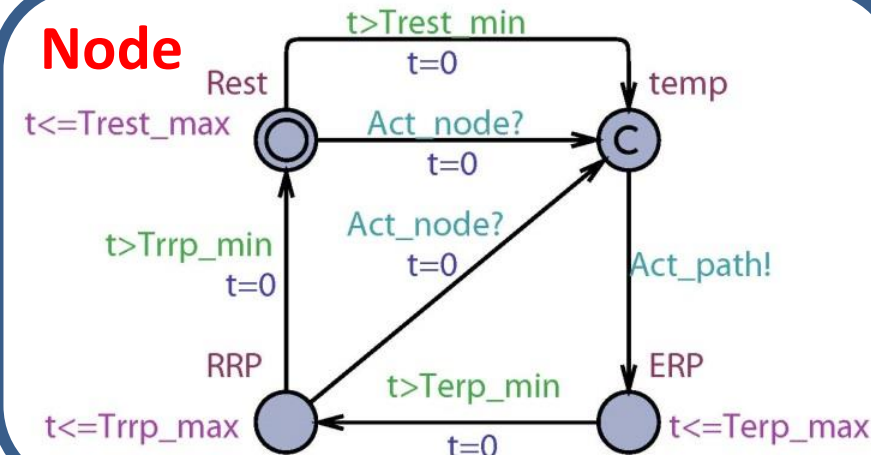
FPGA implementation

Simulink Coder

C Code implementation

Implementation

Node



Path

