

# Mariano L. Acosta

## Electronics Engineer & Programmer

Engineer with 8 years of programming experience. Worked in diverse industries, including Embedded Software design, Power Electronics, Digital Electronics and Printed Circuit Board manufacturing. Published several papers in IEEE about Algorithms, Machine Learning and Signal Processing. Fulbright Alumni. Currently making a career change into Software Engineering through online courses and personal projects.

### Education

2012 - 2018	<b>Degree in Electronics Engineering</b> <i>University of Mar del Plata (UNMDP)</i> <ul style="list-style-type: none"><li>Coursework: Control Systems, Electronics Instrumentation, Signal Processing, Circuit Design, Communications &amp; Data Networks</li><li>Additional courses: Computational Intelligence, Applied Robotics, Operations Research, FPGA Design, Microcontrollers.</li></ul>
2017 - 2017	<b>Fulbright Scholarship - Exchange Student</b> <i>Virginia Tech - United States</i> <ul style="list-style-type: none"><li>Intensive, 200hs English course at VTLCI</li><li>Attended to university courses in Electrical and Computer Engineering</li></ul>

### Experience

2020 - Present	<b>Software Engineer</b> <i>Zondax - Zug, Switzerland</i> <ul style="list-style-type: none"><li>Development for Blockchain and Cryptography applications</li></ul>
2020 - Present	<b>React JS Tutor</b> <i>Coderhouse - Buenos Aires, Argentina</i> <ul style="list-style-type: none"><li>Taught and assisted students with HTML, CSS, Javascript, APIs, React.js, Node.js, CLI, NPM, Firebase, GIT version control and project management.</li><li>Taught how to build an e-commerce app from scratch. Graded assignments.</li></ul>
2018 - 2019	<b>Power Electronics Engineer</b> <i>Lyrtron S.A. - Mar del Plata, Argentina</i> <ul style="list-style-type: none"><li>Developed Stress and Worst Case Circuit Analysis (SWCCA) in Power Inverters and Power Optimizers.</li><li>Used gallium nitride (eGaN) and Silicon Carbide (SiC) transistors.</li><li>Worked for clients such as Advanced Energy (USA) and AMPT (USA).</li></ul>
2016 - 2018	<b>Embedded Systems Engineer</b> <i>ICyTE - Buenos Aires, Argentina</i> <ul style="list-style-type: none"><li>Developed a Compressed Sensing System in a Xilinx ZYNQ 7000 SoC.</li><li>Travelled to Trieste, Italy, for an intensive workshop in SoC design (ICTP)</li><li>Coded in C/C++, VHDL and Xilinx HLS.</li><li>Created a Graphic Interface in Python with Network Capabilities</li><li>Designed and Built a Delta/Sigma Digital-to-Analog converter.</li></ul>
2016 - 2016	<b>Avionics Engineer (Internship)</b> <i>INVAP S.E - Bariloche, Argentina</i> <ul style="list-style-type: none"><li>Developed a Printed Circuit Board for an aerospace system</li><li>Used Microsemi FPGA Pro ASIC3 (CCGA).</li><li>Studied the CCGA technology, footprint, and solder requirements.</li><li>Designed the circuit layout in Altium Designer.</li></ul>

### Published Papers

2016 - 2019	<ul style="list-style-type: none"><li>Hybrid sorting algorithm implemented with HLS (ISSN: 1548-0992)</li><li>Compressed Sensing System for 16x sub-Nyquist DSP (ISBN: 978-1-7281-1404-0)</li><li>Signal Processing System with Xampling (ISBN: 978-1-5090-6286-7)</li><li>Neural Network for Robotic Arm Control in Fixed Point (ISBN: 978-987-45523-8-9)</li><li>FM Audio Synthesizer with Genetic Algorithms (ISSN: 1850-2776)</li></ul>
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### Personal Info

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<b>Personal Website</b> mlacosta.github.io
<b>Github</b> github.com/mlacosta
<b>Linkedin</b> linkedin.com/in/mlacosta
<b>Languages</b> Spanish (Native)   English (Proficient)
<b>Birthdate</b> 13 August 1993 (27 years old)

### Skills

<b>Programming</b> C/C++, C#, Java, Javascript, GIT Version Control, Python, Matlab, Bash Shell
<b>Fullstack Web Development</b> APIs, HTML, CSS, Express.js, Git, Javascript, MongoDB, Mocha & Chai, Netlify, Node JS, npm, OAuth, React.js, SQL, Test-Driven Development.
<b>Machine Learning</b> Deep Learning models, Genetic Algorithms, Fuzzy Logic, Convolutional Networks, LSTM Networks, Keras, Tensorflow, Pandas, Numpy, Scikit-Learn
<b>Embedded Systems</b> SoC, FPGA, Microprocessors, VHDL, Xilinx Vivado, STM32,
<b>Audio &amp; Signal Processing</b> Ableton Live, Algorithmic Composition, Control Systems, DSP, JUCE, Music Production, VST development

### Honors & Awards

<b>Friends of Fulbright Scholarship</b>
<b>Second place at JAIIO 45   National contest in Computer Science</b>
<b>Outstanding work at CASE 2019   Embedded Systems Conference</b>

### Online Courses

<b>Full-Stack Engineer   Career Path</b> Codecademy
<b>Algorithms Specialization</b> Stanford Online
<b>Deep Learning Specialization</b> Coursera
<b>Software Construcction in Java</b> MITx