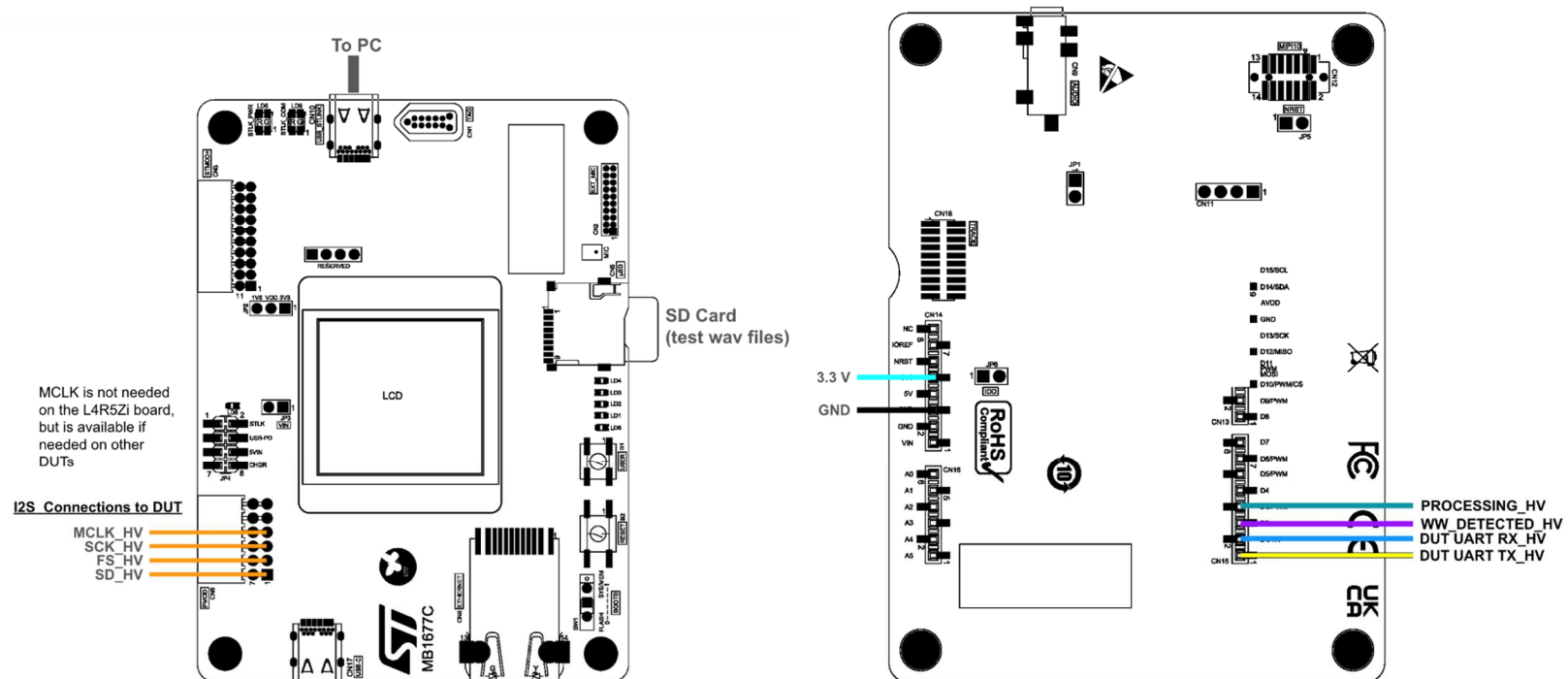
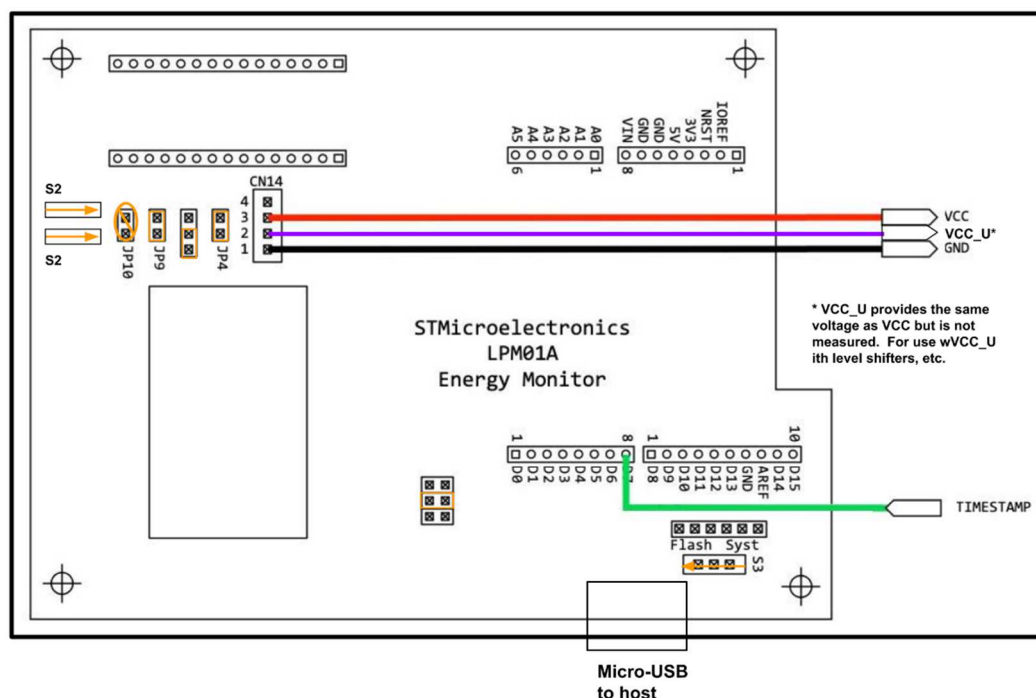


Interface Board (STM32H573I-DK)



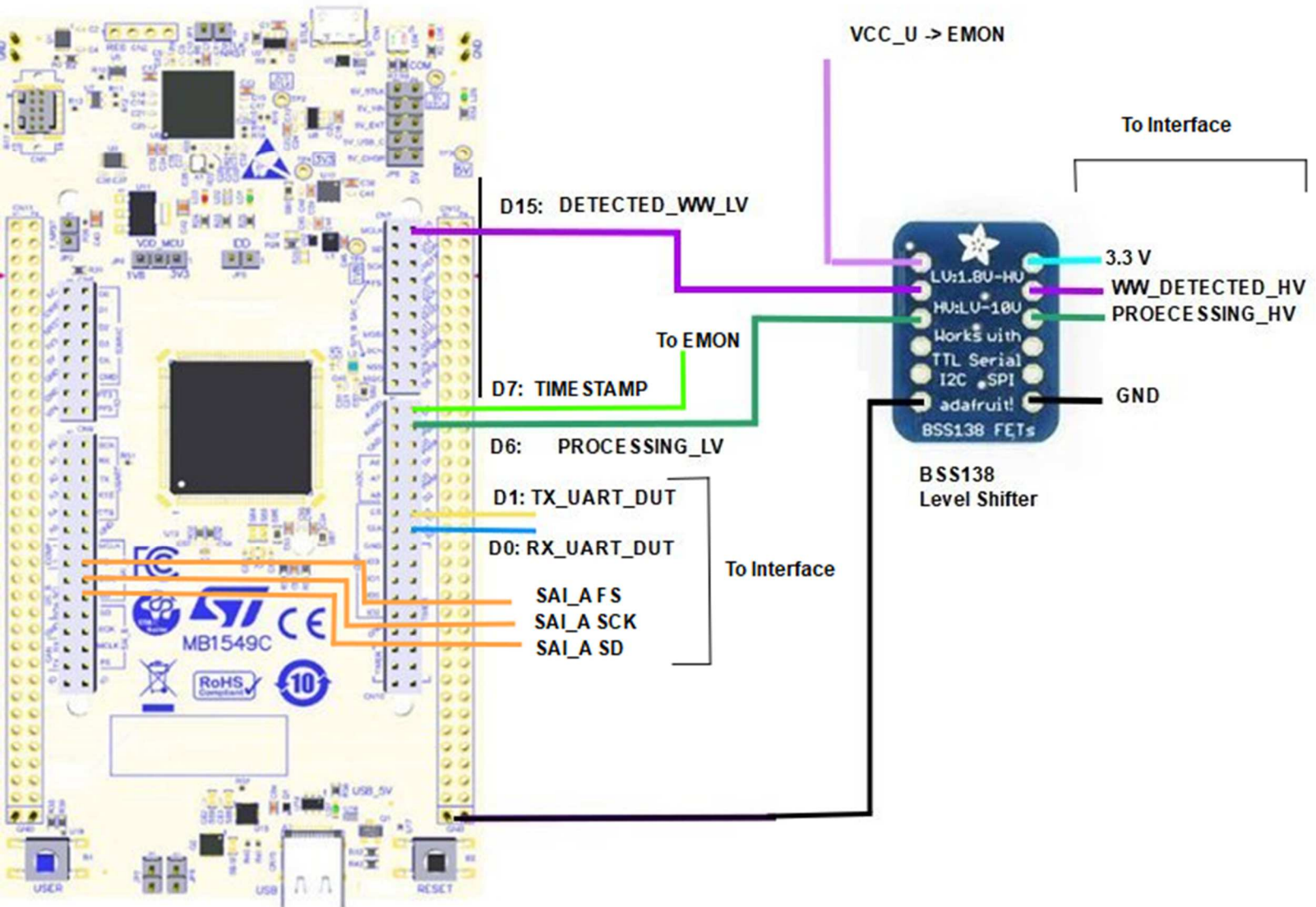
Nucleo144_L4R5ZI-P and Nucleo144_U575ZI_Q Operating at 1.8V

WW_DETECTED and PROCESSING signals need to go through a Level Shifter as we have 1.8 V transmitted signals that will not be detected by a 3.3 V device (The interface Board)

+ 3.3 V -> 1.8 V signals (DUT_UART_RX, SAI signals will saturate but will still be detected)

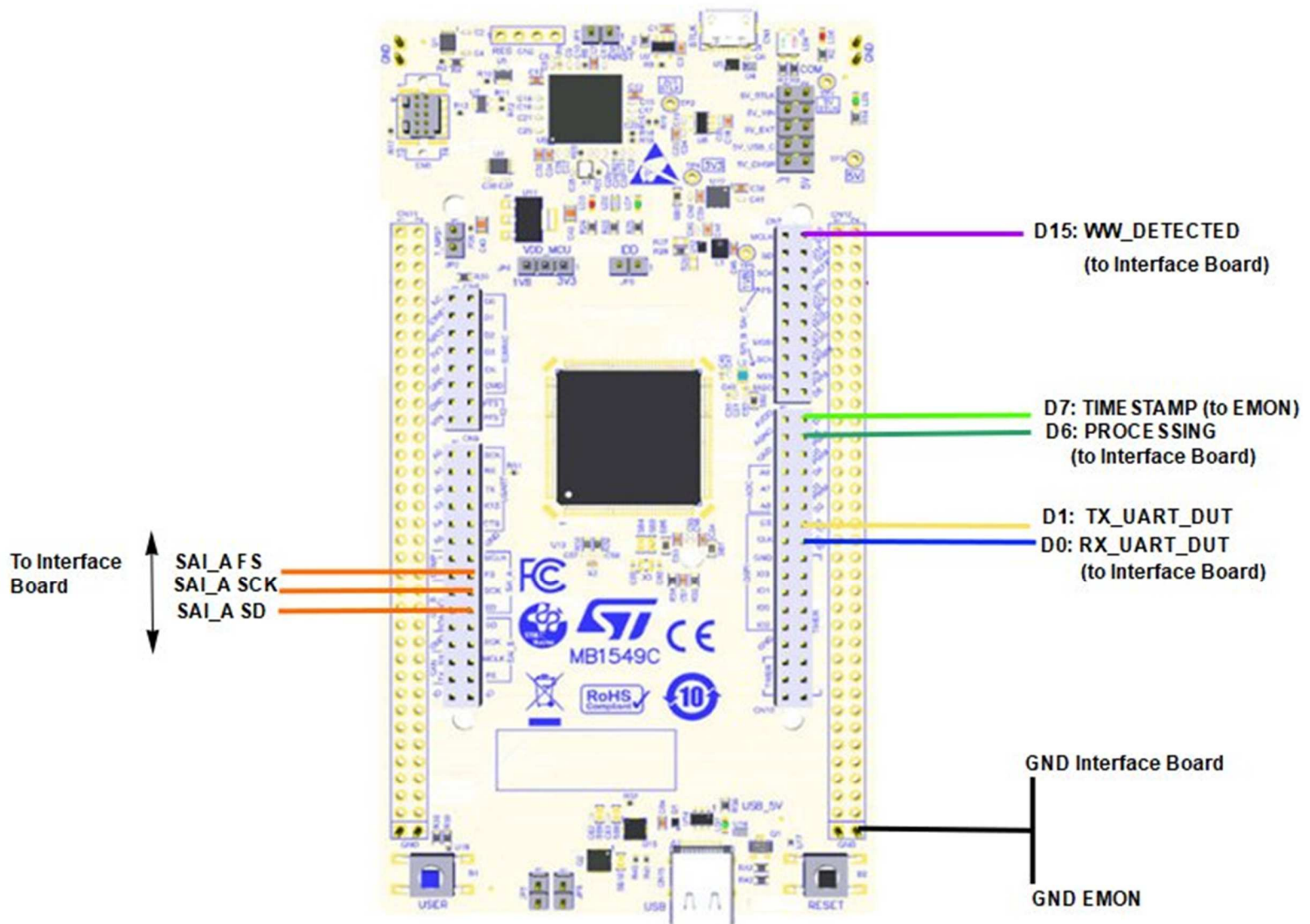
+ DUT_UART_TX and TIMESTAMP will still be detected by the Interface Board (UART) and LPM01A respectively despite the fact they are 1.8 V -> 3.3 V signals?

Putting all signals through a level shifter is ideal but not mandatory.



Nucleo144_H7A3ZI_Q Operating at 3V

No need for any Level Shifter to interface 3V <-> 3.3V



Nucleo144_U385TG Operating at 1.8V

WW_DETECTED and PROCESSING signals need to go through a Level Shifter as we have 1.8 V transmitted signals that will not be detected by a 3.3 V device (The interface Board)

+ 3.3 V -> 1.8 V signals (DUT_UART_RX, SAI signals will saturate but will still be detected)

+ DUT_UART_TX and TIMESTAMP will still be detected by the Interface Board (UART) and LPM01A respectively despite the fact they are 1.8 V -> 3.3 V signals?

Putting all signals through a level shifter is ideal but not mandatory.

