CHAPTER 9

Automata, Grammars and Languages

9.1. Finite State Machines

9.1.1. Finite-State Machines. Combinatorial circuits have no memory or internal states, their output depends only on the current values of their inputs. Finite state machines on the other hand have internal states, so their output may depend not only on its current inputs but also on the past history of those inputs.

A finite-state machine consists of the following:

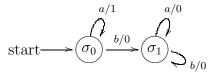
- 1. A finite set of *input symbols* J.
- 2. A finite set of output symbols O.
- 3. A finite set of states S.
- 4. A next-state function $f: \mathbb{S} \times \mathbb{J} \to \mathbb{S}$.
- 5. An output function $q: S \times J \rightarrow O$.
- 6. An initial state $\sigma \in S$.

We represent the machine $M = (\mathfrak{I}, \mathfrak{O}, \mathfrak{S}, f, q, \sigma)$

Example: We describe a finite state machine with two input symbols $\mathcal{I} = \{a, b\}$ and two output symbols $\mathcal{O} = \{0, 1\}$ that accepts any string from \mathcal{I}^* and outputs as many 1's as a's there are at the beginning of the string, then it outputs only 0's. The internal states are $\mathcal{S} = \{\sigma_0, \sigma_1\}$, where σ_0 will the initial state—we interpret it as not having seeing any "b" yet; then the machine will switch to σ_1 as soon as the first "b" arrives. The next-state and output functions are as follows:

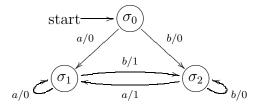
| | | f | | g | |
|------------|---|------------|------------|---|---|
| | J | a | b | a | b |
| S | | | | | |
| σ_0 | | σ_0 | σ_1 | 1 | 0 |
| σ_1 | | σ_1 | σ_1 | 0 | 0 |

This finite-state machine also can be represented with the following transition diagram:

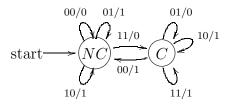


The vertices of the diagram are the states. If in state σ an input i causes the machine to output o and go to state σ' then we draw an arrow from σ to σ' labeled i/o.

Example: The following example is similar to the previous one but the machine outputs 1 only after a change of input symbol, otherwise it outputs 0:



Example: A Serial-Adder. A serial adder accepts two bits and outputs its sum. So the input set is $\mathfrak{I} = \{00,01,10,11\}$. The output set is $\mathfrak{O} = \{0,1\}$. The set of states is $\mathfrak{S} = \{NC,C\}$, which stands for "no carry" and "carry" respectively. The transition diagram is the following:



9.1.2. Finite-State Automata. A finite-state automaton is a finite-state machine with only two output symbols: $O = \{0, 1\}$. Those states for which the last output is 1 are called accepting states.

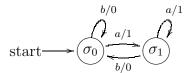
An alternative definition is as follows. A finite-state automaton consists of:

1. A finite set of input symbols J.

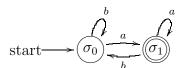
- 2. A finite set of states S.
- 3. A next-state function $f: \mathbb{S} \times \mathbb{J} \to \mathbb{S}$.
- 4. A subset \mathcal{A} of \mathcal{S} of accepting states.
- 5. An initial state $\sigma \in S$.

We represent the automaton $A = (\mathfrak{I}, \mathfrak{S}, f, \mathcal{A}, \sigma)$. We say that an automaton *accepts* a given string of input symbols if that strings takes the automaton from the starting state to an accepting state.

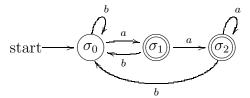
Example: The following transition diagrams represent an automaton accepting any string of a's and b's ending with an a. The first diagram uses the same scheme as with finite-state machines:



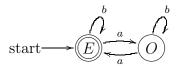
The second kind of diagram omits the outputs and represents the accepting states with double circles:



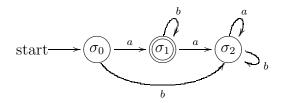
Two finite-state automata that accept exactly the same set of strings are said to be *equivalent*. For instance the following automaton also accepts precisely strings of a's abd b's that end with an a, so it is equivalent to the automaton shown above:



Example: The following automaton accepts strings of a's and b's with exactly an even number of a's:



Example: The following automaton accepts strings starting with one a followed by any number of b's:



Example: The following automaton accepts strings ending with aba:

