## CHAPTER 8

## **Boolean Algebras**

## 8.1. Combinatorial Circuits

**8.1.1.** Introduction. At their lowest level digital computers handle only binary signals, represented with the symbols 0 and 1. The most elementary circuits that combine those signals are called *gates*. Figure 8.1 show three gates: OR, AND and NOT.

OR GATE 
$$\begin{array}{c} x1 \\ x2 \\ \end{array}$$
  $\begin{array}{c} x1 \lor x2 \\ \end{array}$  AND GATE  $\begin{array}{c} x1 \\ x2 \\ \end{array}$   $\begin{array}{c} x1 \land x2 \\ \end{array}$  NOT GATE  $\begin{array}{c} x \\ \end{array}$ 

FIGURE 8.1. Gates.

Their outputs can be expressed as a function of their inputs by the following  $logic\ tables$ :

$x_1$	$x_2$	$x_1 \vee x_2$	
1	1	1	
1	0	1	
0	1	1	
0	0	0	
OR GATE			

	$x_1$	$x_2$	$x_1 \wedge x_2$
Ī	1	1	1
	1	0	0
	0	1	0
	0	0	0
ſ	AND GATE		

x	$\bar{x}$	
1	0	
0	1	
NOT GATE		

These are examples of *combinatorial circuits*. A combinatorial circuit is a circuit whose output is uniquely defined by its inputs. They do not have memory, previous inputs do not affect their outputs. Some combinations of gates can be used to make more complicated combinatorial circuits. For instance figure 8.2 is combinatorial circuit with the logic table shown below, representing the values of the *Boolean expression*  $y = (x_1 \lor x_2) \land x_3$ .

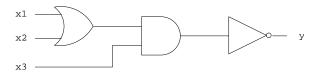


Figure 8.2. A combinatorial circuit.

$x_1$	$x_2$	$x_3$	$y = \overline{(x_1 \vee x_2) \wedge x_3}$
1	1	1	0
1	1	0	1
1	0	1	0
1	0	0	1
0	1	1	0
0	1	0	1
0	0	1	1
0	0	0	1

However the circuit in figure 8.3 is *not* a combinatorial circuit. If  $x_1 = 1$  and  $x_2 = 0$  then y can be 0 or 1. Assume that at a given time y = 0. If we input a signal  $x_2 = 1$ , the output becomes y = 1, and

stays so even after  $x_2$  goes back to its original value 0. That way we can store a bit. We can "delete" it by switching input  $x_1$  to 0.

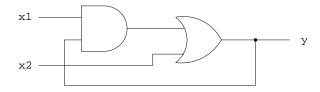


Figure 8.3. Not a combinatorial circuit.

- **8.1.2. Properties of Combinatorial Circuits.** Here  $\mathbb{Z}_2 = \{0, 1\}$  represents the set of signals handled by combinatorial circuits, and the operations performed on those signals by AND, OR and NOT gates are represented by the symbols  $\wedge$ ,  $\vee$  and  $\bar{\phantom{a}}$  respectively. Then their properties are the following (a, b, c) are elements of  $\mathbb{Z}_2$ , i.e., each represents either 0 or 1):
  - 1. Associative

$$(a \lor b) \lor c = a \lor (b \lor c)$$
  
 $(a \land b) \lor c = a \land (b \land c)$ 

2. Commutative

$$a \lor b = b \lor a$$
$$a \land b = b \land a$$

3. Distributive

$$a \wedge (b \vee c) = (a \wedge b) \vee (a \wedge c)$$
$$a \vee (b \wedge c) = (a \vee b) \wedge (a \vee c)$$

4. Identity

$$a \lor 0 = a$$
$$a \land 1 = 1$$

5. Complement

$$a \vee \bar{a} = 1$$
$$a \wedge \bar{a} = 0$$

A system satisfying those properties is called a *Boolean algebra*.

Two Boolean expressions are defined to be *equal* is they have the same values for all possible assignments of values to their literals. *Example*:  $\overline{x \vee y} = \overline{x} \wedge \overline{y}$ , as shown in the following table:

	$\boldsymbol{x}$	y	$\overline{x \vee y}$	$\bar{x} \wedge \bar{y}$
I	1	1	0	0
	1	0	0	0
	0	1	0	0
	0	0	1	0

**8.1.3.** Boolean Algebras. Here we deal with general Boolean algebras; combinatorial circuits are an example, but there are others.

A Boolean algebra  $B = (S, +, \cdot, ', 0, 1)$  is set S containing two distinguished elements 0 and 1, two binary operators + and  $\cdot$  on S, and a unary operator ' on S, satisfying the following properties (x, y, z) are elements of S:

1. Associative

$$(x+y) + z = x + (y+z)$$
$$(x \cdot y) + z = x \cdot (y \cdot z)$$

2. Commutative

$$x + y = y + x$$
$$x \cdot y = y \cdot x$$

3. Distributive

$$x \cdot (y+z) = (x \cdot y) + (x \cdot z)$$
$$x + (y \cdot z) = (x+y) \cdot (x+z)$$

4. Identity

$$x + 0 = x$$
$$x \cdot 1 = x$$

5. Complement

$$x + x' = 1$$
$$x \cdot x' = 0$$

Example:  $(\mathbb{Z}_2, \vee, \wedge, \bar{}, 0, 1)$  is a Boolean algebra.

Example: If U is a universal set and  $\mathcal{P}(U)$ = the power set of S (collection of subsets of S) then  $(\mathcal{P}(U), \cup, \cap, \bar{}, \emptyset, U)$ . is a Boolean algebra.

**8.1.4.** Other Properties of Boolean Algebras. The properties mentions above define a Boolean algebra, but Boolean algebras also have other properties:

1. Idempotent

$$x + x = x$$

$$x \cdot x = x$$

2. Bound

$$x + 1 = 1$$

$$x \cdot 0 = 0$$

3. Absorption

$$x + xy = x$$

$$x \cdot (x + y) = x$$

4. Involution

$$(x')' = x$$

5. 0 and 1

$$0' = 1$$

$$1' = 0$$

6. De Morgan's

$$(x+y)' = x' \cdot y'$$

$$(x \cdot y)' = x' + y'$$

For instance the first idempotent law can be proved like this:  $x = x + 0 = x + x \cdot x' = (x + x) \cdot (x + x') = (x + x) \cdot 1 = x + x$ .