

ODROID-C1 pin map, v1

Wiring	Export	ODROID-C1 pin	Label	Header	Label	ODROID-C1 pin	Export	Wiring
–	–	–	3V3	1 2	5V0	–	–	–
8	74	I2CA_SDA	SDA1	3 4	5V0	–	–	–
9	75	I2CA_SCL	SCL1	5 6	GND	–	–	–
7	83	default w1	GPI0Y.BIT3	#83	7 8	TXD1	TXD_B	113
–	–	–	GND	9 10	RXD1	RXD_B	114	–
0	88		GPI0Y.BIT8	#88	11 12	#87	GPI0Y.BIT7	87
2	116		GPI0X.BIT19	#116	13 14	GND	–	–
3	115		GPI0X.BIT18	#115	15 16	#104	GPI0X.BIT7	104
–	–	–	3V3	17 18	#102	GPI0X.BIT5	102	5
12	107	MOSI, PWM1	GPI0X.BIT10	MOSI	19 20	GND	–	–
13	106	MISO	GPI0X.BIT9	MISO	21 22	#103	GPI0X.BIT6	103
14	105	SCLK	GPI0X.BIT8	SCLK	23 24	CE0	GPI0X.BIT20	CE0
–	–	–	GND	25 26	#118	GPI0X.BIT21	118	11
–	76		I2CB_SDA	SDA2	27 28	SCL2	I2CB_SCL	77
21	101		GPI0X.BIT4	#101	29 30	GND	–	–
22	100		GPI0X.BIT3	#100	31 32	#99	GPI0X.BIT2	99
23	108	PWM0	GPI0X.BIT11	#108	33 34	GND	–	–
24	97		GPI0X.BIT0	#97	35 36	#98	GPI0X.BIT1	98
–	–		ADC.AIN1	AIN1	37 38	1V8	1V8	–
–	–	–	GND	39 40	AIN0	ADC.AIN0	–	–