### Controllable Transformations in MLIR

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# Why?

### Scheduling DSLs in the Wild

```
Input: Algorithm
blurx(x,y) = in(x-1,y)
              + in(x, y)
              + in(x+1,y)
out(x,y) = blurx(x,y-1)
            + blurx(x,y)
            + blurx(x,v+1)
Input: Schedule
blurx: split x by 4 \rightarrow x_0, x_1
        vectorize: x
        store at out.x.
        compute at out.y.
out: split x by 4 \rightarrow x_a, x_b
      split y by 4 \rightarrow y_0, y_1
      reorder: y, x, y, x,
      parallelize: y
      vectorize: x.
```

Halide (Ragan-Kelley et.al. 2013)

```
+ Loop Tiling
yo, xo, ko, yi, xi, ki = s[C].tile(y, x, k, 8, 8, 8)
   for vo in range(128):
     for xo in range(128):
       C[y0*8:y0*8+8][x0*8:x0*8+8] = 0
       for ko in range(128):
         for vi in range(8):
            for xi in range(8):
             for ki in range(8):
               C[yo*8+yi][xo*8+xi] +=
                   A[ko*8+ki][yo*8+yi] * B[ko*8+ki][xo*8+xi]
+ Cache Data on Accelerator Special Buffer
CL = s.cache_write(C, vdla.acc_buffer)
AL = s.cache_read(A, vdla.inp_buffer)
# additional schedule steps omitted ...
+ Map to Accelerator Tensor Instructions
s[CL].tensorize(yi, vdla.gemm8x8)
```

```
tc::IslKernelOptions::makeDefaultM
    .scheduleSpecialize(false)
    .tile(\{4, 32\})
    .mapToThreads({1, 32})
    .mapToBlocks({64, 128})
    .useSharedMemory(true)
    .usePrivateMemory(true)
    .unrollCopyShared(false)
    .unroll(4);
```

TC (Vasilache et.al. 2018)

```
TVM (Chen et.al. 2018)
                                        mm = MatMul(M,N,K)(GL,GL,GL)(Kernel)
h: 0..3
                                                        // resulting intermediate specs below
w: 0..135
                                        .tile(128,128) // MatMul(128,128,K)(GL,GL,GL)(Kernel)
                                          .to(Block)
                                                       // MatMul(128,128,K)(GL,GL,GL)(Block)
    rc: 0..127
                                        .load(A, SH, _) // MatMul(128,128,K)(SH,GL,GL)(Block)
    Unroll h by 8
                                        .load(A, SH, _) // MatMul(128,128,K)(SH,SH,GL)(Block
     Unroll k by 2
                                        .tile(64,32) // MatMul(64, 32, K)(SH,SH,GL)(Block
     Vectorized on
                                          .to(Warp) // MatMul(64, 32, K)(SH,SH,GL)(Warp
                                        .tile(8.8)
                                                       // MatMul(8, 8, K)(SH,SH,GL)(Warp
 iter twice along h
                                          .to(Thread) // MatMul(8, 8, K)(SH,SH,GL)(Thread)
                                        .load(A, RF, _) // MatMul(8, 8, K)(RF,SH,GL)(Thread)
    rc: 0..127
    Unroll h by 13
                                        .load(B, RF, _) // MatMul(8, 8, K)(RF,RF,GL)(Thread)
     Unroll k by 2
                                        .tile(1,1)
                                                        // MatMul(1, 1, K)(RF,RF,GL)(Thread)
                                                       // invoke codeger amit dot micro-kernel
     Vectorized on
                                        .done(dot.cu)
                                                  Fireiron (Hagedorn et.al. 2020)
```

Tile (Tollenaere et.al. 2021)

### Scheduling DSLs in the Wild are Time-Tested

```
# Avoid spurious versioning
                                               # Peel and shift to enable fusion
addContext(C1L1,'ITMAX>=9')
                                               peel (enclose (C3L1,2),'3')
addContext(C1L1, 'doloop ub>=ITMAX')
                                               peel (enclose (C3L1 2,2),'N-3')
addContext(C1L1, 'doloop ub <= ITMAX')
                                               peel (enclose (C3L1 2 1,1),'3')
addContext(C1L1,'N>=500')
                                               peel (enclose (C3L1 2 1 2.1), 'M-3')
addContext(C1L1,'M>=500')
                                               peel (enclose (C1L1, 2), '2')
addContext(C1L1,'MNMIN>=500')
                                               peel (enclose (C1L1 2,2),'N-2')
addContext(C1L1,'MNMIN<=M')
                                               peel (enclose (C1L1 2 1,1),'2')
addContext(C1L1,'MNMIN<=N')
                                               peel (enclose (C1L1 2 1 2,1), 'M-2')
addContext(C1L1,'M<=N')
                                               peel (enclose (C2L1, 2), '1')
addContext(C1L1,'M>=N')
                                               peel (enclose (C2L1 2,2),'N-1')
                                               peel (enclose (C2L1 2 1,1),'3')
                                               peel (enclose (C2L1 2 1 2,1),'M-3')
# Move and shift calc3 backwards
shift(enclose(C3L1), {'1','0','0'})
                                               shift(enclose(C1L1 2 1 2 1), ('0', '1', '1'))
shift (enclose (C3L10), {'1','0'})
                                               shift(enclose(C2L1 2 1 2 1), {'0', '2', '2'})
shift(enclose(C3L11), {'1','0'})
shift(C3L12, {'1'})
                                               # Double fusion of the three nests
shift(C3L13, {'1'})
                                               motion(enclose(C2L1 2 1 2 1), TARGET 2 1 2 1)
shift(C3L14, {'1'})
                                               motion(enclose(C1L1 2 1 2 1), C2L1 2 1 2 1)
                                               motion(enclose(C3L1 2 1 2 1),C1L1 2 1 2 1)
shift(C3L15, {'1'})
shift(C3L16, {'1'})
shift(C3L17, {'1'})
                                               # Register blocking and unrolling (factor 2)
motion (enclose (C3L1), BLOOP)
                                               time stripmine (enclose (C3L1 2 1 2 1,2),2,2)
motion (enclose (C3L10), BLOOP)
                                               time stripmine (enclose (C3L1 2 1 2 1,1),4,2)
motion (enclose (C3L11), BLOOP)
                                               interchange (enclose (C3L1 2 1 2 1,2))
motion (C3L12, BLOOP)
                                               time peel (enclose (C3L1 2 1 2 1,3),4,'2')
motion (C3L13, BLOOP)
                                               time peel (enclose (C3L1 2 1 2 1 2.3), 4, 'N-2')
motion (C3L14, BLOOP)
                                               time peel (enclose (C3L1 2 1 2 1 2 1,1),5,'2')
                                               time peel (enclose (C3L1 2 1 2 1 2 1 2,1),5,'M-2')
motion (C3L15, BLOOP)
                                               fullunrol1(enclose(C3L1 2 1 2 1 2 1 2 1,2))
motion (C3L16, BLOOP)
                                               fullunroll(enclose(C3L1 2 1 2 1 2 1 2 1.1))
motion (C3L17, BLOOP)
```

URUK (Girbal et.al. 2006)

```
Distribution Distribute loop at depth L over the statements D, with statement s_n going into r_n<sup>th</sup> loop.
      Requirements: \forall s_n, s_n \in D \land s_n \in D \Rightarrow \text{loop}(f_n^L) \land L \leq \text{csl}(s_n, s_n)
      Transformation: \forall s_n \in D, replace T_n by [f_n^1, \ldots, f_n^{(L-1)}, \text{syntactic}(r_n), f_n^L, \ldots, f_n^R]
Statement Reordering Reorder statements D at level L so that new position of statement s_n is r_n.
      Requirements: \forall s_v, s_g \mid s_v \in D \land s_g \in D \Rightarrow \text{syntactic}(f_v^L) \land L \leq csl(s_v, s_g) + 1 \land
      Transformation: \forall s_n \in D, replace T_n by [f_n^1, \dots, f_p^{(L-1)}, \text{syntactic}(r_n), f_p^{(L+1)}, \dots, f_n^n]
Fusion Fuse the loops at level L for the statements D with statement s_n going into the r_n<sup>th</sup> loop.
      (L-2 < csl(s_n, s_n) + 2 \Rightarrow r_n = r_n)
      Transformation: \forall s_n \in D, replace T_n by [f_n^1, \dots, f_p^{(L-2)}, \text{syntactic}(r_n), f_p^{(L)}, f_p^{(L-1)}, f_p^{(L+1)}, \dots, f_n^{(n)}]
Unimodular Transformation Apply a k \times k unimodular transformation U to a perfectly nested loop
      containing statements D at depth L \dots L + k. Note: Unimodular transformations include loop inter-
      change, skewing and reversal [Ban 90, WL91b].
      Requirement s: \forall i, s_n, s_o \mid s_n \in D \land s_o \in D \land L \leq i \leq L + k \Rightarrow \text{loop}(f_n^i) \land L + k \leq csl(s_n, s_o)
      Transformation: \forall s_n \in D, replace T_n by [f_n^1, \dots, f_p^{(L-1)}, U[f_p^{(L)}, \dots f_p^{(L+k)}]^\top, f_p^{(L+k+1)}, \dots, f_n^T]
Strip-mining Strip-mine the loop at level L for statements D with block size B
      Requirements: \forall s_n, s_a \mid s_n \in D \land s_g \in D \Rightarrow \text{loop}(f_v^L) \land L \leq csl(s_v, s_g)) \land B is a known integer constant
      Transformation: \forall s_n \in D, replace T_n by [f_n^1, \dots, f_n^{(L-1)}, B(f_n^{(L)} \text{ div } B), f_n^{(L)}, \dots, f_n^{(n)}]
Index Set Splitting Split the index set of statements D using condition C
      Requirements: C is affine expression of symbolic constants and indexes common to statements D.
      Transformation: \forall s_n \in D, replace T_n by (T_n \mid C) \cup (T_n \mid \neg C)
```

Omega (Pugh, 1991)

### Motivation for Schedules in MLIR

- Many successful systems rely on some sort of *schedule representation* to produce state-of-the-art results.
- Schedules allow for *declarative* specification of transformations with arbitrary granularity.
- Schedules are *separable* and can be shipped independently.
- Schedules can support multi-versioning with runtime dispatch.
- Focus transformation on parts of IR ("vertical" sequencing rather than "horizontal" as with passes).

### Schedules in MLIR

In MLIR, everything is an op.

So are schedules.

Such ops live in the Transform dialect.

#### 02

## Simple Transformation Chain

**Source IR**: fully connected layer + ReLU

**Objective**: fuse matmul and addition so it can be replaced by an efficient BLAS gemm call for 32x32 size, keep ReLU apart and vectorize it.

```
%matmul = linalg.matmul ...
%biased = linalg.elemwise_binary {#add} (%matmul, ...)
%relued = linalg.elemwise_binary {#maxf} (%biased, 0.)
```

#### Transform IR

transform.sequence {

#### **Payload IR**

Perform transformations one after another.

```
%matmul = linalg.matmul ...
%biased = linalg.elemwise_binary {#add} (%matmul, ...)
%relued = linalg.elemwise_binary {#maxf} (%biased, 0.)
```

#### **Transform IR**

transform.sequence failures(propagate) {

#### Payload IR

Perform transformations one after another.

Abort the transform and complain to the user if any transformation fails.

```
%matmul = linalg.matmul ...
%biased = linalg.elemwise_binary {#add} (%matmul, ...)
%relued = linalg.elemwise_binary {#maxf} (%biased, 0.)
```

#### **Transform IR**

transform.sequence failures(suppress) {

#### Payload IR

Perform transformations one after another.

Abort the sequence but do not complain to the user. Next one can be attempted.

```
%matmul = linalg.matmul ...
%biased = linalg.elemwise_binary {#add} (%matmul, ...)
%relued = linalg.elemwise_binary {#maxf} (%biased, 0.)
```

#### **Transform IR**

The sequence applies to some payload operations associated with transform IR values, or *handles*.

```
%matmul = linalg.matmul ...
%biased = linalg.elemwise_binary {#add} (%matmul, ...)
%relued = linalg.elemwise_binary {#maxf} (%biased, 0.)
```

#### **Transform IR**

The sequence applies to some payload operations associated with transform IR values, or *handles*.

Handles are typed. The type describes properties of the associated payload operations.

```
%matmul = linalg.matmul ...
%biased = linalg.elemwise_binary {#add} (%matmul, ...)
%relued = linalg.elemwise_binary {#maxf} (%biased, 0.)
```

### Typing in Transforms Leads to Better Errors

#### **Transform IR**

```
%matmul = linalg.matmul ...
%biased = linalg.elemwise_binary {#add} (%matmul, ...)
%relued = linalg.elemwise_binary {#maxf} (%biased, 0.)
```

```
matmul.mlir:21:70: error: incompatible payload operation name
    ^bb0(%root: !pdl.operation, %matmul: !transform.op<"linalg.matmul">, %elemwise: !transform.op<"linalg.elemwise_unary">)
    matmul.mlir:10:13: note: payload operation
    %biased = linalg.elemwise_binary { fun = #linalg.binary_fn }
```

#### **Transform IR**

Handles are associated with *lists* of payload ops.

```
%matmul = linalg.matmul ...
%biased = linalg.elemwise_binary {#add} (%matmul, ...)
%relued = linalg.elemwise_binary {#maxf} (%biased, 0.)
```

#### Transform IR

Handles are associated with *lists* of payload ops.

Handles can be casted to a different type, the verification happens dynamically.

```
%matmul = linalg.matmul ...
%biased = linalg.elemwise_binary {#add} (%matmul, ...)
%relued = linalg.elemwise_binary {#maxf} (%biased, 0.)
```

#### **Transform IR**

```
transform.sequence failures(propagate) {
    ^bb0(...):
    %bias, %relu = transform.split_handles %elemwise in [2]
    ...
    %loop, %tiled = transform.structured.tile_to_forall_op %bias
    tile_sizes [32, 32]
```

Transformations apply to the payload ops associated with handles, tweaked by attributes.

```
%matmul = linalg.matmul ...
%biased = linalg.elemwise_binary {#add} (%matmul, ...)
%relued = linalg.elemwise_binary {#maxf} (%biased, 0.)
```

#### Transform IR

```
transform.sequence failures(propagate) {
    ^bb0(...):
    %bias, %relu = transform.split_handles %elemwise in [2]
    ...
    %loop, %tiled = transform.structured.tile_to_forall_op %bias
    tile_sizes [32, 32]
```

Transformations apply to the payload ops associated with handles, tweaked by attributes.

```
%matmul = linalg.matmul ...
%biased = scf.forall (%i, %j) in (.../32, .../32) {
    %slice = tensor.extract_slice %matmul
    %part = linalg.elemwise_binary {#add} (%matmul, ...)
        "scf.forall.yield_slice" %slice
}
%relued = linalg.elemwise_binary {#maxf} (%biased, 0.)
```

#### Transform IR

```
transform.sequence failures(propagate) {
    ^bb0(...):
    %bias, %relu = transform.split_handles %elemwise in [2]
    ...

%loop, %tiled = transform.structured.tile_to_forall_op %bias
    tile_sizes [32, 32]
```

Transformations apply to the payload ops associated with handles, tweaked by attributes.

Transform ops define new handles for payload ops produced as the result.

```
%matmul = linalg.matmul ...
%biased = scf.forall (%i, %j) in (.../32, .../32) {
    %slice = tensor.extract_slice %matmul
    %part = linalg.elemwise_binary {#add} (%matmul, ...)
        "scf.forall.yield_slice" %slice
}
%relued = linalg.elemwise_binary {#maxf} (%biased, 0.)
```

### Handle Consumption and Invalidation

#### Transform IR

```
transform.sequence failures(propagate) {
    ^bb0(...):
    %bias, %relu = transform.split_handles %elemwise in [2]
    ...
    %loop, %tiled = transform.structured.tile_to_forall_op %bias
    tile_sizes [32, 32]
```

Transform ops may *consume* handles that should no longer be used (associated payload was rewritten).

```
%matmul = linalg.matmul ...
%biased = linalg.elemwise_binary (#add) (%matmul, ...)
%biased = scf.forall (%i, %j) in (.../32, .../32) {
    %slice = tensor.extract_slice %matmul
    %part = linalg.elemwise_binary {#add} (%matmul, ...)
    "scf.forall.yield_slice" %slice
}
%relued = linalg.elemwise_binary {#maxf} (%biased, 0.)
```

### Handle Consumption and Invalidation

#### Transform IR

```
transform.sequence failures(propagate) {
    ^bb0(...):
    %bias, %relu = transform.split_handles %elemwise in [2]
        ...

%loop, %tiled = transform.structured.tile_to_forall_op %bias
    tile_sizes [32, 32]

transform.test_print_remark_at_operand %bias, "help!"
    : !pdl.operation
```

```
%matmul = linalg.matmul ...
%biased = linalg.elemwise_binary (#add) (%matmul, ...)
%biased = scf.forall (%i, %j) in (.../32, .../32) {
    %slice = tensor.extract_slice %matmul
    %part = linalg.elemwise_binary {#add} (%matmul, ...)
    "scf.forall.yield_slice" %slice
}
%relued = linalg.elemwise_binary {#maxf} (%biased, 0.)
```

#### Transform IR

```
transform.sequence failures(propagate) {
    ^bb0(...):
    ...
    %loop, %tiled = transform.structured.tile_to_forall_op %bias

%cast_matmul = transform.cast %matmul
    : !transform.op<"linalg.matmul"> to !pdl.operation

%fused_matmul = transform.structured.fuse_into_containing_op
    %cast_matmul into %loop
```

Transformations can be chained and *precisely* targeted by applying them to specific handles.

```
%matmul = linalg.matmul ...
%biased = scf.forall (%i, %j) in (.../32, .../32) {
    %slice = tensor.extract_slice %matmul
    %part = linalg.elemwise_binary {#add} (%matmul, ...)
    "scf.forall.yield_slice" %slice
}
%relued = linalg.elemwise_binary {#maxf} (%biased, 0.)
```

#### Transform IR

```
transform.sequence failures(propagate) {
    ^bb0(...):
    ...
    %loop, %tiled = transform.structured.tile_to_forall_op %bias

%cast_matmul = transform.cast %matmul
    : !transform.op<"linalg.matmul"> to !pdl.operation

%fused_matmul = transform.structured.fuse_into_containing_op
    %cast_matmul into %loop
```

Transformations can be chained and *precisely* targeted by applying them to specific handles.

This will *only* tile and fuse matmul with addition, and *not* relu, even though addition and relu are identical except for the attribute.

```
%biased = scf.forall (%i, %j) in (.../32, .../32) {
   tensor.extract_slice
   %slice = linalg.matmul ...
   %part = linalg.elemwise_binary {#add} (%matmul, ...)
        "scf.forall.yield_slice" %slice
}
%relued = linalg.elemwise_binary {#maxf} (%biased, 0.)
```

### Precise Error Messages on Failure

#### **Transform IR**

```
transform.sequence failures(propagate) {
    ^bb0(...):
    ...
    %loop, %tiled = transform.structured.tile_to_forall_op %bias

%cast_matmul = transform.cast %matmul
    : !transform.op<"linalg.matmul"> to !pdl.operation

%fused_matmul = transform.structured.fuse_into_containing_op
    %cast_matmul into %tiled
```

```
%biased = scf.forall (%i, %j) in (.../32, .../32) {
   tensor.extract_slice
   %slice = linalg.matmul ...
   %part = linalg.elemwise_binary {#add} (%matmul, ...)
        "scf.forall.yield_slice" %slice
}
%relued = linalg.elemwise_binary {#maxf} (%biased, 0.)
```

```
matmul.mlir:28:19: error: could not find next producer to fuse into container
%fused_matmul = transform.structured.fuse_into_containing_op %cast_matmul into %tiled

matmul.mlir:10:13: note: container
%biased = linalg.elemwise_binary { fun = #linalg.binary_fn }
```

#### **Transform IR**

```
func.call @loop {
    %biased = scf.forall (%i, %j) in (.../32, .../32) {
        tensor.extract_slice
        %slice = linalg.matmul ...
        %part = linalg.elemwise_binary {#add} (%matmul, ...)
        "scf.forall.yield_slice" %slice
    }
    func.return %biased
}

%biased = func.call @loop
    %relued = linalg.elemwise_binary {#maxf} (%biased, 0.)
```

#### Transform IR

#### Payload IR

```
func.call @loop {
    %biased = scf.forall (%i, %j) in (.../32, .../32) {
        tensor.extract_slice
        %slice = linalg.matmul ...
        %part = linalg.elemwise_binary {#add} (%matmul, ...)
        "scf.forall.yield_slice" %slice
    }
    func.return %biased
}
%biased = func.call @loop
```

%relued = arith.maxf (%biased, 0.) : vector<...>

### Handle Invalidation Continued

#### **Transform IR**

```
func.call @loop {
    %biased = scf.forall (%i, %j) in (.../32, .../32) {
        tensor.extract_slice
        %slice = linalg.matmul ...
        %part = linalg.elemwise_binary {#add} (%matmul, ...)
        "scf.forall.yield_slice" %slice
    }
    func.return %biased
}

%biased = func.call @loop
    %relued = arith.maxf (%biased, 0.) : vector<...>
```

### Handle Invalidation Continued

#### Transform IR

```
transform.sequence failures(propagate) {
    ^bb0(...):
    ...
    %loop, %tiled = transform.structured.tile_to_forall_op %bias

%fused_matmul = transform.structured.fuse_into_containing_op
    %cast_matmul into %loop

transform.loop.outline %loop {func_name = "loop"}
    : (!pdl.operation) -> !pdl.operation

transform.test_print_remark_at_operand %fused_matmul, "help!"
    : !pdl.operation
```

Consuming a handle invalidates *all other handles* associated with any of the payload ops nested in the payload ops associated with the consumed handle.

```
func.call @loop {
%biased = scf.forall (%i, %j) in (.../32, .../32) {
   tensor.extract slice
   %slice = linalq.matmul ...
    %part = linalg.elemwise_binary {#add} (%matmul, ...)
    "scf.forall.yield_slice" %slice
 func.return %biased
%biased = func.call @loop
 %relued = arith.maxf (%biased, 0.) : vector<...>
```

### Handle Invalidation Continued

```
Transform IR
                                                                           Payload IR
  transform.sequence failures(propagate) {
  ^bb0(...):
matmul.mlir:33:3: error: op uses a handle invalidated by a previously executed transform op
transform.test_print_remark_at_operand %fused_matmul, "matmul" : !pdl.operation
matmul.mlir:28:19: note: handle to invalidated ops
%fused_matmul = transform.structured.fuse_into_containing_op %cast_matmul into %loop
matmul.mlir:30:3: note: invalidated by this transform op that consumes its operand #0 and invalidates all handles to payload IR
entities associated with this operand and entities nested in them
transform.loop.outline %loop {func_name = "loop"} : (!pdl.operation) -> !pdl.operation
matmul.mlir:10:13: note: ancestor payload op
%biased = linalg.elemwise_binary { fun = #linalg.binary_fn }
matmul.mlir:7:13: note: nested payload op
%matmul = linalg.matmul
                                                                           %biased = func.call @loop
```

%relued = arith.maxf (%biased, 0.) : vector<...>

Source IR: fully connected layer + ReLU

**Objective**: fuse matmul and addition so it can be replaced by an efficient BLAS gemm call for 32x32 size, keep ReLU apart and vectorize it.

```
%matmul = linalg.matmul ...
%biased = linalg.elemwise_binary {#add} (%matmul, ...)
%relued = linalg.elemwise_binary {#maxf} (%biased, 0.)
func.call @loop {
%biased = scf.forall (%i, %j) in (.../32, .../32) {
   tensor.extract_slice
   %slice = linalq.matmul ...
   %part = linalg.elemwise_binary {#add} (%matmul, ...)
    "scf.forall.yield_slice" %slice
func.return %biased
%biased = func.call @loop
%relued = arith.maxf (%biased, 0.) : vector<...>
```

### 03

### Let's generalize!

### **Transform Dialect**

Transformations of the IR are described as a separate piece of IR where:

- Operations describe individual transformations to apply.
- Values (handles) are associated with operations that are being transformed.
- Transform operations may read or "consume" operands.
- Transform operations "produce" operands.
- Consuming a handle invalidates other handles to the same or nested IR.

### Transform Dialect Interpreter

- Maintains the mapping between transform IR values and payload IR operations.
- Drives the application of transformations, including control flow.
- Maintains extra state if desired via the extension mechanism.
- Performs verification and tracks invalidation (expensive, similar to ASAN, disabled by default).
- Can be embedded into passes similarly to pattern application: applyTransforms.

### **Transform Dialect Interfaces**

#### Transform operation interface:

- Specifies how a transform operation applies to payload IR (the interpreter dispatches to this), this may include dispatching to other operations from nested regions.
- Specifies the effects a transform has on handles and payload (reads, consumes, etc.)

#### Transform type interface:

- Specifies the conditions the payload must satisfy so it can be associated with the handle of this type (checked by the interpreter when a handle is produced).

### **Transform Dialect Entry Point**

The application starts from a transformation op with a PossibleTopLevelTransformOpTrait that:

- Has no operands and no results (at least, the current instance of the op).
- Has a region with at least one argument of TransformHandleTypeInterface type.

The call to applyTransforms takes as arguments:

- The payload op to be associated with the first region argument.
- An optional list of lists of objects (ops, values, attributes) to be associated with the following region arguments.

### Semantic Trick for Early Exit

How do we abort in the middle of a transformation sequence when an op is not a terminator?

- When a transformation fails, it sets the "has-failed" flag.
- Any transformation has the (implicit) semantics of doing nothing and associating result handles with empty lists of payload if the "has-failed" flag is set .
- Can be modeled as side effects to control reordering of transform ops.

#### 07

# **Extending the Transform Dialect**

#### Would like a transform op that:

- Takes a handle to scf.forall.
- Triggers rewriting into a nest of scf.for.
- Returns handles to produces ops.

#### Would like a transform op that:

- Takes a handle to scf. forall.
- Triggers rewriting into a nest of scf. for.
- Returns handles to produces ops.

#### Would like a transform op that:

- Takes a handle to scf. forall.
- Triggers rewriting into a nest of scf. for.
- Returns handles to produces ops.

```
def ForallToFor
  : Op<Transform_Dialect, "tutorial.forall_to_for",</pre>
```

#### Things to know:

- Transform ops can be injected into the dialect.

#### Would like a transform op that:

- Takes a handle to scf. forall.
- Triggers rewriting into a nest of scf. for.
- Returns handles to produces ops.

```
def ForallToFor
   : Op<Transform_Dialect, "tutorial.forall_to_for",
      [
         DeclareOpInterfaceMethods<TransformOpInterface>]> {
```

#### Things to know:

- Transform ops can be injected into the dialect.
- Must implement the transform interface.

#### Would like a transform op that:

- Takes a handle to scf. forall.
- Triggers rewriting into a nest of scf. for.
- Returns handles to produces ops.

```
def ForallToFor
   : Op<Transform_Dialect, "tutorial.forall_to_for",
        [DeclareOpInterfaceMethods<MemoryEffectsOpInterface>,
        DeclareOpInterfaceMethods<TransformOpInterface>]> {
```

#### Things to know:

- Transform ops can be injected into the dialect.
- Must implement the transform interface.
- Must describe side effects.

#### Would like a transform op that:

- Takes a handle to scf. forall.
- Triggers rewriting into a nest of scf. for.
- Returns handles to produces ops.

#### Things to know:

- Transform ops can be injected into the dialect.
- Must implement the transform interface.
- Must describe side effects.

Base type interface for handles.

#### Would like a transform op that:

- Takes a handle to scf. forall.
- Triggers rewriting into a nest of scf. for.
- Returns handles to produces ops.

#### Things to know:

- Transform ops can be injected into the dialect.
- Must implement the transform interface.
- Must describe side effects.

```
def ForallToFor
   : Op<Transform_Dialect, "tutorial.forall_to_for",
        [DeclareOpInterfaceMethods<MemoryEffectsOpInterface>,
        DeclareOpInterfaceMethods<TransformOpInterface>]> {
   let arguments = (ins
        TransformHandleTypeInterface:$target);
   let results = (outs
        Variadic<TransformHandleTypeInterface>:$transformed);
   // ...
}
```

.cc

```
DiagnosedSilenceableFailure
transform::ForallToFor::apply(
    transform::TransformResults &results.
    transform::TransformState &state) {
  return DiagnosedSilenceableFailure::success();
```

```
def ForallToFor
   : Op<Transform_Dialect, "tutorial.forall_to_for",
        [DeclareOpInterfaceMethods<MemoryEffectsOpInterface>,
        DeclareOpInterfaceMethods<TransformOpInterface>]> {
    let arguments = (ins
        TransformHandleTypeInterface:$target);
    let results = (outs
        Variadic<TransformHandleTypeInterface>:$transformed);
    // ...
}
```

#### Would like a transform op that:

- Takes a handle to scf. forall.
- Triggers rewriting into a nest of scf.for.
- Returns handles to produces ops.

#### Failure Modes

```
.CC
DiagnosedSilenceableFailure
transform::ForallToFor::apply(
    transform::TransformResults &results,
    transform::TransformState &state) {
  return DiagnosedSilenceableFailure::success();
```

#### Tri-state result object:

- Success: ~LogicalResult::success.
- Definite failure: the diagnostic has been reported to the engine, just propagating LogicalResult::failure.
- Silenceable failure: contains the not yet reported diagnostic. Can be reported to the engine, or silenced and discarded.

#### Arguments

```
DiagnosedSilenceableFailure
transform::ForallToFor::apply(
    transform::TransformResults &results,
    transform::TransformState &state) {
  return DiagnosedSilenceableFailure::success();
```

#### .cc Transform results:

 Populate this with payload IR objects to be associated with the result handles on success.

#### Transform state:

- Query this for the payload IR objects associated with operands and other values.
- Access to various extension points.

```
.cc
DiagnosedSilenceableFailure
transform::ForallToFor::apply(
    transform::TransformResults &results.
    transform::TransformState &state) {
  ArrayRef<Operation *> payload =
    state.getPayloadOps(getTarget());
  return DiagnosedSilenceableFailure::success();
```

```
def ForallToFor
   : Op<Transform_Dialect, "tutorial.forall_to_for",
        [DeclareOpInterfaceMethods<MemoryEffectsOpInterface>,
        DeclareOpInterfaceMethods<TransformOpInterface>]> {
    let arguments = (ins
        TransformHandleTypeInterface:$target);
    let results = (outs
        Variadic<TransformHandleTypeInterface>:$transformed);
    // ...
}
```

1. Get the payload ops associated with the operand.

.cc

```
DiagnosedSilenceableFailure
transform::ForallToFor::apply(
    transform::TransformResults &results.
    transform::TransformState &state) {
  ArrayRef<Operation *> payload =
    state.getPayloadOps(getTarget());
  if (payload.size() != 1) {
    return emitSilenceableError()
        << "expected a single payload op";</pre>
  return DiagnosedSilenceableFailure::success();
```

```
def ForallToFor
   : Op<Transform_Dialect, "tutorial.forall_to_for",
        [DeclareOpInterfaceMethods<MemoryEffectsOpInterface>,
        DeclareOpInterfaceMethods<TransformOpInterface>]> {
    let arguments = (ins
        TransformHandleTypeInterface:$target);
    let results = (outs
        Variadic<TransformHandleTypeInterface>:$transformed);
    // ...
}
```

- 1. Get the payload ops associated with the operand.
- 2. Check well-formedness and report errors.

.cc

```
DiagnosedSilenceableFailure
transform::ForallToFor::apply(
    transform::TransformResults &results.
    transform::TransformState &state) {
  ArrayRef<Operation *> payload =
    state.getPayloadOps(getTarget());
  if (payload.size() != 1) {
    return emitSilenceableError()
        << "expected a single payload op";</pre>
  auto target = dyn_cast<scf::ForallOp>(payload[0]);
  if (!target) {
    return emitSilenceableError()
        << "expected the payload to be scf.forall";</pre>
  return DiagnosedSilenceableFailure::success();
```

```
def ForallToFor
   : Op<Transform_Dialect, "tutorial.forall_to_for",
        [DeclareOpInterfaceMethods<MemoryEffectsOpInterface>,
        DeclareOpInterfaceMethods<TransformOpInterface>]> {
    let arguments = (ins
        TransformHandleTypeInterface:$target);
    let results = (outs
        Variadic<TransformHandleTypeInterface>:$transformed);
    // ...
}
```

- 1. Get the payload ops associated with the operand.
- 2. Check well-formedness and report errors.

CC

```
DiagnosedSilenceableFailure
transform::ForallToFor::apply(
    transform::TransformResults &results.
    transform::TransformState &state) {
  ArrayRef<Operation *> payload =
    state.getPayloadOps(getTarget());
  if (payload.size() != 1) {
    return emitSilenceableError()
        << "expected a single payload op";</pre>
       target = dyn_cast<sef::ForallOp>(payload)
  if (!target) {
    return-emitSilenceableError()
        <-- "expected the payload to be sef.forall";</pre>
  return DiagnosedSilenceableFailure::success();
```

Specific implementations of the Transform type interface can supply a runtime checks that are performed when payload is associated with the handle, and produce silenceable errors on mismatch

.cc

```
DiagnosedSilenceableFailure
transform::ForallToFor::apply(
    transform::TransformResults &results.
    transform::TransformState &state) {
  ArrayRef<Operation *> payload =
    state.getPayloadOps(getTarget());
  if (payload.size() != 1) {
    return emitSilenceableError()
        << "expected a single payload op";</pre>
  SmallVector<scf::ForOp> loops =
    doActualRewrite(cast<scf::ForallOp>(payload[0]));
  return DiagnosedSilenceableFailure::success();
```

```
def ForallToFor
   : Op<Transform_Dialect, "tutorial.forall_to_for",
        [DeclareOpInterfaceMethods<MemoryEffectsOpInterface>,
        DeclareOpInterfaceMethods<TransformOpInterface>]> {
    let arguments = (ins
        Transform_ConcreteOpType<"scf.forall">:$target);
    let results = (outs
        Variadic<TransformHandleTypeInterface>:$transformed);
    // ...
}
```

- 1. Get the payload ops associated with the operand.
- 2. Check well-formedness and report errors.
- 3. Do the actual rewrite.

```
.cc
DiagnosedSilenceableFailure
transform::ForallToFor::apply(
    transform::TransformResults &results,
    transform::TransformState &state) {
  ArrayRef<Operation *> payload =
    state.getPayloadOps(getTarget());
  if (payload.size() != 1) {
    return emitSilenceableError()
        << "expected a single payload op";</pre>
  SmallVector<scf::ForOp> loops =
    doActualRewrite(cast<scf::ForallOp>(payload[0]));
  for (auto &&[res, loop]
       : llvm::zip(getTransformed(), loops)) {
    results.set(cast<OpResult>(res), loop);
  return DiagnosedSilenceableFailure::success();
```

```
def ForallToFor
   : Op<Transform_Dialect, "tutorial.forall_to_for",
        [DeclareOpInterfaceMethods<MemoryEffectsOpInterface>,
        DeclareOpInterfaceMethods<TransformOpInterface>]> {
    let arguments = (ins
        Transform_ConcreteOpType<"scf.forall">:$target);
    let results = (outs
        Variadic<TransformHandleTypeInterface>:$transformed);
    // ...
}
```

- 1. Get the payload ops associated with the operand.
- 2. Check well-formedness and report errors.
- 3. Do the actual rewrite.
- 4. Associate result handles with results.

```
.CC
void transform::TakeAssumedBranchOp::getEffects(
    SmallVectorImpl<MemoryEffects::EffectInstance> &
   effects) {
```

```
def ForallToFor
   : Op<Transform_Dialect, "tutorial.forall_to_for",
        [DeclareOpInterfaceMethods<MemoryEffectsOpInterface>,
        DeclareOpInterfaceMethods<TransformOpInterface>]> {
    let arguments = (ins
        Transform_ConcreteOpType<"scf.forall">:$target);
    let results = (outs
        Variadic<TransformHandleTypeInterface>:$transformed);
    // ...
}
```

```
void transform::TakeAssumedBranchOp::getEffects(
    SmallVectorImpl<MemoryEffects::EffectInstance> &
    effects) {
    consumesHandle(getTarget(), effects);
}
```

```
def ForallToFor
   : Op<Transform_Dialect, "tutorial.forall_to_for",
        [DeclareOpInterfaceMethods<MemoryEffectsOpInterface>,
        DeclareOpInterfaceMethods<TransformOpInterface>]> {
    let arguments = (ins
                Transform_ConcreteOpType<"scf.forall">:$target);
    let results = (outs
                     Variadic<TransformHandleTypeInterface>:$transformed);
    // ...
}
```

 The target handle is consumed because the rewrite replaces the original payload op.

.cc

```
void transform::TakeAssumedBranchOp::getEffects(
    SmallVectorImpl<MemoryEffects::EffectInstance> &
    effects) {
  consumesHandle(getTarget(), effects);
  producesHandle(getTransformed(), effects);
```

```
def ForallToFor
   : Op<Transform_Dialect, "tutorial.forall_to_for",
        [DeclareOpInterfaceMethods<MemoryEffectsOpInterface>,
        DeclareOpInterfaceMethods<TransformOpInterface>]> {
    let arguments = (ins
            Transform_ConcreteOpType<"scf.forall">:$target);
    let results = (outs
            Variadic<TransformHandleTypeInterface>:$transformed);
    // ...
}
```

- 1. The target handle is consumed because the rewrite replaces the original payload op.
- 2. The result handles are produced.

```
void transform::TakeAssumedBranchOp::getEffects(
    SmallVectorImpl<MemoryEffects::EffectInstance> &
    effects) {
    consumesHandle(getTarget(), effects);
    producesHandle(getTransformed(), effects);
    modifiesPayload(effects);
}
```

```
def ForallToFor
   : Op<Transform_Dialect, "tutorial.forall_to_for",
        [DeclareOpInterfaceMethods<MemoryEffectsOpInterface>,
        DeclareOpInterfaceMethods<TransformOpInterface>]> {
    let arguments = (ins
            Transform_ConcreteOpType<"scf.forall">:$target);
    let results = (outs
            Variadic<TransformHandleTypeInterface>:$transformed);
    // ...
}
```

- 1. The target handle is consumed because the rewrite replaces the original payload op.
- 2. The result handles are produced.
- Also indicate that payload is modified to prevent reordering.

# Thank you!