NEX CA REA CH	(2 68 13 14 23 25 30)  EMOR  TMTG:  PUIZ LIKELY  TREEWALK?)  TO CH 1-3  I WHAT IS SW?  INTRO QUESTIONS.  WHO SO LONG DEV TIME?  WHO SO LONG DEV TIME?  - MY TO IT BITS  # USE IT TO CONTROL  EXECUTION PATHS	- Kender	EXITY FAIL	3. W 811 PG -:
10	Ni S	O REDUCE # DF	10 who	

- 2. WHY DEV COSTS HIGH?

   HARD TO PREDICT/
  ESTIMATE EFFORT

  DUE TO COMPLEXITY

  (2 UNFORESEEN BUGS)

   LABOR INTENSIVE

  DOST EFFORT IN
- 3. WHY ARE THERE

  BUGS IN COMPLETED

  PGIMS?

   # OF PATHWAY

  COMBINATIONS

  TOO BIG TO TEST,

  EVER.
- 4. WHY DO WE MAINTAIN EXISTING (AKA "LEGACY") PGIMS FOR SO LONG? (AKA "CASH COWS") 5. WHY IS IT HARD TO MEASURE DEV PROGRESS. - WBS HELPS A LOT. L>(80% OVERRUM) ) MOST METRICS ARE POOR \$ HAVE (HIDDEN) GIANT "ERROR BAR"

E) BIG 7 OF PROJECTS

FIND/FIX RT BUGS

3. WHY ARE THERE

BUGS IN COMPLETED

PGIMS?

- # OF PATHWAY

COMBINATIONS

TOO BIG TO TEST,

EVER.

4. WHY DO WE MAINTAIN
EXISTING (AKA "LEGACY")
PGIMS FOR SO LONG?

(AKA "CASH COWS")

5. WHY IS IT HARD TO
MEASURE DEV PROGREST?

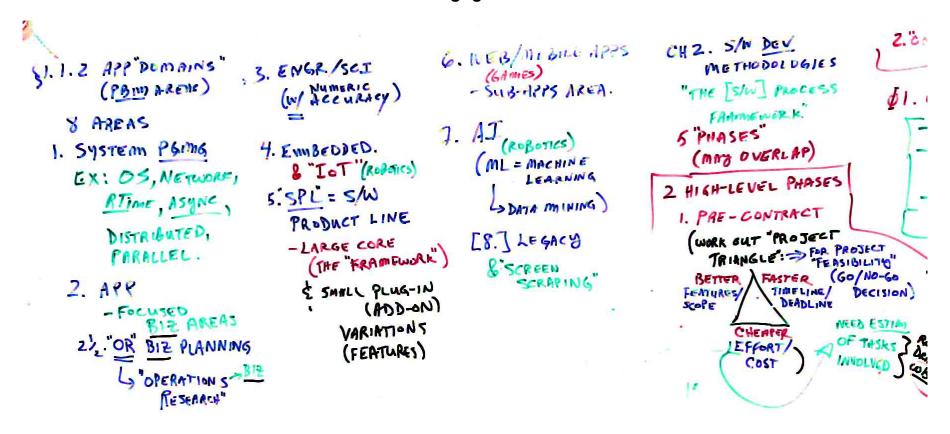
- WBS HELPS A LOT.

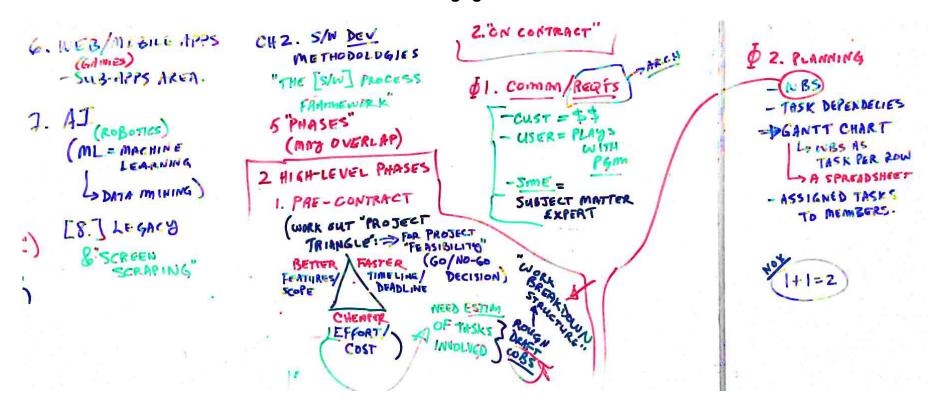
- "90-90" RULE.

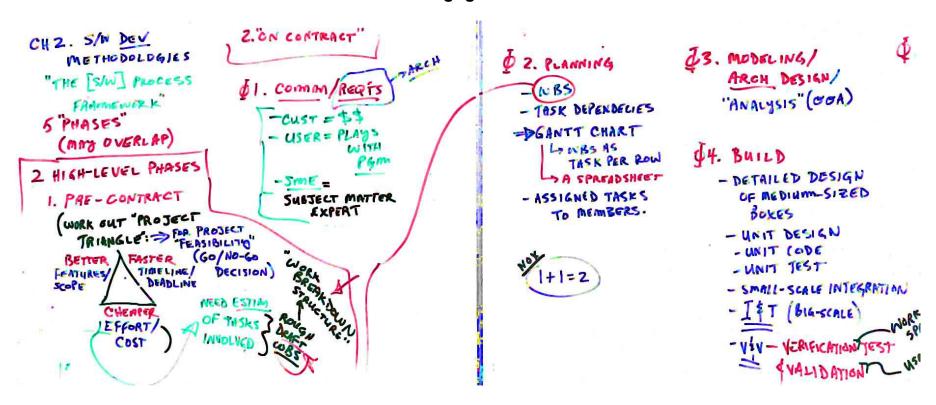
MOST METRICS ARE POOR HAVE (HIDNEN)
GIANT "ERROR BAR"

16.3 WHEN IS NEW-DEV EASY & LOW RISK? - USE SPECIALTY-AREA "FRAMEWORK" (OR "LIBRARY") WHERE YOUR "PEM" 13 REDUCED TO SIMPLE "BIZ LOGIC" 1. YOU WRITE 10-15% OF THE TOTAL CODE. 2. FRAMEWORK IS "PROVEN". 3, NEED YOUR PBM TO BE

ECTS







- CILBS

- TASK DEPENDECIES

- TASK DEPENDECIES

- DEPENDECIES

- TASK PER ROW

- ASSIGNED TASKS

TO MEMBERS.

HOY |+ | = 2

ARCH DESIGN/
"ANALYSIS" (COA)

4. Build

- DETAILED DESIGN
OF MEDIUM-SIZED
BOKES

- UNIT DESIGN

- UNIT CODE

- UNIT TEST

- SMALL- SCALE INTEGRATION

- I & T (BIG-SCALE)

VIV - YERIFICATION TEST

\$ 5, DEPLOY/SHIP

- POCKAGE

- INSTALL TEST

ACCEPTANCE TEST

- MANUALS

- TRAINING