

# Embedded Medical Devices

BME554L (Fall 2025)

## Table of contents

Personnel . . . . .	2
Instructor . . . . .	2
Teaching Assistants . . . . .	2
Course Times & Locations . . . . .	2
Lecture . . . . .	2
Labs . . . . .	2
Course Objectives . . . . .	3
Prerequisites . . . . .	3
Mandatory . . . . .	3
Learning Management System . . . . .	4
Class Schedule . . . . .	4
Learning Module Schedule . . . . .	4
Special Dates . . . . .	5
Attendance & Participation . . . . .	5
Assignments & Grading . . . . .	6
Grading . . . . .	6
Course Grade . . . . .	6
Regrades . . . . .	6
Late Policy / SDAO Accomodations . . . . .	6
Duke Community Standard . . . . .	7
FAQ . . . . .	7
Can I collaborate with other students? . . . . .	7
Can I use AI? . . . . .	7

## Personnel

### Instructor

Dr. Mark Palmeri (mlp6)

- Email: [mark.palmeri@duke.edu](mailto:mark.palmeri@duke.edu)
- Ed Discussion (private message, TAs included)
- Office Hours: TBD (Zoom / 258 Hudson Hall Annex)

### Teaching Assistants

- Ceci Schmidz (cls157)
- Evan Smith (jes114)
- Harvey Shi (hys3)

#### Tip

Questions that can be answered by Dr. Palmeri or a teaching assistant should be posted on Ed Discussion.

## Course Times & Locations

### Lecture

Monday & Wednesday, 08:30-09:45, Wilkinson 136 (Panopto recorded)

### Labs

Location: CIEMAS B209

There are no formally scheduled lab sections, but you will need to use the lab equipment to perform testing of your devices. Your TAs will announce times when they will hold lab hours.

#### Important

Please review the [lab policies](#) before using the lab for the first time this semester.

### Warning

No food or drink is allowed in the lab! Failure to adhere to this policy will have consequences on your lab grades.

## Course Objectives

This course will give students experience with the design, function and deployment of embedded medical devices. Students will have hands on experience with electronic hardware and firmware (software) development, along with gaining experience with biosignal transduction into circuits.

Upon completion of this course, students should be able to:

- Version control software / firmware development using `git`.
- Use an Integrated Development Environment (IDE) for firmware development.
- Describe hardware using a Devicetree hierarchical data structure.
- Develop firmware using Zephyr as a bare-metal super-loop and a Realtime Operating System (RTOS)
- Implement state machines and generate state diagrams using the Unified Modeling Language (UML).
- Utilize callbacks / interrupt service routines for realtime event detection and response.
- Utilize threads and work queues.
- Develop firmware to control common peripherals, including GPIO, ADC and PWM.
- Use different serial communication protocols, including UART, I2C, SPI, and BLE.
- Utilize firmware logging at different levels.
- Test firmware implementation on the `nRF52833DK` using electronic bench equipment and generate technical reports with data analysis for device verification.
- Develop firmware with workflows that adhere to relevant industry and safety standards (e.g., UL, IEC60601, IEC62304) for FDA 510k clearance.

## Prerequisites

### Mandatory

- EGR105L or equivalent experience [git, Python]
- Instrumentation (BME354L) / Mechatronics
- Signals & Systems (BME271/671 or equivalent experience [filtering, FFTs])

## Learning Management System

We will be using [Canvas](#) as the learning management system for this course. Most resources will be linked to the course website. All grades will be posted via Canvas/Gradescope.

Duke's [GitLab](#) server will be used for most course lab exercises, and code-related questions will be submitted to Dr. Palmeri / TAs using GitLab Issues.

Ed Discussion will be used for general course questions and discussion.

## Class Schedule

This class is organized in a sequence of modules. Specific details surrounding dates for assignments associated with each module will be posted to Gradescope and linked below.

This course uses a version of [Mastery Learning](#), where “mastery” of a given module is necessary to progress onto the subsequent module. Quizzes are used to evaluate “knowledge”; lab exercises are used to demonstrate application of skills. In this course, assignments of later modules depends on the successful completion of earlier modules.

## Learning Module Schedule

Module	Materials	Assessment
Zephyr & Nordic SoC Overview	<a href="#">Lecture Notes &amp; Slides</a>	<a href="#">Completion Survey</a> (Due: TBD) <a href="#">Software Installs &amp; Tutorials</a> (Due: TBD)
Event-Driven State Machines	<a href="#">Lecture Notes &amp; Slides</a>	<a href="#">Wireless HRM Event-Driven State Machine Lab</a>
Version Control ( <a href="#">git</a> )	<a href="#">Lecture Notes &amp; Slides</a>	<a href="#">Git Fundamentals Quiz</a> <a href="#">Git Fundamentals Lab</a>
C Programming	<a href="#">Lecture Notes &amp; Slides</a>	<a href="#">C Programming Quiz</a> <a href="#">C Programming Lab</a> <a href="#">C Programming Lab</a>
Devicetree, GPIO & Callbacks	<a href="#">Lecture Notes &amp; Slides</a> <a href="#">Nordic DevAcademy Lesson 2: Reading Buttons &amp; Controlling LEDs</a>	<a href="#">DT/GPIO/CB Lab</a>
Timers & Work Queues	<a href="#">Lecture Notes &amp; Slides</a>	<a href="#">Timers Lab</a>
Threads & Kernel Events	<a href="#">Lecture Notes &amp; Slides</a> <a href="#">Zephyr RTOS: Beyond the basics</a>	<a href="#">Heartbeat &amp; Kernel Events</a> <a href="#">Refactor Lab</a>

Module	Materials	Assessment
State Machine Framework	<a href="#">Lecture Notes &amp; Slides</a>	<a href="#">State Machine Framework Refactor Lab</a>
Debugging	<a href="#">Debugging DevAcademy Module</a>	None
Analog-to-Digital Conversion (ADC)	<a href="#">Lecture Notes &amp; Slides</a> <a href="#">Nordic DevAcademy: ADC</a>	<a href="#">ADC Lab</a>
Pulse Width Modulation (PWM)	<a href="#">Lecture Notes &amp; Slides</a> <a href="#">Nordic DevAcademy: PWM</a>	<a href="#">PWM Lab</a>
Serial Communications	<a href="#">Lecture Notes &amp; Slides</a> <a href="#">UART DevAcademy Module</a> <a href="#">Serial Communication DevAcademy Module</a>	None
Bluetooth Low Energy (BLE)	<a href="#">Lecture Notes &amp; Slides</a> <a href="#">DevAcademic: BLE (Lessons 1-4)</a>	<a href="#">ECG &amp; Temperature Sensing BLE Device</a>

### Special Dates

- Labor Day (Sep 01, 2025)
- Fall Break (Oct 11-14, 2025)
- LDOC (Nov 25, 2025)

### Attendance & Participation

Class participation in lecture and utilize lab time with your TAs is strongly encouraged. Lecture will be used to provide skill overview and live demonstrations, many of which will kickstart your efforts for your project. Lab time will provide you access to equipment and the TAs for assistance.

Students are responsible for obtaining missed lecture content from other students in the class. All lecture slides/presented content will be made available online (Canvas/Gitlab), and lectures will be recorded via Panopto and posted to Canvas.

Participation on Ed Discussion is also encouraged, in the form of:

- Asking questions about the course material (ideally, publicly, so that others can benefit (Anonymous okay))
- Answering questions from other students
- Sharing interesting articles or resources related to the course material

## Assignments & Grading

### Grading

Fundamental knowledge will be assessed with online quizzes (~30%).

Lab exercises will focus on implementing key functionality towards the final working device (a wireless ECG and temperature sensor). Each lab exercise will be evaluated with code reviews and technical reports that assess functionality (~50%).

Testing of the final working device will be presented in a final technical report (~20%).

All assignment grades will be posted to Gradescope (and linked to the Canvas gradebook) throughout the semester to track your performance.

### Course Grade

This course is not “curved” (i.e., a distribution of grades will not be enforced), and a traditional grading scheme will be used (e.g., 90-93 = A-, 94-97 = A, 97-100 = A+). Participation throughout the semester will influence rounding up/down for fractional grades.

Failing the course can happen with a cumulative score  $< 70$  (C-).

### Regrades

Any regrading requests need to be made **within one week of grades for a given assignment being released**. You must make the request via Gradescope and provide a description of why you feel a regrade is appropriate. Requesting a regrade could lead to additional loss of credit when an assignment is re-evaluated.

Some assignments will have an opportunity to be resubmitted based on grading feedback at the discretion of Dr. Palmeri.

### Late Policy / SDAO Accommodations

Late submission windows will be available for all assignments, minus the final project, and should be used to accommodate acute illness, travel, high workload from other classes and other unforeseen circumstances. This late submission window can be utilized without penalty and without prior approval.

Students with SDAO accommodations for extended time on assignments can use this extended late submission window for all assignments.

Any assignments submitted after the late submission window will only be accepted for partial credit at the discretion of Dr. Palmeri or if prior approval was sought **before the original due date**.

## Duke Community Standard

All students are expected to adhere to all principles of the [Duke Community Standard](#). Violations of the Duke Community Standard will be referred immediately to the Office of Student Conduct. Please do not hesitate to talk with Dr. Palmeri about any situations involving academic honor, especially if it is ambiguous what should be done.

## FAQ

### Can I collaborate with other students?

Engineering is inherently a collaborative field, and in this class, you are encouraged to work collaboratively on your projects. That being said, all of the work that you submit must be generated by you and reflect your understanding of the material.

#### Important

All resources used in your projects that were developed by another person or company must be properly acknowledged using comments in your code and lab reports.

### Can I use AI?

The use of artificial intelligence is a rapidly developing resource / tool in engineering. In software development, there are many levels of AI-assistance available. Such form of assistance include the [IntelliCode](#) tools and [GitHub CoPilot](#) (free to students through the [GitHub Education](#) program). These tools can be leveraged to help with syntax.

#### Caution

You are strongly cautioned to not rely on these tools for logical implementation.