

CSI - 3105 Design & Analysis of Algorithms

Course 19

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Fall 2019

Theorem

The relation \leq_P is transitive:

$$L \leq_P L' \quad \text{and} \quad L' \leq_P L'' \quad \implies \quad L \leq_P L''$$

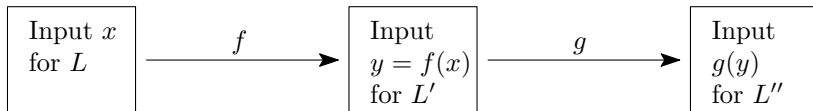
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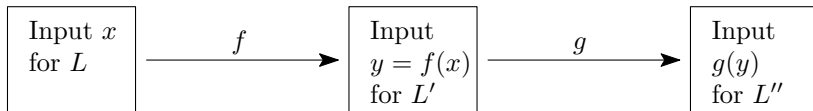


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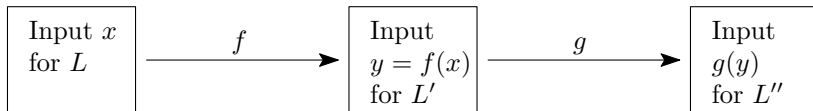
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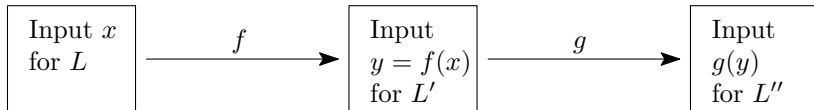
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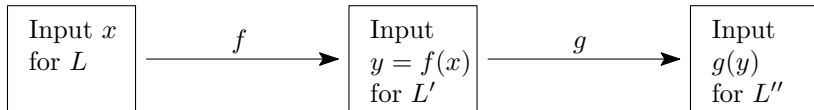
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The reduction from L to L'' is given by the function $g \circ f$. Given x , $(g \circ f)(x) = g(f(x))$ can be computed in time that is polynomial in the length of x (do you see why?)

§6.4 NP -Hard and NP -Complete

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The language L is NP -Complete if

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Intuitively, this means that L belongs to the most difficult problems in NP .

This is what we were looking for in §6.2.

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It is not even clear whether such a problem exists!!!

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We will show that CIRCUIT-SAT is NP -Complete.

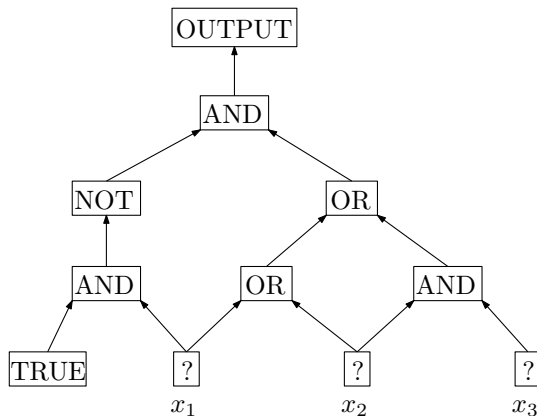
CIRCUIT-SAT

input: A Boolean circuit.

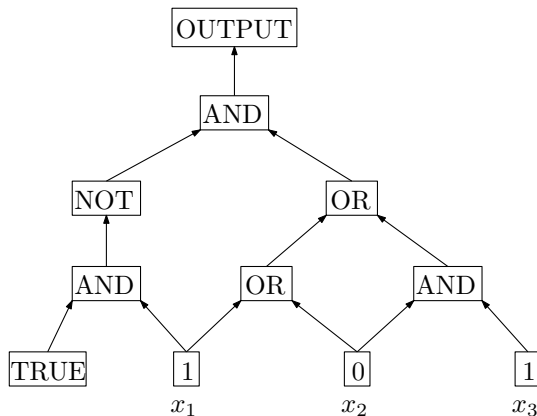
- Directed acyclic graph, where vertices are gates
- AND-gates and OR-gates have indegree 2
- Known input gates have indegree 0 and are labeled TRUE or FALSE.
- Unknown input gates have indegree 0 and are labeled “?”.
- There is one output gate (whose outdegree is 0).

question: Is it possible to assign a truth-value to each unknown input gate, such that the output of the circuit is TRUE?

CIRCUIT-SAT

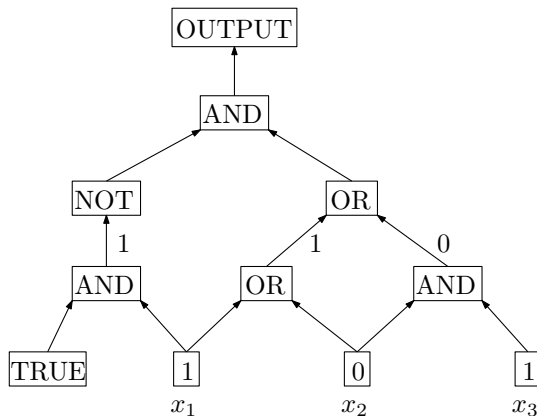


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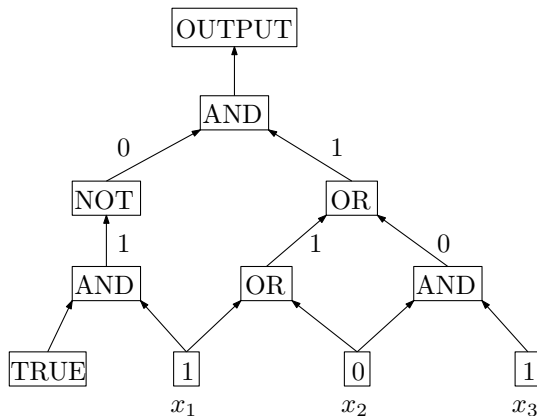
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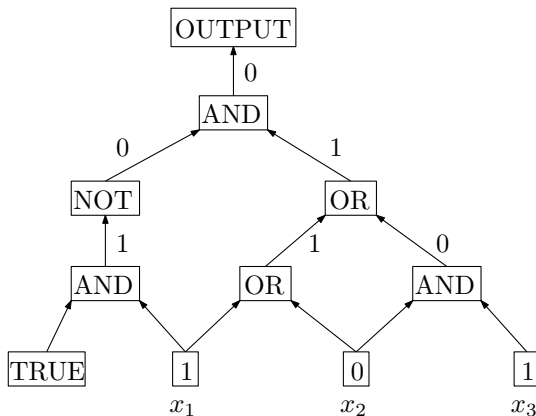
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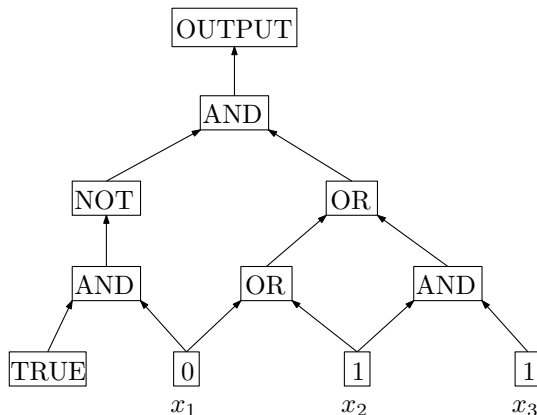
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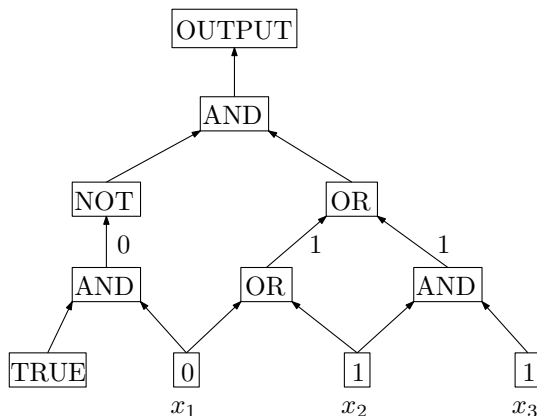
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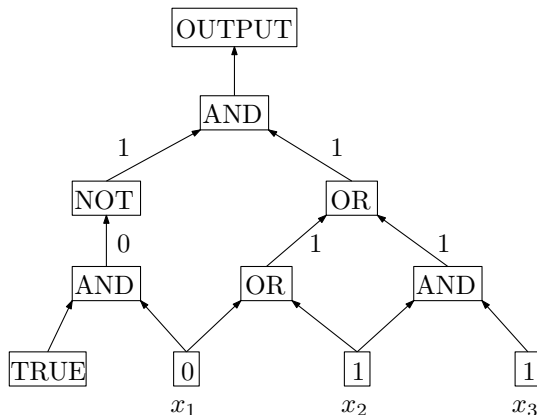
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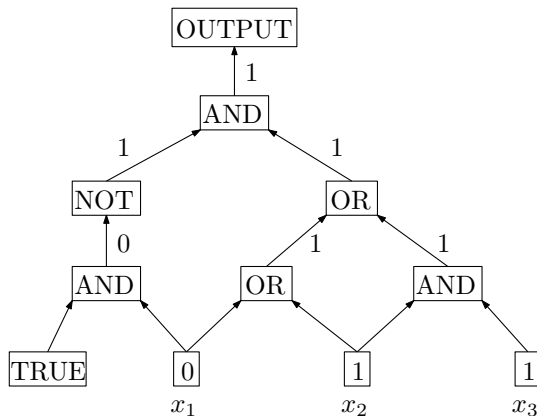
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- ③ The time to compute B is polynomial in the length of x .

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- The input to V is (x, y) , where x is an input for L and y is a certificate.
- For every input x to L ,

$x \in L \iff$ there exists a certificate y such that

- $|y| \leq |x|^c$,
- $V(x, y)$ returns YES
- and the running time of $V(x, y)$ is at most $|x|^{c'}$.

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Observe:

- Running time of Algorithm V_x is at most $|x|^{c'}$.
-

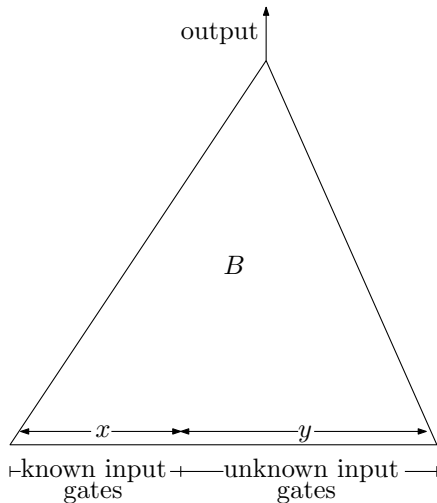
$$x \in L \iff \text{there exists an input } y \text{ for Algorithm } V_x$$

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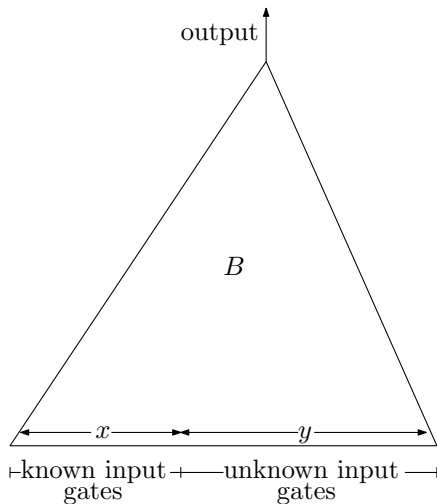
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Conclusion: CIRCUIT-SAT is NP-COMPLETE!

Remember the following theorem (refer to Course 20).

Theorem

$$\left. \begin{array}{l} L \text{ is } NP\text{-Complete} \\ L \leq_P L' \\ L' \in NP \end{array} \right\} \implies L' \text{ is } NP\text{-Complete}$$

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- ① $3SAT$ is in NP .
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- ① 3SAT is in NP .
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The first item is easy: for a given truth-assignment of the variables, we can verify in polynomial time if the Boolean formula is true.

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- 1 f transforms any input (Boolean circuit) B for $CIRCUIT - SAT$ and produces an input $\phi = f(B)$ (Boolean formula) for $3SAT$.

2

There exist truth-values for the unknown input gates such that B 's output is true



There exist truth-values for the variables such that ϕ is true

- 3 $\phi = f(B)$ can be computed in time that is polynomial in the size of B .