Minahil Raza **Electrical Engineer**

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♀ Lahore, Pakistan



Passionate Computer Vision Researcher with strong technical knowledge, programming, and design skills who always looks forward to taking initiatives and trying out new things. Interested in Embedded Systems and Computer Vision. Equipped with strong leadership and interpersonal skills.



EDUCATION

Present September 2016

Bachelor of Electrical Engineering

National University of Sciences and Technology (NUST), Pakistan

- > CGPA: 3.98 / 4.00
- > Received "Academic Excellence Award", ranked 3rd in the batch
- > Relevant course work and projects in Programming (C++, MATLAB and Assembly Language), Microcontrollers, Internet of Things, Computer Vision, Embedded Systems and Digital Logic Design

May 2019 January 2019

Exchange Semester on Global Undergraduate Exchange Program

Western Carolina University (WCU), Cullowhee, NC, USA

- > CGPA: 4.00 / 4.00
- > Admitted to "Chancellor's and Dean's List"
- > Received full tuition waiver and stipend from United States Government's Bureau of Educational and Cultural Affairs under UGRAD program
- > Relevant course work and projects in Javascript and Entrpreneurship

May 2016 September 2014

Intermediate (Pre-Engineering)

Kinnaird College for Women University (KCWU), Pakistan

- > Marks: 1041 / 1100
- > Awarded Silver Medal for securing 2nd position among Pre-Engineering Girls Group in Lahore Board.



M Work Experience

Present June 2018

Deep Learning Research Intern

TUKL NUST RnD Lab for Artificial Intelligence and Machine Learning, Pakistan

Key Responsibilities

- > Implemented popular Deep Learning topologies for Computer Vision on Embedded Platforms using Xilinx's libraries
- > Successfully trained and implemented Tiny YOLO for Real-Time Object Detection on Xilinx's ZC706 Evaluation board.
- > Worked as an immediate supervisor to 3 interns for "ML on Chip" project since June 2019

Skills Acquired and Honed: Machine Learning in Python, FPGA development in C++, Pytorch, Tensorflow, Keras, BNN-PYNQ

August 2019

Deep Learning Research Intern

June 2019 Micro-Electronics Design Research Group at TU Kaiserslautern, Germany

> Summer internship funded by DAAD (German Academic Exchange Service) under German-Pakistani Research Collaboration Program

Key Responsibilities

- > Trained Deep Learning Networks with Brevitas and transformed them for Winograd Convolutions.
- > Designed IP cores for Fast Convolution Algorithms using Vivado HLS.

Skills Acquired and Honed: Brevitas (framework for Quantized Training), High Level Synthesis in C++, Vivado HLS Design Suite



Technical skills Embedded Platforms and Microprocessors- ARM, Arduino and FPGAs (Intel and Xilinx)

> Hardware Design using Verilog and High Level Synthesis in C++ Signal Processing and Control Systems in MATLAB and Simulink

Circuit Design in PSPICE and LTSPICE

Computer Vision and AI in Python and Javascript

Organizational skills Coaching skills acquired from volunteering as an instructor at Beaconsfields High School

> Analytical Leadership skills acquired by participating in leadership workshops by PEEF and IREX Collaboration and Teamwork gained by working in teams at student organizations and for course pro-

jects

Adaptability gained by working in multicultural environments

Programming Languages

C, C++ and Embedded C Python and related frameworks for AI Javascript and is frameworks for AI **MATLAB**



OS Preference

Linux Windows





AUTONOMOUS LANDING OF FIXED WING UAV ON RUNWAYS USING COMPUTER VISION TECHNIQUES - Undergraduate Thesis

I worked on the training an Object Detection deep learning algorithm on a dataset comprising of runway images. The algorithm was binarized to improve throughput and power efficiency so that it could be deployed on reconfigurable logic. A real-time performance of 13 FPS was achieved.

WINOGRAD-BASED HARDWARE ACCELERATOR FOR CNNs - Microelectronic Design Research Group, TUKL, Germany

I worked on hardware-aware quantized training of Convolution Neural Networks and transformed them according to Winograd Algorithm to deploy them on FPGAs. I also wrote Python scripts for writing flexible C++ header files for high-level synthesis in Vivado

DATASET FOR INTERACTION DETECTION USING CONVOLUTIONAL NEURAL NETWORKS - Computer Vision

I worked on the collection of a dataset for an Anti-Theft Algorithm which uses a video feed to detect if someone interacts with an object. My group identified the weaknesses of the algorithm and improved it by extending the data-set and modifying the hyper-parameters. Moreover, we collected a dataset with images of Development Kits. Accuracy of over 95% was achieved in both cases.

DATASET FOR IMAGE SEGMENTATION - In collaboration with Dr. Naeem-Ullah Khan from Torr Vision Group at University of Oxford I am working on the development of a segmentation benchmark by collecting images with complex texture. Moreover, a set of metrics for performance and an evaluation server will also be developed.

WILDET - AN IOT BASED REAL-TIME WILDFIRE DETECTION SYSTEM - Embedded System Design

I worked on mbedOS for deploying a Real Time Operating System (RTOS) on ARM-Cortex M4 to detect wildfires and communicate the fire alerts over long distance through LoRaWAN protocol. ARM acts as the mother node while Dragino shields are used as daughter nodes for the Wireless Sensing Network.

MARVIN- THE CLEANING ROBOT - Microprocessor Systems

I design an automatic cleaning robot using an 8051 microcontroller and infrared sensors. The robot was able to clean the room while avoiding obstacles in its path. The code was written in assembly language.