Sunday, October 22, 2017 1:27 PM

include 'mbed.h'

Disital Out Led Pin (PTD2);

int main()

Led Pin = 1;

while (1) {

Led Pin = 0;

wait (0.5);

Led Pin = 1;

KEIL

Lactivite Clak

2-Define CPIO

Jefine +

3-Output

4-output o, 1

5-Wait, Jelay

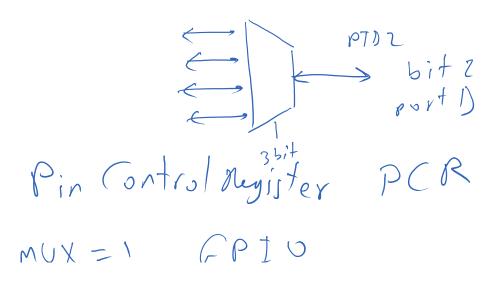
program

SCGCR5 bit 12 port D

12th bit

It define SIM_SCGCR5_PORTD_MASK +2

SIM-> SCG(S5 | - SIM_SCG(R5_pORTD_MASK;



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TRQC no interrupt on vising edge interrupt on vising edge " falling " level 1 32 + 1 + 1 + 1 | TSFR
Interrupt Flatus flag Aggister

PURTD -> PCR[LedPin] 1= PURT-PCR_MUX(1)

include "MKLZTZ4.h"

define LedPin 2

/* ende (lock e/
SIM-> SCGC5 |= SIM-SCG(5, PORTI)_MASK,

LO Select GBIO # /

PORTD-> PCR (LedPin) I=PORT_PCR_MUX(I);

/* LedPin output-p/

PTD-PDDR |= MASK (LedPin);

/* Light LED-/

PTD-> PDOR = MASK (LedPin);

while (I) {

delay (cycles);

PTD-PDOR = MASK(ledlin);

Lley (cycles);

PTD-RDOR = MASK(Ledlin);

While (cycles--);