

RZ/A1 Microsoft Azure SDK

Smart Pin Configurator

EU_00xxx Rev.1.00 May 5, 2021

Introduction

This describes how to use the RZA1 Smart Configurator to generate pin configurations.

Target Device

RZ/A1

Software Requirements

The SDK contains python scripts to generate the code. Below are the required software to run the script.

- Python v3.6
- RZA1Smart Configurator (NOTE: This is not the same as the RZA2M Smart Configurator)

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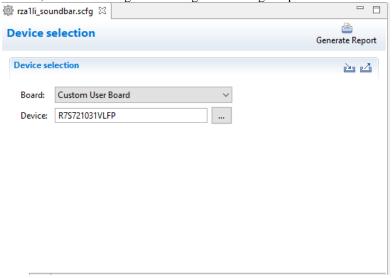
1. Install Smart Configurator

To download RZA1 Smart Configurator go to <u>Renesas</u> in the search bar search for Smart Configurator. Select the Smart Configurator.



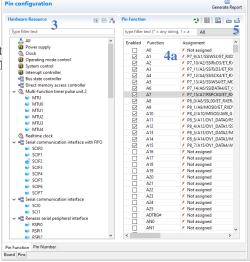
2. Usage

- Open one of the existing Smart Configurations. For the RZA1 M or H select the Display-it configuration.
 For RZA1 LU, LC, or L select the one of the Stream-it configurations. Because the Stream-it PCB maps
 the LCD and ethernet to the same pints, two configurations for the Stream-it are provided. One for LCD
 display and one for the Ethernet.
 - rza1h_Displayit.scfg
 - rza1lu_Streamit.scfg
 - rza1lu_Streamit_Ethernet.scfg
- 2. The default view is the board view. Select the Device. Once the Device is selected pin setting may be deleted, so do not change this setting after setting the pins.



- 3. Open the Pin View
- 4. Select the peripheral from the Hardware Resource panel.
 - a. In the Pin Function panel check the pin.
 - b. In the Assignment pane select the port pin from t conflicted with another pin select. Once the conflicted with another pin select.





Please select one of the following options:

1 - RZA1H Displayit (RSK)
2 - RZA1LU Streamit
3 - RZA1LU Streamit Ethernet (no Display)

5. Generate code.

- a. In the Pin View -> Pin Function View press the export pin assignment button 2
- b. Select one of the following files.
- Execute the windows batch file SC_Code_Generator.bat.

Your choice: _ d. Select the board. The Port Pin Code is Generated here $rz_bsp\src\renesas\drivers\r_gpio\inc\. For RZA1H or M the \r_rza1h_port_sc_cfg.h is generated$ all others are r_rza1l_port_sc_cfg.h.

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Revision Record

| | | Description | | |
|---------------------------------------|--------------|-------------|----------------------|--|
| Rev. | Date | Page | Summary | |
| 1.00 | Sep 28, 2017 | _ | First edition issued | |
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