



TECNICAS DIGITALES III



Facultad Regional Córdoba

LA INSTRUMENTACIÓN VIRTUAL

Agenda

- -¿De que hablamos cuando hablamos de instrumentación virtual?.
- -¿LabView como software de desarrollo para instrumentos virtuales.
- Librerías PyVisa para desarrollo de instrumentos virtuales.

Instrumento

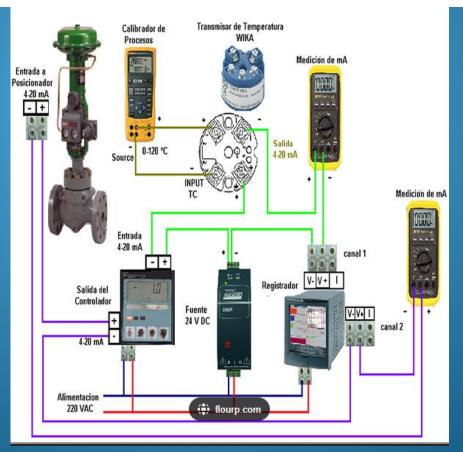
Un instrumento de medición es aquel que permite medir la longitud, volumen, extensión o capacidad por comparación de un elemento estandarizado el cual es tomado como referencia para posteriormente asignarle un valor numérico mediante algún instrumento graduado con dicha unidad.





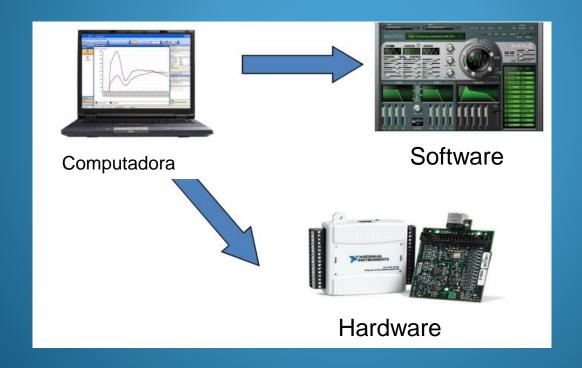
Instrumentación

Instrumentación es el proceso en el que el conjunto de varios instrumentos eléctricos/electrónicos, de medición y control interconectados para medir, analizar y controlar las magnitudes físicas eléctricas y no eléctricas.



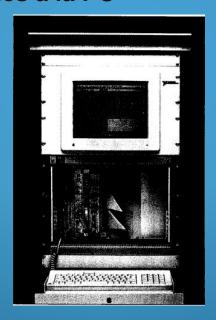
Paradigma de la instrumentación virtual

Un instrumento virtual es aquel cuya funcionalidad es determinada por software.



Historia de los instrumentos virtuales

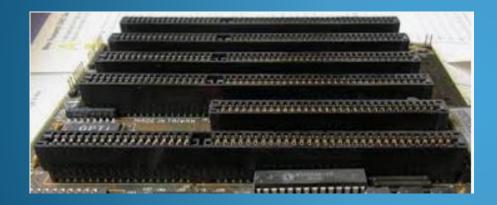
El concepto de instrumentos virtuales nace a fines de 1970, cuando la tecnología de los microprocesadores habilita a que la funcionalidad de una maquina sea fácilmente modificable por software. Hasta principios de los 80 consistían en conectar instrumentos tradicionales a la PC

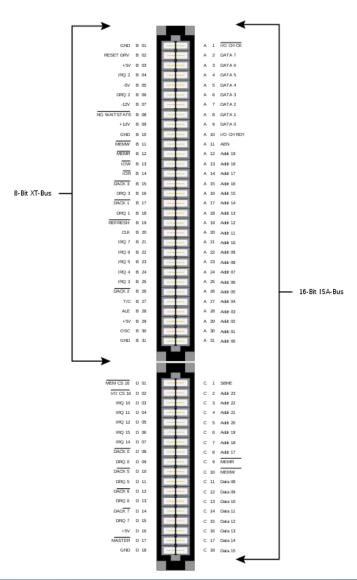


M. Santori, "An instrument that isn't really", IEEE Spectrum 27(8), 36 - 39 (1990).

Primeros instrumentos e interfaz paralelo

BUS ISA (Industry Standard Architecture)

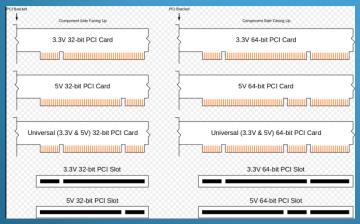




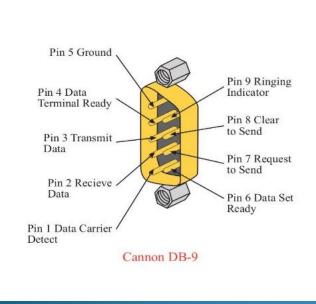
Interfaz paralelo

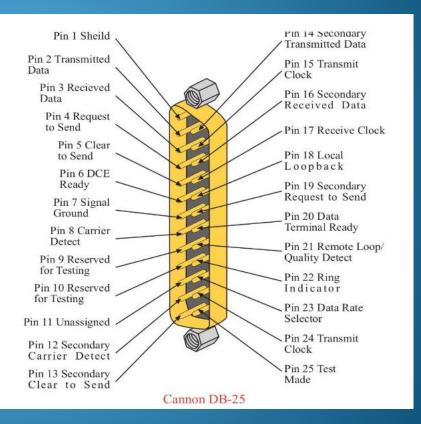
BUS PCI (Peripheral Component Interconnect)

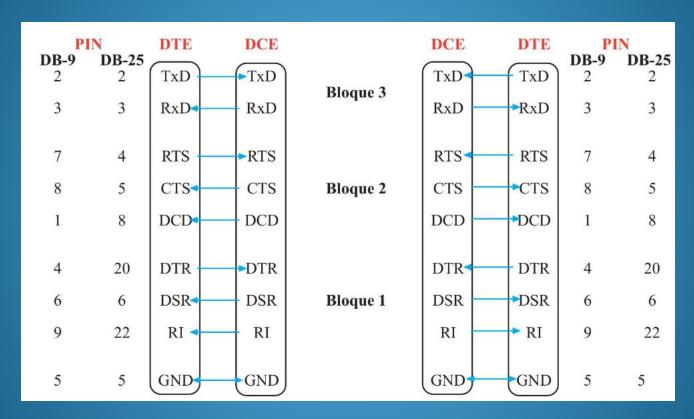


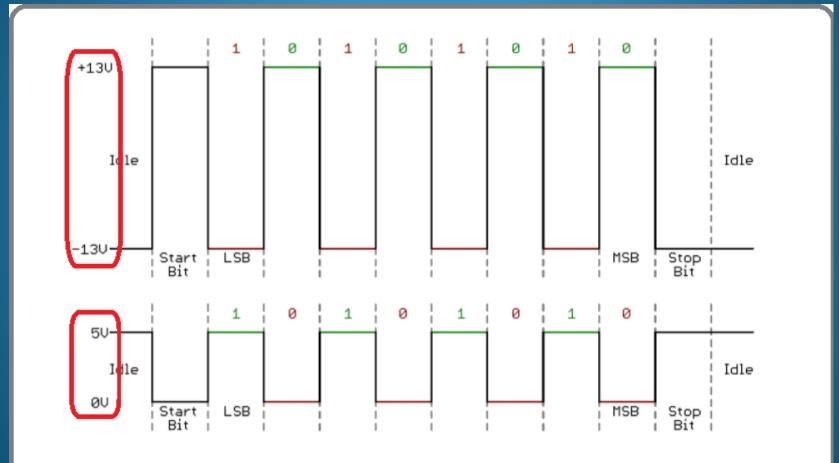


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	867	A67 -	Racity 64-bit
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+V I/O - Address SP - Address S7 -	871	All -	Address 58
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Ground -	876	ATE -	Address 52
Address St -		ATT -	Address SR Ground
+V E/D [-	879	ATP -	Address 48
Address 67 -		A88 -	Address 46
Address 45 - Ground -	881 882	A81 -	Address 44
Address 48 -	882	ARR -	Address 42
Address 41 -	885	A84	+V 1/0
Ground -	885 886	A85 -	Address de Address de
Address 29 -	887	A82 -	Ground
+V E/D -	888	A88 -	Address 86
Address its -		A89 -	
Address XX - Ground -		A91 -	Address k2
Reserved -		A92 -	Reserved
Received - Ground -	892	A93 -	Ground
would -	892 893	A94 -	Reserved









This timing diagram shows both a TTL (bottom) and RS-232 signal sending 0b01010101

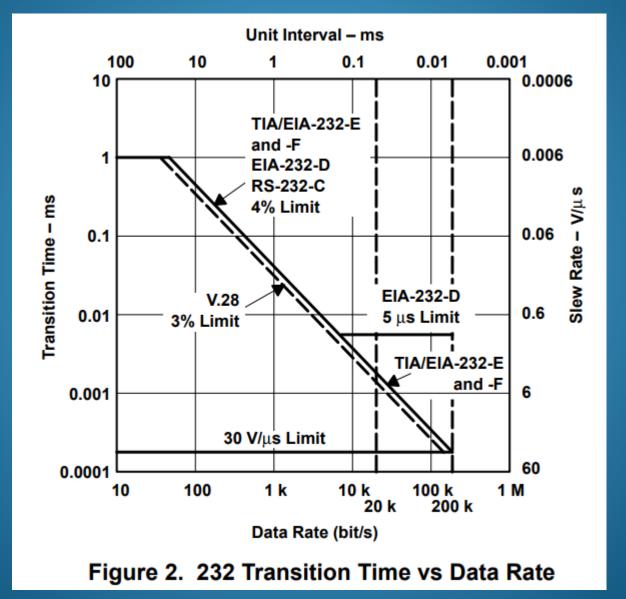
Nota de aplicación RS232



Interface Circuits for TIA/EIA-232-F

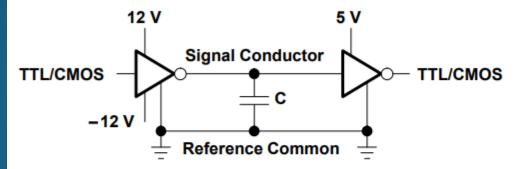
Design Notes

RS232- Detalles de la señal



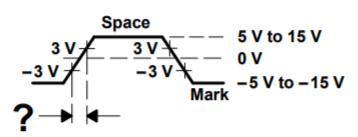
Especificaciones electrica

Single Interface Line (1 of 25 Maximum)



- Receiver Input Impedance, 3 kΩ to 7 kΩ
- Driver Power-Off Impedance, > 300 Ω
- Load Capacitance < 2500 pF Includes Receiver Input

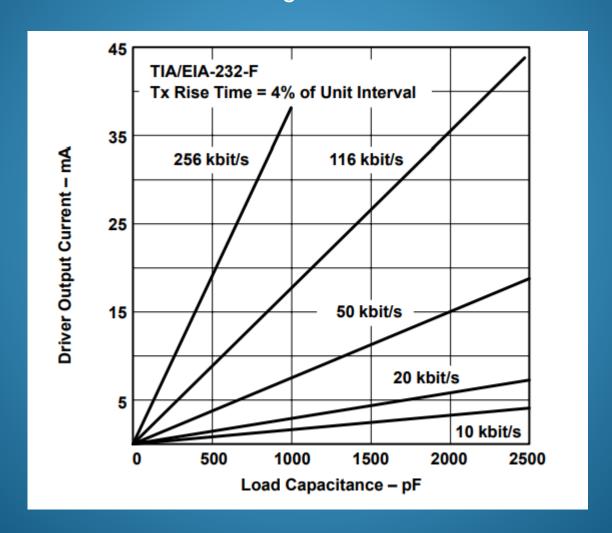
Interchange Signal



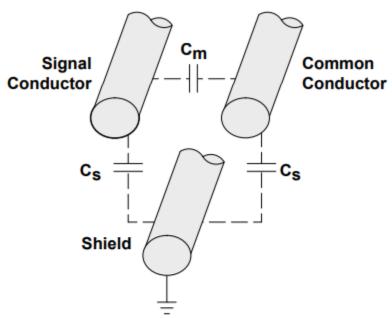
- Tx Rise/Fall Time Within Transition Region
 1 ms Below 40 bit/s
 - 4% of Unit Interval 30 bit/s to 20 kbit/s
- Slew Rate: 30 V/μs max

Figure 3. TIA/EIA-232-F Electrical Specification

RS232 – Relación entre la capacidad distribuida del cable y la longitud



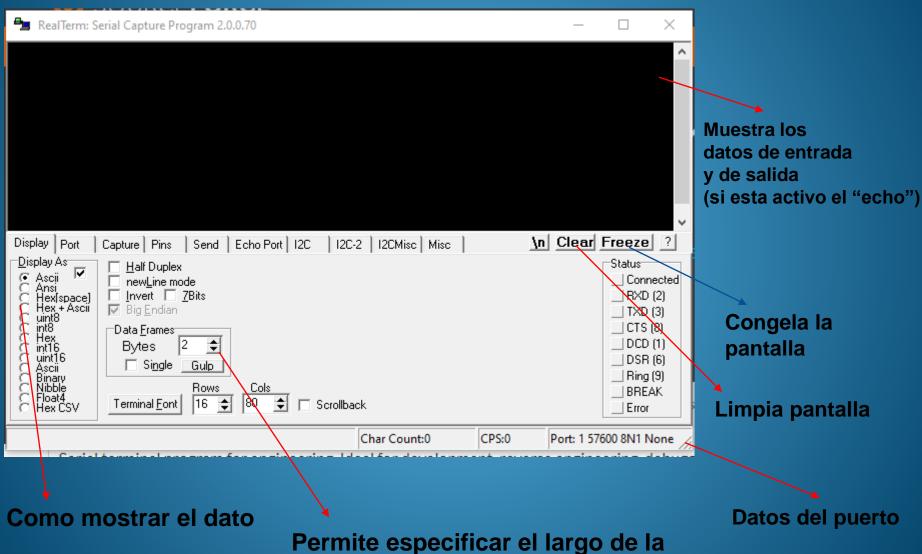
RS232 – Calculo de la distancia máxima de comunicaciones



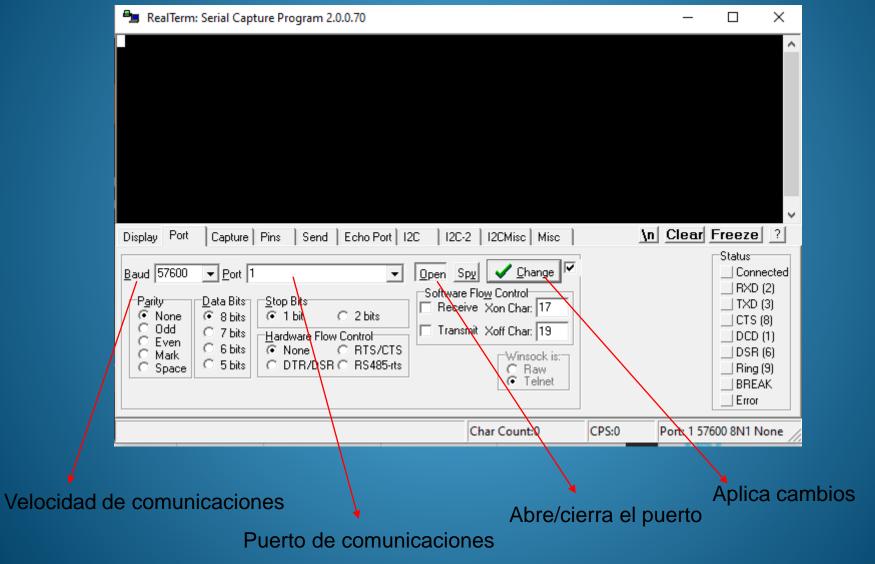
Data-Rate Calculation					
Unit Interval = 1/3	0.04				
one more 170	$C_C \ln \left(\frac{I_O + 1}{I_O - 1} \right)$				
IO = Short-Circuit Cu	irrent of Driver				

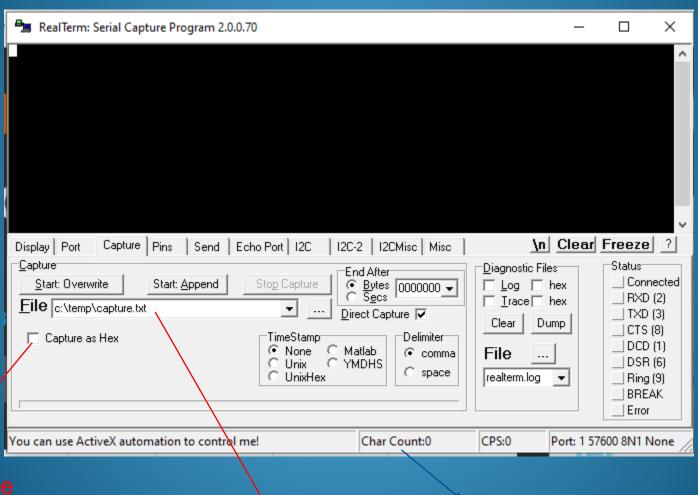
Line-Length Calcula	Line-Length Calculation							
Maximum Capacitance	= 2500 pF							
Receiver I/P Capacitance	< 20 pF							
Maximum Line Capacitance	= 2480 pF							
Total Line Capacitance/m Mutual Capacitance of Cable/m Stray Capacitance/m	$C_C = C_M + C_S$ $C_M \approx 100 \text{ pF}$ $C_S \approx 200 \text{ pF}$							
Maximum Line Length Standard Cable C _M Maximum Line Length Shielded	= 2480/C _C = 24 pF/m = 10 Meters							

Figure 5. Calculating Line Length and Data Rate



trama de datos

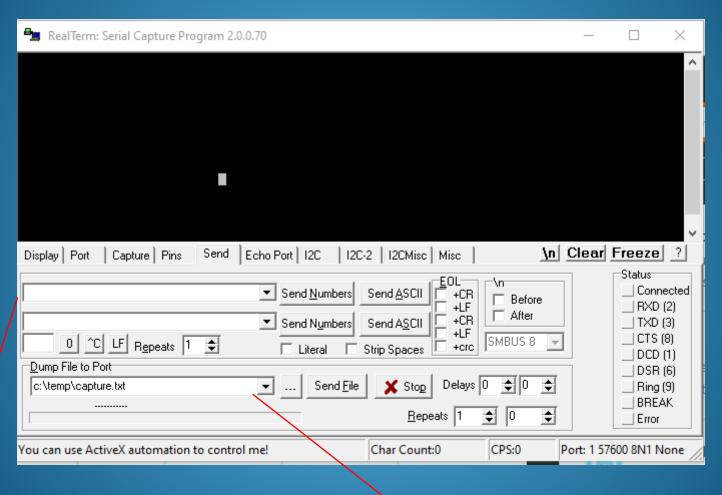




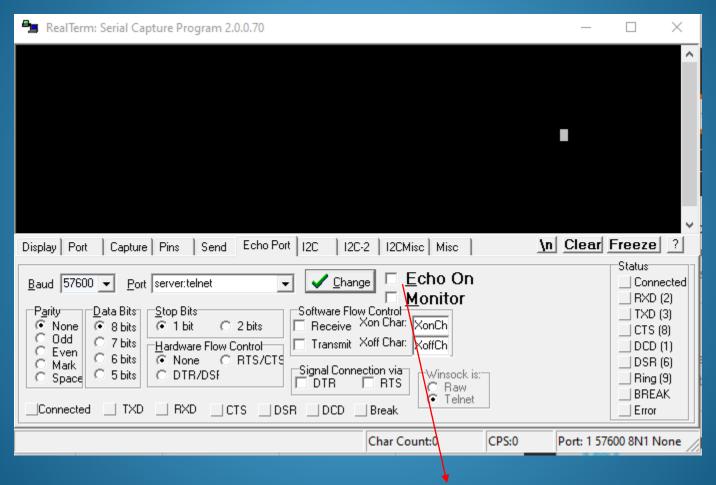
Forma de almacenamient o, hexadecimal o binario

Caracteres adquiridos

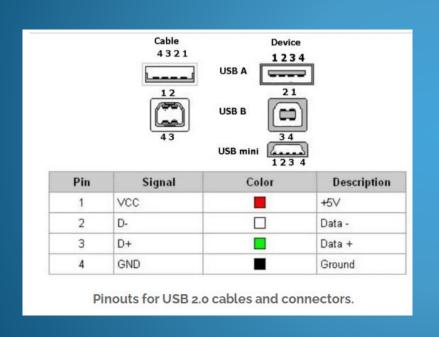
Ubicación y nombre de archivo



Permite enviar una cadena de datos, puede enviarse en ASII o en Permite enviar un archivo completo hexadecimal



Permite ver lo que se envía por el terminal



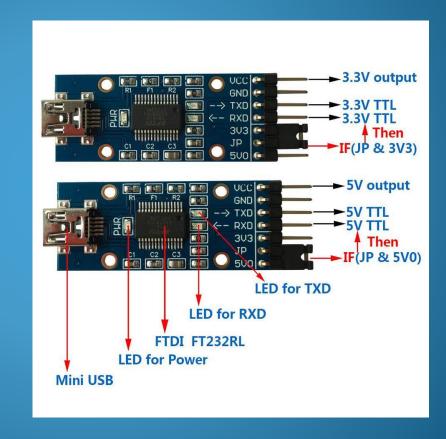
USB (Universal Serial Bus)



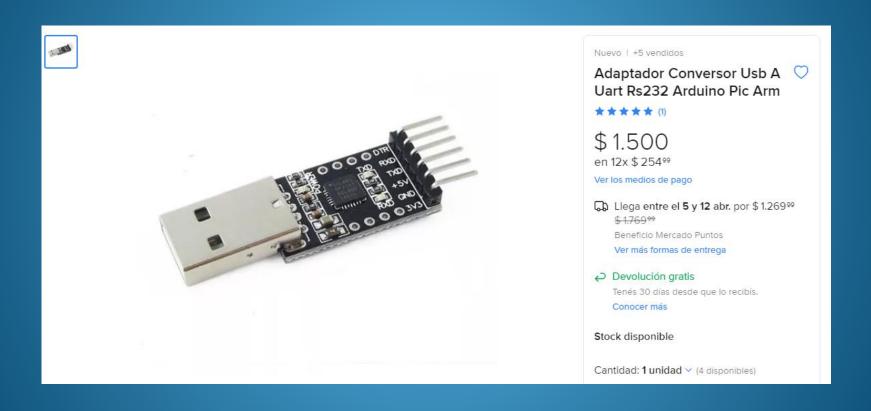


Interfaces serie RS232 -USB

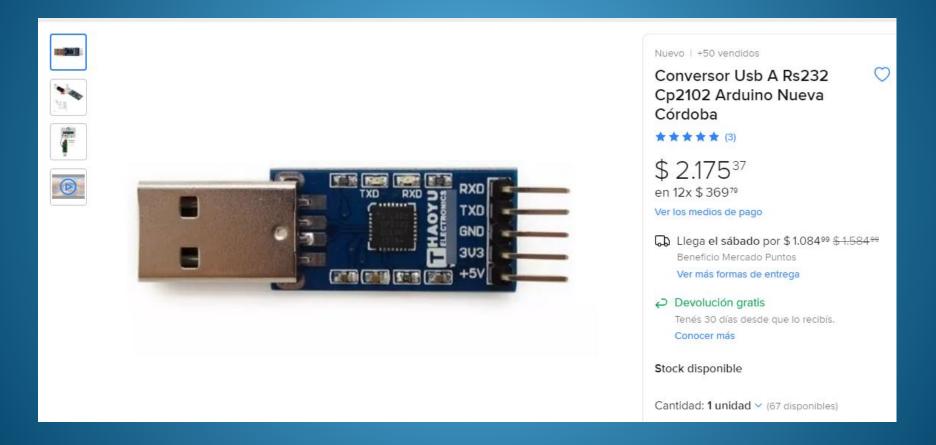




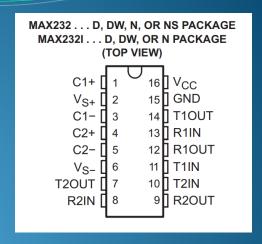
Interfaces serie RS232 -USB



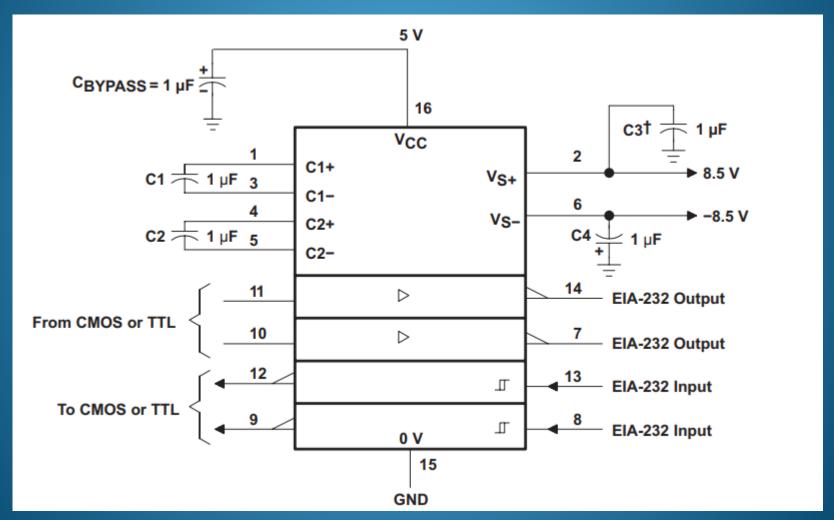
Interfaces serie RS232 -USB

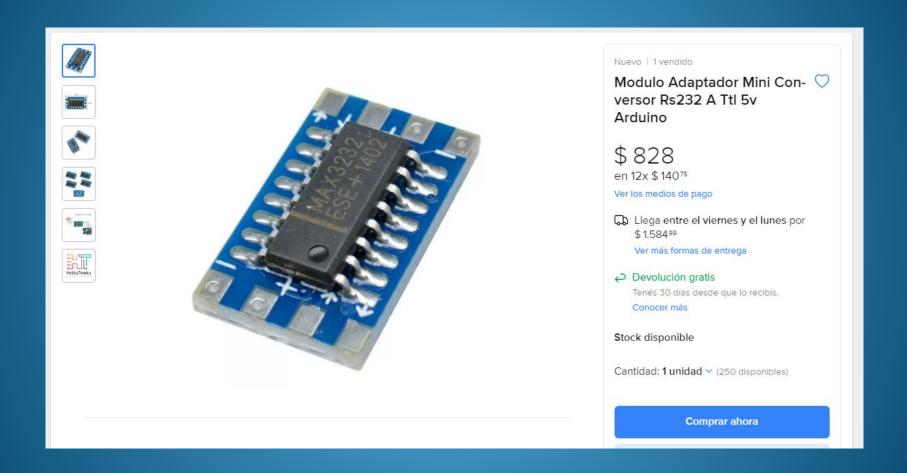


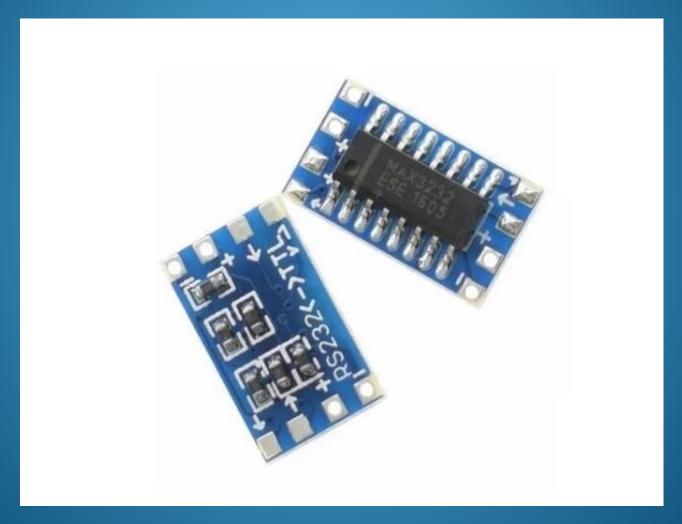
12 11 10 8 7 6 5 8 7 8 7 6 5 9 8 2 10 9 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 Time (µs) Figure 2. Driver to Receiver Loopback Timing Waveform



PIN		TYPE	DESCRIPTION
NAME NO.		ITPE	DESCRIPTION
C1+	1	_	Positive lead of C1 capacitor
VS+	2	0	Positive charge pump output for storage capacitor only
C1-	3	_	Negative lead of C1 capacitor
C2+	4	_	Positive lead of C2 capacitor
C2-	5	_	Negative lead of C2 capacitor
VS-	6	0	Negative charge pump output for storage capacitor only
T2OUT, T1OUT	7, 14	0	RS232 line data output (to remote RS232 system)
R2IN, R1IN	8, 13	I	RS232 line data input (from remote RS232 system)
R2OUT, R1OUT	9, 12	0	Logic data output (to UART)
T2IN, T1IN	10, 11	I	Logic data input (from UART)
GND	15	_	Ground
V _{CC}	16	_	Supply Voltage, Connect to external 5V power supply

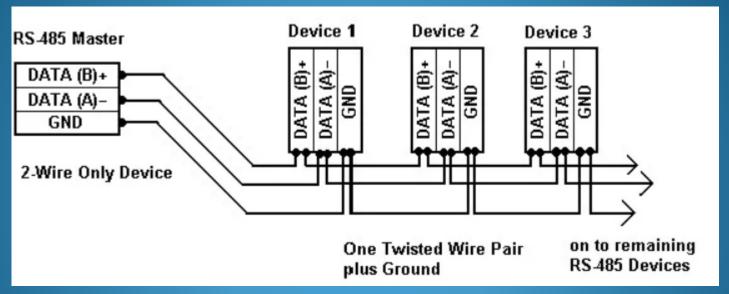


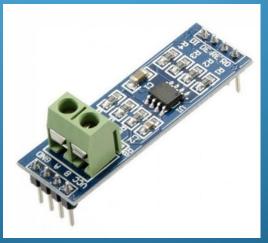


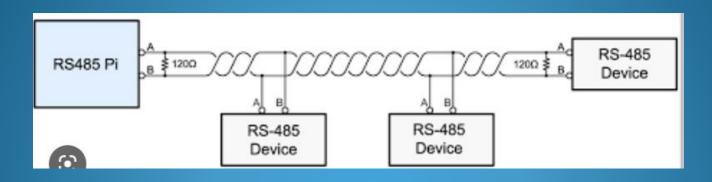


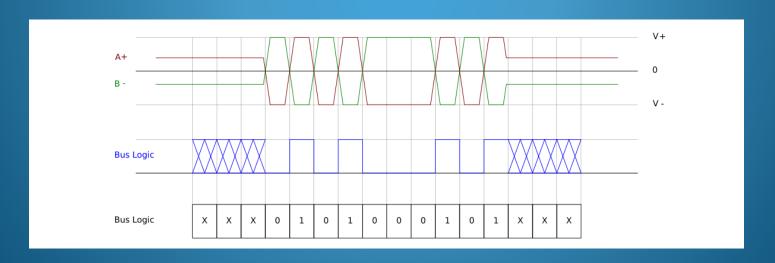
Interfaces serie USB – RS485

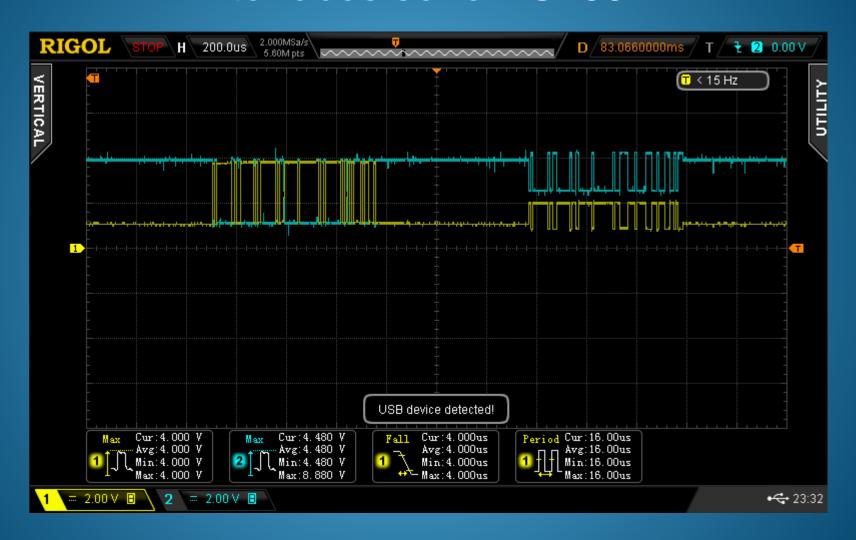


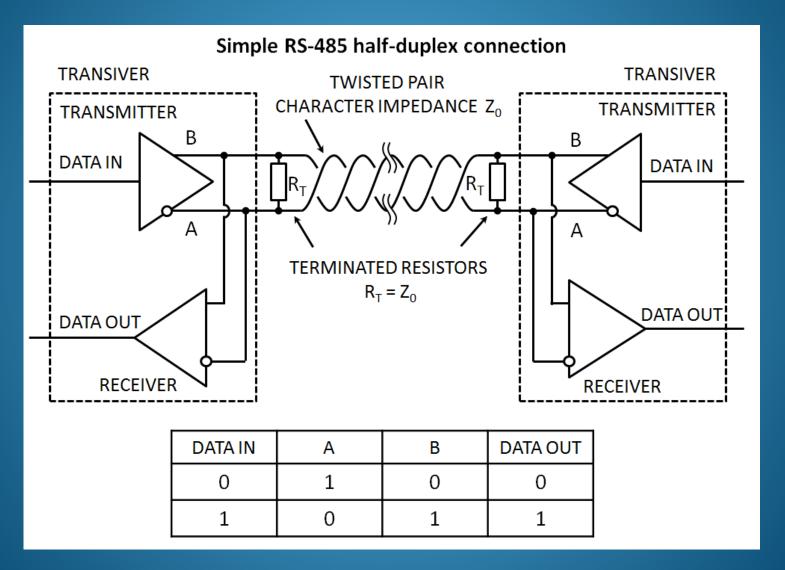


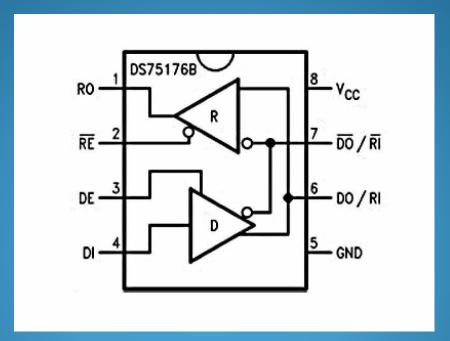


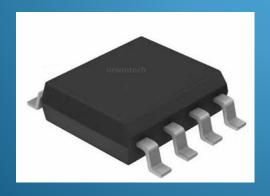


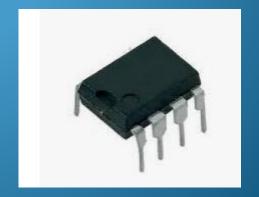


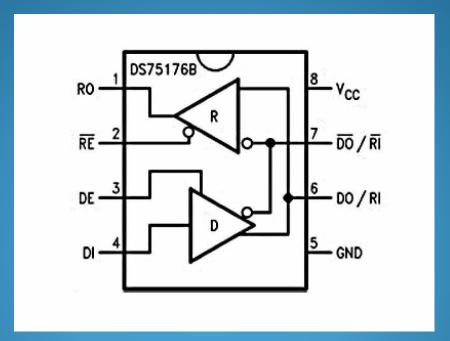




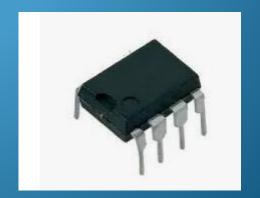












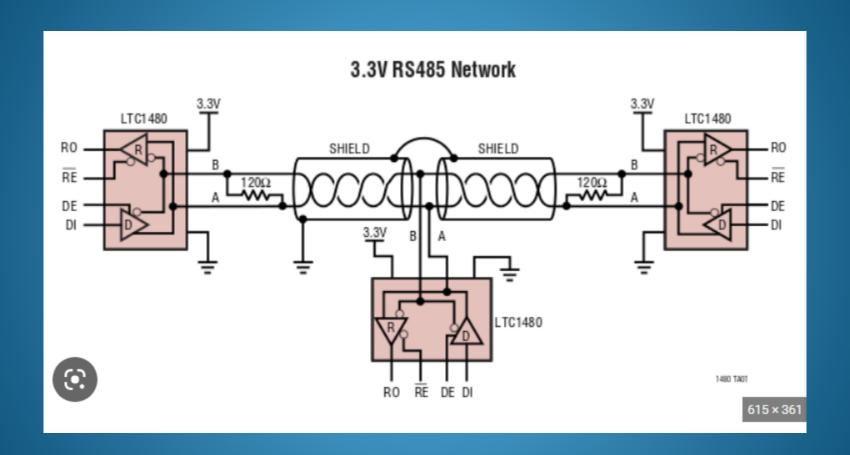
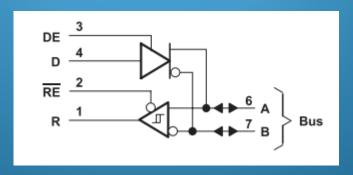






Table 5-1. Pin Functions

P	PIN		DESCRIPTION
NAME	NO.	TYPE	DESCRIPTION
R	1	0	Logic Data Output from RS-485 Receiver
RE	2	I	Receive Enable (active low)
DE	3	I	Driver Enable (active high)
D	4	I	Logic Data Input to RS-485 Driver
GND	5	_	Device Ground Pin
A	6	I/O	RS-422 or RS-485 Data Line
В	7	I/O	RS-422 or RS-485 Data Line
V _{CC}	8	_	Power Input. Connect to 5-V Power Source.



6.1 Absolute Maximum Ratings

over operating free-air temperature range (unless otherwise noted)(1)

		MIN	MAX	UNIT	
V _{CC}	Supply Voltage ⁽²⁾		7	V	
	Voltage range at any bus terminal	-10	15	V	
VI	Enable input voltage		5.5	V	
	Continuous Total power Dissipation	See Dissipation Rating Table			
T _A	Operating free-air temperature range	0	70	°C	
T _{stg}	Storage temperature range	65	150	°C	

6.2 Recommended Operating Conditions

over operating free-air temperature range (unless otherwise noted)

			MIN	TYP	MAX	UNIT
V _{CC}	Supply Voltage	Supply Voltage			5.25	V
V _I or V _{IC}	Voltage at any buss terminal (separately or common mode)		-7		12	٧
V _{IH}	High-level input voltage	D, DE, and RE	2			٧
V _{IL}	Low-level input voltage	D, DE, and RE			0.8	٧
V _{ID}	Differential input voltage ⁽¹⁾				±12	٧
	High level extent except	Driver			-60	mA
Іон	High-level output current	Receiver			-400	μA
		Driver			60	т Л
loL	Low-level output current	Receiver			8	mA
T _A	Operating free-air temperature		0		70	°C

6.5 Electrical Characteristics - Driver

over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted)

	PARAMETER	TEST CO	NDITIONS	MIN	TYP ⁽¹⁾	MAX	UNIT
V _{IK}	Input clamp voltage	I _I = -18 mA			-1.5	٧	
V _{OH}	High-level output voltage	V _{IH} = 2 V, V _{IL} = 0.8 V,	I _{OH} = -33 mA		3.7		٧
V _{OL}	Low-level output voltage	V _{IH} = 2 V, V _{IL} = 0.8 V,	I _{OH} = 33 mA		1.1		٧
V _{OD1}	Differential output voltage	I _O = 0				2V _{OD2}	٧
IVI	Differential output voltage	RL = 100 Ω, see Figu	re 7-1	2	2.7		v
V _{OD2}	Differential output voltage	RL = 54 Ω, see Figure	7-1	1.5	2.4		v
Δ V _{OD}	Change in magnitude of differential output voltage ⁽²⁾				±0.2	٧	
Voc	Common-mode output voltage(3)	RL = 54 Ω or 100 Ω, s			3	٧	
Δ V _{oc}	Change in magnitude of common-mode output voltage ⁽²⁾				±0.2	٧	
	0.1.1	Output disabled ⁽⁴⁾	V _O = 12 V			1	mA
lo	Output current	Output disabled	V _O = -7 V			-0.8	mA
I _{IH}	High-level input current	V _I = 2.4 V	•			20	μА
I _{IL}	Low-level input current	V _I = 0.4 V				-400	μA
	Short-circuit output current	V _O = -7 V				-250	
los		V _O = V _{CC}				250	mA
		V _O = 12 V				500	
	Supply support (total accluses)	No load	Outputs enabled		35	50	А
Icc	Supply current (total package)	No load	Outputs disabled		26	40	mA

6.6 Electrical Characteristics - Receiver

over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted)

	PARAMETER	TEST CO	NDITIONS	MIN	TYP ⁽¹⁾	MAX	UNIT
V _{IT+}	Positive-going input threshold voltage	V _O = 2.7 V, I _O = -0.4 r	V _O = 2.7 V, I _O = -0.4 mA			0.2	V
V _{IT} _	Negative-going input threshold voltage	V _O = 0.5 V, I _O = 8 mA		-0.2			٧
V _{hys}	Input hysteresis voltage (V _{IT} + - V _{IT-})	•			50		mV
VIK	Enable clamp voltage	I _I = -18 mA	I _I = -18 mA			-1.5	٧
V _{OH}	High-level output voltage	V _{ID} = 200 mV, I _{OH} = -4	V _{ID} = 200 mV, I _{OH} = -400 μA See Figure 7-2				V
V _{OL}	Low-level output voltage	V _{ID} = 200 mV, I _{OH} = 8	V _{ID} = 200 mV, I _{OH} = 8 mA See Figure 7-2			0.45	V
loz	High-impedance-state output current	V _O = 0.4 V to 2.4 V	V _O = 0.4 V to 2.4 V			±20	μA
	Line input current	Other input = 0 V(2)	V _I = 12 V			1	mA
4	Line input current	Other input = 0 V(=)	V _I = -7 V			-0.8	mA
I _{IH}	High-level enable input current	V _{IH} = 2.7 V	•			20	μA
I _{IL}	Low-level enable input current	V _{IL} = 0.4 V	V _{IL} = 0.4 V			-100	μA
rį	Input resistance	•		12			kΩ
los	Short-circuit output current			-15		-85	mA

6.6 Electrical Characteristics – Receiver (continued)

over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted)

PARAMETER		TEST CONDITIONS		MIN	TYP ⁽¹⁾	MAX	UNIT
	Supply current (total package)	No load	Outputs enabled		35	50	mA
Icc	Supply current (total package)		Outputs disabled		26	40	IIIA

- All typical values are at V_{CC} = 5 V, TA = 25°C.
- (2) This applies for both power on and power off. Refer to ANSI Standard EIA/TIA-422-B for exact conditions.

6.7 Switching Characteristics - Driver

V_{CC} = 5 V, T_A = 25°C

	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
t _{d(OD)}	Differential-output delay time	R _I = 60 Ω, See Figure 7-3		40	60	ns
$t_{t(OD)}$	Differential-output transition time	NL = 00 12, See Figure 7-5		65	95	ns
t _{PZH}	Output enable time to high level	R _L = 110 Ω, See Figure 7-4		55	90	ns
t _{PZL}	Output enable time to low level	R _L = 110 Ω, See Figure 7-5		30	50	ns
t _{PHZ}	Output disable time form high level	R _L = 110 Ω, See Figure 7-4		85	130	ns
t _{PLZ}	Output disable time from low level	R _L = 110 Ω, See Figure 7-5		20	40	ns

6.8 Switching Characteristics - Receiver

 $V_{CC} = 5 \text{ V}, C_1 = 15 \text{ pF}, T_A = 25^{\circ}\text{C}$

	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
t _{PLH}	Propagation delay time, low-to-high- level output	V _{In} = -1.5 V to 1.5 V, See Figure 7-6		21	35	ns
t _{PHL}	Propagation delay time, high-to-low- level output	1 V _{ID} = -1.5 V to 1.5 V, See Figure 7-0		23	35	ns
t _{PZH}	Output enable time to high level	See Figure 7.7		10	30	ns
t _{PZL}	Output enable time to low level	See Figure 7-7		12	30	ns
t _{PHZ}	Output disable time from high level	05		20	35	ns
t _{PLZ}	Output disable time from low level	See Figure 7-7		17	25	ns