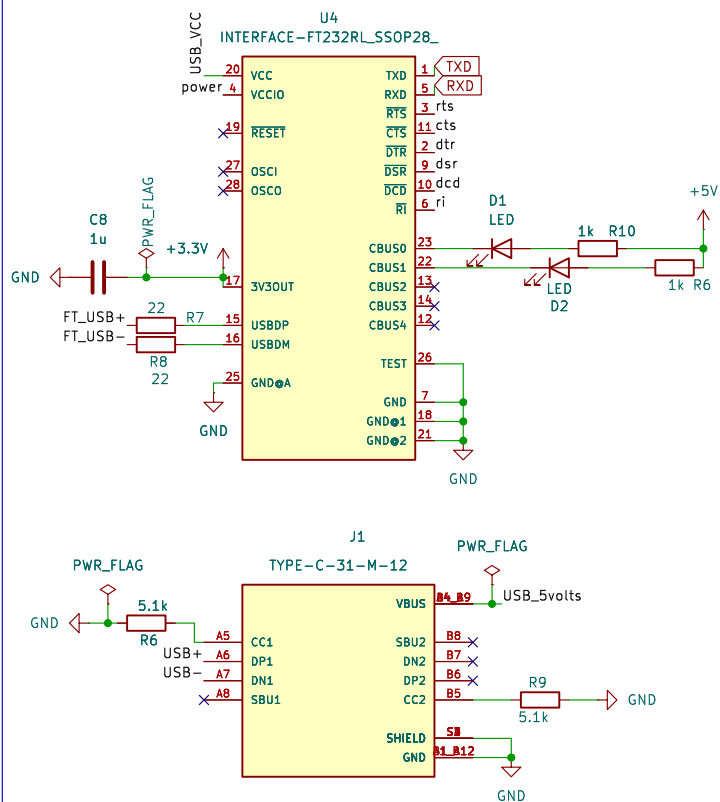
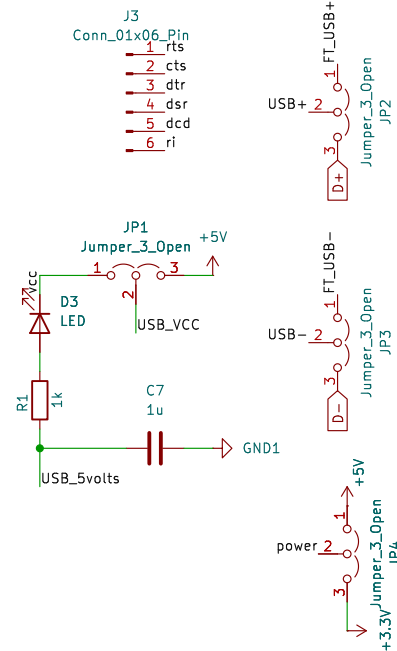


	1	2	3	4	5	6
A	<div>controller</div> <div>File: esp32-s3.kicad_sch</div>					A
B	<div>signal_genenerator</div> <div>File: signal_genenerator.kicad_sch</div>					B
C	<div>signal_receptor</div> <div>File: signal_receptor.kicad_sch</div>					C
D	<div><div>Sheet: / File: ECTdevice.kicad_sch</div><div><div>Title:</div><div>Size: A4 KiCad E.D.A. 9.0.3</div><div>Date:</div><div>Rev: Id: 1/4</div></div></div>					D
	1	2	3	4	5	6

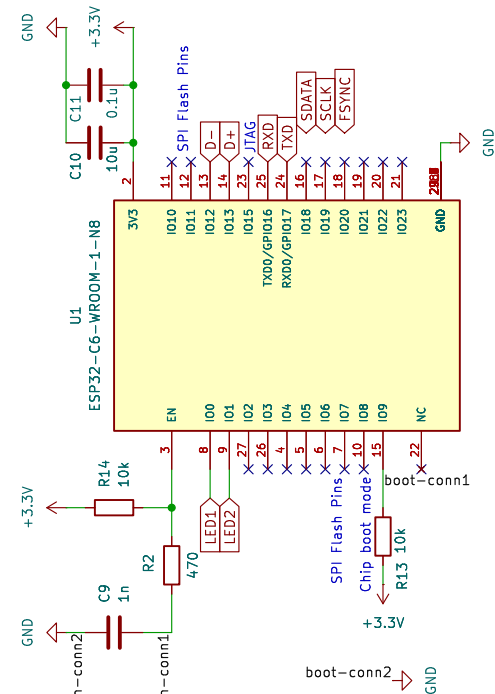
# FT232RL USB-SERIAL CONNECTIONS



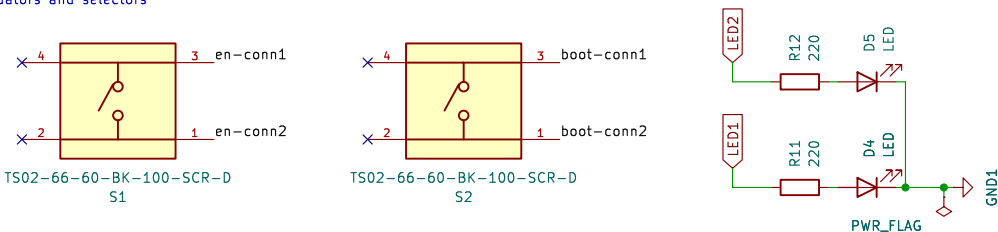
# SELECTOR CONNECTORS



# esp32 c6 controller



# actuators and selectors



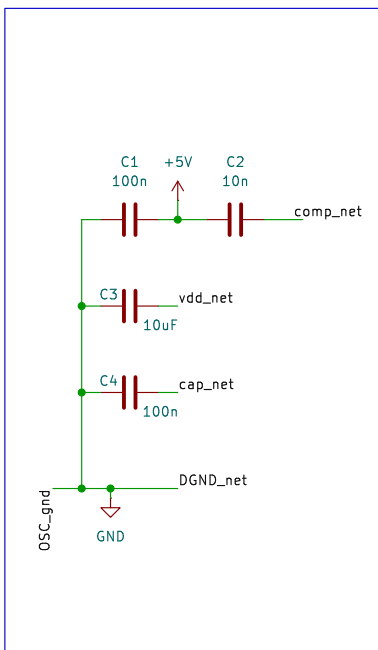
Sheet: /controller/  
File: esp32-s3.kicad\_sch

Title:

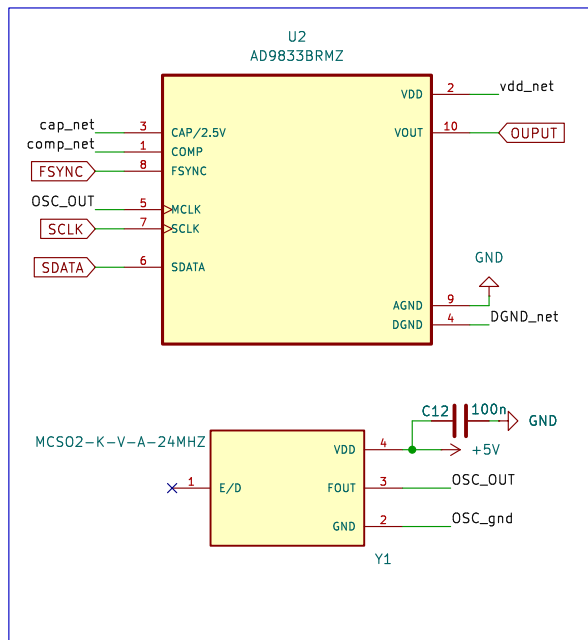
Size: A4  
KiCad E.D.A. 9.0.3

Date:

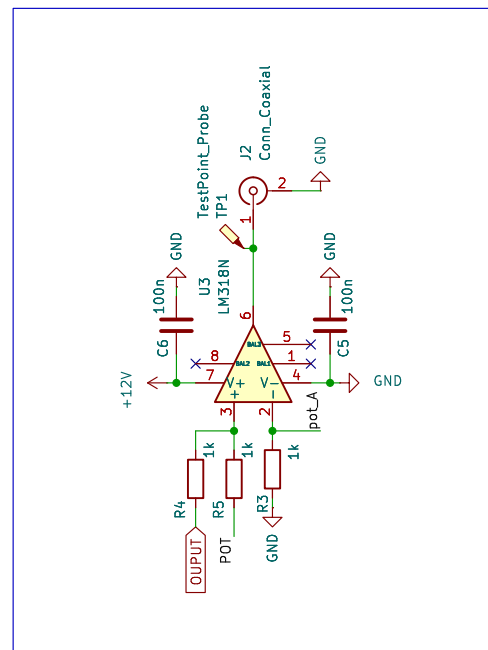
Rev:  
Id: 2/4



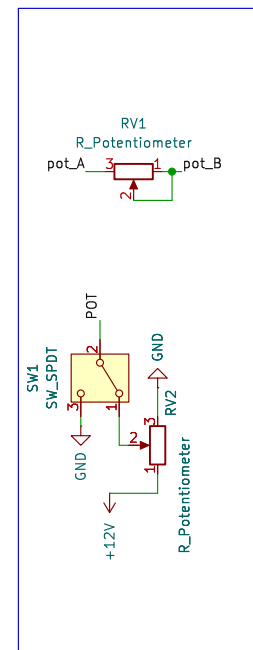
decoupling



DAC



Amplification



Offset & Amplitude control

Sheet: /signal\_genenerator/  
File: signal\_genenerator.kicad\_sch

**Title:**

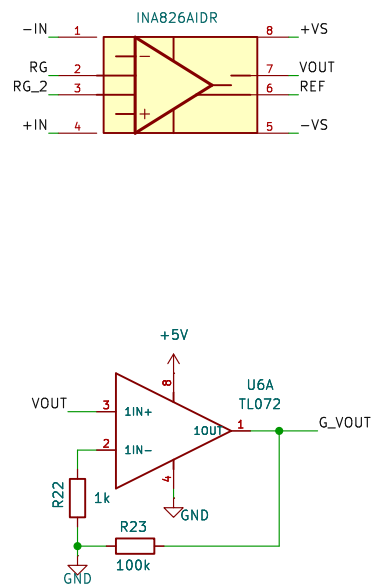
Size: A4 Date:

KiCad E.D.A. 9.0.3

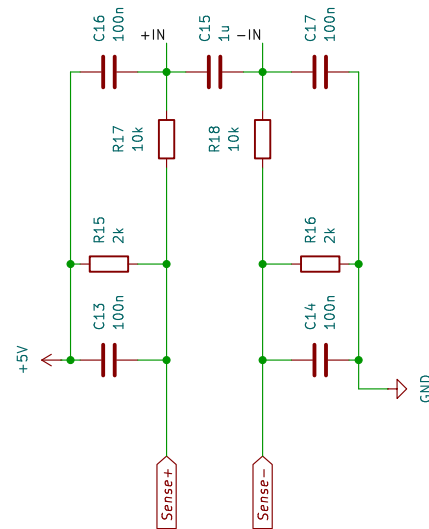
**Rev:**

Id: 3/4

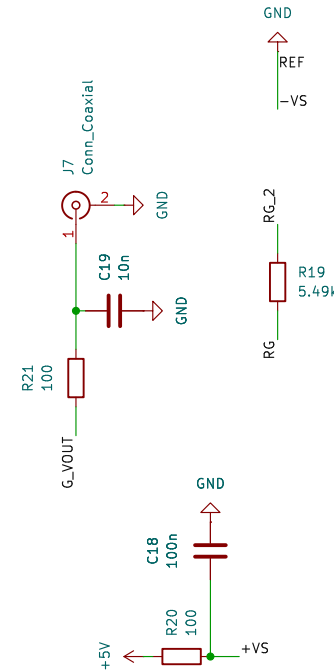
## DIGITAL AMPLIFIER CHIP



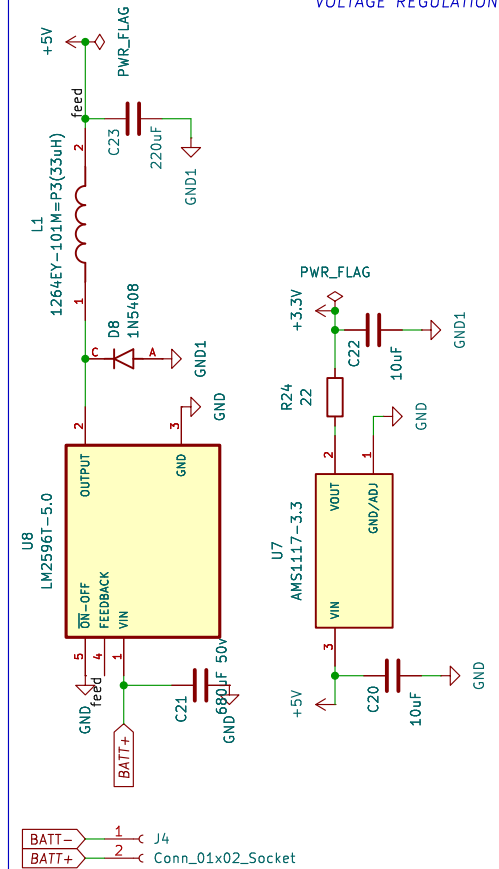
## AMPLIFICATION CIRCUITRY



## TEST POINT



## VOLTAGE REGULATION



Sheet: /signal\_receptor/  
File: signal\_receptor.kicad\_sch

### Title:

Size: A4 Date:

KiCad E.D.A. 9.0.3

Rev:

Id: 4/4