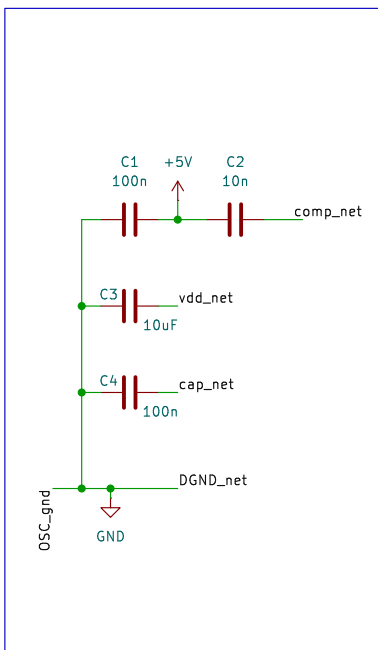
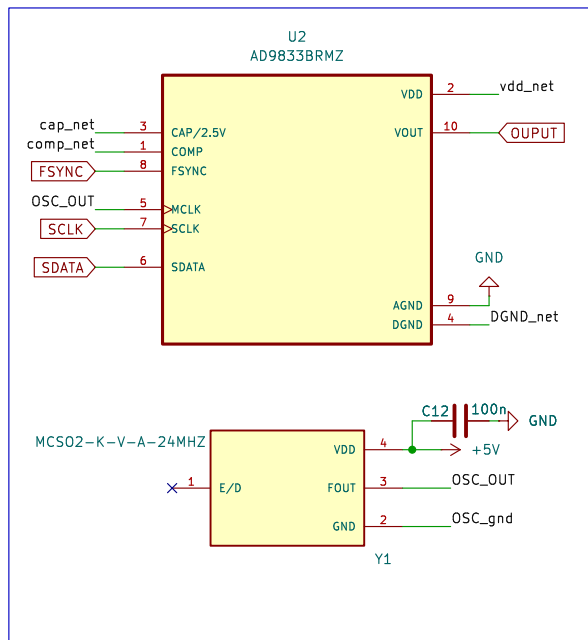


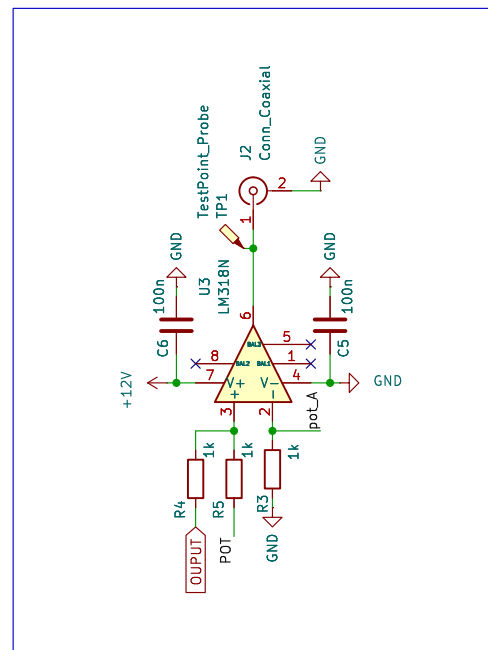
	1	2	3	4	5	6
A	<div>controller</div> <div>File: esp32-s3.kicad_sch</div>					A
B	<div>signal_genenerator</div> <div>File: signal_genenerator.kicad_sch</div>					B
C	<div>signal_receptor</div> <div>File: signal_receptor.kicad_sch</div>					C
D	<div></div> <div>Sheet: / File: ECTdevice.kicad_sch</div> <div>Title:</div> <div>Size: A4Date:KiCad E.D.A. 9.0.3</div> <div>Rev:Id: 1/4</div>					D
	1	2	3	4	5	6



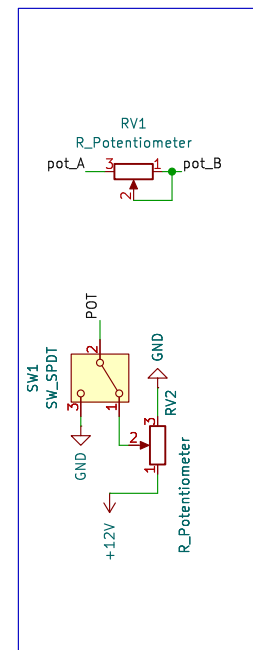
decoupling



DAC



Amplification



Offset & Amplitude control

Sheet: /signal_genenerator/
File: signal_genenerator.kicad_sch

Title:

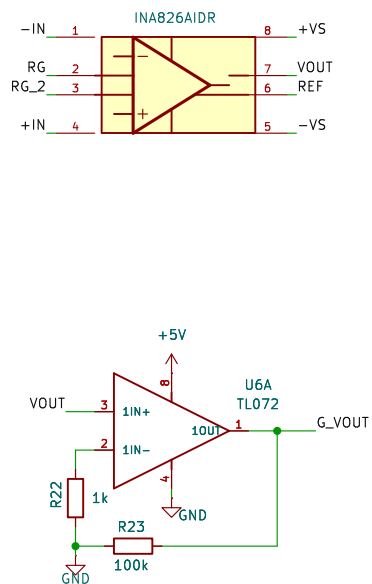
Size: A4 Date:

KiCad E.D.A. 9.0.3

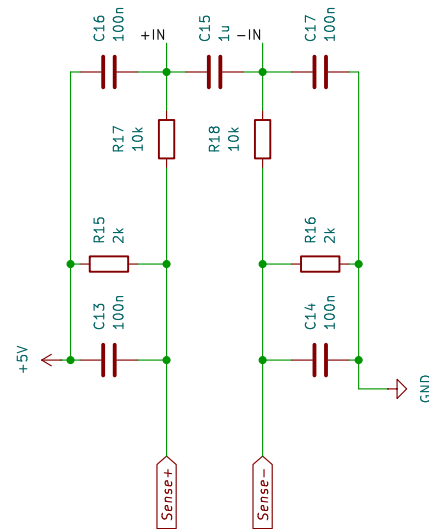
Rev:

Id: 3/4

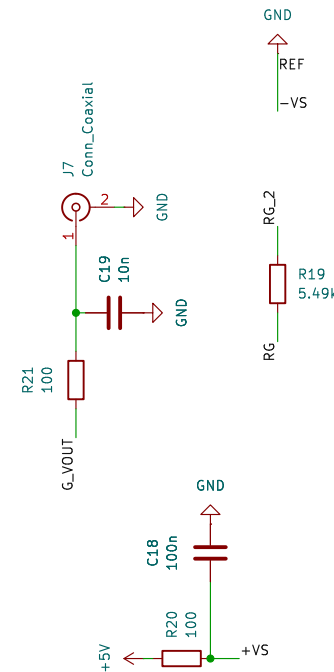
DIGITAL AMPLIFIER CHIP



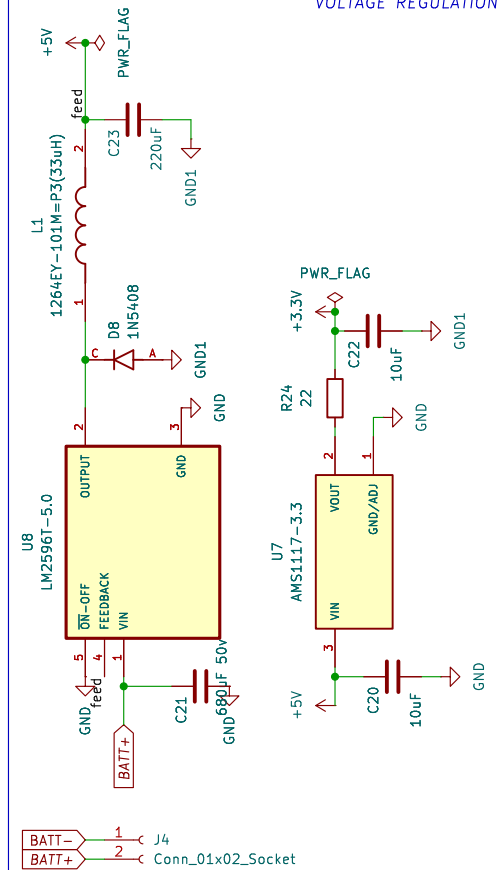
AMPLIFICATION CIRCUITRY



TEST POINT



VOLTAGE REGULATION



Sheet: /signal_receptor/
File: signal_receptor.kicad_sch

Title:

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Date:

KiCad E.D.A. 9.0.3

Rev:

Id: 4/4