# UNR421x Series (UN421x Series)

### Silicon NPN epitaxial planar type

#### For digital circuits

#### ■ Features

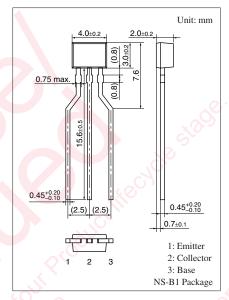
- Costs can be reduced through downsizing of the equipment and reduction of the number of parts
- New S type package, allowing supply with the radial taping

#### ■ Resistance by Part Number

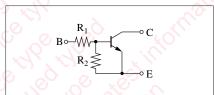
| _                                    |                        |                        |
|--------------------------------------|------------------------|------------------------|
|                                      | $(R_1)$                | $(R_2)$                |
| • UNR4210 (UN4210)                   | 47 kΩ                  |                        |
| • UNR4211 (UN4211)                   | 10 kΩ                  | 10 kΩ                  |
| • UNR4212 (UN4212)                   | 22 kΩ                  | 22 kΩ                  |
| • UNR4213 (UN4213)                   | 47 kΩ                  | 47 kΩ                  |
| • UNR4214 (UN4214)                   | 10 kΩ                  | 47 kΩ                  |
| • UNR4215 (UN4215)                   | 10 kΩ                  | <b>→</b>               |
| • UNR4216 (UN4216)                   | $4.7~\mathrm{k}\Omega$ | _                      |
| • UNR4217 (UN4217)                   | 22 kΩ                  |                        |
| • UNR4218 (UN4218)                   | $0.51 \text{ k}\Omega$ | 5.1 kΩ                 |
| • UNR4219 (UN4219)                   | 1 kΩ                   | $10 \text{ k}\Omega$   |
| • UNR421D (UN421D)                   | 47 kΩ                  | 10 kΩ                  |
| <ul> <li>UNR421E (UN421E)</li> </ul> | 47 kΩ                  | 22 kΩ 🦠                |
| • UNR421F (UN421F)                   | $4.7 \text{ k}\Omega$  | 10 kΩ                  |
| • UNR421K (UN421K)                   | $10~\mathrm{k}\Omega$  | $4.7~\mathrm{k}\Omega$ |
| • UNR421L (UN421L)                   | $4.7~\mathrm{k}\Omega$ | $4.7~\mathrm{k}\Omega$ |
|                                      |                        |                        |

### ■ Absolute Maximum Ratings T<sub>a</sub> = 25°C

| Parameter                             | Symbol           | Rating      | Unit |
|---------------------------------------|------------------|-------------|------|
| Collector-base voltage (Emitter open) | $V_{CBO}$        | 50          | v    |
| Collector-emitter voltage (Base open) | V <sub>CEO</sub> | 50          | v    |
| Collector current                     | $I_C$            | 100         | mA   |
| Total power dissipation               | $P_{T}$          | 300         | mW   |
| Junction temperature                  | $T_j$            | 150         | °C   |
| Storage temperature                   | T <sub>stg</sub> | -55 to +150 | °C   |
|                                       |                  |             |      |



#### Internal Connection



Note) The part numbers in the parenthesis show conventional part number.

### ■ Electrical Characteristics $T_a = 25$ °C $\pm 3$ °C

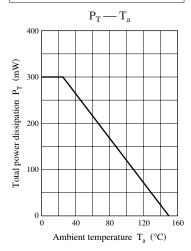
| F                                     | Parameter                    | Symbol                         | Conditions   | Min  | Тур  | Max  | Unit |
|---------------------------------------|------------------------------|--------------------------------|--|------|------|------|------|
| Collector-base                        | voltage (Emitter open)       | $V_{CBO}$                      | $I_C = 10 \mu\text{A},  I_E = 0$   | 50   |      |      | V    |
| Collector-emitter voltage (Base open) |                              | V <sub>CEO</sub>               | $I_C = 2 \text{ mA}, I_B = 0$  | 50   |      |      | V    |
| Collector-base c                      | utoff current (Emitter open) | $I_{CBO}$                      | $V_{CB} = 50 \text{ V}, I_{E} = 0$                                       |      |      | 0.1  | μΑ   |
| Collector-emitte                      | r cutoff current (Base open) | $I_{CEO}$                      | $V_{CE} = 50 \text{ V}, I_{B} = 0$                                       |      |      | 0.5  | μΑ   |
| Emitter-base                          | UNR4210/4215/4216/4217       | $I_{EBO}$                      | $V_{EB} = 6 \text{ V}, I_{C} = 0$  |      |      | 0.01 | mA   |
| cutoff current                        | UNR4213                      |                                |  |      |      | 0.1  |      |
| (Collector open)                      | UNR4212/4214/421D/421E       |                                |  |      |      | 0.2  |      |
|                                       | UNR4211                      |                                |  |      |      | 0.5  |      |
|                                       | UNR421F/421K                 |                                |  |      |      | 1.0  |      |
|                                       | UNR4219                      |                                |  |      |      | 1.5  |      |
|                                       | UNR4218/421L                 |                                |  |      |      | 2.0  |      |
| Forward current                       | UNR4218/421K/421L            | h <sub>FE</sub>                | $V_{CE} = 10 \text{ V}, I_{C} = 5 \text{ mA}$                            | 20   |      |      | _    |
| transfer ratio                        | UNR4219/421D/421F            |                                |  | 30   |      |      |      |
|                                       | UNR4211                      |                                |  | 35   |      |      |      |
|                                       | UNR4212/421E                 |                                |  | 60   |      |      |      |
|                                       | UNR4213/4214                 |                                |  | 80   |      |      |      |
|                                       | UNR4210 */4215 */4216 */     |                                |  | 160  |      | 460  |      |
|                                       | 4217 *                       |                                |  |      |      |      |      |
|                                       | ter saturation voltage       | V <sub>CE(sat)</sub>           | $I_C = 10 \text{ mA}, I_B = 0.3 \text{ mA}$                              |      |      | 0.25 | V    |
| Output voltage                        | e high-level                 | V <sub>OH</sub>                | $V_{CC} = 5 \text{ V}, V_{B} = 0.5 \text{ V}, R_{L} = 1 \text{ k}\Omega$ | 4.9  |      |      | V    |
| Output voltage                        | e low-level                  | $V_{OL}$                       | $V_{CC} = 5 \text{ V}, V_{B} = 2.5 \text{ V}, R_{L} = 1 \text{ k}\Omega$ |      |      | 0.2  | V    |
|                                       | UNR4213/421K                 |                                | $V_{CC} = 5 \text{ V}, V_{B} = 3.5 \text{ V}, R_{L} = 1 \text{ k}\Omega$ |      |      |      |      |
|                                       | UNR421D                      |                                | $V_{CC} = 5 \text{ V}, V_{B} = 10 \text{ V}, R_{L} = 1 \text{ k}\Omega$  |      |      |      |      |
|                                       | UNR421E                      |                                | $V_{CC} = 5 \text{ V}, V_{B} = 6 \text{ V}, R_{L} = 1 \text{ k}\Omega$   |      |      |      |      |
| Transition free                       | quency                       | $f_T$                          | $V_{CB} = 10 \text{ V}, I_E = -2 \text{ mA}, f = 200 \text{ MHz}$        |      | 150  |      | MHz  |
| Input                                 | UNR4218                      | $R_1$                          |  | -30% | 0.51 | +30% | kΩ   |
| resistance                            | UNR4219                      |                                |  |      | 1.0  |      |      |
|                                       | UNR4216/421F/421L            |                                |  |      | 4.7  |      |      |
|                                       | UNR4211/4214/4215/421K       |                                |  |      | 10   |      |      |
|                                       | UNR4212/4217                 |                                |  |      | 22   |      |      |
|                                       | UNR4210/4213/421D/421E       |                                |  |      | 47   |      |      |
| Resistance                            | UNR4218/4219                 | R <sub>1</sub> /R <sub>2</sub> |  | 0.08 | 0.10 | 0.12 | _    |
| ratio                                 | UNR4214                      |                                |  | 0.17 | 0.21 | 0.25 |      |
|                                       | UNR421F                      |                                |  | 0.37 | 0.47 | 0.57 |      |
|                                       | UNR4211/4212/4213/421L       |                                |  | 0.8  | 1.0  | 1.2  |      |
|                                       | UNR421K                      |                                |  | 1.70 | 2.13 | 2.60 |      |
|                                       | UNR421E                      |                                |  | 1.70 | 2.14 | 2.60 |      |
|                                       | UNR421D                      |                                |  | 3.7  | 4.7  | 5.7  |      |

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

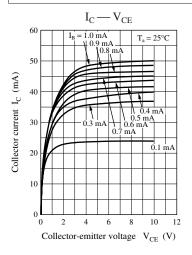
#### 2. \*: Rank classification

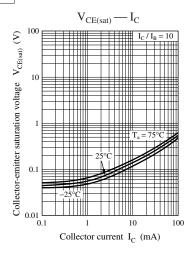
| Rank     | Q          | R          | S          | No-rank    |
|----------|------------|------------|------------|------------|
| $h_{FE}$ | 160 to 260 | 210 to 340 | 290 to 460 | 160 to 460 |

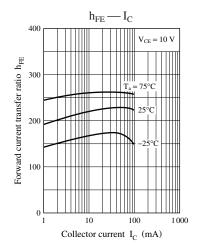
#### Common characteristics chart

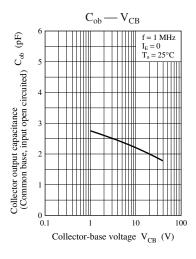


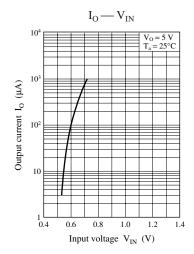
#### Characteristics charts of UNR4210

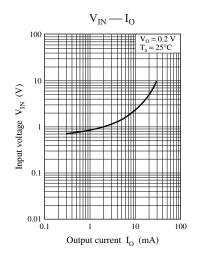




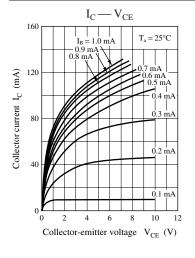


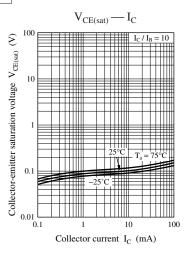


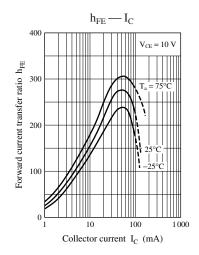


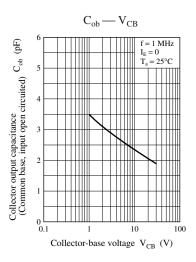


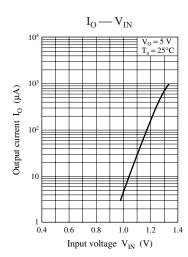
#### Characteristics charts of UNR4211

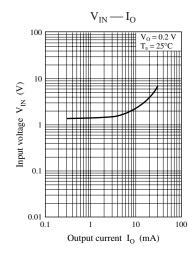




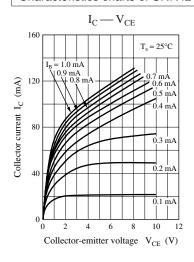


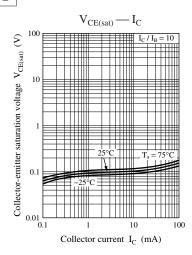


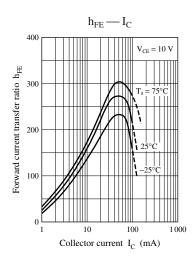


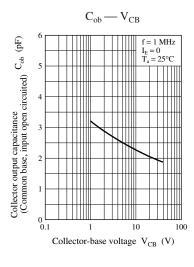


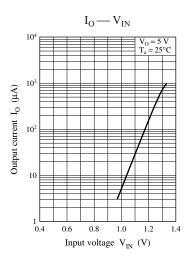
#### Characteristics charts of UNR4212

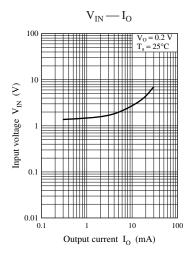




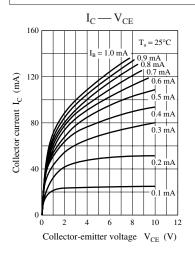


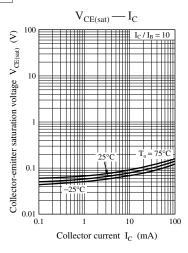


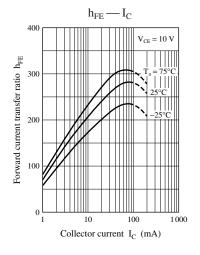


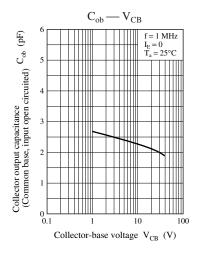


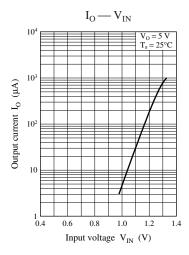
#### Characteristics charts of UNR4213

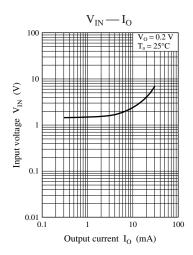




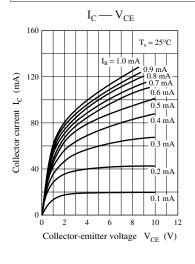


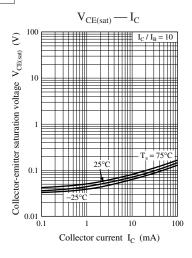


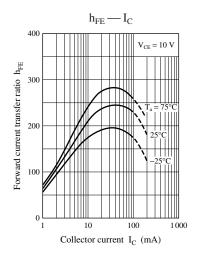


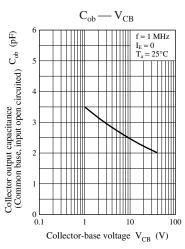


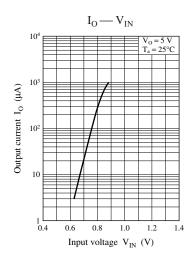
#### Characteristics charts of UNR4214

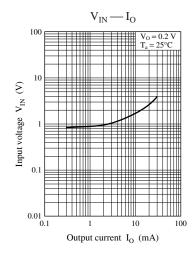




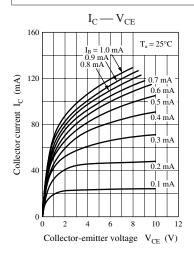


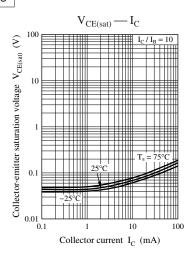


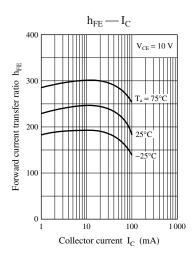


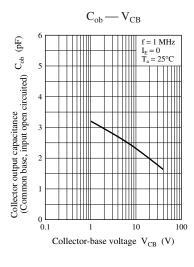


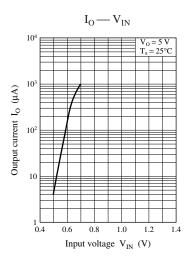
#### Characteristics charts of UNR4215

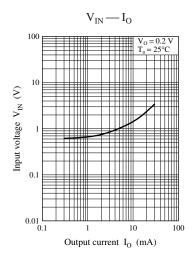




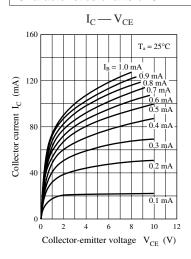


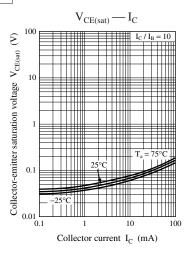


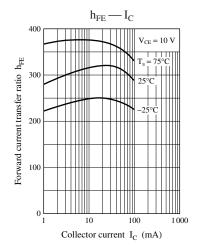


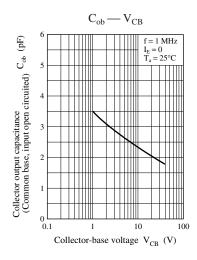


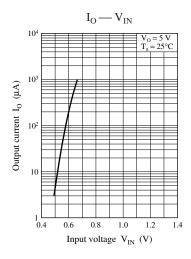
#### Characteristics charts of UNR4216

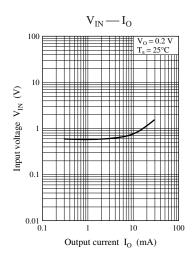




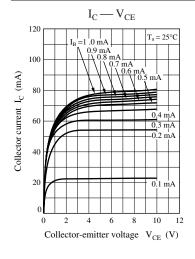


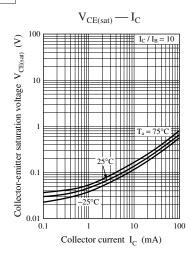


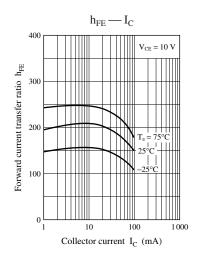


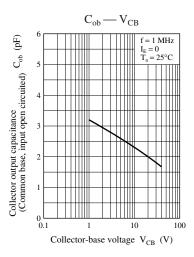


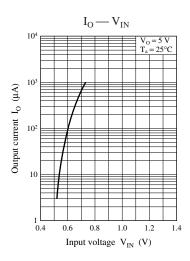
#### Characteristics charts of UNR4217

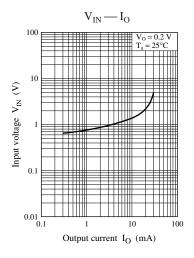




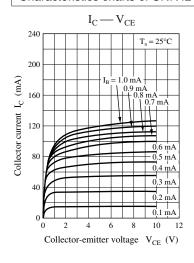


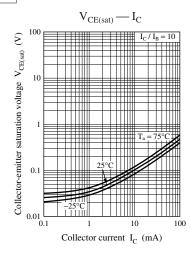


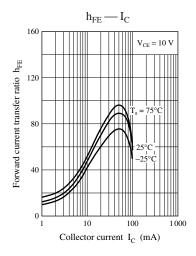


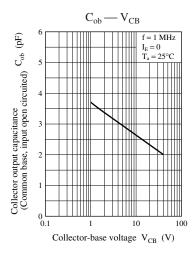


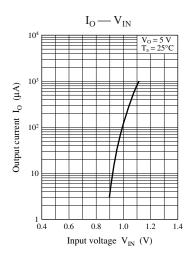
#### Characteristics charts of UNR4218

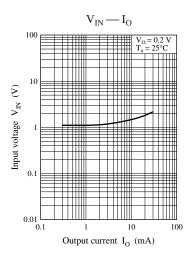




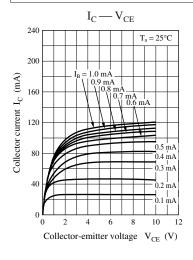


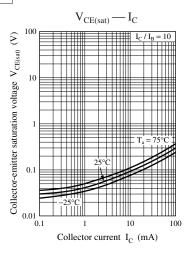


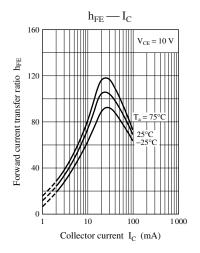


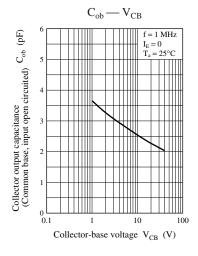


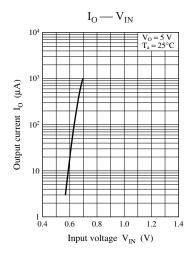
#### Characteristics charts of UNR4219

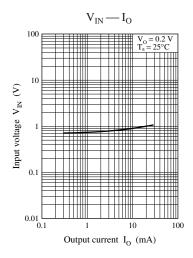




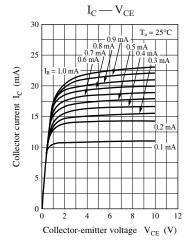


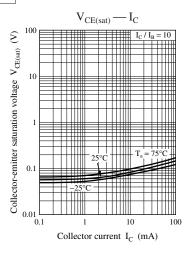


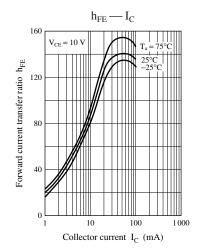


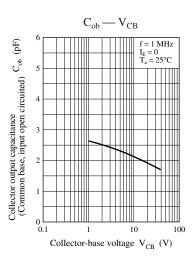


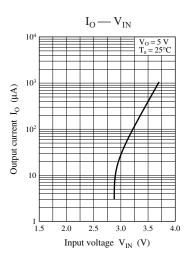
#### Characteristics charts of UNR421D

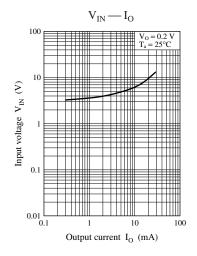




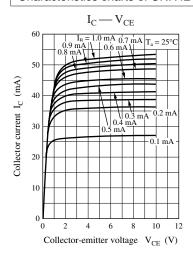


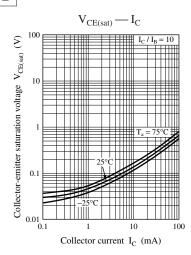


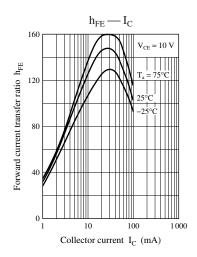


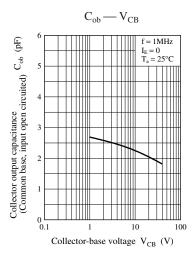


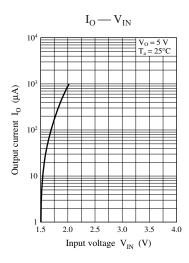
#### Characteristics charts of UNR421E

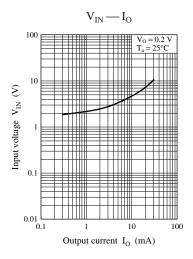




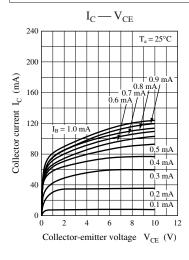


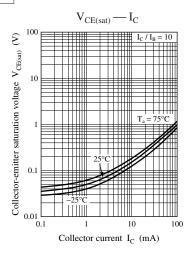


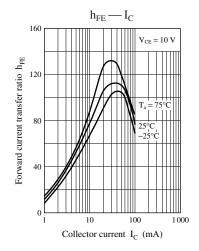


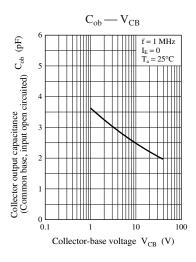


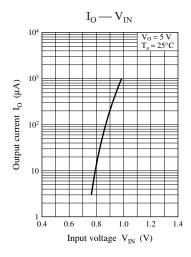
#### Characteristics charts of UNR421F

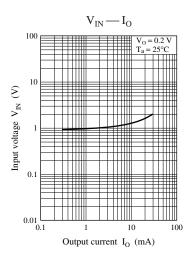




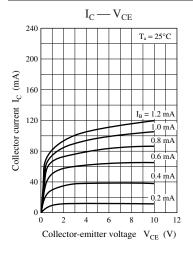


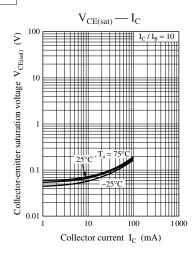


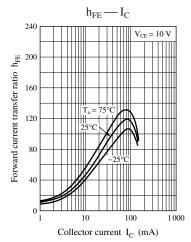


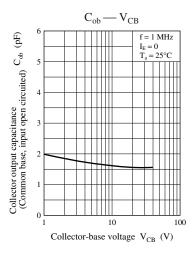


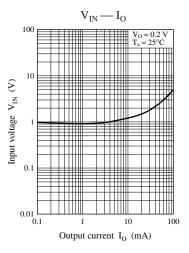
#### Characteristics charts of UNR421K



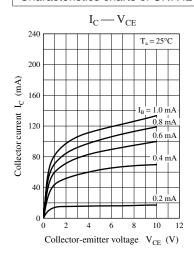


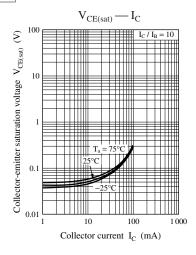


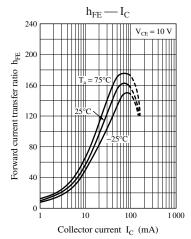


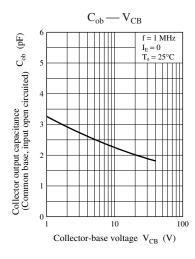


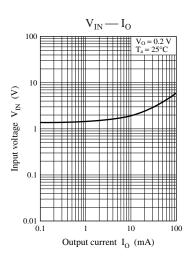
#### Characteristics charts of UNR421L











## Request for your special attention and precautions in using the technical information and semiconductors described in this book

- (1) If any of the products or technical information described in this book is to be exported or provided to non-residents, the laws and regulations of the exporting country, especially, those with regard to security export control, must be observed.
- (2) The technical information described in this book is intended only to show the main characteristics and application circuit examples of the products, and no license is granted under any intellectual property right or other right owned by our company or any other company. Therefore, no responsibility is assumed by our company as to the infringement upon any such right owned by any other company which may arise as a result of the use of technical information described in this book.
- (3) The products described in this book are intended to be used for standard applications or general electronic equipment (such as office equipment, communications equipment, measuring instruments and household appliances).

  Consult our sales staff in advance for information on the following applications:
  - Special applications (such as for airplanes, aerospace, automobiles, traffic control equipment, combustion equipment, life support systems and safety devices) in which exceptional quality and reliability are required, or if the failure or malfunction of the products may directly jeopardize life or harm the human body.
  - Any applications other than the standard applications intended.
- (4) The products and product specifications described in this book are subject to change without notice for modification and/or improvement. At the final stage of your design, purchasing, or use of the products, therefore, ask for the most up-to-date Product Standards in advance to make sure that the latest specifications satisfy your requirements.
- (5) When designing your equipment, comply with the range of absolute maximum rating and the guaranteed operating conditions (operating power supply voltage and operating environment etc.). Especially, please be careful not to exceed the range of absolute maximum rating on the transient state, such as power-on, power-off and mode-switching. Otherwise, we will not be liable for any defect which may arise later in your equipment.
- Even when the products are used within the guaranteed values, take into the consideration of incidence of break down and failure mode, possible to occur to semiconductor products. Measures on the systems such as redundant design, arresting the spread of fire or preventing glitch are recommended in order to prevent physical injury, fire, social damages, for example, by using the products.
- (6) Comply with the instructions for use in order to prevent breakdown and characteristics change due to external factors (ESD, EOS, thermal stress and mechanical stress) at the time of handling, mounting or at customer's process. When using products for which damp-proof packing is required, satisfy the conditions, such as shelf life and the elapsed time since first opening the packages.
- (7) This book may be not reprinted or reproduced whether wholly or partially, without the prior written permission of Matsushita Electric Industrial Co., Ltd.