



Investigating the impact of layer properties on the performance of p-graphene/CH₃NH₃PbI₃/n-cSi solar cell using numerical modelling

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ABSTRACT

The use of multilayer p-graphene as hole transporting layer has been successfully shown improve the performance of perovskite solar cell. The structure of p-graphene/CH₃NH₃PbI₃/n-cSi is designed and simulated in AFORS-HET software. We optimized the parameters of single layer graphene and obtained power conversion efficiency (PCE) of 12.21% under an illumination of 1.5G. With an increase in the number of p-graphene layers, the PCE falls down to 10.01%. The optimization of active layer parameters increases the PCE up to 12.27%. Further optimization of n-cSi parameters lead to the highest PCE of 16.75%. A significant effect of the operating temperature on the solar cell performance is observed. The effect of textured front surface on the solar cell performance is also studied and a PCE of 17.90% is obtained for textured surface as compared to 16.75% for planar surface. Our studies indicated that p-graphene may act as an efficient hole transporting layer in perovskite solar cell.

1. Introduction

The organic-inorganic hybrid perovskite solar cells (PSCs) have received great attention over the recent years because of their tunable band gap and high absorption coefficient that produce large PCE [1–5]. A typical structure of PSC consists of the perovskite layer sandwiched between an electron transporting layer (ETL) and a hole transporting layer (HTL) along with front and back contacts. The ETL materials are used to extract and transport the electrons from the perovskite layer to the corresponding electrode. TiO₂ is widely used ETL material for PSCs. However, TiO₂ material has some disadvantages such as intrinsic low mobility and production of deep traps by ultraviolet light, which results in charge accumulation, recombination in this layer and numerous current-voltage hysteresis [6–10]. The holes generated in perovskite layer are extracted and transported by the HTL materials which are broadly classified into two types i.e. one is organic HTL and the other is inorganic HTL. These HTL materials are selected based on their HOMO level, optical properties along with its doping ability. The organic HTL 2, 2', 7, 7' - tetrakis (N, N-DI-P-methoxyphenyl)-9,9-spirobifluorene (spiro-MeOTAD) is used for achieving large PCE [11]. The poly (3,4-ethylenedioxythiophene): polystyrene sulfonate (PEDOT: PSS) has also been employed as HTL in PSCs [12]. In contrast to the more expensive organic HTL materials, the inorganic HTL materials such as transition metal oxides NiO_x [13,14], CuO_x [15,16] and CrO_x [17,18] also established widespread interest.

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Fig. 1. Simulated hybrid PSC structure.

Recently, it has been observed that graphene with its exceptional properties have positive impact on the development of PSCs. Tl low-cost graphene with its high carrier mobility ($2 \times 10^5 \text{ cm}^2/\text{V}\cdot\text{s}$) [19], large optical transparency [20], large thermal conductivity [21], and high melting point [22] can be employed either as HTL or ETL, or both, at the electrodes in PSCs. Watanabe et al. [23] studied the PSC by introducing the graphene as HTL in place of spiro-OMeTAD and obtained an efficiency of 7.1% with J_{sc} of 21.3 mA/cm².

They also compared the two PSC structures, i.e., FTO/TiO₂/CH₃NH₃PbI₃/graphene/Au and FTO/TiO₂/CH₃NH₃PbI₃/spiro-OMeTAD/Au. They found that the PSC structure based on graphene as HTL is more stable as compared to spiro-OMeTAD based PSC. The authors also suggested that the PSC performance may be improved by changing the work function of graphene and proposed doping an approach to modify the work function of the graphene. Cao et al. [24] introduced functionalized nano-graphene (perthiolate trisulfur-annulated hexa-peri-hexabenzocoronene, TSHBC) as HTL in PSC which accelerated the hole transportation rate. The authors obtained an efficiency of 12.81% with the device structure of FTO/TiO₂/CH₃NH₃PbI₃/TSHBC/Au. They also suggested that graphene has better stability as compared to spiro-OMeTAD and the device fabrication can be finished with 45% humidity in air, while the condition is not suitable for spiro-OMeTAD. The hole-transportation can be improved by introducing extra graphene into TSHBC as the PSC reached an efficiency of 14%. Yan et al. [25] reported that the hole extraction rate for multilayer and single layer graphene 5.1 and 3.7 ns⁻¹, respectively and obtained an efficiency of 11.5% with multilayer graphene. According to the findings of the authors, Schottky barrier is formed at the perovskite/graphene interface and an increase in the hole transportation and extraction rate achieved by decreasing the recombination rate as well as blocking the electrons.

Moreover, graphene has been used as an additive in ZnO and TiO₂ based ETLs, and showed an increase in efficiency [26,27]. Wang et al. [27] further compared different PSC structures with TiO₂ ETL, graphene only ETL, without ETL and high temperature sintered TiO₂ ETL. They found that the highest efficiency was achieved for the cell with TiO₂ + graphene as an ETL, but nonetheless graphene-based PSC showed better results when compared to the cell without an ETL. This demonstrates that graphene is a suitable ETL for PSCs, and the PCE and life time of solar cell devices could thus be increased using graphene as a buffer layer.

The PSCs using p-Si as an inorganic HTL have been previously modelled [28], and it has been further indicated that p-Si has good chemical stability, low cost and higher hole mobility [29]. Yan et al. [30] also studied a low-cost Si-nanowire/perovskite hybrid solar cell and obtained a PCE of 13.3%. Therefore, PSCs with n-cSi (crystalline silicon) layer may also be modelled to investigate its role as an efficient ETL.

Having the above observations in mind, the PSCs performance can be upgraded by inserting the perovskite layer in between the graphene and Si layers. In the present work, we designed the following configuration p-graphene/CH₃NH₃PbI₃/n-cSi for hybrid solar cell using AFORS-HET (Automat for Simulation of Heterostructures) software [31–33]. We optimized each layer and obtained the parameters for highest efficiency. The impact of operating temperature and textured front surface on the performance of the solar cell is also examined. Our findings indicate that p-graphene and n-cSi can act as an efficient HTL and ETL, respectively, in PSC and promising hybrid solar cells may be achieved with superior performance.

2. Simulating structure

To realize the idea related to the usage of graphene as HTL material, we used a theoretical approach to study hybrid PSCs with silicon layer. We designed a p-i-n based structure as p-graphene/CH₃NH₃PbI₃/n-cSi, with ITO (indium tin oxide) and Ag (silver) as the front and back contacts in AFORS-HET software. AFORS-HET uses the one-dimensional semiconductor equations related to Shockley-Reed Hall statistics solver and Lambert-Beer law for optical parameters of different photovoltaic devices. The complete PSC configuration is ITO (80 nm)/p-graphene (0.334–6.68 nm)/CH₃NH₃PbI₃ (50–1000 nm)/n-cSi (50–300 μm)/Ag (0.001 cm) as shown in Fig. 1. Here, the light enters through the graphene layer and generates electron-hole pairs in the active layer. The generated electrons and holes are collected and transferred by n-cSi and p-graphene to the respective electrodes. We optimized various parameters such as layer thickness, electron affinity (χ), energy band gap (E_g), dielectric constant (ϵ_k), doping concentrations (N_D/N_A), effective conduction

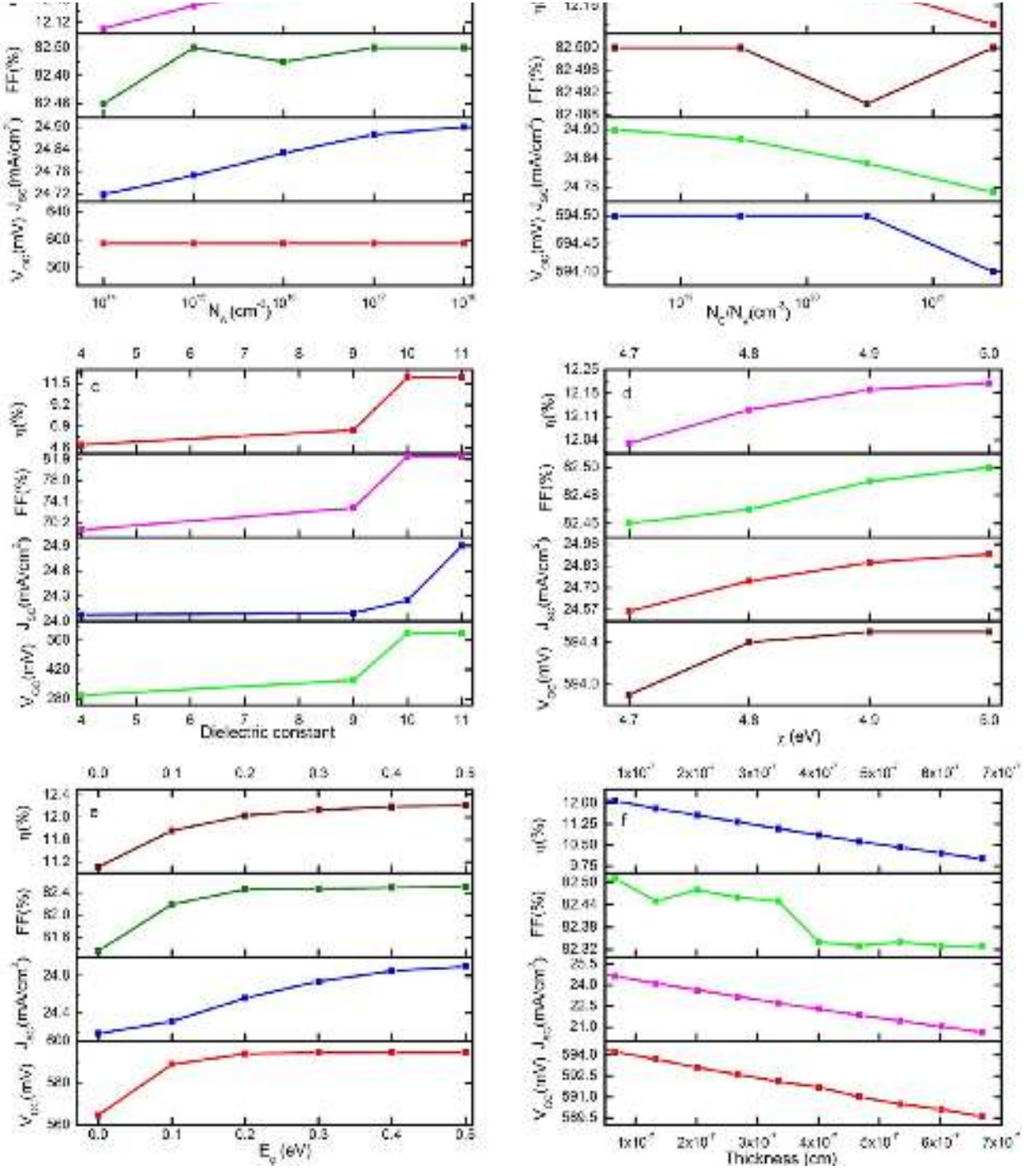


Fig. 2. PSC performance with the p-graphene parameters (a) N_A (cm⁻³), (b) N_C/N_V (cm⁻³), (c) dielectric constant (dk), (d) electron affinity (χ) (eV) (e) energy band gap E_g (eV) and (f) thickness, where the value of perovskite and n-cSi parameters used were: $N_C/N_V = 2.5 \times 10^{20}$ cm⁻³ and 3×10^{20} cm⁻³, $N_D = 2 \times 10^{14}$ cm⁻³ and 1×10^{16} cm⁻³, $E_g = 1.5$ eV and 1.12 eV, $\chi = 3.9$ eV and 4.05 eV, dk = 30 and 11.9, and thickness = 50 nm and 1 μ m, respectively.

valence band density (N_C/N_V) to observe the effect on the PCE (η), open circuit voltage (V_{oc}), fill factor (FF) and current density (J_{sc}) PSC. The simulation parameters used in the software are taken from different experimental and theoretical reports [34–40].

To increase the efficiency of solar cells, the front contact surface can be made textured. This textured surface reduces light reflection; assists in light trapping and consequently may increase the overall device efficiency due to increase in current. In this work both non-textured and textured front contacts have been applied to model the solar cell performance.

| | | | |
|---|-----------------------|----------------------|--------------------|
| Thickness(cm) | 3.34×10^{-8} | 5×10^{-6} | 0.03 |
| dk | 11 | 6.5 | 11.9 |
| $N_A(\text{cm}^{-3})$ | 1×10^{18} | 1×10^{13} | – |
| $N_D(\text{cm}^{-3})$ | – | 1×10^{13} | 1×10^{17} |
| $N_C(\text{cm}^{-3})$ | 3×10^{18} | 2.5×10^{20} | 3×10^{19} |
| $N_V(\text{cm}^{-3})$ | 3×10^{18} | 2.5×10^{20} | 3×10^{19} |
| $\mu_e (\text{cm}^2\text{V}^{-1}\text{s}^{-1})$ | 1×10^5 | 50 | 1350 |
| $\mu_h (\text{cm}^2\text{V}^{-1}\text{s}^{-1})$ | 10 | 50 | 450 |
| χ (eV) | 5.0 | 3.9 | 4.4 |
| $E_g(\text{eV})$ | 0.5 | 1.5 | 1.12 |

3. Results and discussions

3.1. Optimization of p-graphene layer

The graphene can be made p-type material by doping with trivalent atoms like boron with the help of different techniques such thermal annealing, ball milling, liquid, solid and gaseous phase chemical doping, and in-situ doping during chemical vapour deposition (CVD) [41,42]. The heteroatom doping onto a graphene sheet can influence the chemical, physical, electronic and photon properties of the material. The solar cell parameter optimization was started by varying the parameters of p-graphene layer first, and taking some initial values of the perovskite and n-cSi layers. The values of perovskite layer parameters were fixed at $N_A = 3 \times 10^{18} \text{ cm}^{-3}$, $N_C/N_V = 2.5 \times 10^{20} \text{ cm}^{-3}$, $E_g = 1.5 \text{ eV}$, $\chi = 3.9 \text{ eV}$, $dk = 30$ and thickness = 50 nm. The n-cSi parameters used were $N_D = 1 \times 10^{16} \text{ cm}^{-3}$, $N_C/N_V = 3 \times 10^{19} \text{ cm}^{-3}$, $E_g = 1.12 \text{ eV}$, $\chi = 4.05 \text{ eV}$, $dk = 11.9$ and thickness = 100 μm . The p-graphene layer parameters kept at constant values were $N_C/N_V = 3 \times 10^{18} \text{ cm}^{-3}$, $E_g = 0.5 \text{ eV}$, $\chi = 5.0 \text{ eV}$, $dk = 11$ and thickness 0.334 nm. We varied the acceptor concentration (N_A) of the p-graphene layer from 1×10^{14} to $1 \times 10^{18} \text{ cm}^{-3}$ and observed its effect on PSC parameters. The variation of solar cell parameters with N_A is mentioned in Fig. 2(a). From the figure, it is observed that as N_A increases from 1×10^{14} to $1 \times 10^{18} \text{ cm}^{-3}$, the J_{sc} varies from 24.72 mA/cm^2 to 24.90 mA/cm^2 , V_{oc} remains constant, PCE changes from 12.11% to 12.21%, and FF varies from 82.46% to 82.50%. As we increase the acceptor concentration in graphene sheet, the graphene work function is lowered and the conductivity rises. This leads to an increase in J_{sc} , PCE and FF, but the V_{oc} does not change. Next, the parameters N_C and N_V are changed from 3×10^{18} to $3 \times 10^{21} \text{ cm}^{-3}$ for the monolayer graphene and the simulated results are given in Fig. 2(b). Again, the V_{oc} remained constant, but J_{sc} reduced from 24.90 mA/cm^2 to 24.77 mA/cm^2 , FF slightly changes from 82.50% to 81.49% and the overall efficiency also changed from 12.21% to 12.14% with increase in N_C/N_V value. The minority charge carrier density decreases as N_C/N_V is increased because of trapping of free carrier charges and small life time of photo induced carriers. The V_{oc} reduces from 594.50 mV to 594.40 mV beyond $\sim 5 \times 10^{20} \text{ cm}^{-3}$ as such high values of N_C/N_V reduces the barrier height and the built-in voltage [43]. Thus, reducing N_C/N_V below 10^{19} cm^{-3} gives high overall PCE. In both the cases of varying N_A and N_C/N_V , it is found that significant changes in the solar cell parameters are not observed in the range under study. This implies that the acceptor concentration and the band densities do not affect the charge transport to a great extent in this case.

The dielectric constant (dk) of p-graphene layer was varied keeping other parameters constant as given in Fig. 2(c). The dk value for single layer graphene is reported to be between 4 and 11 [44]. The maximum value of all the solar cell parameters is obtained at $dk = 11$ and the maximum values of V_{oc} , J_{sc} , FF and PCE are 594.50 mV, 24.90 mA/cm^2 , 82.50% and 12.21% respectively. The electron affinity values of p-graphene vary in the range 4.7–5 eV. The observations presented in Fig. 2(d) reveal that the PCE of the simulated cell increased from 12.03% to 12.21%, being maximum at 5 eV, because on increasing the electron affinity, the barrier height is increased which impedes the flow of electrons towards ITO leading to lesser recombination and results in rising J_{sc} from 24.56 mA/cm^2 to 24.90 mA/cm^2 . Thus, high electron affinity is useful for increasing the device efficiency. This is also in agreement with other reports where high electron affinity materials are shown to be preferable for HTL [45]. The change in the overall efficiency is low with change in electron affinity because even with the starting value of $\chi = 4.7$, the barrier height between the HTL/perovskite interface is appropriate for hole extraction.

Some studies [34,43] showed that graphene has a band gap that varies from 0 to 0.25 eV, whereas, Nevis et al. [35] have reported that graphene has a band gap of 0.5 eV. In view of these studies, we varied the energy gap of graphene from 0 to 0.5 eV as depicted in Fig. 2(e). The PCE increases from 11.11% to 12.21%, J_{sc} changes from 24.18 mA/cm^2 to 24.90 mA/cm^2 and V_{oc} rises from 564.70 mV to 594.50 mV with increase in the energy band gap of graphene. The band gap of graphene affects the built-in potential which may have caused the observed increase in V_{oc} . Only a small change in J_{sc} is observed since it is governed by the production of photocurrent in the active layer and not the band gap of the HTL. Moreover, a greater barrier at the interface between HTL and perovskite layer sufficiently separates the electron hole pair aiding in an increase in current. Consequently, the FF and PCE increase primarily due to increase in V_{oc} , but tends to saturate fast at high band gap.

The impact of number of graphene layers on solar cell performance is also investigated. The number of graphene layers directly influences the sheet resistance as well as the optical transparency. The sheet resistance and optical transparency is reduced with increase in the graphene layers. To prove this concept, the number of graphene layers is varied up to 20 layers. The highest efficiency of 12.21% is obtained for single layer graphene having thickness of 0.334 nm. As we increase the number of graphene layers the

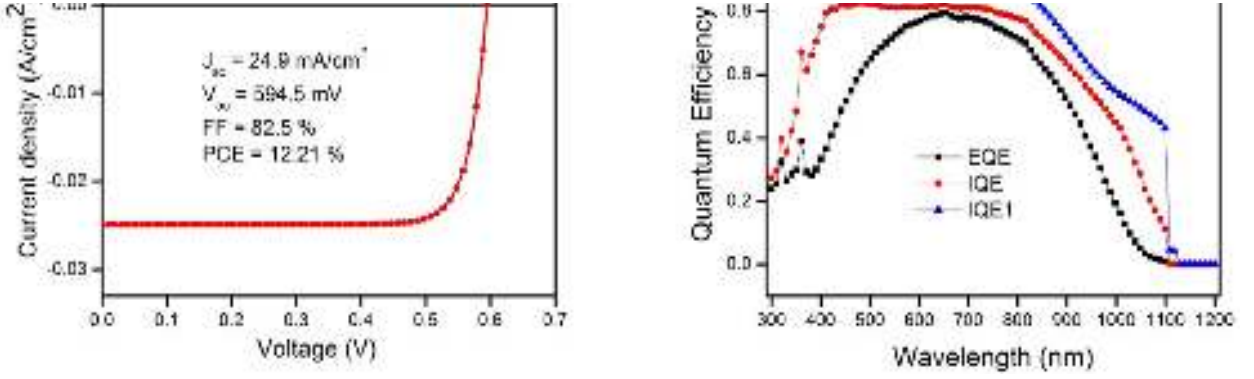


Fig. 3. (a) Current density-voltage curve for p-graphene optimized thickness and (b) Quantum Efficiency with wavelength.

efficiency decreases to 10.01%, while FF varies from 82.50% to 82.33%. The V_{oc} and J_{sc} also decrease from 594.50 mV to 589.60 mV and 24.90 mA/cm² to 20.63 mA/cm², respectively, with the number of layers or increase in thickness of graphene layer. The parameter variations are shown in Fig. 2(f). The decrease in performance may be attributed to lesser light reaching the active layer lowering the photocurrent. The monolayer graphene shows an optical transparency of 97.7% which is further reduced with increase in graphene layers [46]. An addition of each layer reduces the transparency by 2.3%.

After optimization of graphene parameters, we were able to achieve the best-suited properties of graphene layer that can be employed as a HTL in PSC. The optimized parameters are tabulated in Table 1. The current density-voltage (J-V) characteristics and the quantum efficiency (QE) variation versus wavelength for optimized p-graphene layer are plotted in Fig. 3(a) and (b) respectively. The figures depict the J-V characteristics and QE of the cell using optimized value of p-graphene parameter only and with the initial value mentioned above for perovskite and n-cSi layers. For the simulated structure, quantum efficiency has been calculated in the range 300–1200 nm under the illumination condition of $1 \times 10^{16} \text{ cm}^{-2} \text{ s}^{-1}$ monochromatic intensity and 10 nm spectral widths. Here the external quantum efficiency (EQE) and internal quantum efficiency (IQE and IQE1) of the optimized cell have been given in Fig. 3(b). The EQE gives the ratio of the charge carriers collected by the solar cell to the total number of incident photons by the external source regardless of whether the photons are absorbed, reflected or transmitted. On the other hand, the IQE considers only the photons absorbed by the solar cell but does not consider whether these absorbed photons contribute to the current or not. Thirdly, IQE1 further corrects the IQE by subtracting those photons which may be absorbed by the layers other than the active layer, i.e., the contact layer and thereby, not contributing to the current. Due to this, the EQE is always less than the IQE and IQE1 as observed in Fig. 3(b) as we

The IQE and EQE of the cell increase and reach a maximum value, and thereafter decrease with wavelength [39]. It may be interpreted from Fig. 3(b) that the IQE1 becomes almost unity in the wavelength range of 300–700 nm indicating that the photons absorbed by the perovskite layer contributes sufficiently to the output current. The IQE decreases for the whole wavelength range and is approximately 80% in the 400–800 nm range, implying significant absorption by the layers other than the perovskite layer. Further reduction in the quantum efficiency is found in EQE, suggesting a loss of incident photons. Thus, this points towards the need of device engineering so that improvement in the absorption of incident photons can be achieved by reducing the reflection losses.

3.2. Optimization of perovskite layer

After optimizing the p-graphene layer parameters, we optimized the perovskite layer parameters by using the values of p-graphene parameters as mentioned in Table 1 and keeping the n-cSi parameter values as given in section 3.1. The properties of the perovskite layer have been modified in order to obtain the largest PCE for this configuration. The dielectric constant (ϵ_k) of the active layer depicts the nature of Coulombic forces among the electron-hole pairs and other charges in the material. The behaviour of solar cell characteristics with ϵ_k is shown in Fig. 4(a). From the figure it can be seen that the PCE of the device varies from 12.27% to 12.21% with ϵ_k varying from 6.5 to 30 [47,48]. The FF of the device remains constant with value 82.51%, for ϵ_k 6.5–25, and after this FF falls up to the value 82.50% for ϵ_k from 25 to 30. The open circuit voltage (V_{oc}) of the device remains constant with value 594.50 mV and J_{sc} varies from 25.01 mA/cm² to 24.90 mA/cm² under the ϵ_k variation from 6.5 to 30. The optimized value of $\epsilon_k = 6.5$ is taken for further calculations.

After optimizing the ϵ_k , we varied the N_C/N_V within the range of 2.5×10^{18} to $2.5 \times 10^{20} \text{ cm}^{-3}$. As we increase N_C/N_V the minority charge carrier density decreases. Consequently, V_{oc} was constant at value 594.70 mV, fill factor varied from 82.27% to 82.49% and J_{sc} varied from 24.99 mA/cm² to 25.01 mA/cm² while the efficiency changed slightly from 12.23% to 12.27% (Fig. 4(b)). Further, the N_D is changed in the range 1×10^{13} to $1 \times 10^{17} \text{ cm}^{-3}$, and the simulated values in Fig. 4(c) illustrate that the defect states exist in the perovskite layer resulting a noticeable change in J_{sc} and efficiency value change from 12.27% to 12.01%.

The thickness of the absorbing layer plays a very important role in the working of the device. Keeping the optimized values of above-mentioned parameters for the absorbing layer, we varied the thickness of the active layer in the range 50–1000 nm [39]. With



Fig. 4. PSC performance with perovskite parameters optimization (a) dk , (b) N_C/N_V (cm⁻³), (c) N_A/N_D (cm⁻³), (d) thickness (nm) and (e) mobility (μ_e/μ_h) (cm²/Vs) where p-graphene and n-cSi parameters used were: $N_D = 0$ and 1×10^{16} cm⁻³, $N_C/N_V = 3 \times 10^{18}$ and 3×10^{19} cm⁻³, $E_g = 0.5$ eV and 1.12 eV, $\chi = 5$ and 4.05 eV, $dk = 11$ and 11.9, and thickness = 0.334 nm and 100 μ m, respectively.

the increase in absorber thickness, the absorption of light in the layer also increases. The variation in PCE, FF, J_{sc} and V_{oc} with absorber layer thickness is shown in Fig. 4(d). The PCE and J_{sc} decreases from 12.27% to 9.11% and 25.01 mA/cm² to 18.57 mA/cm² respectively with thickness because a thicker layer may result into increased recombination and lower charge collection efficiency. Hence, using an optimum thickness would ensure a maximum current output from the device. The value of active layer thickness taken around 50 nm in this case. After optimizing the thickness of the active layer, we varied the charge carrier mobility (μ_e/μ_h) in the range of 10^{-3} cm²/V.s to 50 cm²/V.s [49] and noted its effect on the performance of the cell. From Fig. 4(e) it is observed that all the



Fig. 5. (a) Current density-voltage curve for perovskite optimized thickness and (b) Quantum Efficiency with wavelength.



Fig. 6. PSC performance with the variation in n-cSi parameters, (a) N_D (cm⁻³), (b) N_C/N_V (cm⁻³), (c) χ (eV) and (d) thickness of n-cSi layer (μm) considering the parameters of p-graphene and perovskite layers mentioned in Table 1.

parameters i.e., J_{sc} , FF and efficiency increase significantly up to $10 \text{ cm}^2/\text{V.s}$ from the initial 13 mA/cm^2 to 25.01 mA/cm^2 , 18.79% to 82.48% and 1.68% – 12.27% respectively, except V_{oc} with mobility and become almost constant up to $50 \text{ cm}^2/\text{V.s}$. This may be due to the decrease in recombination of charge carriers as a result of increase in mobility. The optimized parameters for the perovskite layer are summarized in Table 1. Thus, it may be stated that the thickness and mobility values of the perovskite layer dominantly affect the performance of the solar cell. On the other hand, the parameters like dielectric constant, N_C/N_V , N_A/N_D only slightly improve the performance of the solar cell. This may be because at the initial value of the perovskite thickness taken as 50 nm , the charge transport



Fig. 7. (a) Current density-voltage curve for n-cSi optimized parameters and (b) Quantum Efficiency with wavelength.



Fig. 8. PSC parameters variations with operating temperature.

adequate due to the long diffusion length of the perovskite material and the suitable built-in voltage at the HTL/perovskite interface saturating the efficiency of the solar cell without much change in the parameter range under study. The solar cell J-V characteristics well as quantum efficiency variation with wavelength for optimized parameters are plotted in Fig. 5(a) and (b). The behaviour of the solar cell is shown for the optimized value of perovskite layer and p-graphene parameters mentioned in Table 1, while the parameters for n-cSi mentioned in section 3.1 were used.

3.3. Optimization of n-crystalline silicon layer

For obtaining the best possible structure, layer parameters were optimized by considering the parameters of p-graphene and perovskite layers mentioned in Table 1. As, the donor concentration (N_D) in n-cSi is increased from 1×10^{14} to $1 \times 10^{17} \text{ cm}^{-3}$, the work function of the ETL is increased, which results to an increase in V_{oc} . Thus, the values of the performance parameters of the device increased with increase in N_D and the results are shown in Fig. 6(a). Keeping $N_D = 1 \times 10^{17} \text{ cm}^{-3}$, the value of N_C/N_V was changed from 3×10^{19} to $3 \times 10^{21} \text{ cm}^{-3}$ and the variation in the device performance is depicted in Fig. 6(b). From the graph it is seen that the V_{oc} significantly reduces from 655.70 mV to 419 mV, while J_{sc} decreased slightly from 26.63 mA/cm^2 to 26.53 mA/cm^2 with the variation in N_C/N_V . The efficiency is reduced to 6.89%. Then taking the values of $N_C/N_V = 3 \times 10^{19} \text{ cm}^{-3}$ and $N_D = 1 \times 10^{17} \text{ cm}^{-3}$, we varied the electron affinity (χ) from 3.8 eV to 4.4 eV. In this range V_{oc} was found to change from 676.80 mV to 655.70 mV but J_{sc} decreased, and PCE of the cell increased initially and then remained constant as can be seen from Fig. 6(c). At $\chi = 4.2 \text{ eV}$, the highest value of PCE was attained and have a value 14.62%. Next, the thickness of the n-cSi layer was varied from 50 μm to 300 μm and the changes are depicted in Fig. 6(d). The highest PCE is found to be 16.75% at a thickness of 300 μm . The simulation parameters are mentioned in Table 1. The J-V characteristics and the QE for optimized thickness of n-cSi are plotted in Fig. 7(a and b) and confirm the PCE 16.75% for this configuration.



Fig. 9. PSC performance variation with textured front surface.

3.4. Influence of operating temperature on the PSC performance

The different layer parameters in heterojunction solar cell have been optimized to obtain the best possible efficiency of the device. To further understand the working of the device, the influence of operating temperature needs to be analysed. In the present study, we vary the temperature from 300 K to 550 K. The temperature change is taken as an external parameter in the simulation. As the temperature increases across the device, the energy band gap of the layer and carrier mobility across the layer interface is affected. The carrier recombination increases with the rise in temperature. The degradation in the values of performance parameters has also been previously attributed to the deformation and stress enhancement in device under the influence of temperature. These effects cause poor interconnectivity between the layers and create more interfacial defects. A high recombination rate prevails resulting into an increase in the series resistance and reduction in carrier diffusion length [50]. This is confirmed by our modelling results as depicted in Fig. 10. The highest PCE is obtained at 300 K and has a value of 16.75%.

3.5. Effect of textured surface on the PSC performance

The working of a device is also influenced by the nature of front contact surface i.e. whether the front contact is plane or textured. When the light falls on the front contact having textured surface with pyramidal topology, its angle of incidence with reference to the inclined surfaces of the pyramids is changed. The front textured surface not only reduces the reflection, but also in effect increases the optical path length as well as the optical thickness of the absorber. We simulated the PSC structure with and without textured front surface. The variations in solar cell parameters with the angle of inclination of the pyramidal texture, henceforth referred to as the angle of texture, are presented in Fig. 9. The PCE with textured surface is 17.90% as compared to 16.75% without textured surface. Here, we varied the angle of texture from 0° to 80°. The value of J_{sc} is increased from 28.97 mA/cm² to 30.87 mA/cm². This variation in J_{sc} is related to the optical confinement of the incident light due to multiple reflections; the short circuit current density being proportional to incident light. The value of V_{oc} increases from 686.3 mV to 687.9 mV with the angle of texture varying from 0° to 80°. The FF of the device also varies from 84.22% to 84.31%. This behaviour is due to the increased optical confinement with increase in the angle of textured surface.

The PCE of 17.90% for textured front surface is in good agreement with the other graphene based experimental studies [51] and is also enhanced as compared to other Si/perovskite hybrid solar cell [30].

4. Conclusion

In the present study, we used p-graphene and n-cSi as a hole transporting material and electron transporting material respectively in the perovskite solar cell and simulated the structure using AFORS-HET software. We noticed the effect of different layers on the working of the device. The power conversion efficiency 16.75% is obtained for optimized structure. We studied the effect of operating temperature as well as textured surface of front contact on the device performance. The device parameters decrease with the rise in temperature. The efficiency for textured front surface is 17.90% as compared to 16.75% for plane front surface. This theoretical study shows that p-graphene and n-cSi can be used as an efficient HTL and ETL respectively in hybrid perovskite solar cell.

Authors Statement

All the authors equally contributed for the problem, mention in the manuscript.

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Appendix A. Supplementary data

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