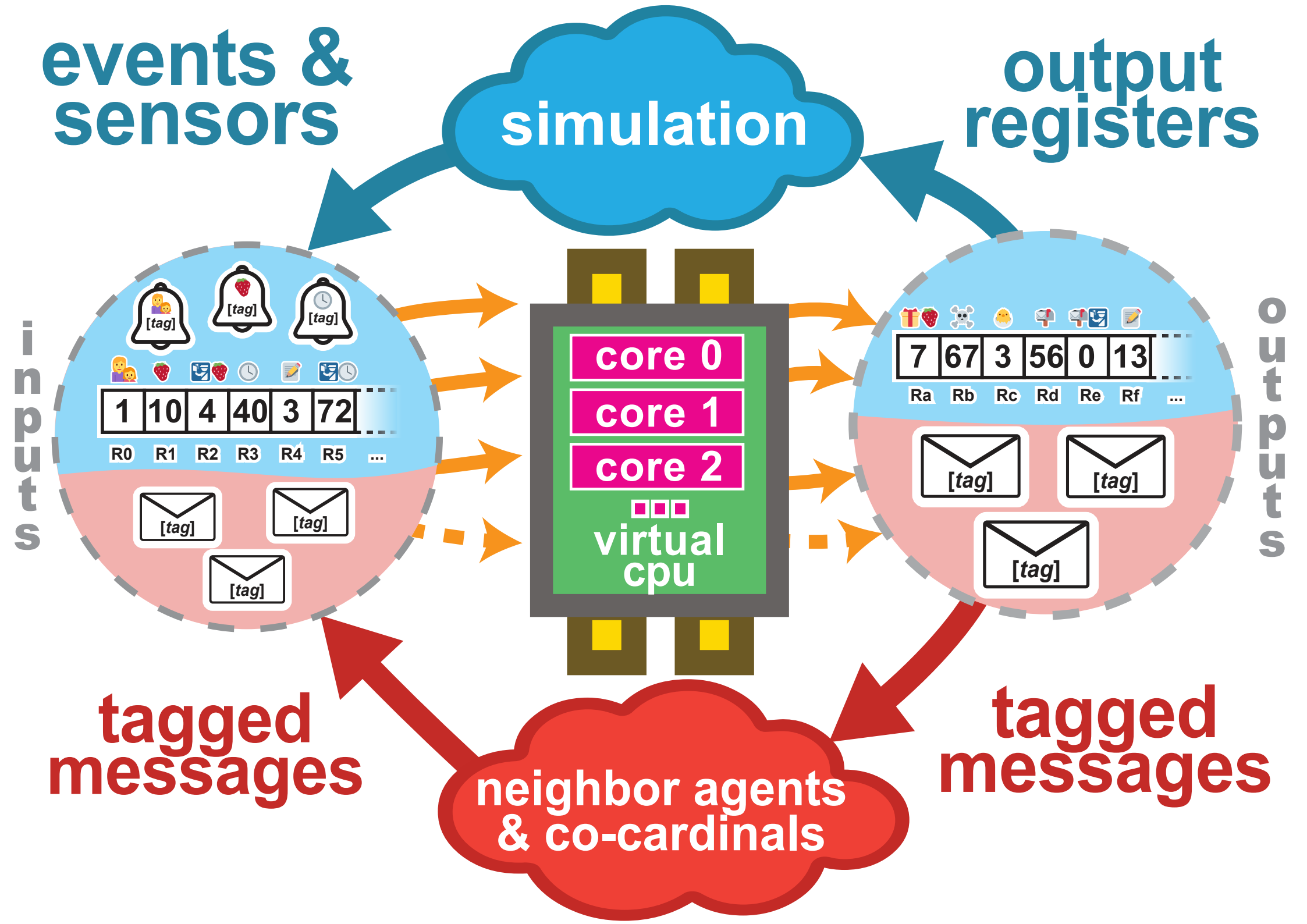
events & sensors
simulation
output registers
inputs
outputs
core 0
core 1
core 2
virtual cpu
1 10 4 40 3 72
R0 R1 R2 R3 R4 R5 ...
7 67 3 56 0 13
Ra Rb Rc Rd Re Rf ...
[tag]
tagged messages
neighbor agents & co-cardinals
tagged messages