Given a cache hierarchy with 8 words with a cache access time of 5 ns and a memory access time of 50ns, and the following set of memory accesses representing the tag and index: 22, 28, 31, 22, 16, 13, 31, 8, 5, 13, 8, and 5.

a) If the cache is a direct-mapped cache, show the sequence for accessing the cache and calculate the total access time. Draw the final cache table

b) If the cache is a 2-way set associative cache, show the sequence for accessing the cache and calculate the total access time.

c) Determine which cache has the better performance and by how much.

Average Memory Access Time

Given a Level-1 cache with a hit rate of 95%, a cache access time of 5 ns, and a memory access time of 100ns, find the average memory access time

The processor in the Example is given a L2 cache with a hit rate of 85%, and a cache access time of 7ns. Find the average memory access time

The processor in the Example is given a L3 cache with a hit rate of 80%, and a cache access time of 10ns. Find the average memory access time