Given a Direct-Mapped Cache with 8 words and the address references and the address references representing the tag and index: 22, 26, 22, 26, 16, 3, 16 and 18, and show the sequence for accessing the cache. Assume that the cache is initialized as empty. Draw the final cache table

Index	V	Tag	Data
000	~~N~~ Y	10	m(16,
001	N		
010	~~N~~ Y	11→10	m(26, 18)
011	~~N~~ Y	00	m(3,
100	N		
101	N		
110	~~N~~ Y	10	m(22,
111	N		

22(miss), 26(miss), 22(Hit), 26(Hit), 16(miss), 3(miss)
16(Hit), 18(miss) => 5M, 3H

Given a 2-way Set Associative Cache with 8 words and the address references representing the tag and index: 22, 26, 22, 16, 3, 16, and 18, show the sequence for accessing the cache. Assume that the cache is initialized as empty. Draw the final cache table.

Index	V	Tag	Data	V	Tag	Data
00	~~N~~ Y	100	m(16	N		
01	N	~~100~~		N		
→ 10	~~N~~ Y	101→110→100	m(22, ~~26~~, 18)	~~N~~ Y	101→110	m(22, ~~26~~)
11	~~N~~ Y	000	m(3~~5~~	N		

22(miss), 26(miss), 22(hit), 26(hit), 3(miss), 16(miss), 18(miss)

18→10010 3H→5M