


# Mayuresh Rajwadkar

 Education



m.m.rajwadkar@ieee.org  
<https://bit.ly/mmrlin>  
+1-626-MAYUR-MR  
Stoughton MA

**F.W. Olin Graduate School of Business / Babson College**  
**MBA** Entrepreneurship 08/2018  
Management-Consulting  
Partnership Model and GTM strategy for hi-tech product



**Harvard Business School – HBX**  
Negotiation Mastery 2017  
Disruptive Strategy 2016  
Credential Of REadiness (CORE) 2015



**Indian Institute of Technology, Bombay**  
**M. Tech.** Microelectronics 1998  
**M. Sc.** Physics 1996



**University of Mumbai**  
**B. Sc.** Physics, Instrumentation 1994



 Work

## Analog Devices (ADI) – 2001-2018

Electronic Design Automation (EDA) Engineer

Norwood MA 2013-2018

- Designed, developed and maintained a IC design and software management system and lead a global team to support it.
- Mentored and directed Corp IT Engineers for data security and IP protection for chip design databases and access control.
- Vendor management for EDA tools and licenses.
- EDA design support for internal chip design teams.

EDA Engineer

Austin TX 2006-2013

- EDA design support for internal chip design teams
- Vendor management for EDA tools and licenses
- Mentored & Trained Salt Lake City, team for digital SOC development
- IT management and support for center 2009 - 2013

IC Design Engineer

San Jose CA 2001-2006

- Lead IT integration, planing and implementation of business unit post acquisition.
- Design & Development of SOC products and embeded software
- EDA and IT Management of design center
- Global EDA support partnership development for SOC products
- Lead technology seperation, planning and implementation of business unit after divesture.

## Chiplogic Inc > Analog Devices

Santa Clara CA 1999-2001

## Philips Software Center > NXP

Bangalore, India 1999-1999

## Duet Technologies Ltd > Qualcomm

Noida, India 1998-1999

 Volunteer

## IIT Association of Greater New England (IITAGNE)

VP IIT Bombay, Digital Marketing and Boston South Suburbs Outreach

Indian Institutes of Technology Association of Greater New England (IIT AGNE) is a 501(c)(3) non-profit engaging in and supporting educational and scientific research activities. The organization leverages the successes of its peers to serve as role models and exemplars in STEM fields in the New England area

2016 IIT Leadership Conference, Providence RI

Lead the Digital Marketing activities of the conference

MBA (2018) candidate at Babson College, a Semiconductor and IT Professional with ~20 years of technical, operational, management and leadership experience in the industry. Worked in multiple locations, multi-cultural and multi-ethnic environments. Expert at solving and navigating through hard situations and problems with innovative ideas, approaches and grit.

## Skills Profile

Leadership  
Team  
Problem Solving  
Strategic Thinking  
Change Mgmt.  
Global Mgmt.  
Technology  
Operations

