Name: Miftah Uddin Md Ihsan   ID: 1710125 9   Section: 2

Department of Computer Science and Engineering
CSE340: Computer Architecture
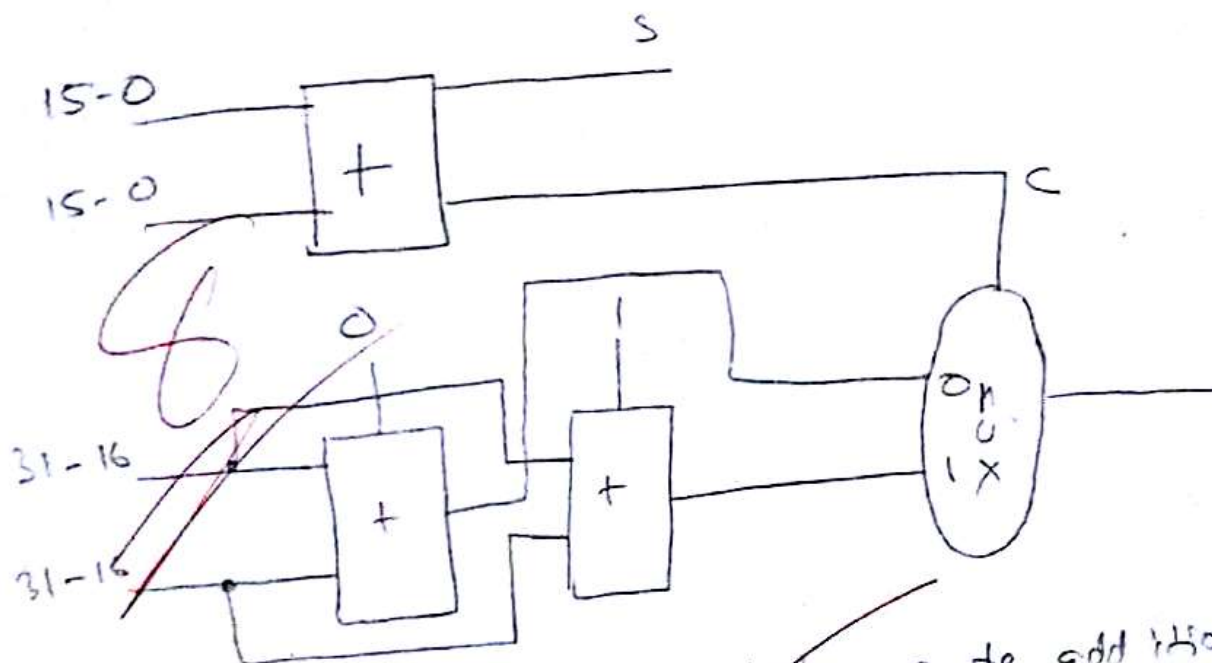### Summer 2017
Quiz-1, D
Full Marks: 15 Time: 20 Mins

1. Design a "Conditional Sum" adder and explain its operation.

A conditional sum adder is used /because to add this is
... done ... by /using ... two ... carries
O & 1 . what even carray is needed to perform this operation
is entered in the selecting bit of the mux & from there the mux
chooses which one to take. It is also faster then a ripple carry add
as it does not have to wait for the carry from the

1. Consider the below function. Write the equivalent MIPS code ant also its encoded form. x, y and z are Previous
in registers $10,$11,$12.

{ $10 $10 $11 $12

$$x = x + y + z;$$
$$y = 4x - z;$$
}

Name: Mifrah Uddin Md Ihsan   ID: 1710125   Section: 02

Department of Computer Science and Engineering
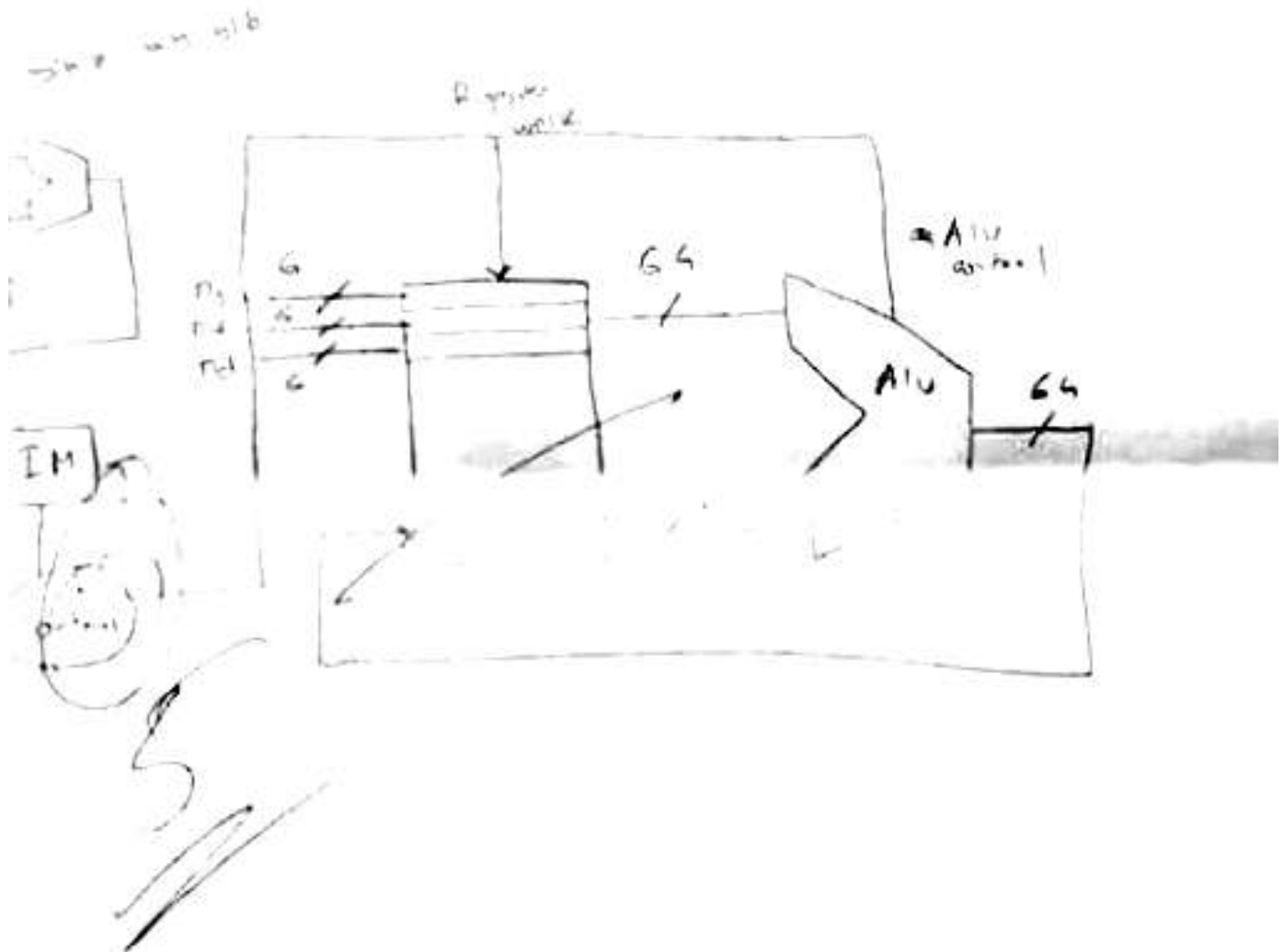## CSE340: Computer Architecture
Summer 2017
Quiz-2, B
Full Marks: 15 Time: 20 Mins

1. Design a 64-bit register file.   5

2. Write MIPS code for the following C code: if (A[6]!=A[3]) f[3]=g[6]-C[2]+9; else f[5]=g[6]+A[9];
   Assume base addresses for A, g and C are $s0,$s1 and $s2 respectively and base address of f is in $s3.