**Name: \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ ID: \_\_\_\_\_\_\_\_\_\_\_\_\_**

**Department of Computer Science and Engineering**

**CSE340: Computer Architecture   
Summer 2014**

**Quiz-1**

**Full Marks:15 Time: 20 Mins**

1. Consider the functiondraw a combinational logic circuit that implements this function. Also draw the Truth table for this function. **6**
2. Design and explain operation of a circuit built with D flip-flop that can implement left shift, right shift and data store operation. The circuit should also have the provision to clear all flip- flop when required**.** **6**
3. Design an ALU with logical gates. **3**