**Name: \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ ID: \_\_\_\_\_\_\_\_\_\_\_\_\_**

**Department of Computer Science and Engineering**

**CSE340: Computer Architecture   
Spring 2015**

**Quiz-2**

**Full Marks: 15 Time: 20 Mins**

1. What do you mean by zero and sign extended? **2**
2. Write MIPS instruction for the expression **f=(g\*h)+(i\*j),**  using minimum number of registers. **5**
3. Convert ***bgt $s1,s2****, Exit* into equivalent MIPS code. [bgt= branch if greater than] **3**

1. Encode the following also mention their types: **5**
   1. srl $t1,$t2,8.
   2. slt $t1,$t2,$t3.