**Name: \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ ID: \_\_\_\_\_\_\_\_\_\_\_\_\_**

**Department of Computer Science and Engineering**

**CSE340: Computer Architecture   
Spring 2014**

**Quiz-1**

**Full Marks:15 Time: 20 Mins**

1. Define **compiler** and **assembler.**  **2**
2. Encode the MIPS instruction **addi $16,$17, 18** and **lw $16,40($17)** also find their type**.** **3**
3. Write MIPS instruction for the expression **f=(g-h)+(i-j),**  using minimum number of

Register. **3**

1. What type instruction is **JAL**? What value does $**ra**/**$31** stores**?**   **3**
2. Convert ***blt $s1,s2****, Exit* into equivalent MIPS code. [blt= branch if less than] **4**