**Name: \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ ID: \_\_\_\_\_\_\_\_\_\_\_\_\_**

**Department of Computer Science and Engineering**

**CSE340: Computer Architecture   
Fall 2014**

**Quiz-3**

**Full Marks:15 Time: 30 Mins**

1. Why do you need multiple unites of same component in a single cycle datapath? **2**
2. Let A= (0 1010 101)2 and B= (0 1101 110)2 perform (A+B) using IEEE754 floating point addition**.** Also show the decimal equivalent. **4**
3. What do you mean by Response time, Throughput, CPI and effective CPI? **4**
4. Draw the datapath for instruction: add $10,$11,$12. Also show the dataflow. **5**