**Name: \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ ID: \_\_\_\_\_\_\_\_\_\_\_\_\_**

**Department of Computer Science and Engineering**

**CSE340: Computer Architecture   
Summer 2014**

**Quiz-3**

**Full Marks: 15 Time: 25 Mins**

1. State single cycle datapath advantages and disadvantages? **3**
2. What do you mean by CPU time, CPI and effective CPI? **3**
3. Explain the five steps of load instruction. ` **4**
4. What is pipelining? What makes Pipelining easy? Why it is hard at the same time? **5**