Course Schedule

Course Schedule Spring 2022

EE5143: Design of Digital Circuits
Course Schedule

Due Dates by Module

Assignment	Due	Points
Module 1: Broad Overview of the Landscape of Digital Design (Jan. 10 – Jan. 16)		
M1 Exercises	Sun., Jan. 16 @ 11:59 p.m. P.T.	10
Module 2: Architecture and Functioning of Modern Programmable Digital ICs (Jan. 17 – Jan. 23)		
M2 Exercises	Sun., Jan. 23 @ 11:59 p.m. PT	10
Module 3: Design Entities and Architecture in VHDL Language (Jan. 24 – Jan. 30)		
M3 Exercises	Sun., Jan. 30 @ 11:59 p.m. PT	10
M3 Lab	Sun., Jan. 30 @ 11:59 p.m. PT	15
Module 4: Concurrency and Associated Topics in VHDL (Jan. 31 – Feb. 6)		
M4 Exercises	Sun., Feb. 6 @ 11:59 p.m. PT	10
M4 Lab	Sun., Feb. 6 @ 11:59 p.m. PT	25
Module 5: Identifiers, Signals, Constants, Variables and Types in VHDL (Feb. 7 – Feb. 13)		
M5 Final Project Proposal	Sun., Feb. 13 @ 11:59 p.m. PT	30
M5 Lab	Sun., Feb. 13 @ 11:59 p.m. PT	25
Module 6: Code Reusability: Libraries, Subprograms and Packages in VHDL (Feb. 14 – Feb. 20)		
M6 Exercises	Sun., Feb. 20 @ 11:59 p.m. PT	10