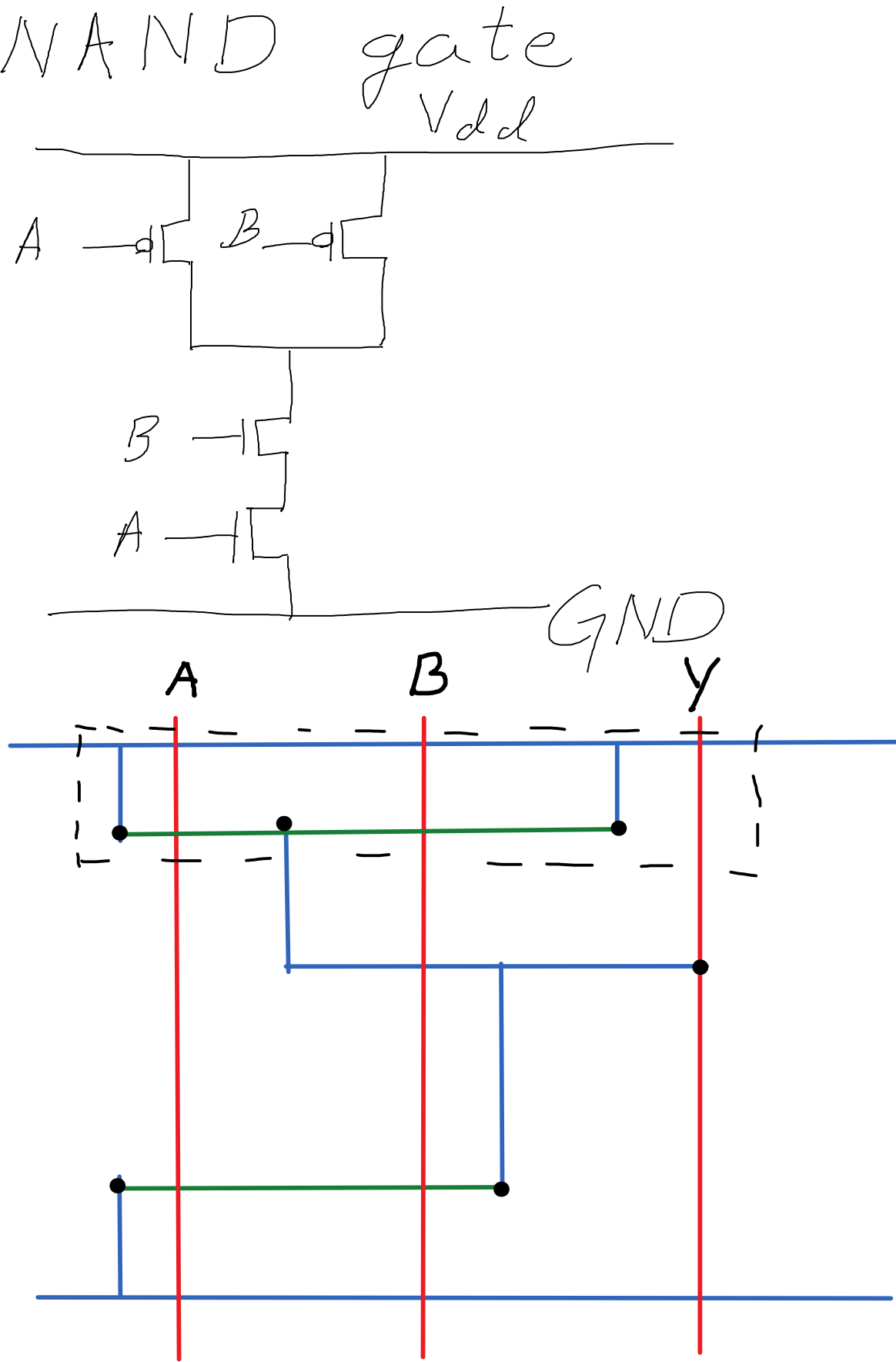
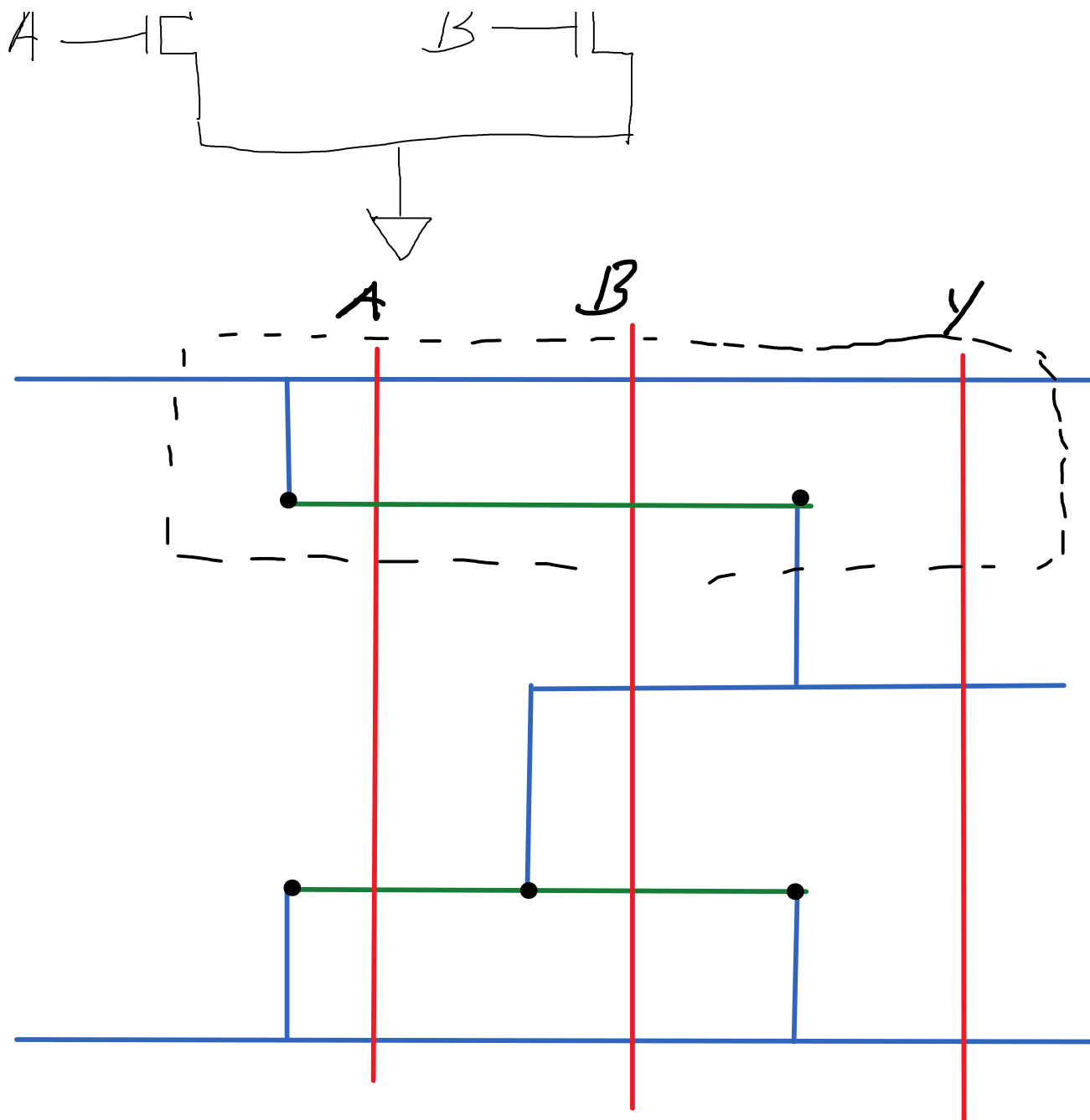


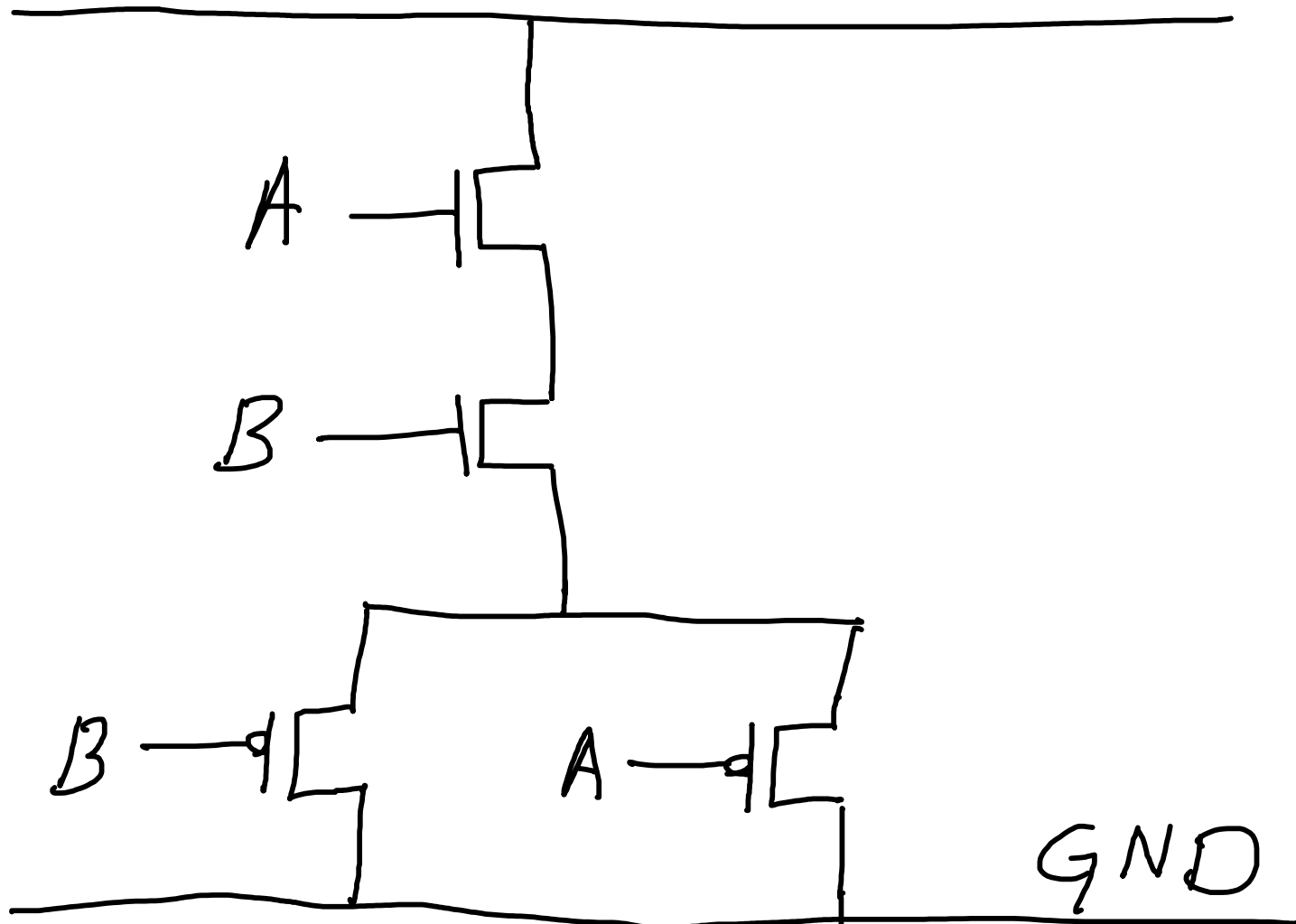
Q1. Draw stick diagrams for a 2-input NAND gate, a 2-input NOR gate, a 2-input AND gate and a 2-input OR gate.





AND Gate

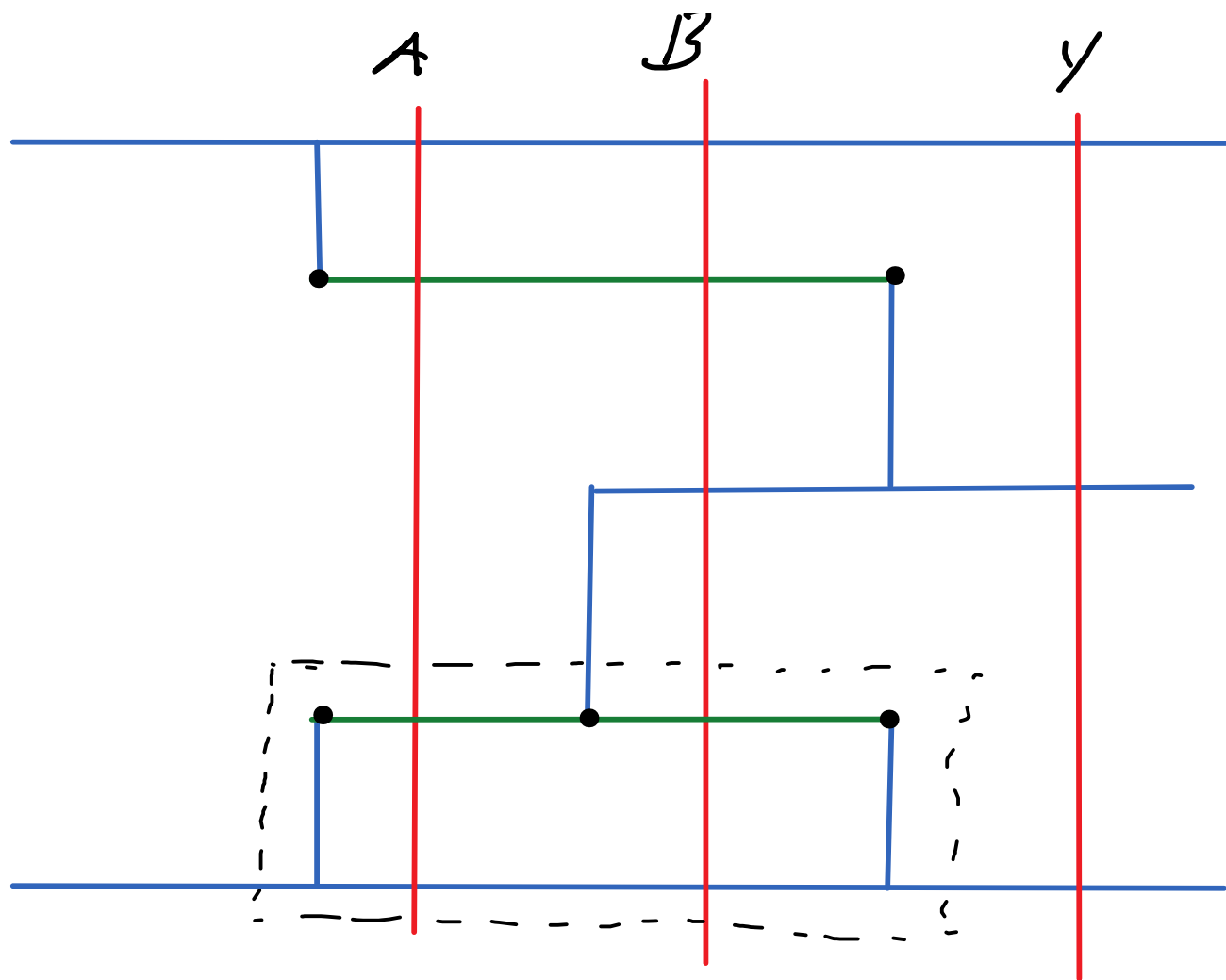
V_{DD}



A

B

Y



OR Gate

