

Test Nr.	CPU0 [MHz]	CPU1 [MHz]	CPU2 [MHz]	Communication	Bus busy % of master cyc	Bus transferring % of master cyc	Energy [uJ]	Time [ms]
1	200	200	200	multiproc	56.25	20.63	605.5	9.979
2	200	200	200	shared	44.89	16.20	894.5	14.147
3	100	100	100	shared	23.57	8.25	589.2	26.264
4	67	67	67	shared	16.54	5.77	578.6	37.747
5	50	50	50	shared	12.71	4.43	580.7	49.284
6	100	200	200	shared	37.39	13.30	910.5	18.875
7	200	100	200	shared	39.42	14.22	768.9	14.116
8	200	200	100	shared	36.61	13	999.6	21.524
9	200	100	100	shared	30.81	10.91	811.4	21.436