

DATA SHEET

SURFACE-MOUNT CERAMIC MULTILAYER CAPACITORS

Mid-voltage

NPO/X7R

100 V TO 630 V

0.47 pF to 2.2 μF

RoHS compliant & Halogen Free



YAGEO Phi(comp



Surface-Mount Ceramic Multilayer Capacitors Mid-voltage

NP0/X7R 100 V to 630 V

SCOPE

This specification describes Midvoltage NP0/X7R series chip capacitors with lead-free terminations.

APPLICATIONS

PCs, Hard disk, Game PCs Power supplies LCD panel ADSL, Modem

FEATURES

Supplied in tape on reel Nickel-barrier end termination RoHS compliant Halogen Free compliant

ORDERING INFORMATION - GLOBAL PART NUMBER, PHYCOMP CTC & I2NC

All part numbers are identified by the series, size, tolerance, TC material, packing style, voltage, process code, termination and capacitance value.

YAGEO BRAND ordering code

GLOBAL PART NUMBER (PREFERRED)

CC XXXX X X XXX X B X XXX (1) (2) (3) (4) (5) (6) (7)

(I) SIZE - INCH BASED (METRIC)

0201 (0603) / 0402 (1005) / 0603 (1608) / 0805 (2012) / 1206 (3216) / 1210 (3225) 1808 (4520) / 1812 (4532)

(2) TOLERANCE

 $C = \pm 0.25 pF$

 $D = \pm 0.5 pF$

 $F = \pm 1\%$

 $G = \pm 2\%$

 $J = \pm 5\%$

 $K = \pm 10\%$

 $M = \pm 20\%$

(3) PACKING STYLE

R = Paper/PE taping reel; Reel 7 inch

K = Blister taping reel; Reel 7 inch

P = Paper/PE taping reel; Reel 13 inch

F = Blister taping reel; Reel 13 inch

C = Bulk case

(4) TC MATERIAL

NPO

X7R

(5) RATED VOLTAGE

0 = 100 V

A = 200 V

Y = 250 V

B = 500 V

Z = 630 V

(6) PROCESS

N = NP0

B = Class 2 MLCC

(7) CAPACITANCE VALUE

2 significant digits+number of zeros

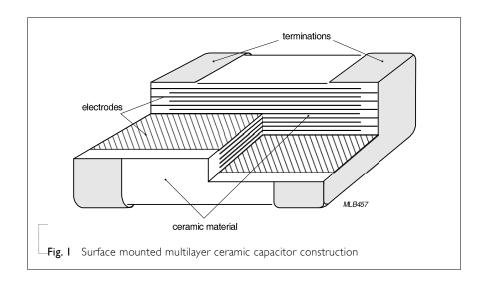
The 3rd digit signifies the multiplying factor, and letter R is decimal point

Example: $121 = 12 \times 10^{1} = 120 \text{ pF}$

CONSTRUCTION

The capacitor consists of a rectangular block of ceramic dielectric in which a number of interleaved metal electrodes are contained. This structure gives rise to a high capacitance per unit volume.

The inner electrodes are connected to the two end terminations and finally covered with a layer of plated tin (NiSn). The terminations are lead-free. A cross section of the structure is shown in Fig. I.

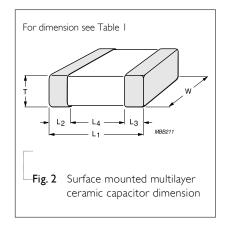


DIMENSION

Table I For outlines see fig. 2

TYPE	L _I (mm)	W (mm)	T (MM)	L ₂ / L ₃ (I min.	mm) max.	L ₄ (mm) min.
0201	0.6 ±0.03	0.3±0.03	0.3±0.03	0.10	0.20	0.20
0402	1.0 ±0.05	0.5 ±0.05	0.5 ±0.05	0.15	0.30	0.40
0603	1.6 ±0.10	0.8 ±0.10	0.8 ±0.10	0.20	0.60	0.40
0805	2.0 ±0.20	1.25 ±0.20	0.6 ±0.10 0.85 ±0.10 1.25 ±0.20	0.25	0.75	0.70
1206	3.2 ±0.30	1.6 ±0.20	0.6 ±0.10 0.85 ±0.10 1.25 ±0.20 1.6 ±0.20	0.25	0.75	1.40
	3.2 ±0.30	1.6 ±0.30	1.6 ±0.30			
1210	3.2 ±0.30	2.5 ±0.20	0.85 ±0.10 1.25 ±0.20 2.0 ±0.20	0.25	0.75	1.40
1808	4.5 ±0.40	2.0 ±0.30	1.25 ±0.20	0.25	0.75	2.20
1812	4.5 ±0.40	3.2 ±0.30	0.85 ±0.10 1.25 ±0.20 1.6 ±0.20	0.25	0.75	2.20

OUTLINES





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Surface-Mount Ceramic Multilayer Capacitors | Mid-voltage | NP0/X7R | 100 V to 630 V

CAPACITANCE RANGE & THICKNESS FOR NPO

	0.200 0 0.20 .	2.422		
Table 2	Sizes from 0201	to 0805		

0.22 pF 0.47 pF 0.56 pF 0.68 pF	250 V 500 V) V 630V
0.47 pF 0.56 pF		
0.56 pF		
0.68 pF		
0.82 pF		
1.0 pF		
1.2 pF		
1.5 pF		
1.8 pF		
2.2 pF		
2.7 pF		
3.3 pF		
3.9 pF		
4.7 pF		
5.6 pF 0.3±0.03 0.5±0.05 0.8±0.1 0.8±0.1 0.8±0.1 0.6±0.1 0.6±0.1	6±0.1 0.6±0.	0.6±0.1
6.8 pF		
8.2 pF		
10 pF 12 pF		
15 pF		
18 pF		
22 pF		
27 pF		
33 pF		
39 pF		
47 pF		
56 pF		
68 pF		
82 pF		
100 pF		

- I. Values in shaded cells indicate thickness class in mm
- 2. Capacitance value of non E-12 series is on request

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Surface-Mount Ceramic Multilayer Capacitors | Mid-voltage | NP0/X7R | 100 V to 630 V

CAPACITANCE RANGE & THICKNESS FOR NPO

Table 4 Sizes from 0603 to 0805 (continued)

120 pF 150 pF	100 V	100 V	200 V	250 V	100.17			=001/	456
150 pF				230 V	100 V	200 V	250 V	500 V	630 V
						0.6± 0.1	0.6± 0.1	0.6± 0.1	0.6± 0.1
180 pF									
220 pF									
270 pF			0.8± 0.1	0.8± 0.1					
330 pF	0.5± 0.05		010= 011	010= 011	0.6± 0.1			0.85±0.1	0.85±0.1
390 pF		0.8± 0.1							
470 pF						0.85±0.1	0.85±0.1		
560 pF									
680 pF								1.25±0.2	1.25±0.2
820 pF									
I.0 nF									
I.2 nF									
I.5 nF					0.85±0.1				
I.8 nF									
2.2 nF						1.25±0.2	1.25±0.2		
2.7 nF									
3.3 nF									
3.9 nF					125.02				
4.7 nF					1.25±0.2				
5.6 nF									
6.8 nF									
8.2 nF 10 nF									
I2 nF									
15 nF									
13 mF									
22 nF									

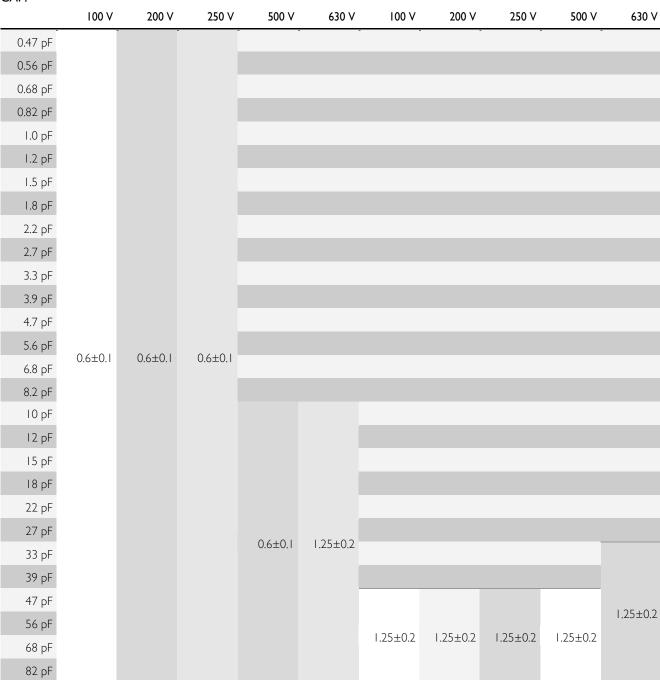
- 1. Values in shaded cells indicate thickness class in mm
- 2. Capacitance value of non E-12 series is on request



CAPACITANCE RANGE & THICKNESS FOR NPO

Table 5 Sizes from 1206 to 1210





- 1. Values in shaded cells indicate thickness class in mm
- 2. Capacitance value of non E-12 series is on request

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CAPACITANCE RANGE & THICKNESS FOR NPO

Table 7 Sizes from 1206 to 1210 (continued)

CAP.	1206	n 1206 to 12	TO (COITUITU	eu)		1210				
	100 V	200 V	250 V	500 V	630 V	100 V	200 V	250 V	500 V	630 V
100 pF										
120 pF										
150 pF										
180 pF										
220 pF										
270 pF		0.6±0.1	0.6±0.1	0.6±0.1						
330 pF										1.25±0.2
390 pF					1.25±0.2					
470 pF	0.6±0.1									
560 pF										
680 pF	-						1.25±0.2	1.25±0.2	1.25±0.2	
820 pF						125.02				
1.0 nF		0.85±0.1	0.85±0.1	0.85±0.1		1.25±0.2				
1.5 nF									1	
1.8 nF									ı	
2.2 nF		1.25±0.2	1.25±0.2	1.25±0.2					1	
2.7 nF									i	
3.3 nF									1	
3.9 nF									i	
4.7 nF	0.85±0.1									
5.6 nF										
6.8 nF										
8.2 nF	1.25±0.2									
IO nF	1,2310,2									
12 nF										
15 nF						1.6±0.2				
18 nF										
22 nF										

- 1. Values in shaded cells indicate thickness class in mm
- 2. Capacitance value of non E-12 series is on request

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Surface-Mount Ceramic Multilayer Capacitors Mid-voltage NP0/X7R | 100 V to 630 V

CAPACITANCE RANGE & THICKNESS FOR NPO

Table 8 Sizes 1812

CAP.	1812

CAP.	1812				
	100 V	200 V	250 V	500 V	630V
10 pF	-	-	-	-	
12 pF					
15 pF					
18 pF					
22 pF					
27 pF					
33 pF					
39 pF					
47 pF					
56 pF					
68 pF					
82 pF					
100 pF					
120 pF					
150 pF					
180 pF					
220 pF					
270 pF					
330 pF					1.25±0.2
390 pF					
470 pF					
560 pF				1.25±0.2	
680 pF					
820 pF					
I nF					
I.2 nF			1.25±0.2		
I.5 nF		1.25±0.2			
I.8 nF					
2,2 nF					
2.7 nF	1.25±0.2				
3.3 nF					
3.9 nF					
4.7 nF					
5.6 nF					
6.8 nF					
8.2 nF					
10 nF					
12 nF					
15 nF					
18 nF					
22 nF					
22					

- 1. Values in shaded cells indicate thickness class in mm
- 2. Capacitance value of non E-I2 series is on request



Surface-Mount Ceramic Multilayer Capacitors Mid-voltage NP0/X7R 100 V to 630 V

CAPACITANCE RANGE & THICKNESS FOR X7R

Table 10 Sizes from 0402 to 0805

0402 CAP. 0603 0805 100 V 100 V 100 V 250 V 200 V 250 V 500 V 630 V 100 pF 150 pF 220 pF 330 pF 470 pF 680 pF 1.0 nF 0.5±0.05 0.85±0.1 0.85±0.1 0.85 ± 0.1 0.85 ± 0.1 1.5 nF 0.6±0.1 2.2 nF 0.8±0.1 3.3 nF 0.8±0.1 4.7 nF 6.8 nF 10 nF 1.25±0.2 15 nF 1.25±0.2 0.85 ± 0.1 1.25±0.2 1.25±0.2 22 nF 33 nF 47 nF 68 nF 1.25 ± 0.2 100 nF 150 nF 220 nF 330 nF 470 nF

- 1. Values in shaded cells indicate thickness class in mm
- 2. Capacitance value of non E-6 series is on request
- 3. For special ordering code, please contact local sales force before order
- 4. For product with 5% tolerance, please contact local sales force before order

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Surface-Mount Ceramic Multilayer Capacitors | Mid-voltage | NP0/X7R | 100 V to 630 V

CAPACITANCE RANGE & THICKNESS FOR X7R

Table 11 Sizes from 1206 to 1210

CAP. 1206 1210

100 V 630V 200 V 250 V 500 V 630 V 100 V 200 V 250 V 500 V 100 pF 150 pF 220 pF 330 pF 470 pF 680 pF 1.0 nF 1.5 nF 0.85±0.1 0.85±0.1 1.25±0.2 2.2 nF 1.25±0.2 3.3 nF 0.85±0.1 4.7 nF 0.85±0.1 0.85±0.1 6.8 nF 1.25±0.2 10 nF 1.25±0.2 15 nF 0.85±0.1 22 nF 1.6±0.2 33 nF 1.6±0.2 1.6±0.2 1.25±0.2 1.25±0.2 47 nF 68 nF 1.25±0.2 1.25±0.2 100 nF 1.6±0.2 1.6±0.2 1.25±0.2 150 nF 220 nF 1.25±0.2 330 nF 1.6±0.2 470 nF 680 nF 1.6±0.2 2.0±0.2 ΙμF 1.6±0.3 $2.2 \mu F$

- 1. Values in shaded cells indicate thickness class in mm
- 2. Capacitance value of non E-6 series is on request
- 3. For product with 5% tolerance, please contact local sales force before order

Surface-Mount Ceramic Multilayer Capacitors Mid-voltage

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CAPACITANCE RANGE & THICKNESS FOR X7R

Table 12 Sizes from 1808 to 1812

1808 1812 CAP.

100 V 100 V 200 V 250 V 500 V 250 V 500 V 200 V 630 V 100 pF 150 pF 220 pF 330 pF 470 pF 680 pF 1.0 nF 1.5 nF 2.2 nF 3.3 nF 4.7 nF 1.35±0.2 0.85±0.1 0.85±0.1 6.8 nF 0.85±0.1 1.25±0.2 10 nF 1.25±0.2 15 nF 1.25±0.2 1.25±0.2 1.25±0.2 22 nF 1.6±0.2 33 nF 47 nF 68 nF 1.25±0.2 1.25±0.2 100 nF 1.6±0.2 150 nF 1.25±0.2 220 nF 330 nF 1.6±0.2 1.6±0.2

1.6±0.2

470 nF 680 nF

ΙμF

- 1. Values in shaded cells indicate thickness class in mm
- 2. Capacitance value of non E-6 series is on request
- 3. For product with 5% tolerance, please contact local sales force before order

Surface-Mount Ceramic Multilayer Capacitors Mid-voltage NP0/X7R 100 V to 630 V

THICKNESS CLASSES AND PACKING QUANTITY

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Table I			G100 MM	/ 7 IN ICH	G220 MM	/ 12 12 15 15 15 15 15 15	
SIZE CODE	THICKNESS CLASSIFICATION	TAPE WIDTH QUANTITY PER REEL	Ø180 MM Paper	Blister	Ø330 MM Paper	Blister	QUANTITY PER BULK CASE
0201	0.3 ±0.03 mm	8 mm	15,000		50,000		
0402	0.5 ±0.05 mm	8 mm	10,000		50,000		50,000
0603	0.8 ±0.1 mm	8 mm	4,000		15,000		15,000
	0.6 ±0.1 mm	8 mm	4,000		20,000		10,000
0805	0.8 / 0.85 ±0.1 mm	8 mm	4,000		15,000		8,000
	1.25 ±0.2 mm	8 mm		3,000		10,000	5,000
	0.6 ±0.1 mm	8 mm	4,000		20,000		
	0.8 / 0.85 ±0.1 mm	8 mm	4,000		15,000		
1206	1.00 / 1.15 ±0.1 mm	8 mm		3,000		10,000	
1200	1.25 ±0.2 mm	8 mm		3,000		10,000	
	1.6 ±0.15 mm	8 mm		2,500		10,000	
	1.6 ±0.2 mm	8 mm		2,000		8,000	
	0.6 / 0.7 ±0.1 mm	8 mm		4,000		15,000	
	0.85 ±0.1 mm	8 mm		4,000		10,000	
	1.15 ±0.1 mm	8 mm		3,000		10,000	
	1.15 ±0.15 mm	8 mm		3,000		10,000	
	1.25 ±0.2 mm	8 mm		3,000			
1210	1.5 ±0.1 mm	8 mm		2,000			
	1.6 / 1.9 ±0.2 mm	8 mm		2,000			
	2.0 ±0.2 mm	8 mm		2,000 1,000			
	2.5 ±0.2 mm	8 mm		1,000 500			
	1.15 ±0.15 mm	I2 mm		3,000			
	1.25 ±0.2 mm	I2 mm		3,000			
1808	1.35 ±0.15 mm	I2 mm		2,000			
1000	1.5 ±0.1 mm	I2 mm		2,000			
	1.6 ±0.2 mm	I2 mm		2,000		8,000	
	2.0 ±0.2 mm	I2 mm		2,000			
	0.6 / 0.85 ±0.1 mm	I2 mm		2,000			
	1.15 ±0.1 mm	I2 mm		1,000			
	1.15 ±0.15 mm	I2 mm		1,000			
	1.25 ±0.2 mm	I2 mm		1,000			
1812	1.35 ±0.15 mm	I2 mm		1,000			
	1.5 ±0.1 mm	I2 mm		1,000			
	1.6 ±0.2 mm	I2 mm		1,000			
	2.0 ±0.2 mm	I2 mm		1,000			
	2.5 ±0.2 mm	I2 mm		500			

Surface-Mount Ceramic Multilaver Capacitors

Mid-voltage

NP0/X7R 100 V to 630 V

ELECTRICAL CHARACTERISTICS

NP0/X7R DIELECTRIC CAPACITORS; NISN TERMINATIONS

Unless otherwise specified, all test and measurements shall be made under standard atmospheric conditions for testing as given in 5.3 of IEC 60068-1:

- Temperature: 15 °C to 35 °C - Relative humidity: 25% to 75% - Air pressure: 86 kPa to 106 kPa

Before the measurements are made, the capacitor shall be stored at the measuring temperature for a time sufficient to allow the entire capacitor to reach this temperature.

The period as prescribed for recovery at the end of a test is normally sufficient for this purpose.

Table	e 14		
DESCRIP	PTION		VALUE
Capacitar	nce range	0.47	pF to 2.2 μF
Capacitar	nce tolerance		
NP0	C < 10 pF	±0.25	5 pF, ±0.5 pF
	C ≥ 10 pF	±2%,	±5%, ±10%
X7R		±5% ⁽¹⁾ , ±	±10%, ±20%
Dissipatio	on factor (D.F.)		
NP0	C < 30 pF	≤ /(.	400 + 20C)
	C ≥ 30 pF		≤ 0.1 %
X7R			≤ 2.5 %
Exception	n	X7R/ 0603/100V, 12nF ≤ C ≤ 100nF, X7R/1206/2.2uF/100V	≤ 5%
		X7R/1206/100V/1uF; X7R/1210/100V/1uF and 2.2uF;	≤ 3.5%
Insulation	n resistance after 1 minute at U_r (DC)	$R_{ins} \ge 10 \text{ G}\Omega \text{ or } R_{ins} \times C \ge 500 seconds which$	chever is less
	n capacitance change as a function of tempe ture characteristic/coefficient):	rature	
NP0		=	±30 ppm/°C
X7R			±15%
	g temperature range:		
NP0/X7	7R	–55 °C	to +125 °C

NOTE

1. Capacitance tolerance ±5% doesn't available for X7R full product range, please contact local sales force before order



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NP0/X7R 100 V to 630 V

SOLDERING RECO	<u>OMMENDATION</u>
Table 15	
SOI DERING	SIZE

Surface-Mount Ceramic Multilayer Capacitors Mid-voltage

METHOD	0201	0402	0603	0805	1206	≥ 1210
Reflow	Reflow only	> 100 nF	> 1.0 µF	> 2.2 µF	> 2.2 µF	Reflow only
Reflow/Wave		≤ 100 nF	≤ 1.0 µF	≤ 2.2 µF	≤ 2.2 µF	

TESTS AND REQUIREMENTS

Table	16	Test procedures and requirements	

TEST	TEST METHOD		PROCEDURE	REQUIREMENTS	
Mounting	IEC 60384- 21/22	4.3	The capacitors may be mounted on printed-circuit boards or ceramic substrates	No visible damage	
Visual Inspection and Dimension Check		4.4	Any applicable method using × 10 magnification	In accordance with specification	
Capacitance		4.5.1	Class I: $f = I \text{ MHz for } C \leq I \text{ nF, measuring at voltage } I \text{ V}_{rms} \text{ at } 20 \text{ °C}$ $f = I \text{ KHz for } C > I \text{ nF, measuring at voltage } I \text{ V}_{rms} \text{ at } 20 \text{ °C}$ Class 2: $f = I \text{ KHz for } C \leq I0 \mu\text{F, measuring at voltage } I \text{ V}_{rms} \text{ at } 20 \text{ °C}$	Within specified tolerance	
Dissipation Factor (D.F.)		4.5.2	Class I: $f = I \text{ MHz for C} \leq I \text{ nF}, \text{ measuring at voltage } I \text{ V}_{rms} \text{ at } 20 \text{ °C}$ $f = I \text{ KHz for C} > I \text{ nF}, \text{ measuring at voltage } I \text{ V}_{rms} \text{ at } 20 \text{ °C}$ Class 2: $f = I \text{ KHz for C} \leq I0 \text{ µF}, \text{ measuring at voltage } I \text{ V}_{rms} \text{ at } 20 \text{ °C}$	In accordance with specification	
Insulation Resistance	$O_r \subseteq 500 \text{ V. At Of 101 I fillinate}$		$U_r \le 500 \text{ V: At Ur for I minute}$ $U_r > 500 \text{ V: At } 500 \text{ V for I minute}$	In accordance with specification	

Surface-Mount Ceramic Multilayer Capacitors | Mid-voltage | NP0/X7R | 100 V to 630 V

TEST	TEST METI	HOD	PROCEDURE	REQUIREMENTS	
Temperature coefficient		4.6	Capacitance shall be measured by the steps shown in the following table. The capacitance change should be measured after 5 min at each specified temperature stage.	<pre><general purpose="" series=""> Class I: Δ C/C: ±30ppm Class 2:</general></pre>	
			Step Temperature(°C)	X7R: Δ C/C: ±15% Y5V: Δ C/C: 22~-82%	
			a 25±2	- Wigh Canacitanse series	
			b Lower temperature±3°C	<high capacitance="" series=""> Class2:</high>	
			c 25±2	X7R/X5R: Δ C/C: ±15%	
			d Upper Temperature±2°C	Y5V: Δ C/C: 22~-82%	
			e 25±2		
			(I) Class I		
			Temperature Coefficient shall be calculated from the formula as below		
			Temp, Coefficient = $\frac{C2 - C1}{C1 \times \Delta T} \times 10^6 \text{ [ppm/°C]}$		
			C1: Capacitance at step c		
			C2: Capacitance at 125°C		
			Δ T: 100° C (=125° C -25° C)		
			(2) Class II		
			Capacitance Change shall be calculated from the		
			formula as below		
			$\Delta C = \frac{C2 - C1}{C1} \times 100\%$		
			C1: Capacitance at step c C2: Capacitance at step b or d		
Adhesion	IEC 60384- 21/22	4.7	A force applied for 10 seconds to the line joining the terminations and in a plane parallel to the substrate	Force size ≥ 0603: 5N	
Bending Strength		4.8	Mounting in accordance with IEC 60384-22 paragraph 4.3	No visible damage	
			Conditions: bending I mm at a rate of I mm/s, radius jig 5 mm	ΔC/C Class I:	
				NP0: within $\pm 1\%$ or 0.5 pF, whichever is greater Class2: $\times 7R$: $\pm 10\%$	
				7014 21070	

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Surface-Mount Ceramic Multilayer Capacitors | Mid-voltage | NP0/X7R | 100 V to 630 V

PROCEDURE REQUIREMENTS TEST TEST METHOD Resistance to Precondition: 150 +0/-10 °C for I hour, Dissolution of the end face plating shall not exceed Soldering then keep for 24 ±1 hours at room 25% of the length of the edge concerned Heat temperature Δ C/C Preheating: for size ≤ 1206: 120 °C to 150 Class I: °C for I minute NPO: within ±0.5% or 0.5 pF, whichever is greater Preheating: for size >1206: 100 °C to 120 Class2: °C for I minute and I70 °C to 200 °C for I X7R: ±10% minute D.F. within initial specified value Solder bath temperature: 260 ±5 °C R_{ins} within initial specified value Dipping time: 10 ±0.5 seconds Recovery time: 24 ±2 hours Solderability 4.10 Preheated the temperature of 80 °C to 140 The solder should cover over 95% of the critical area of each termination °C and maintained for 30 seconds to 60 seconds. I. Temperature: 235±5°C / Dipping time: $2 \pm 0.5 s$ 2. Temperature: 245±5°C / Dipping time: 3 ±0.5 s (lead free) Depth of immersion: 10mm Rapid Change IEC 60384-4.11 Preconditioning; No visual damage of 21/22 150 +0/-10 °C for I hour, then keep for Temperature 24 ±1 hours at room temperature Δ C/C Class I: 5 cycles with following detail: NP0: within $\pm 1\%$ or 1 pF, whichever is greater 30 minutes at lower category temperature Class2: 30 minutes at upper category temperature X7R: ±15% Recovery time 24 ±2 hours D.F. meet initial specified value R_{ins} meet initial specified value

Surface-Mount Ceramic Multilaver Capacitors

Mid-voltage

NP0/X7R | 100 V to 630 V

TEST **TEST METHOD PROCEDURE REQUIREMENTS** Damp Heat No visual damage after recovery 3. Preconditioning, class 2 only: 150 +0/-10 °C /1 hour, then keep for Δ C/C 24 ±1 hour at room temp Class I: 4. Initial measure: NPO: within ±2% or I pF, whichever is greater Spec: refer initial spec C, D, IR Class2: 5. Damp heat test: X7R: ±15% 500 ± 12 hours at 40 ± 2 °C; D.F. 90 to 95% R.H. Class I: 6. Recovery: NP0: $\leq 2 \times$ specified value Class I: 6 to 24 hours Class2: Class 2: 24 ±2 hours X7R: ≥ 25 V: ≤ 5% 7. Final measure: C, D, IR R_{ins} P.S. If the capacitance value is less than the Class I: minimum value permitted, then after the other NP0: \geq 2,500 M Ω or $R_{ins} \times C_r \geq$ 25s whichever measurements have been made the capacitor shall is less be precondition according to "IEC 60384 4.1" and then the requirement shall be met. Class2: X7R: \geq 500 M Ω or R $_{\rm ins}$ x C $_{\rm r}$ \geq 25s whichever is **Endurance** IEC 60384-4.14 1. Preconditioning, class 2 only: No visual damage 150 +0/-10 °C /1 hour, then keep for 21/22 24 ±1 hour at room temp Δ C/C 2. Initial measure: Class I: Spec: refer initial spec C, D, IR NP0: within ±2% or I pF, whichever is greater 3. Endurance test: Class2: Temperature: NP0/X7R: 125 °C X7R: ±15% Specified stress voltage applied for 1,000 hours: D.F. 4. High voltage series follows with below stress condition: Class I: NP0: $\leq 2 \times$ specified value NPO X7R Voltage Class2: ≤ 100V $2.0 \times Ur$ $2.0 \times Ur$ X7R: ≥ 25 V: ≤ 5% 200/250V $1.5 \times Ur$ $1.5 \times Ur$ R_{ins} 500/630V 1.3 x Ur $1.2 \times Ur$ Class I: ≥ IKV $1.2 \times Ur$ $I.I \times Ur$ NP0: $\geq 4.000 \text{ M}\Omega$ or $R_{ins} \times C_r \ge 40s$ whichever is less 5. Recovery time: 24 ±2 hours Class2: 6. Final measure: C, D, IR X7R: ≥ 1,000 MΩ or P.S. If the capacitance value is less than the $R_{ins} \times C_r \ge 50s$ whichever is less minimum value permitted, then after the other measurements have been made the capacitor shall be precondition according to "IEC 60384 4.1" and then the requirement shall be met.

Voltage Proof

Specified stress voltage applied for 1~5 seconds 4.6 Ur ≤ 100 V: series applied 2.5 Ur

100 V < Ur ≤ 200 V series applied

(1.5 Ur + 100)

200 V < Ur ≤ 500 V series applied

(1.3 Ur + 100) $\dot{U}r > 500 \text{ V: } 1.3 \text{ Ur}$ Ur≥ 1000 V: 1.2 Ur

Charge/Discharge current is less than 50 mA

No breakdown or flashover

Surface-Mount Ceramic Multilayer Capacitors | Mid-voltage | NP0/X7R | 100 V to 630 V

REVISION HISTORY

REVISION	DATE	CHANGE NOTIFICATION	DESCRIPTION
Version 21	Jul. 13, 2018	-	- Add NPO/0402/120pF to InF/100V, NPO/0603/1.2nF to 1.5nF/100V, NPO/1206/1.8nF/630V, NPO/1210/12nF to 22nF/100V
			- Add X7R/0805/33nF to 47nF/200 to 250V
Version 20	Sep. 14, 2017	-	- Dimension outlines updated
Version 19	Mar 7, 2017	-	- 0805 L4 spec updated
Version 18	Dec 9, 2016	-	- Soldering recommendation update
Version 17	Aug 16, 2016	-	- Capacitance range & thickness update
Version 16	Apr. 16, 2015	-	- Capacitance range & thickness
Version 15	Apr. 16, 2015	-	- Electrical characteristics update
Version 14	Sep. 25, 2014	-	- Electrical characteristics update
Version 13	Apr. 21, 2014	-	- Electrical characteristics update
Version 12	Dec. 12, 2013	-	- Electrical characteristics update
Version 11	Jun. 17, 2013	-	- Test method and procedure updated
Version 10	Nov 22, 2012	-	- Test method and procedure updated
Version 9	Feb 02, 2012	-	- Test method and procedure updated
Version 8	Apr 22, 2011	-	- NP0 0402 100V added
Version 7	Mar 01, 2011	-	- Dimension updated
Version 6	Sep 30, 2010	-	- Update the thickness of 0805 100V
Version 5	Sep 28, 2010	-	- Product range updated
			- Thickness classes and packing quantity table updated
Version 4	Jun 17, 2010	-	- Update the dimension of 0805, 1206 and 1812
Version 3	Mar 25, 2010	-	- Product range update
Version 2	Mar 15, 2010	-	- Product range update
Version I	Oct 30, 2009	-	- Change to dual brand datasheet that describe Mid-voltage NP0/X7R series with RoHS compliant
			- Replace the "100V to 630V" part of pdf files: UP-NP0X7R_MV_100-to-500V_0, UY-NP0X7R_MV_100-to-500V_0, NP0_16V-to-100V_6, NP0_50-to-500V_10, X7R_16-to-500V_9 and X7R_16V-to-100V_9
			- Define global part number
			- Description of "Halogen Free compliant" added
			- Test method and procedure updated
Version 0	Sep 08, 2005	-	- New