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ECE 453 Tutorial Board  
TEAM MEMBER NAMES: Conner Huff, Tiffany Voss

Revision History:

Date:	Revision:	Comments:
1/12/2015	1	Initial Design for ECE 453 Spring 2016 Tutorial Board

Important Notes about this Schematic:

DESIGN NOTE:  
Design notes in gray text boxes give information about design requirements and descisions.

LAYOUT NOTE:  
Layout notes in red text boxes give information regarding layout requirements.

Revision History and Table of Contents

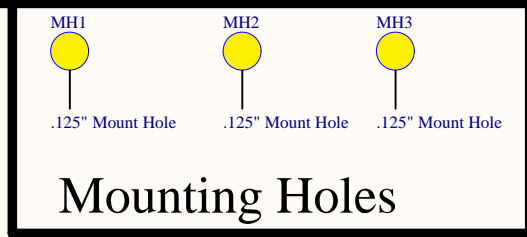
Title ECE453TutorialBoard		
Size B	Number	Revision
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**P1**

Pin	Function	Pin	Function
1	GPIO1_D0	21	GPIO1_D18
2	GPIO1_D1	22	GPIO1_D19
3	GPIO1_D2	23	GPIO1_D20
4	GPIO1_D3	24	GPIO1_D21
5	GPIO1_D4	25	GPIO1_D22
6	GPIO1_D5	26	GPIO1_D23
7	GPIO1_D6	27	GPIO1_D24
8	GPIO1_D7	28	GPIO1_D25
9	GPIO1_D8	29	3V3
10	GPIO1_D9	30	GND
11	5V0	31	GPIO1_D26
12	GND	32	GPIO1_D27
13	GPIO1_D10	33	GPIO1_D28
14	GPIO1_D11	34	GPIO1_D29
15	GPIO1_D12	35	GPIO1_D30
16	GPIO1_D13	36	GPIO1_D31
17	GPIO1_D14	37	GPIO1_D32
18	GPIO1_D15	38	GPIO1_D33
19	GPIO1_D16	39	GPIO1_D34
20	GPIO1_D17	40	GPIO1_D35

**Header 20X2**

Pin	Function	Pin	Function
1	GPIO1_D0	21	GPIO1_D18
2	GPIO1_D1	22	GPIO1_D19
3	GPIO1_D2	23	GPIO1_D20
4	GPIO1_D3	24	GPIO1_D21
5	GPIO1_D4	25	GPIO1_D22
6	GPIO1_D5	26	GPIO1_D23
7	GPIO1_D6	27	GPIO1_D24
8	GPIO1_D7	28	GPIO1_D25
9	GPIO1_D8	29	3V3
10	GPIO1_D9	30	GND
11	5V0	31	GPIO1_D26
12	GND	32	GPIO1_D27
13	GPIO1_D10	33	GPIO1_D28
14	GPIO1_D11	34	GPIO1_D29
15	GPIO1_D12	35	GPIO1_D30
16	GPIO1_D13	36	GPIO1_D31
17	GPIO1_D14	37	GPIO1_D32
18	GPIO1_D15	38	GPIO1_D33
19	GPIO1_D16	39	GPIO1_D34
20	GPIO1_D17	40	GPIO1_D35

[illegible]

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