

TO WHOM IT MAY CONCERN

This is to certify that **Mohammad Ibraheem Abdeltawab Fasada**
has attended the Course entitled:

CMOS Analog IC Design

From: 14 July 2024 to 09 September 2024.

Topic	Lectures (Hours)	Labs (Hours)	Self Study	Total Hours
Analog IC Design Fundamentals (MOSFET models, single-stage and cascode amplifiers, frequency response, current mirrors, differential amplifier, 5T OTA)	18	18	36	72
The gm/ID Design Methodology	6	6	12	24
Advanced Analog IC Design (Negative feedback, stability and compensation, noise, CMFB, SR, PSRR, variability and mismatch, biasing and references)	21	9	30	60
Project: Design of Two-Stage Miller OTA			6	12
Project: Design of fully differential capacitive feedback amplifier using folded cascode OTA			6	12
Total	45	33	90	180

This certificate was issued upon his/her request without any responsibility
on the institute side

Dr. Heba Saleh

Ahmed Salem
ITI - Aswan Branch Manager

Chairman, Information Technology Institute