

Passant:

SWI		10	
B/BL		10	
Barrel Shifter	Immediate	10	Rotate is incorrect, however it was correct in shift register
	Shifted Register – 5 bit Shift	9	Barrel shifter don't update the register itself Rotate Right Extended function not implemented
	Shifted Register – Bottom byte of another register	0	Not implemented
Conditional Execution		0	Not implemented
Data Processing Instruction	ADD, ADDC...	8	Set Status Flags not checked correctly. Not all instruction implemented
	TST, TEQ, CMP and CMN	5	Incorrect updates of the N,Z,C and V flags of CPSR
MUL and MULA		9	Excellent except you forget to update the status flags
Single Data Transfer		5	You seems on the right track
These marks are not set to be summed up. i.e. larger sum doesn't indicate better implementation			The most readable well organized code, as if you know your way from the first line you wrote till the end. And such dedication to submit a working well tested solution.