Part 1 (version 1):
Arduino Uno/Duemillanove

POWER
CONN_01X08
+3.3V
3.3V
5V
GND
GND
GND

AD
CONN_01X06

CONN_01X10
IOH
10 9 8 7 6 5 4 3 2 1
GND
SCK
MISO
MOSI
CS
PB0

CONN_01X08
IOL
8 7 6 5 4 3 2 1

Part 2 (version 1):
Resistor-Divider from 5V Circuit

CS — 10k — CS_L — 15k — GND
MOSI — 10k — MOSI_L — 15k — GND
SCK — 10k — SCK_L — 15k — GND

Alternative resistor values:
10k 15k (as above, this is for reference)
5k   10k (5k = 2 x 10k in parallel)
2.2k 4.7k

MISO — MISO_L

Part 2 (version 2):
Like version 1 + Open-Collector $\overline{CS}$

This is the pinout used by the SPI part
of frser-m328lpcspi firmware and the
associated shield.

MOSI, MISO and SCK as per version 1

+3.3V
4k7
PB0 — CS_L

Part 3 (version 1):

DIL-8 or SO-8 SPI Flash pinout
Signal name _L means lowered to 3.3V

+3.3V
SPI_FLASH
DIL8
+3.3V
CS_L — 1 — 8 — VCC
MISO_L — 2 — 7 — HOLD (connect to 3.3V)
WP — 3 — 6 — SCK_L
GND — 4 — 5 — MOSI_L
GND

Note: MISO_L is output from flash chip and usually attached
directly to MISO, but is seperated to allow for descriptions
of buffering in both directions.

Part 3 (version 2):
SOIC-16 SPI Flash pinout

+3.3V
SPI_FLASH16
SOIC-16
HOLD — 1 — 16 — SCK_L
VCC — 2 — 15 — MOSI_L
3 — 14
4 — 13
5 — 12
6 — 11
CS_L — 7 — 10 — GND
MISO_L — 8 — 9 — WP
+3.3V
GND

Part 2 (version 3):
Level-translating buffer (4050)

+3.3V
HEF4050B
DIL16
1 — 16
CS_L — 1Y 2 — 15 — 6Y
CS — 1A 3 — 14 — 6A
MOSI_L — 2Y 4 — 13
MOSI — 2A 5 — 12 — 5Y
SCK_L — 3Y 6 — 11 — 5A
SCK — 3A 7 — 10 — 4Y
8 — 9 — 4A
GND

MISO — MISO_L

Input nA is copied to output nY (at 3.3V),
pinout optimization is allowed :)

Part 1 (version 2):
Arduino Mega/Mega2560

POWERM
CONN_01X08
+3.3V
3.3V
5V
GND
GND
GND

ADCL
CONN_01X08

ADCH
CONN_01X08

GND

CONN_01X10
IOHM
10 9 8 7 6 5 4 3 2 1

CONN_01X08
IOLM
8 7 6 5 4 3 2 1

CONN_01X08
COMM

XIO
SCK
MISO
5V
36 34 32 30 28 26 24 22 20 18 16 14 12 10 8 6 4 2
35 33 31 29 27 25 23 21 19 17 15 13 11 9 7 5 3 1
GND
GND
CS
MOSI
5V

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