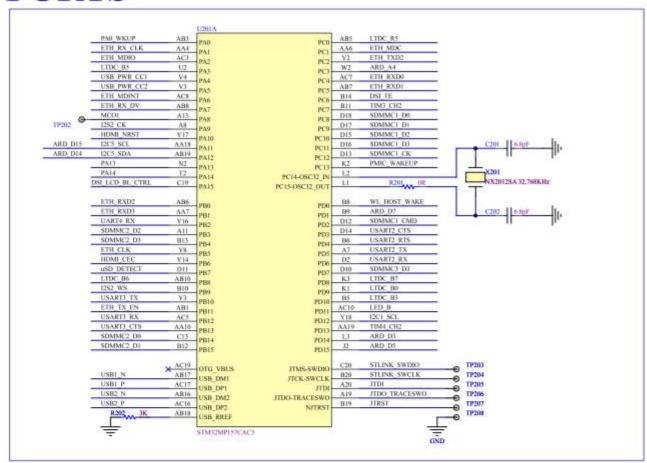
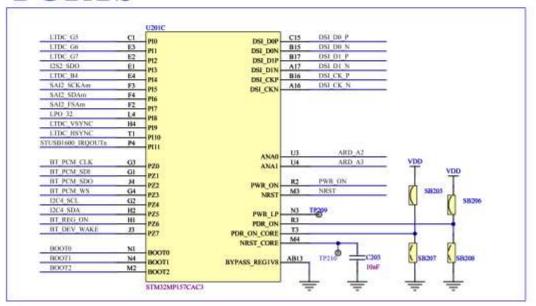


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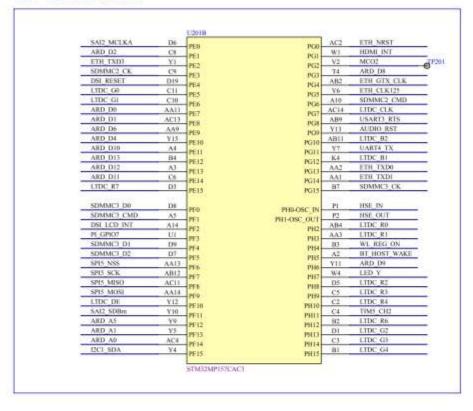
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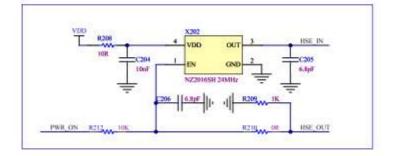
### **PORTS**



### **PORTS**

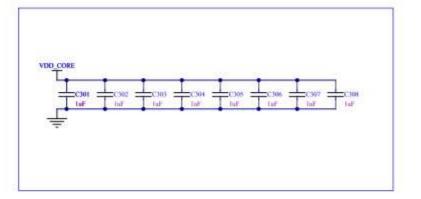


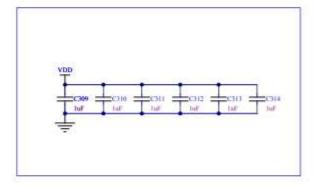
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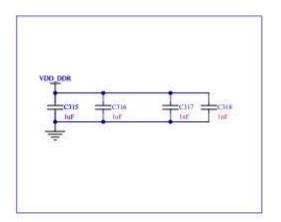


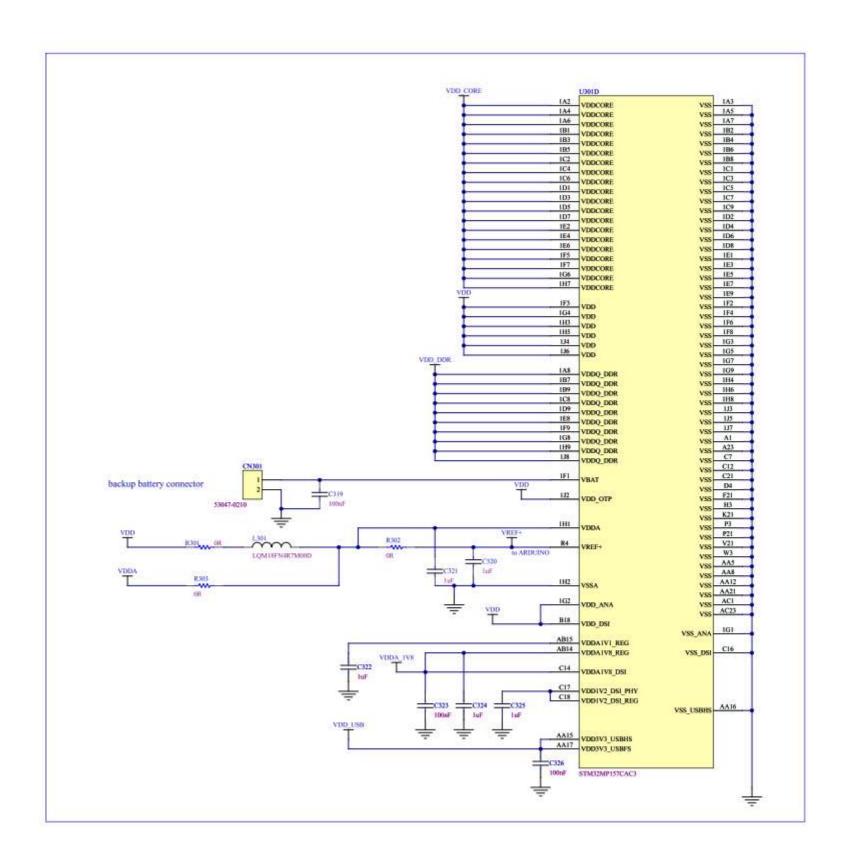
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# 2.POWER\_MPU

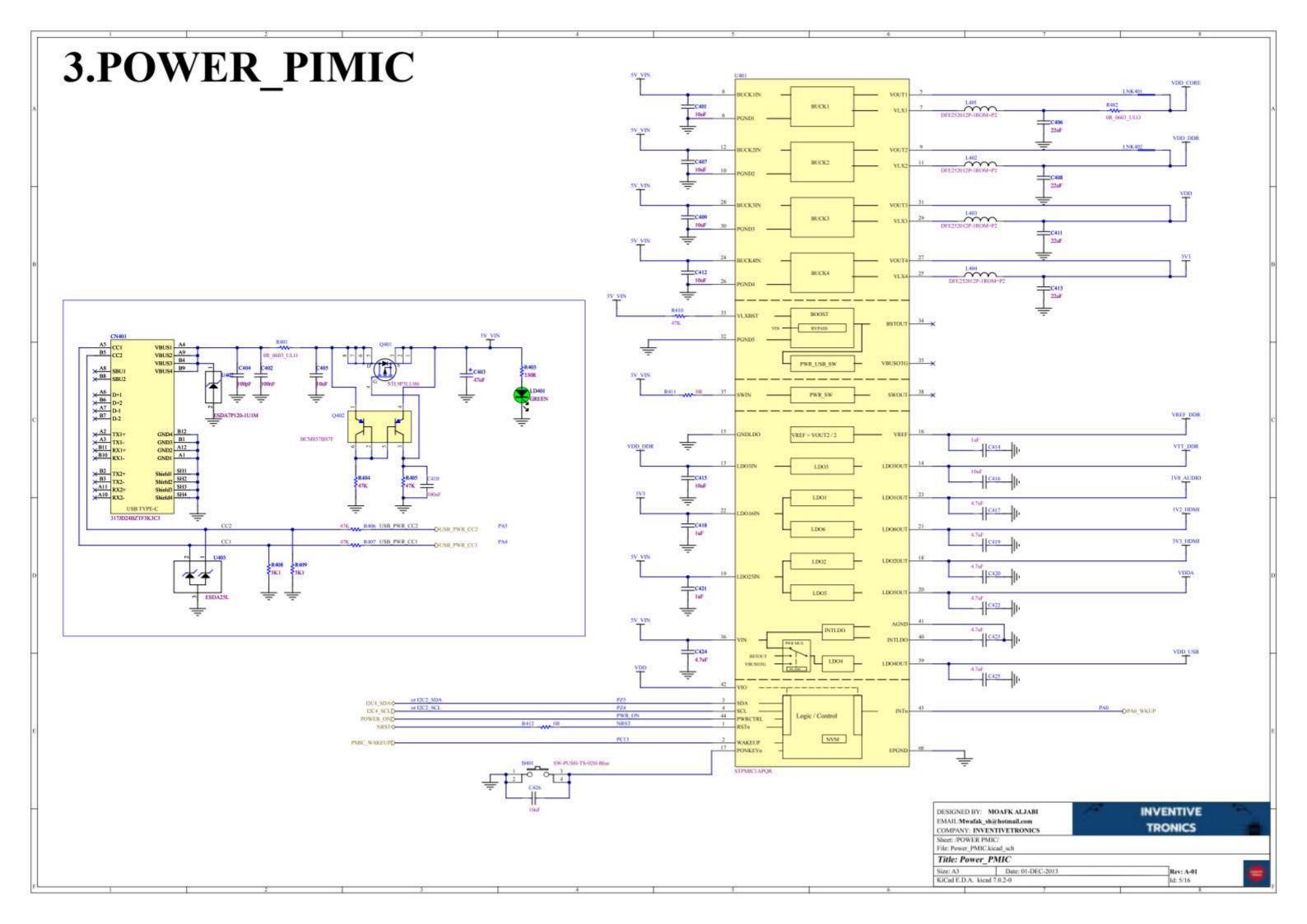




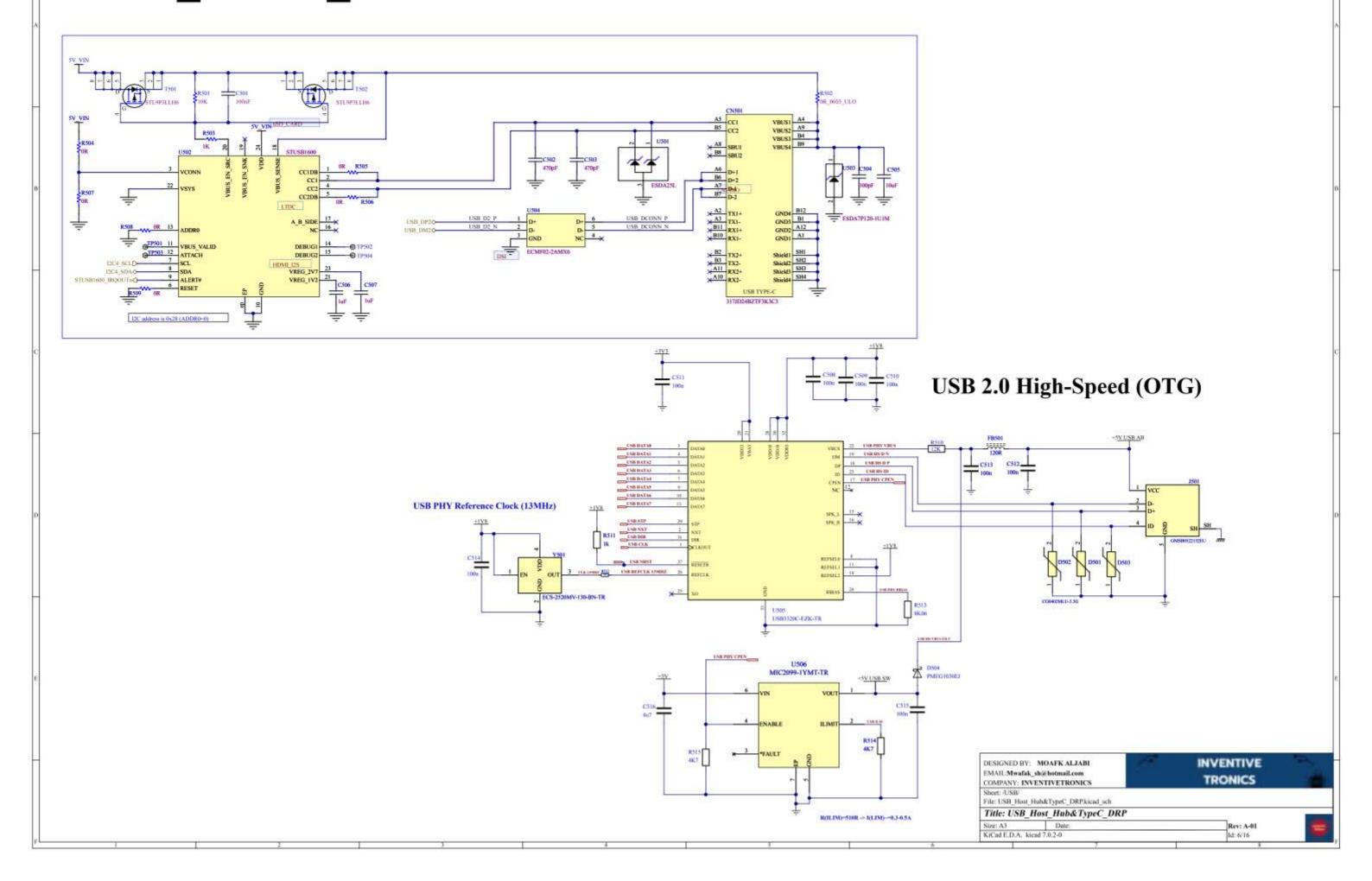




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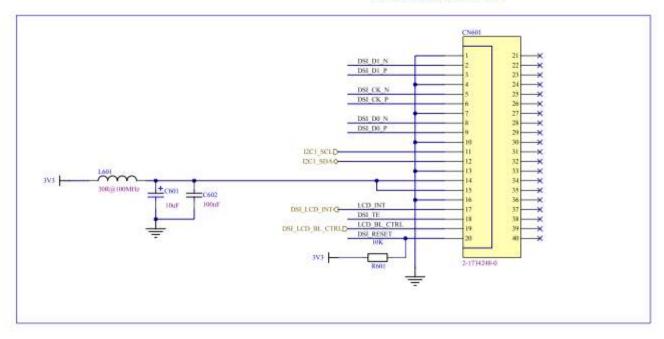


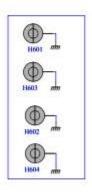
# 4.USB\_HOST\_HUB&TYPEC



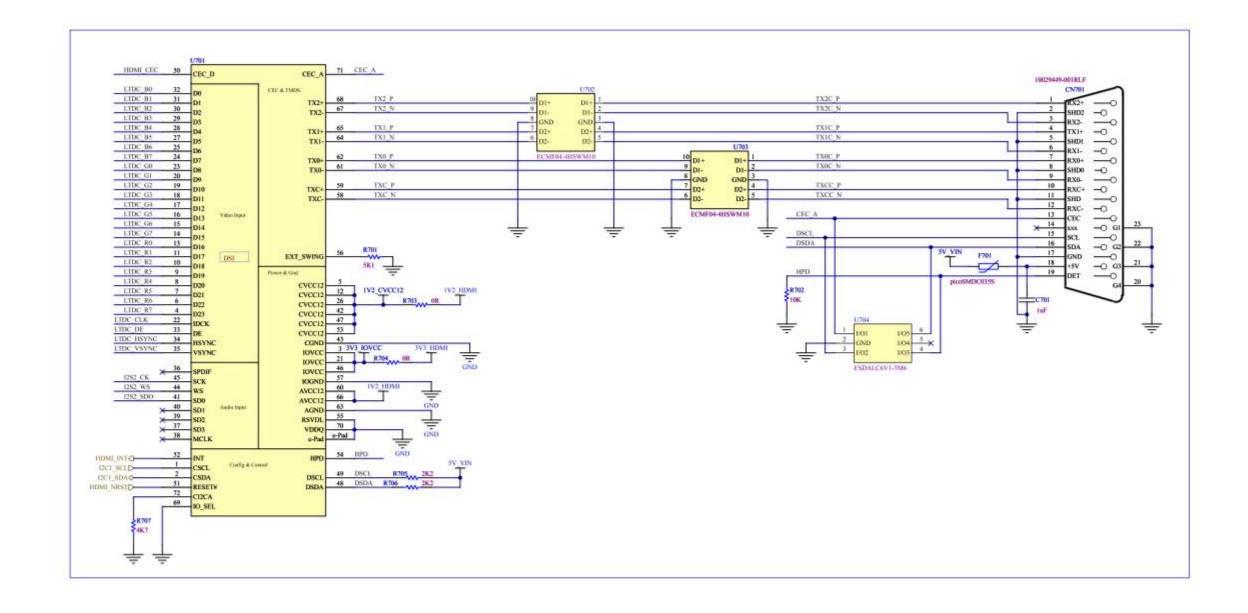
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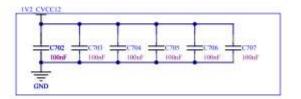
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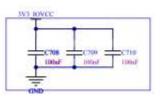


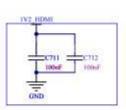


## 6.HDMI





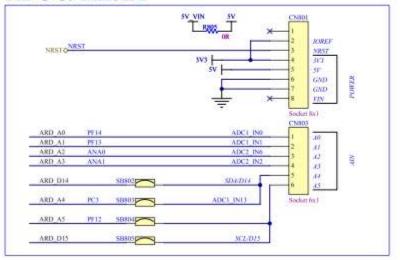




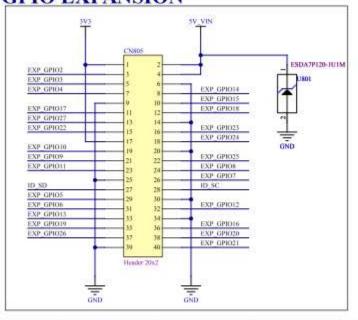
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### 7.GPIO CONNECTORS

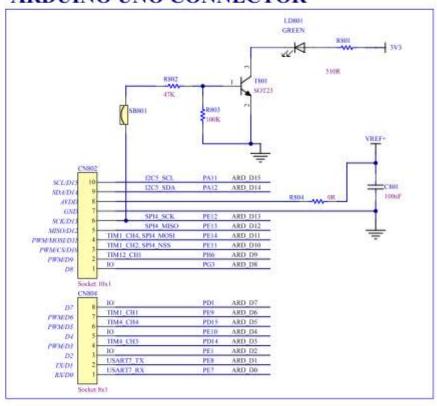
#### ADC & RESET



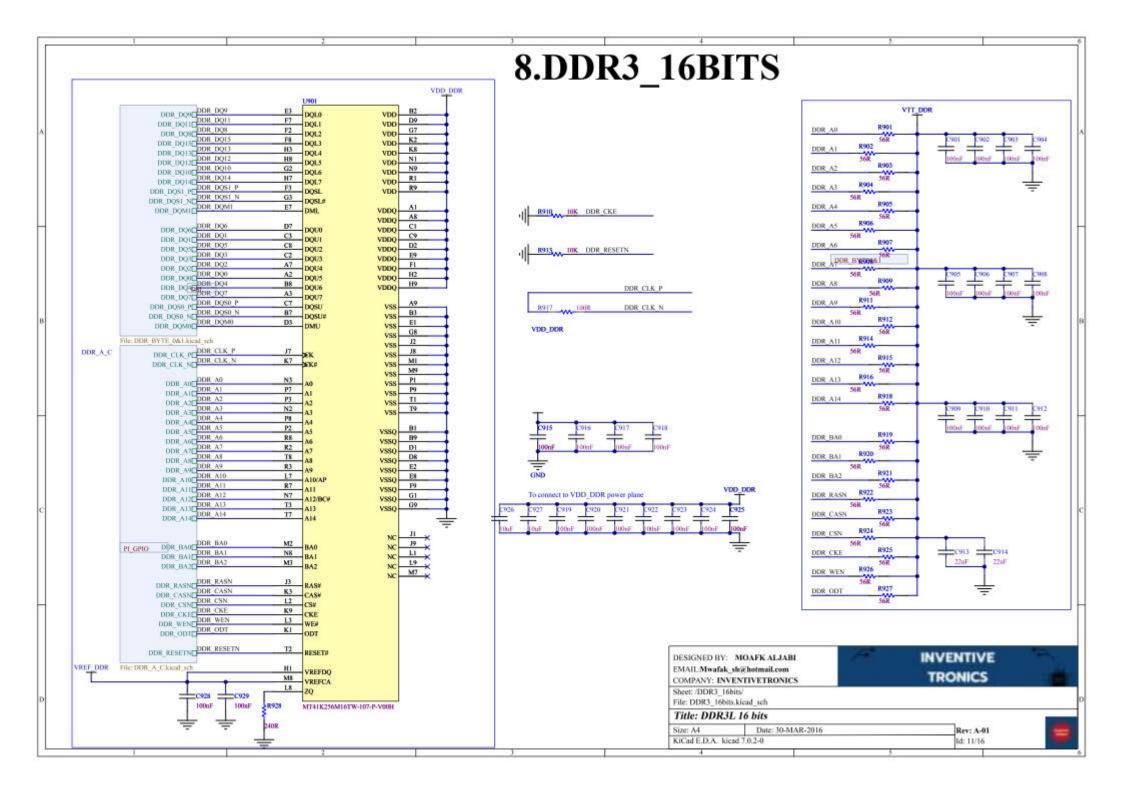
#### **GPIO EXPANSION**



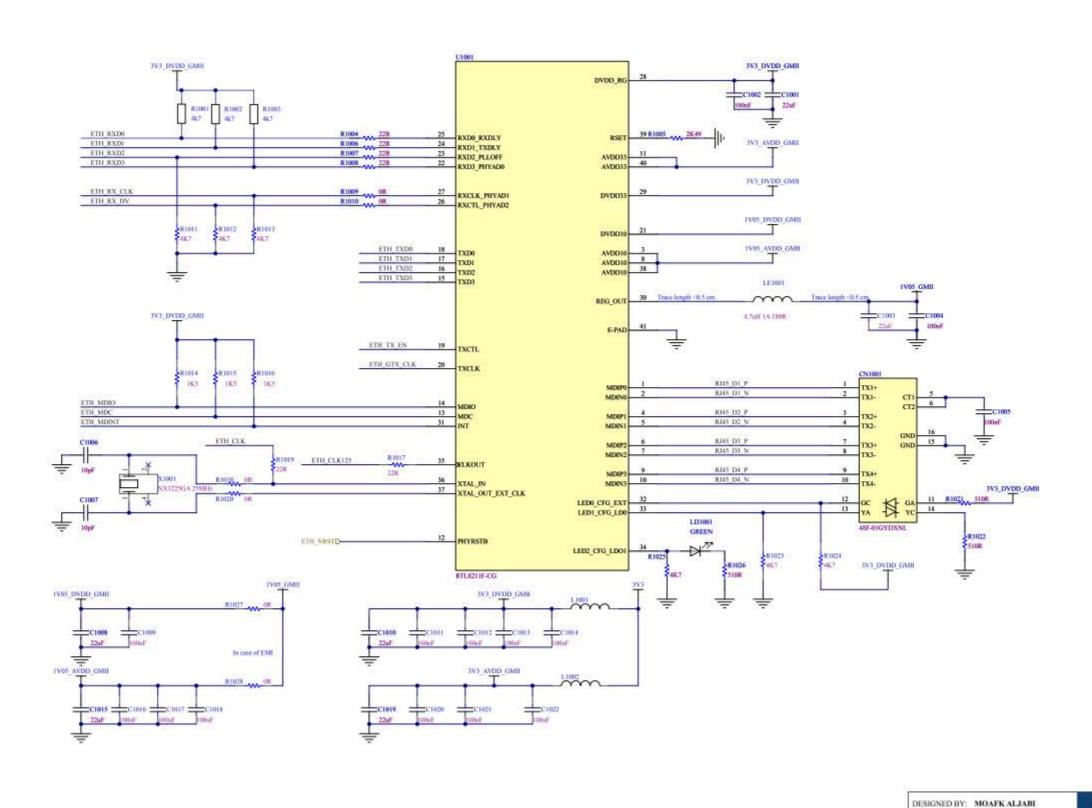
#### ARDUINO UNO CONNECTOR



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# 9. ETHERNET



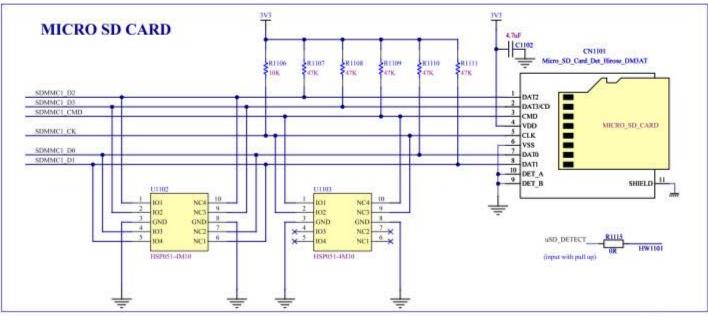
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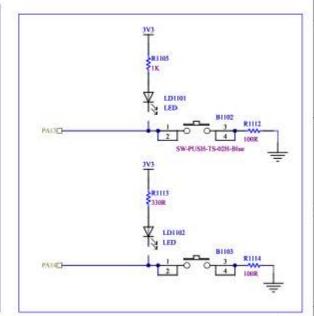
TRONICS

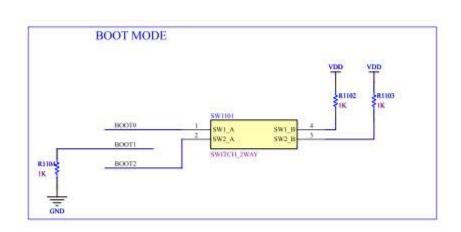
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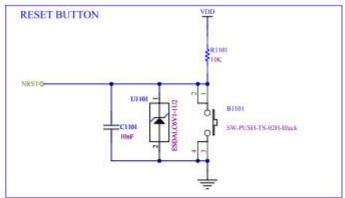
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## 10.PERIPHERALS



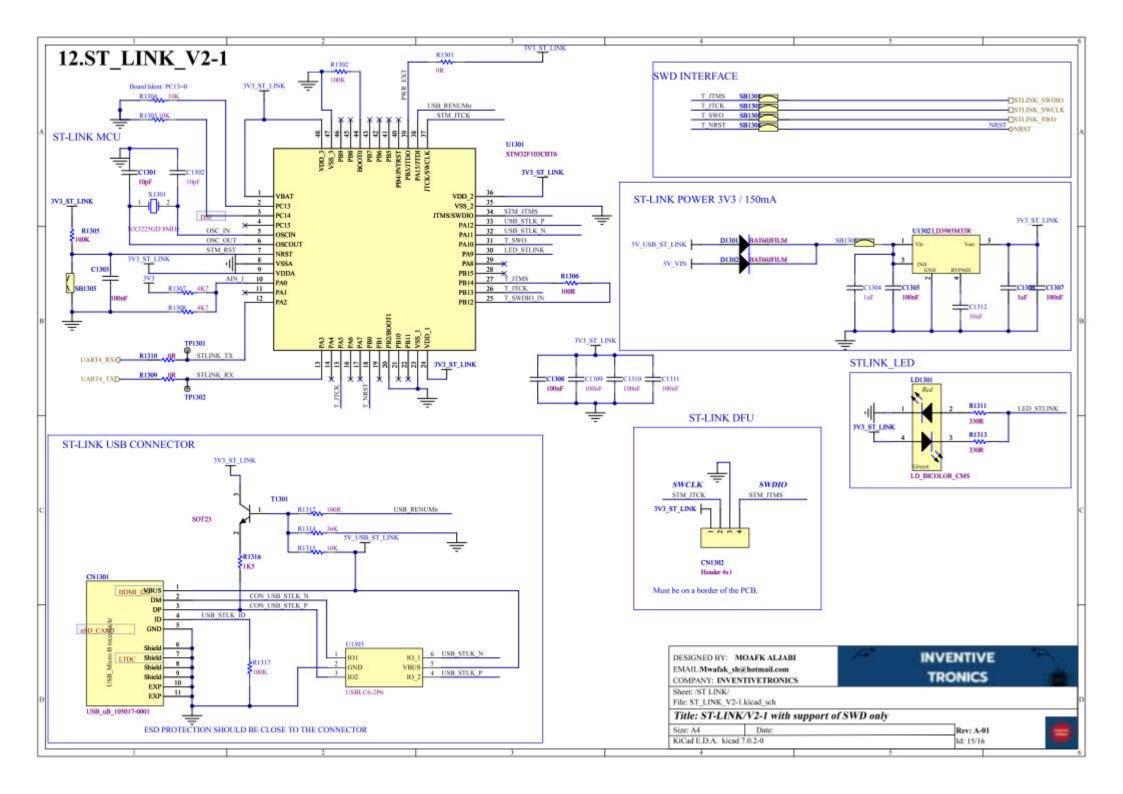




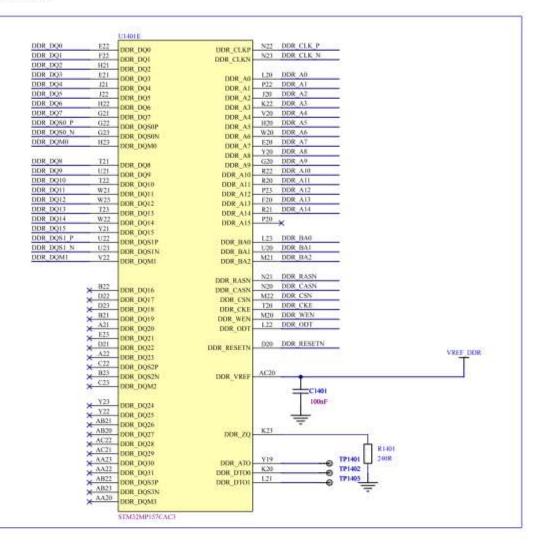


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#### 11.WLAN BLUETOOTH L1201 R1202 LQW15AN2N4O00x P2ML4453-1-A.DXF USART2 TX SBMMC2 D3 TP1201 BT WART RXD SDIO DATA R1204 47K R1201 47K R1206 47K USART2\_RX SDMMC2\_D2 BT\_UART\_TXD SDIO\_DATA\_2 TP1202 USART2 RTS SDMMC2 D1 BT UART CTS N SDIO DATA I USARTZ CTS SDMMC2 DO BT WART RTS N SDIO DATA 0 SDMMC2\_CMD SDIO\_CMD BT PCM WS SDMMC2 CK: BT PCM SYNC SDIO CLK BT PCM SDO TP1203 BT\_PCM\_IN BT\_PCM = 1281 BT PCM SDI BT PCM OUT BT PCM CLK BT PCM CLK BT REG OND BT REG ON WL REG ON QWL REG ON BT\_HOST\_WAKEQ 38 BT\_HOST\_WAKE WL\_GPIO\_0\_HOST\_ WAKE DWL HOST WAKE 39 BT DEV WAKED-BT\_DEV\_WAKE LOM INPAZRZMICH VIN LDO WL\_GPIO\_4 BT\_128\_dO TP1206 WL GPIO 2 TP1207 WL\_GPIO\_I TP1208 BT GPIO\_3 C1206 C1207 TP1209 BT GPIO 4 BT\_GPIO\_5 GND LPO\_IN\_(32kHz) LBEE5KLIDX-883 INVENTIVE DESIGNED BY: MOAFK ALJABI EMAIL:Mwafak\_sh@hotmail.com **TRONICS** COMPANY: INVENTIVETRONICS Sheet: /WLAN BLUETOOTH/ File: WLAN BLUETOOTH kiead sch Title: WLAN BLUETOOTH Date: 21-MAR-2018 Rev: B-01 KiCad E.D.A. kicad 7.0.2-0 Id: 14/16



## 13.DDR3 STM



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