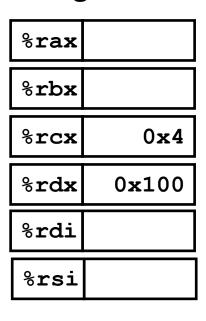
# leaq vs. movq example

## **Registers**



### **Memory**

	Address
0x400	0x120
0xf	0x118
0x8	0x110
0x10	0x108
0x1	0x100

```
leaq (%rdx,%rcx,4), %rax
movq (%rdx,%rcx,4), %rbx
leaq (%rdx), %rdi
movq (%rdx), %rsi
```

# leaq vs. movq example (solution)

### **Registers**

%rax	0x110
%rbx	0 <b>x</b> 8
%rcx	0×4
%rdx	0x100
%rdi	0x100
%rsi	0x1

### Memory

	Address
0x400	0x120
0xf	0x118
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```
leaq (%rdx,%rcx,4), %rax
movq (%rdx,%rcx,4), %rbx
leaq (%rdx), %rdi
movq (%rdx), %rsi
```