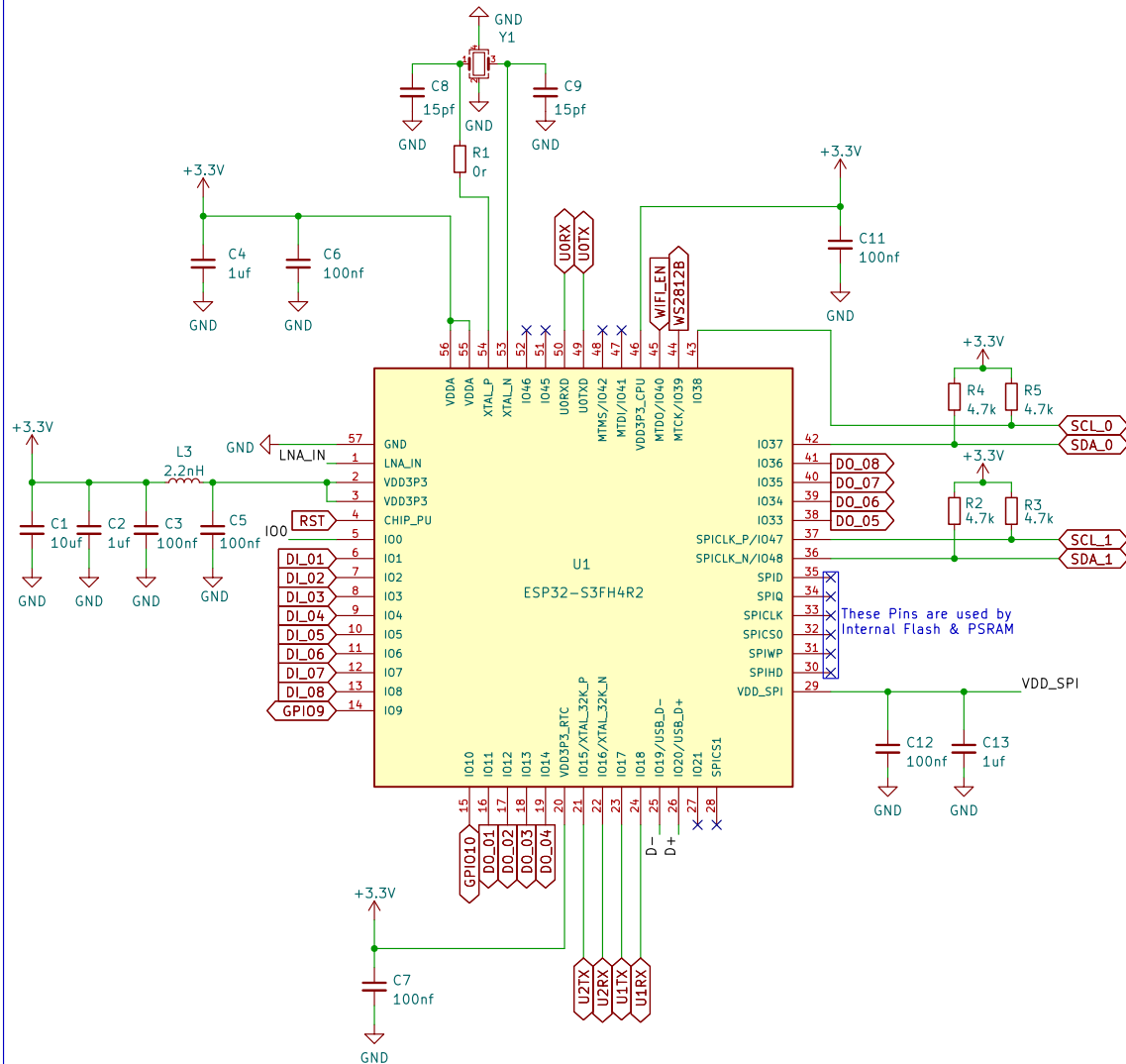
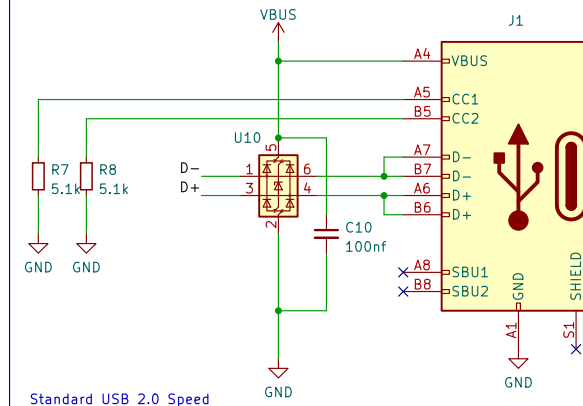


Notes:  
Schematic design from reference hardware guide line.  
Design based from model with internal flash and psram  
Model: ESP32-S3FH4R2  
See ESP-32-S3 Datasheet for pin details  
JLCPCB VIA thickness is 0.018mm  
Calculated via current for 0.2mm hole is 1.12A

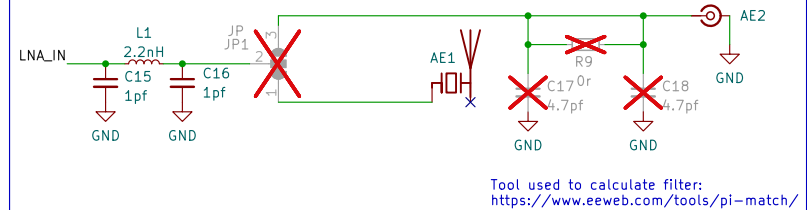
## ESP32-S3



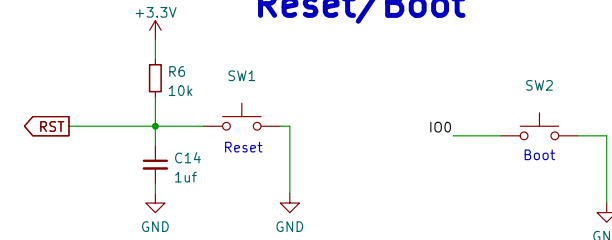
## USB-C



## Antenna Ufl/chip



## Reset/Boot



Sheet: /  
File: ModBee-Node-U10.kicad\_sch

**Title: ModBee Node UIO**

Size: A4 Date: 2025-05-16

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**Rev: 0.01**

Id: 1/5

5-24v Supply



## 2

A



## 4

A



## 5

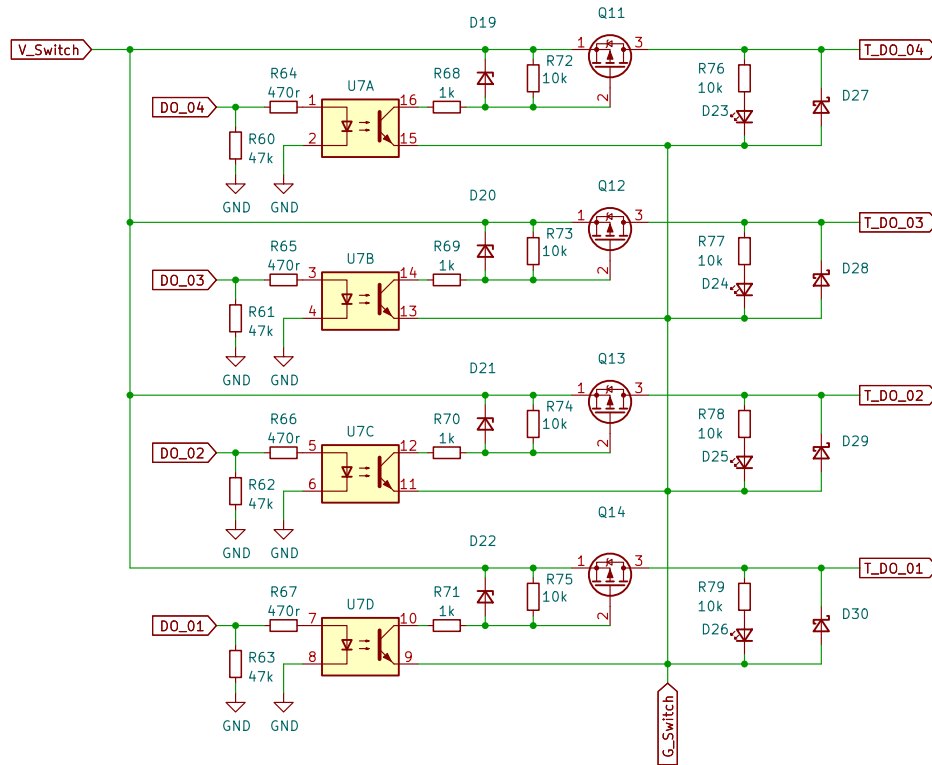


D

Id: 3/5

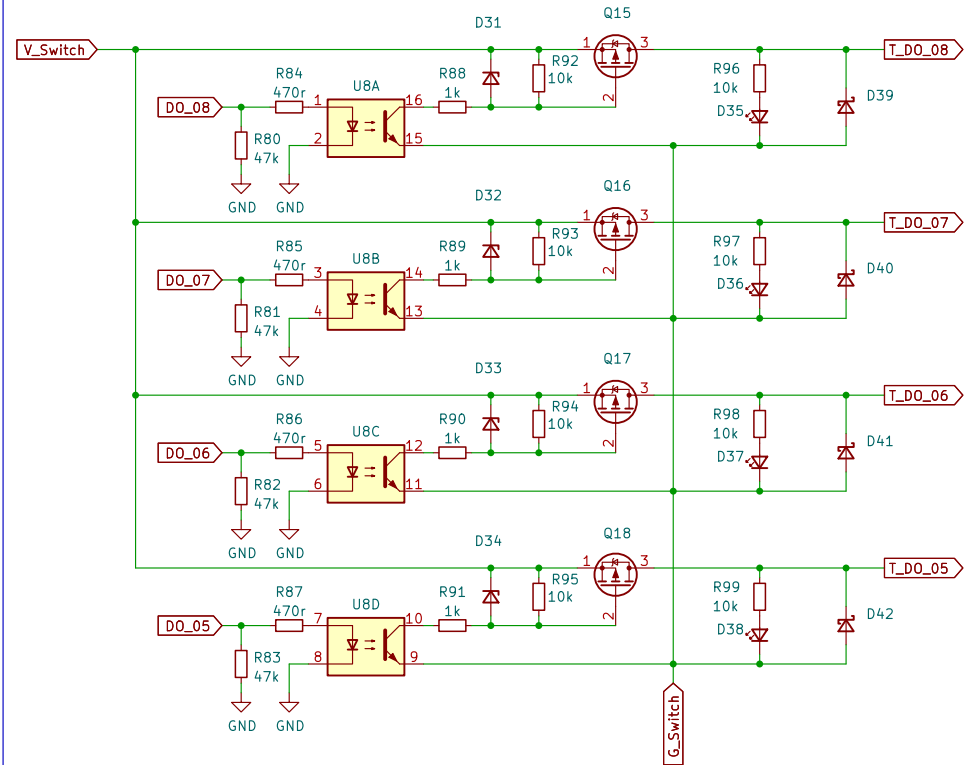
## A Digital Outputs 1–4

5–24v Outputs switched from Input Supply by default 100ma MAX per output  
If jumpers cut or separte supply wire used in terminal 5 500ma MAX per output



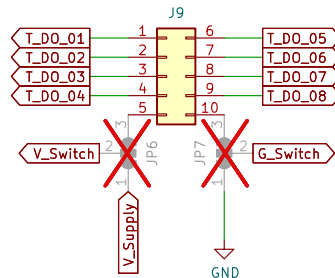
## B Digital Outputs 5–8

5–24v Outputs switched from Input Supply by default 100ma MAX per output  
If jumpers cut or separte supply wire used in terminal 5 500ma MAX per output



## 10 Pin Plug Terminal

Cut Bridge 1&2 to use an Isolated separte supply and Ground



Sheet: /Digita\_Outputs/  
File: Digital\_Outputs.kicad\_sch

**Title: ModBee Node UIO**

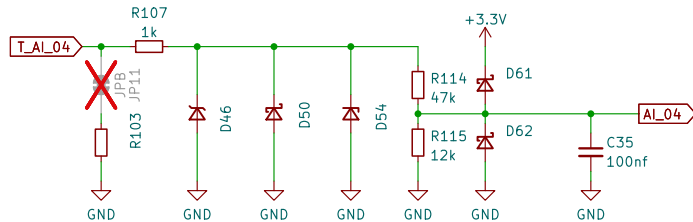
Size: A4 Date: 2025-05-16

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**Rev: 0.01**

Id: 4/5

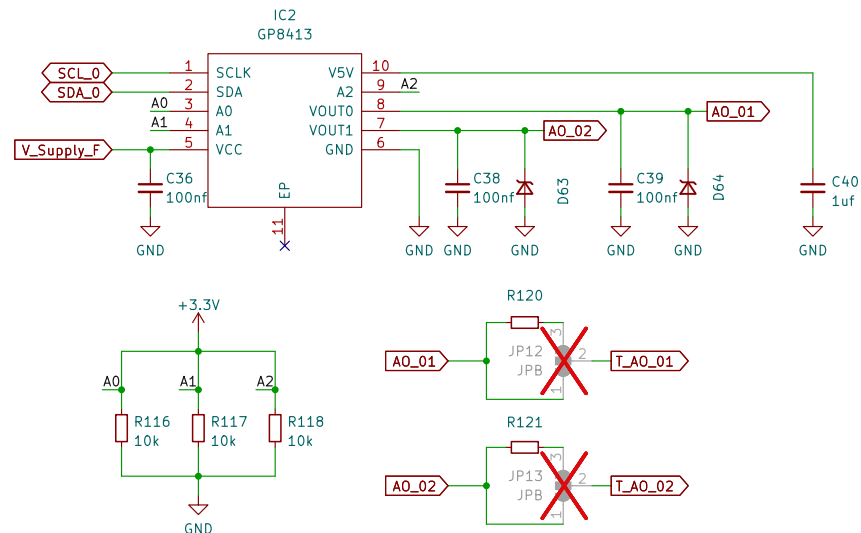
0-20ma Default Cut Bridge to use 0-10v



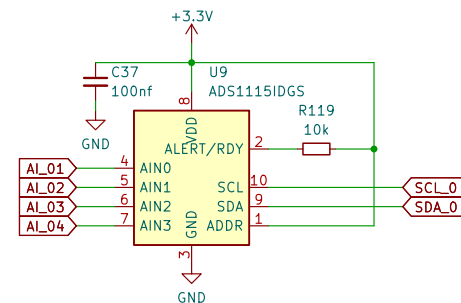
## Plug In Terminals



0-10v Default Cut Bridge 1&2 to use 0-20ma  
Default I2c Address 0x5F



Use GAIN\_TWO (for an input range of  $\pm 2.048\text{V}$ )  
Default I2C Address 0x49



Sheet: /Analog\_I0/  
File: Analog\_I0.kicad\_sch

**Title: ModBee Node UIO**

Size: A4	Date: 2025-05-16
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KiCad E.D.A. 9.0.1

Rev: 0.01

Id: 5/5