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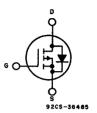
P-Channel Enhancement-Mode Power Field-Effect Transistors

10 A, -120V and -150 V $r_{DS(on)} = 0.5 \Omega$

Features:

- SOA is power-dissipation limited
- Nanosecond switching speeds
- Linear transfer characteristics
- High input impedance
- Majority carrier device

TERMINAL DIAGRAM



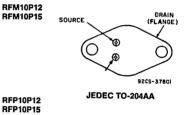
P-CHANNEL ENHANCEMENT MODE

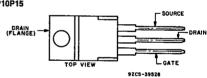
TERMINAL DESIGNATIONS

The RFM10P12 and RFM10P15 and the RFP10P12 and RFP10P15* are p-channel enhancement-mode silicon-gate power field-effect transistors designed for applications such as switching regulators, switching converters, motor drivers, relay drivers, and drivers for high-power bipolar switching transistors requiring high speed and low gate-drive power. These types can be operated directly from integrated circuits.

The RFM-types are supplied in the JEDEC TO-204 A $\bf A$ steel package and the RFP-types in the JEDEC TO-220AB plastic package.

*The RFM and RFP series were formerly RCA developmental TA9404 and TA9405, respectively.





JEDEC TO-220AB

MAXIMUM RATINGS, Absolute-Maximum Values (Tc = 25° C):

,	RFM10P12	RFM10P15	RFP10P12	RFP10P15	
DRAIN-SOURCE VOLTAGE	-120 -120	-150 -150	-120 -120	-150	v
GATE-SOURCE VOLTAGE			20	-150	. V
Pulsed			30		. A . A
Derate above T _C = 25°C	100 0.8	100 0.8	75 0.6	75 0.6	W/°C
OPERATING AND STORAGE TEMPERATURE			o +150		°C.

RFM10P12, RFM10P15, RFP10P12, RFP10P15 erature (Tc = 25°C) unless otherwise specified 7.39-2

ELECTRICAL CHARACTERISTICS, At Case Temperature (T_C = 25°C) unless otherwise specified

CHARACTERISTIC	SYMBOL	TEST . CONDITIONS					
			RFM10P12 RFP10P12		RFM10P15 RFP10P15		UNITS
			MIN.	MAX.	MIN.	MAX.	1
Drain-Source Breakdown	BV _{DSS}	f _D = 1 mA	-120	_	-150		V
Voltage		$V_{GS} = 0$					
Gate-Threshold Voltage	V _{GS(th)}	V _{GS} = V _{DS}	-2	-4	-2	-4	V
		I _D = 1 mA					1
Zero-Gate Voltage Drain	loss	V _{DS} = -100 V		1	-	_	
Current		V _{DS} = -120 V	-			1	
		T _C = 125° C					μΑ
		$V_{DS} = -100 \text{ V}$		50	-		
		$V_{DS} = -120 \text{ V}$	_	_	_	50	
Gate-Source Leakage Current	lass	V _{GS} = ±20 V	_	100		100	nA
		V _{DS} = 0					
Drain-Source On Voltage	V _{DS(on)} a	I _D = 5 A	_	-2.5	_	-2.5	
		$V_{GS} = -10 \text{ V}$					l
		I _D = 10 A	_	-6.0	_	-6.0	\ \
		V _{GS} = -10 V					1
Static Drain-Source On	r _{DS(on)} a	I _D = 5 A		0.5		0.5	Ω
Resistance		V _{GS} = -10 V					
Forward Transconductance	g _{ts} a	V _{DS} = -10 V	2		2	_	mho
-	Ţ	I _D = 5 A					
Input Capacitance	Ciss	V _{DS} = -25 V	_	1700	_	1700	1
Output Capacitance	Coss	$V_{GS} = 0 V$	_	600		600	pF
Reverse Transfer Capacitance	Cras	f = 1MHz	_	150	_	150	1
Turn-On Delay Time	t _{d(on)}	V _{DS} = -75 V	24(typ)	50	24(typ)	50	
Rise Time	t _r	I _D = 5 A	74(typ)	150	74(typ)	150	ns
Turn-Off Delay Time	tatom	$R_{gen} = R_{gs} = 50 \Omega$	138(typ)	225	138(typ)	225	7
Fall Time	tr	V _{GS} = -10 V	61(typ)	100	61(typ)	100	1
Thermal Resistance	R <i>θ</i> _{JC}	RFM10P12,					
Junction-to-Case		RFM10P15	-	1.25	-	1.25	
		RFP10P12,		1 67		1.07	°C/W
		RFP10P15	-	1.67	-	1.67	

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

CHARACTERISTIC SYMB		L TEST CONDITIONS	LIMITS				
	SYMBOL		RFM10P12 RFP10P12		RFM10P15 RFP10P15		UNITS
			MIN.	MAX.	MIN.	MAX.	
Diode Forward Voltage	V _{SD} &	I _{SD} = 5A	_	1.4	_	1.4	V
Reverse Recovery Time	t _{rr}	$I_F = 4A$, $d_{1F}/d_1 = 100A/\mu s$	210	(typ.)	210	210 (typ.)	

[♣] Pulsed: Pulse duration = 300 μs max., duty cycle = 2%.

RFM10P12, RFM10P15, RFP10P12, RFP10P15

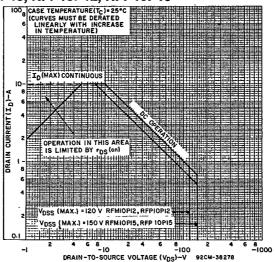


Fig. 1 - Maximum safe operating areas for all types.

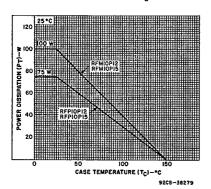


Fig. 2 - Power dissipation vs. case temperature derating curve for all types.

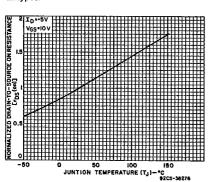


Fig. 4 - Normalized drain-to-source on resistance as as function of junction temperature for all types.

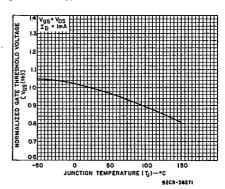


Fig. 3 - Typical normalized gate threshold voltage as a function of junction temperature for all types.

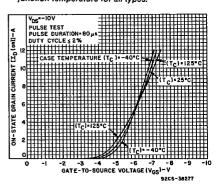


Fig. 5 - Typical transfer characteristics for all types.

RFM10P12, RFM10P15, RFP10P12, RFP10P15

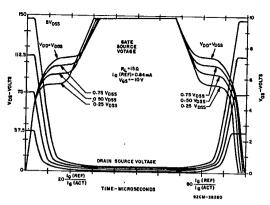


Fig. 6 - Normalized switching waveforms for constant gate-current drive. Refer to RCA Power MOSFETs PMP411A.

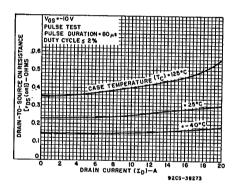


Fig. 8 - Typical drain-to-source on resistance as a function of drain current for all types.

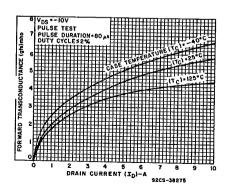


Fig. 10 - Typical forward transconductance as a function of drain current for all types.

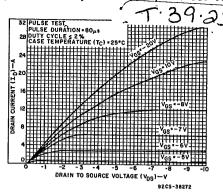


Fig. 7 - Typical saturation characteristics for all types.

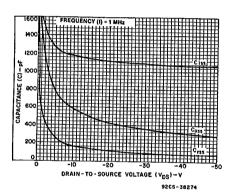


Fig. 9 - Capacitance as a function of drain-to-source voltage for all types.

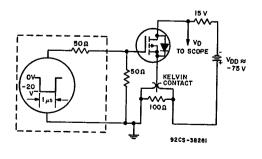


Fig. 11 - Switching Time Test Circuit.