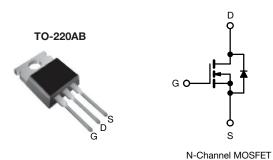
HALOGEN FREE



## **Power MOSFET**



| PRODUCT SUMMAI           | RY                     |      |
|--------------------------|------------------------|------|
| V <sub>DS</sub> (V)      | 10                     | 00   |
| $R_{DS(on)}(\Omega)$     | V <sub>GS</sub> = 10 V | 0.16 |
| Q <sub>g</sub> max. (nC) | 2                      | 6    |
| Q <sub>gs</sub> (nC)     | 5.                     | .5   |
| Q <sub>gd</sub> (nC)     | 1                      | 1    |
| Configuration            | Sin                    | gle  |

### **FEATURES**

- Dynamic dV/dt rating
- Repetitive avalanche rated
- 175 °C operating temperature
- · Fast switching
- · Ease of paralleling
- Simple drive requirements
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### Note

\* This datasheet provides information about parts that are RoHS-compliant and / or parts that are non RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information / tables in this datasheet for details

## **DESCRIPTION**

Third generation power MOSFETs from Vishay provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-220AB package is universally preferred for all commercial-industrial applications at power dissipation levels to approximately 50 W. The low thermal resistance and low package cost of the TO-220AB contribute to its wide acceptance throughout the industry.

| ORDERING INFORMATION            |               |
|---------------------------------|---------------|
| Package                         | TO-220AB      |
| Lead (Pb)-free                  | IRF530PbF     |
| Lead (Pb)-free and halogen-free | IRF530PbF-BE3 |

| ABSOLUTE MAXIMUM RATINGS (T <sub>C</sub> = 25 °C, unless otherwise parameter |                         | SYMBOL  | LIMIT           | UNIT |          |
|--|-------------------------|---|-----------------|------|----------|
| Drain-source voltage   |                         | V <sub>DS</sub>                               | 100             | - V  |          |
| Gate-source voltage  |                         |   | V <sub>GS</sub> |      | ± 20     |
| Outline and distance and   | V <sub>GS</sub> at 10 V | T <sub>C</sub> = 25 °C                        |                 | 14   |          |
| Continuous drain current   |                         | $T_C = 25 ^{\circ}C$<br>$T_C = 100 ^{\circ}C$ | I <sub>D</sub>  | 10   | А        |
| Pulsed drain current <sup>a</sup>  |                         | I <sub>DM</sub>                               | 56              |      |          |
| Linear derating factor   |                         |   | 0.59            | W/°C |          |
| Single pulse avalanche energy <sup>b</sup>                                   |                         | E <sub>AS</sub>                               | 69              | mJ   |          |
| Repetitive avalanche current a   |                         | I <sub>AR</sub>                               | 14              | А    |          |
| Repetitive avalanche energy a  |                         |   | E <sub>AR</sub> | 8.8  | mJ       |
| Maximum power dissipation T <sub>C</sub> = 25 °C                             |                         | P <sub>D</sub>                                | 88              | W    |          |
| Peak diode recovery dV/dt <sup>c</sup>                                       |                         | dV/dt   | 5.5             | V/ns |          |
| Operating junction and storage temperature range                             |                         | T <sub>J</sub> , T <sub>stg</sub>             | -55 to +175     | °C   |          |
| Soldering recommendations (peak temperature) <sup>d</sup> For 10 s           |                         |   | 300             |      |          |
| Mounting toward  | 6-32 or M3 screw        |   |                 | 10   | lbf ⋅ in |
| Mounting torque  |                         |   |                 | 1.1  | N · m    |

#### Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11)
- b.  $V_{DD}$  = 25 V, starting  $T_J$  = 25 °C, L = 528  $\mu$ H,  $R_g$  = 25  $\Omega$ ,  $I_{AS}$  = 14 A (see fig. 12)
- c.  $I_{SD} \le 14 \text{ A}$ ,  $dI/dt \le 140 \text{ A/}\mu\text{s}$ ,  $V_{DD} \le V_{DS}$ ,  $T_J \le 175 \text{ °C}$
- d. 1.6 mm from case



# Vishay Siliconix

| THERMAL RESISTANCE RAT              | INGS              |      |      |      |
|-------------------------------------|-------------------|------|------|------|
| PARAMETER                           | SYMBOL            | TYP. | MAX. | UNIT |
| Maximum junction-to-ambient         | R <sub>thJA</sub> | -    | 62   |      |
| Case-to-sink, flat, greased surface | R <sub>thCS</sub> | 0.50 | -    | °C/W |
| Maximum junction-to-case (drain)    | R <sub>thJC</sub> | -    | 1.7  |      |

| PARAMETER                                 | SYMBOL   | TEST CONDITIONS   |  | MIN.      | TYP.      | MAX.                 | UNIT             |
|---|--|---|--|-----------|-----------|----------------------|------------------|
| Static                                    |  |   |  |           |           | •                    | ,                |
| Drain-source breakdown voltage            | V <sub>DS</sub>  | $V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$   |  | 100       | -         | -                    | V                |
| V <sub>DS</sub> temperature coefficient   | $\Delta V_{DS}/T_{J}$  | Reference t   | to 25 °C, I <sub>D</sub> = 1 mA  | -         | 0.12      | -                    | V/°C             |
| Gate-source threshold voltage             | V <sub>GS(th)</sub>  | $V_{DS} = V_{C}$  | <sub>GS</sub> , I <sub>D</sub> = 250 μA  | 2.0       | -         | 4.0                  | V                |
| Gate-source leakage                       | I <sub>GSS</sub>   | V <sub>G</sub>  | <sub>S</sub> = ± 20 V  | -         | -         | ± 100                | nA               |
| Zana mata walka na dinaina numanat        | e voltage drain current $I_{DSS} = V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}$ $V_{DS} = 80 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 150 \text{ °C}$ | V <sub>DS</sub> = 10  | V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V                                   |           | -         | 25                   | ,.,              |
| Zero gate voltage drain current           |  | <sub>GS</sub> = 0 V, T <sub>J</sub> = 150 °C  | -  | -         | 250       | μA                   |                  |
| Drain-source on-state resistance          | R <sub>DS(on)</sub>  | V <sub>GS</sub> = 10 V  | I <sub>D</sub> = 8.4 A <sup>b</sup>  | -         | -         | 0.16                 | Ω                |
| Forward transconductance                  | 9 <sub>fs</sub>  | V <sub>DS</sub> = 50  | O V, I <sub>D</sub> = 8.4 A <sup>b</sup>   | 5.1       | -         | -                    | S                |
| Dynamic                                   |  |   |  |           |           |                      |                  |
| Input capacitance                         | C <sub>iss</sub>   | $V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V},$  |  | -         | 670       | -                    | pF               |
| Output capacitance                        | C <sub>oss</sub>   |   |  | -         | 250       | -                    |                  |
| Reverse transfer capacitance              | $C_{rss}$  | f = 1.0 I   | MHz, see fig. 5  | -         | 60        | -                    |                  |
| Total gate charge                         | $Q_g$  |   | I <sub>D</sub> = 14 A, V <sub>DS</sub> = 80 V,<br>see fig. 6 and 13 <sup>b</sup> | -         | -         | 26                   | nC               |
| Gate-source charge                        | $Q_{gs}$   | V <sub>GS</sub> = 10 V  |  | -         | -         | 5.5                  |                  |
| Gate-drain charge                         | $Q_{gd}$   |   |  | -         | -         | 11                   |                  |
| Turn-on delay time                        | t <sub>d(on)</sub>   |   |  | -         | 10        | -                    |                  |
| Rise time                                 | t <sub>r</sub>   | $V_{DD}$ = 50 V, $I_{D}$ = 14 A $R_{g}$ = 12 $\Omega$ , $R_{D}$ = 3.6 $\Omega$ , see fig. 10 <sup>b</sup> |  | -         | 34        | -                    | ns               |
| Turn-off delay time                       | t <sub>d(off)</sub>  |   |  | -         | 23        | -                    |                  |
| Fall time                                 | t <sub>f</sub>   |   |  | -         | 24        | -                    |                  |
| Gate input resistance                     | $R_g$  | f = 1 MHz, open drain   |  | 1.0       | -         | 4.7                  | Ω                |
| Internal drain inductance                 | L <sub>D</sub>   | Between lead,<br>6 mm (0.25") from<br>package and center of<br>die contact                                |  | -         | 4.5       | -                    | الم              |
| Internal source inductance                | L <sub>S</sub>   |   |  | -         | 7.5       | -                    | - nH             |
| Drain-Source Body Diode Characteristic    | cs   |   |  |           |           |                      |                  |
| Continuous source-drain diode current     | I <sub>S</sub>   | MOSFET symbol showing the integral reverse p - n junction diode   |  | -         | -         | 14                   | Α                |
| Pulsed diode forward current <sup>a</sup> | I <sub>SM</sub>  |   |  | -         | -         | 56                   | ^                |
| Body diode voltage                        | $V_{SD}$   | T <sub>J</sub> = 25 °C, I <sub>S</sub>  | <sub>S</sub> = 14 A, V <sub>GS</sub> = 0 V <sup>b</sup>                          | -         | -         | 2.5                  | V                |
| Body diode reverse recovery time          | t <sub>rr</sub>  | T _ 2F °C   | 14 A dI/d+ = 100 A/c h   | -         | 150       | 280                  | ns               |
| Body diode reverse recovery charge        | Q <sub>rr</sub>  | $T_J = 25 ^{\circ}\text{C}, I_F = 14 \text{A},  \text{dI/dt} = 100 \text{A/}\mu\text{s}^{\text{b}}$       |  | -         | 0.85      | 1.7                  | μC               |
| Forward turn-on time                      | t <sub>on</sub>  | Intrinsic turn  | on time is negligible (turr  | -on is do | minated b | y L <sub>S</sub> and | L <sub>D</sub> ) |

## Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11)
- b. Pulse width  $\leq$  300  $\mu$ s; duty cycle  $\leq$  2 %



## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

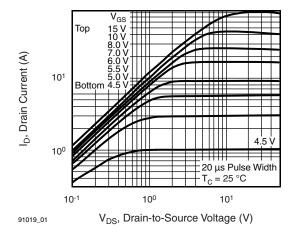


Fig. 1 - Typical Output Characteristics, T<sub>C</sub> = 25 °C

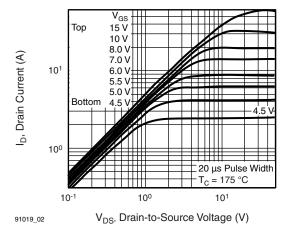


Fig. 2 - Typical Output Characteristics,  $T_C = 175$  °C

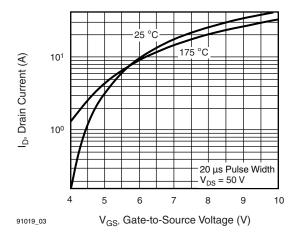


Fig. 3 - Typical Transfer Characteristics

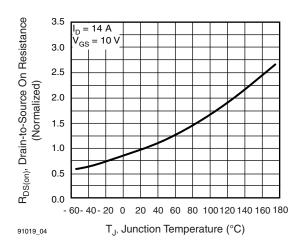


Fig. 4 - Normalized On-Resistance vs. Temperature

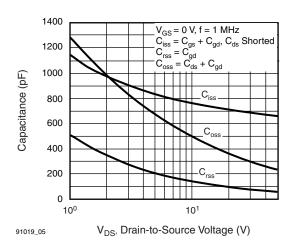


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

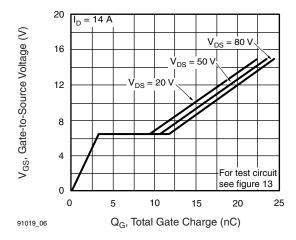


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage



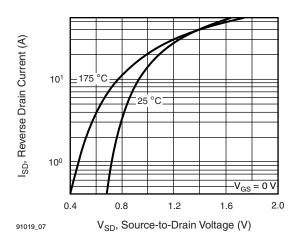


Fig. 7 - Typical Source-Drain Diode Forward Voltage

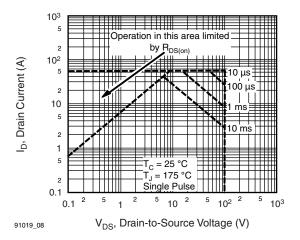


Fig. 8 - Maximum Safe Operating Area

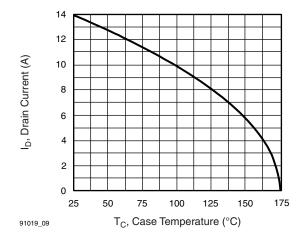


Fig. 9 - Maximum Drain Current vs. Case Temperature

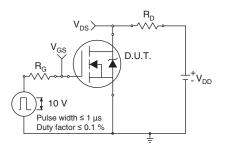


Fig. 10a - Switching Time Test Circuit

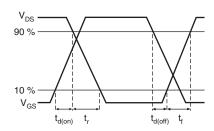


Fig. 10b - Switching Time Waveforms



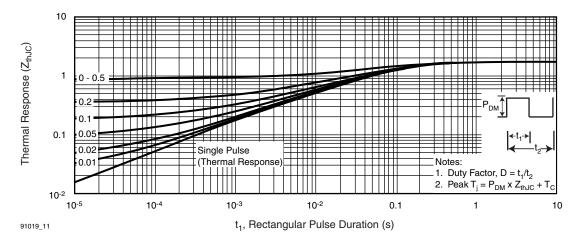


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case

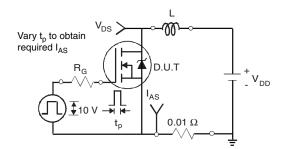


Fig. 12a - Unclamped Inductive Test Circuit

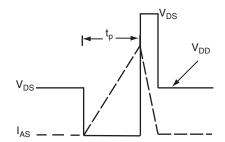


Fig. 12b - Unclamped Inductive Waveforms

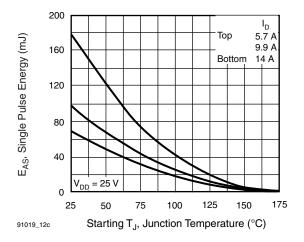


Fig. 12c - Maximum Avalanche Energy vs. Drain Current



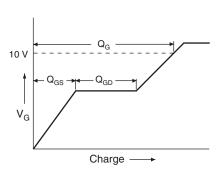


Fig. 13a - Basic Gate Charge Waveform

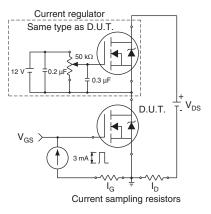
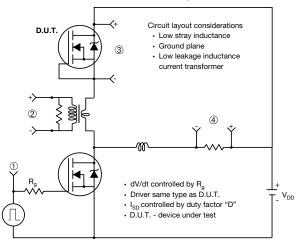


Fig. 13b - Gate Charge Test Circuit

## Peak Diode Recovery dV/dt Test Circuit



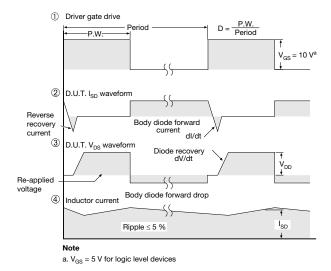


Fig. 14 - For N-Channel

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# TO-220-1



| DIM. | MILLIM | METERS | INC   | HES   |
|------|--------|--------|-------|-------|
|      | MIN.   | MAX.   | MIN.  | MAX.  |
| Α    | 4.24   | 4.65   | 0.167 | 0.183 |
| b    | 0.69   | 1.02   | 0.027 | 0.040 |
| b(1) | 1.14   | 1.78   | 0.045 | 0.070 |
| С    | 0.36   | 0.61   | 0.014 | 0.024 |
| D    | 14.33  | 15.85  | 0.564 | 0.624 |
| Е    | 9.96   | 10.52  | 0.392 | 0.414 |
| е    | 2.41   | 2.67   | 0.095 | 0.105 |
| e(1) | 4.88   | 5.28   | 0.192 | 0.208 |
| F    | 1.14   | 1.40   | 0.045 | 0.055 |
| H(1) | 6.10   | 6.71   | 0.240 | 0.264 |
| J(1) | 2.41   | 2.92   | 0.095 | 0.115 |
| L    | 13.36  | 14.40  | 0.526 | 0.567 |
| L(1) | 3.33   | 4.04   | 0.131 | 0.159 |
| ØP   | 3.53   | 3.94   | 0.139 | 0.155 |
| Q    | 2.54   | 3.00   | 0.100 | 0.118 |

## Note

DWG: 6031

•  $M^* = 0.052$  inches to 0.064 inches (dimension including protrusion), heatsink hole for HVM



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