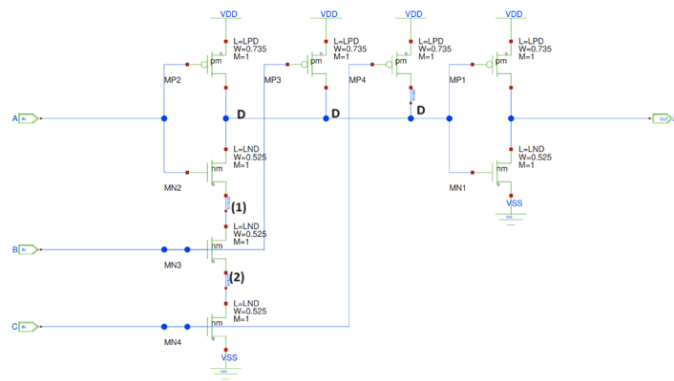
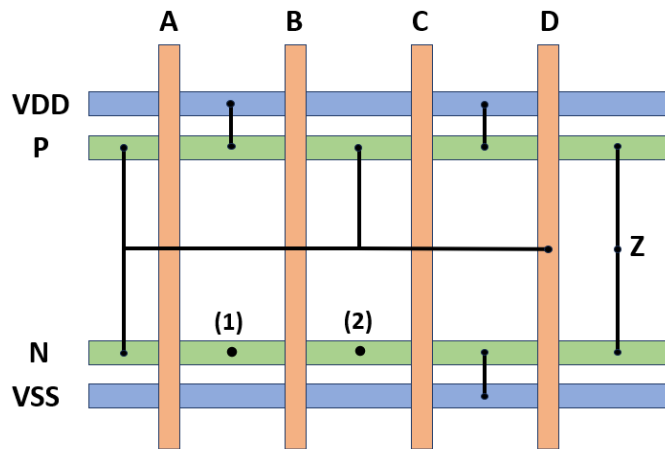


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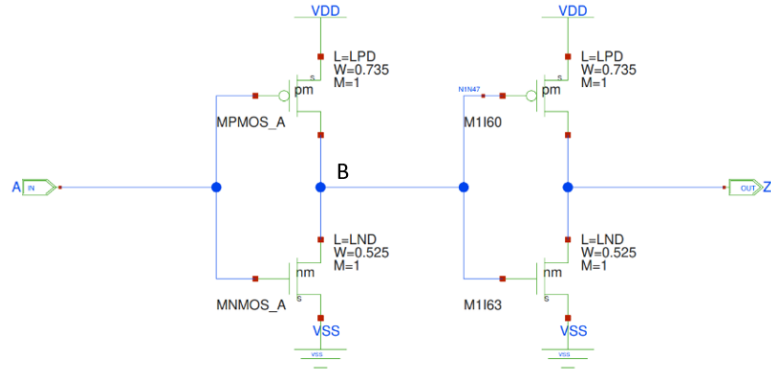


pMOS: D → A → VDD → B → D → VDD → Z →
nMOS: D → A → (1) → B → (2) → VSS → Z →

dti_55g_10t_and3x1.pdf

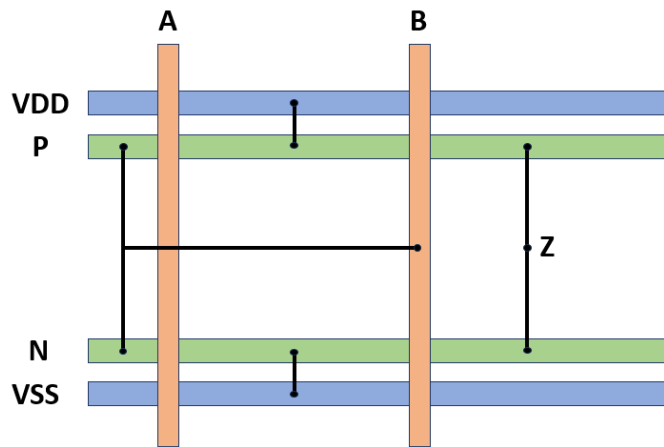


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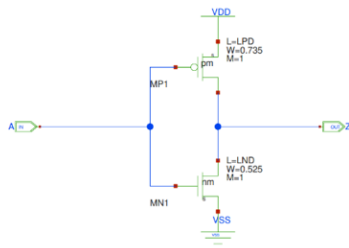


pMOS: $\xrightarrow{B} A \xrightarrow{VDD} B \xrightarrow{Z}$
 nMOS: $\xrightarrow{B} A \xrightarrow{VSS} B \xrightarrow{Z}$

dti_55g_10t_bufx1.pdf

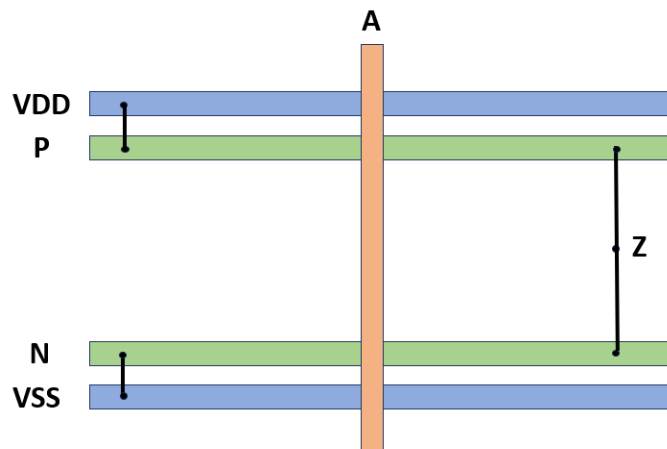


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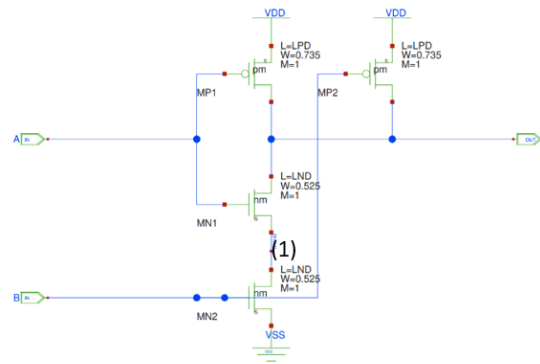


pMOS: $\xrightarrow{\text{VDD}} \text{A} \xrightarrow{\text{Z}}$
nMOS: $\xrightarrow{\text{VSS}} \text{A} \xrightarrow{\text{Z}}$

dti_55g_10t_invx1.pdf

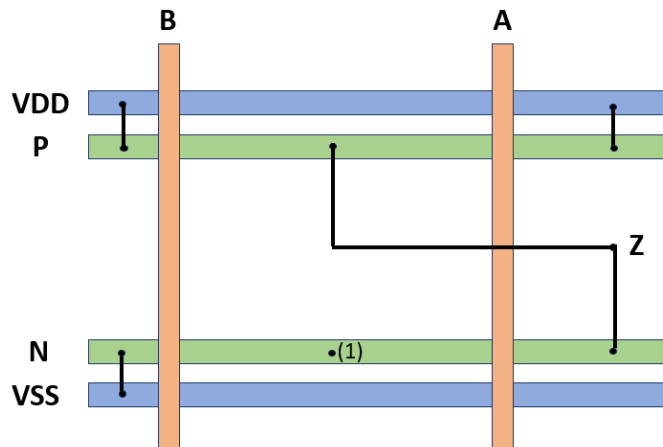


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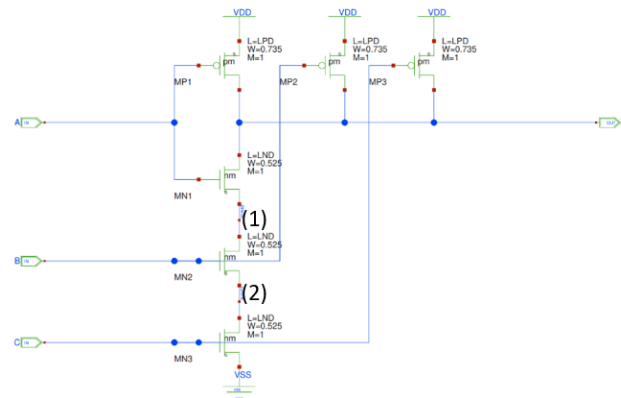


pMOS: $\xrightarrow{\text{VDD}} \text{B} \xrightarrow{\text{Z}} \text{A} \xrightarrow{\text{VDD}}$
 nMOS: $\xrightarrow{\text{VSS}} \text{B} \xrightarrow{(1)} \text{A} \xrightarrow{\text{Z}}$

dti_55g_10t_nand2x1.pdf

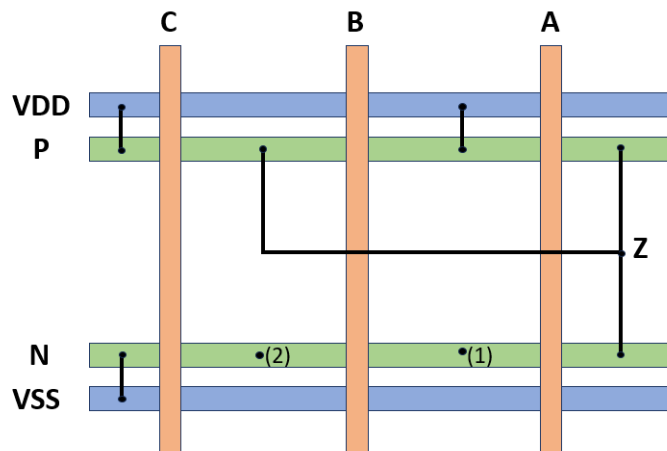


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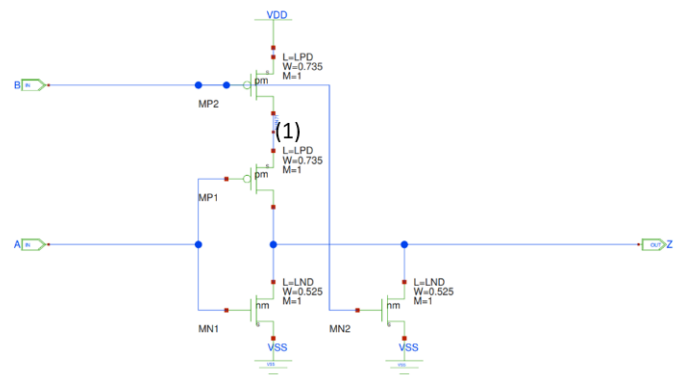


pMOS: $\text{VDD} \xrightarrow{\text{C}} \text{Z} \xrightarrow{\text{B}} \text{VDD} \xrightarrow{\text{A}} \text{Z}$
nMOS: $\text{VSS} \xrightarrow{\text{C}} \text{(2)} \xrightarrow{\text{B}} \text{(1)} \xrightarrow{\text{A}} \text{Z}$

dti_55g_10t_nand3x1.pdf

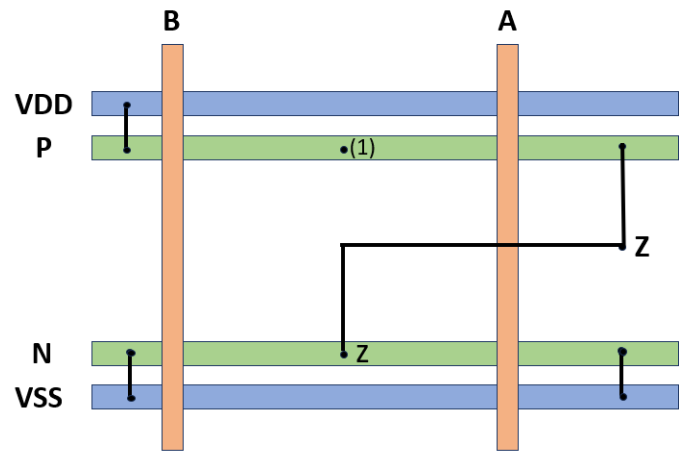


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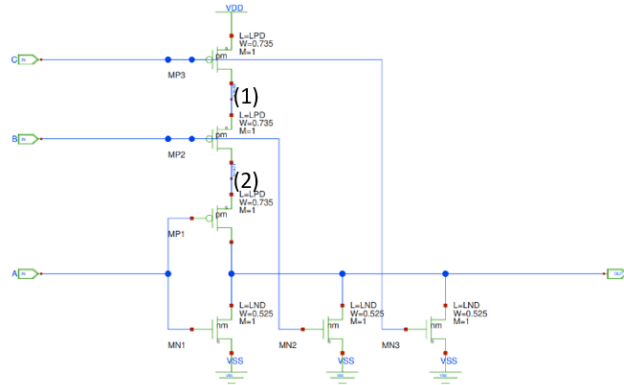


pMOS: VDD (1) Z
nMOS: VSS Z VSS

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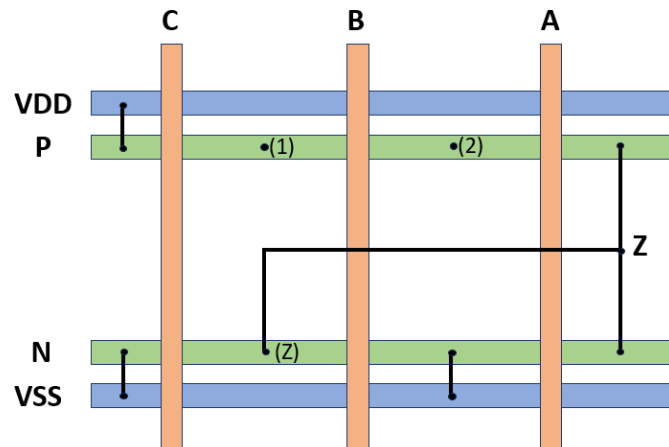


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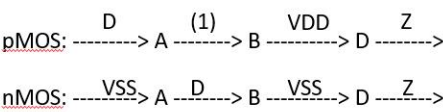
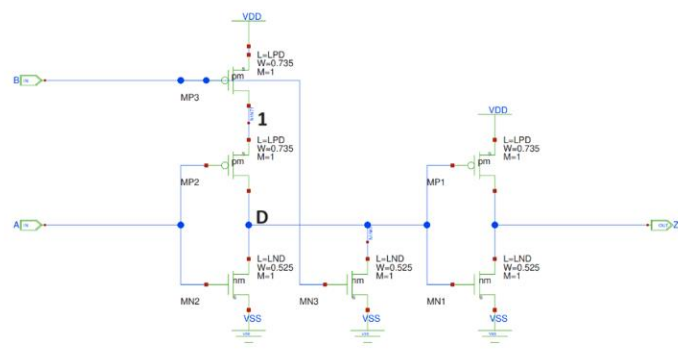


pMOS: $\text{VDD} \xrightarrow{(1)} \text{C} \xrightarrow{(2)} \text{B} \xrightarrow{\text{Z}} \text{A} \xrightarrow{\text{Z}}$
 nMOS: $\text{VSS} \xrightarrow{\text{Z}} \text{C} \xrightarrow{\text{Z}} \text{B} \xrightarrow{\text{VSS}} \text{A} \xrightarrow{\text{Z}}$

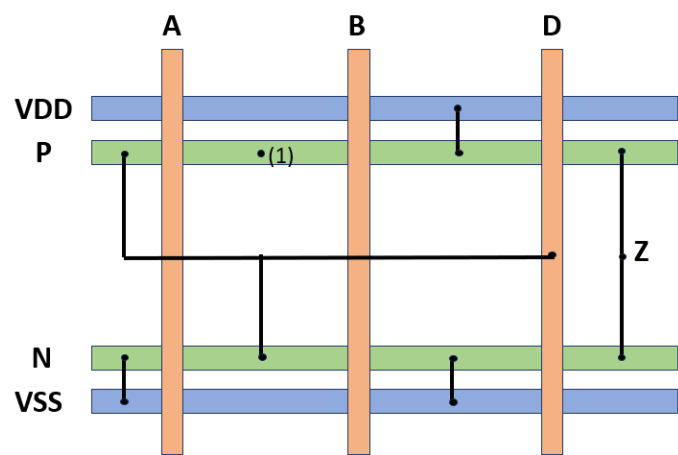
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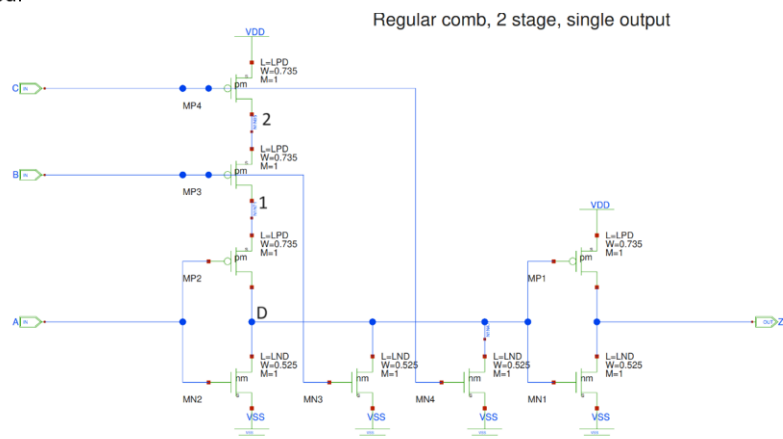
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dti_55g_10t_or2x1.pdf

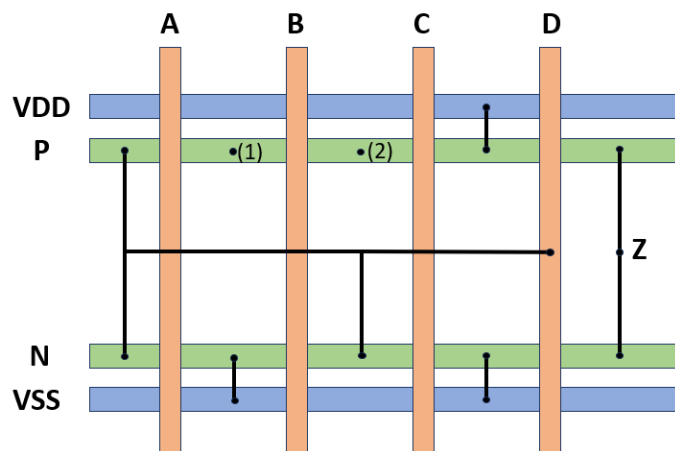


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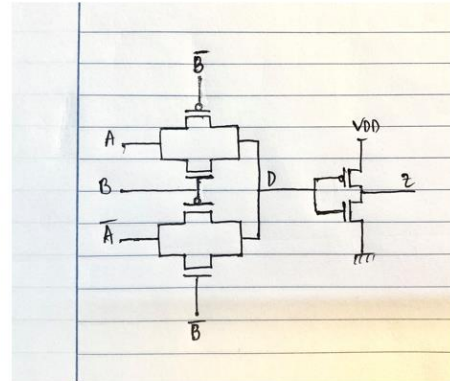
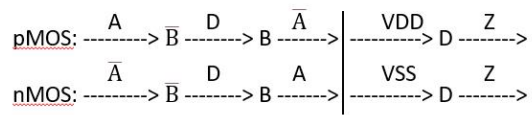
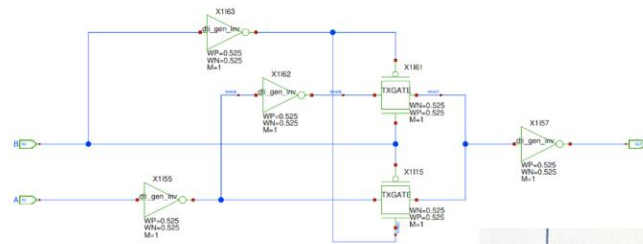


pMOS: D -----> A -----> B -----> C -----> D -----> Z
nMOS: D -----> A -----> B -----> C -----> D -----> Z

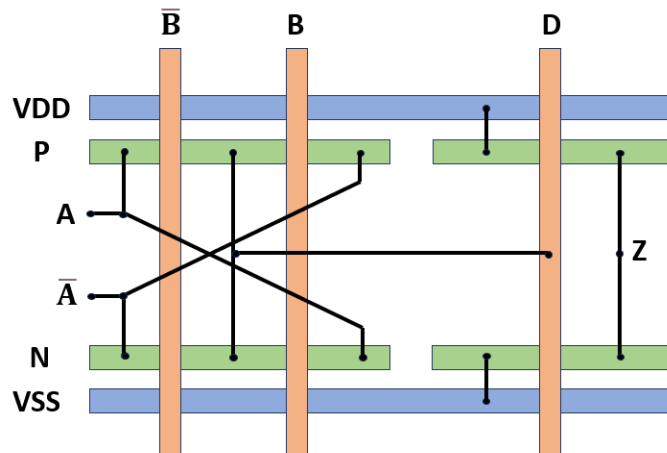
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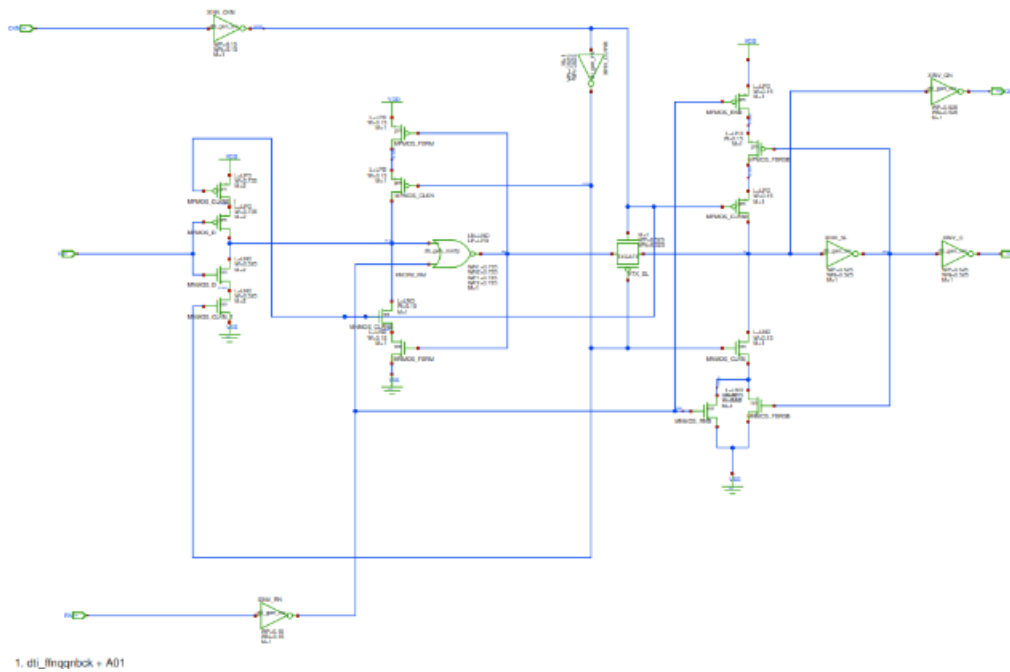


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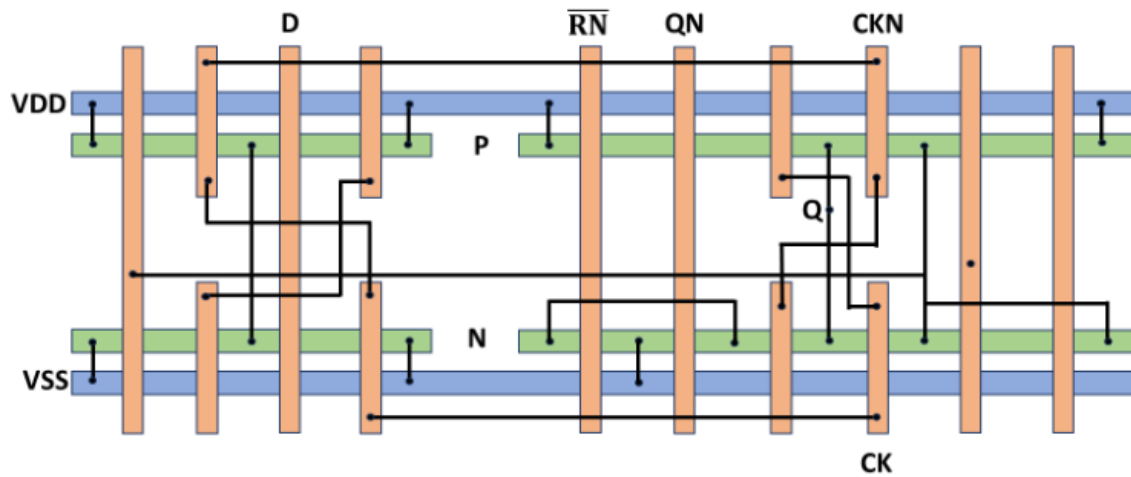


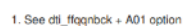
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dti_55g_10t_ffnqnbcka01x1.pdf





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