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Section: 005

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CS/ECE 252 Introduction to Computer Engineering

Fall 2018
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Homework 3 Deadline: October 3rd 2018

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This Homework covers problems from chapter 2 and chapter 3 of the textbook and is worth 32 points.

For each question below you need to show the complete working to receive full points. Please utilize the space provided under each question. Please upload a PDF version on canvas.

Problem 1

(2 pts)

Convert the ASCII string "CS252" to its hexadecimal representation. Only represent the characters within the quotation marks and assume it is null terminated.

43 53 32 35 32₁₆
C S 2 5 2

Problem 2

(6 pts)

single: 8 bits

single: 23 bits

double: 11 bits

double: 52 bits

S	Exponent	Fraction
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(1). What is the most positive number that can be represented by the single precision floating point representation? Give both the floating-point representation and the equivalent decimal number (give answer up to fifth fractional digits).

S	Exponent	Fraction
0	1111110	11111

Decimal Value	3.4×10^{39}
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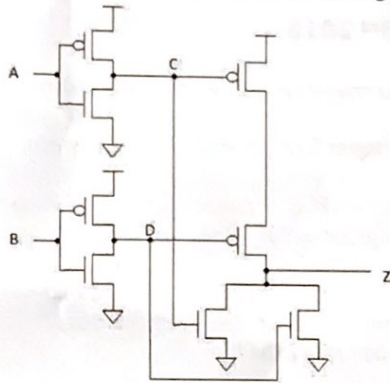
(2). Represent 80.5 with single precision floating point representation.

S	Exponent	Fraction
0	10000101	01000010000...

Problem 3:

(6 pts)

Finish the truth table for the following CMOS circuit:



A	B	C	D	Z
0	0	1	1	0
0	1	1	0	0
1	0	0	1	0
1	1	0	0	1

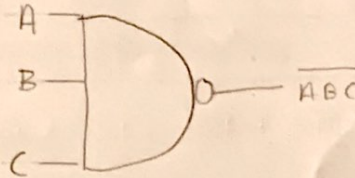
Problem 4:

(8 pts)

Draw a 3-input CMOS gate that satisfies the following Boolean expression, and then fill out its truth table:

$$OUT = \overline{ABC}$$

A	B	C	OUT
0	0	0	1
0	0	1	1
0	1	0	1
0	1	1	1
1	0	0	1
1	0	1	1
1	1	0	1
1	1	1	0

**Problem 5**

(4 pts)

(1). Given the following truth table, express the output Z in terms of A B, and C.

A	B	C	Z
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	0

1	0	0	1	$\overline{A}\overline{B}C$
1	0	1	1	$\overline{A}B\overline{C}$
1	1	0	1	$A\overline{B}\overline{C}$
1	1	1	1	ABC

$$Z = \overline{A}\overline{B}C + \overline{A}B\overline{C} + A\overline{B}\overline{C} + ABC$$

Problem 6

(4 pts)

Simplify the following equation:

(Hint: use De Morgan's laws.)

$$Z = \overline{\overline{A}\overline{B}} \cdot \overline{\overline{B}\overline{C}} + \overline{\overline{A}C}$$

$$\overline{\overline{A}\overline{B}} + \overline{\overline{B}\overline{C}} \cdot \overline{\overline{A}C}$$

DeMorgan's Law

$$AB + BC \cdot \overline{A}C$$

Double negation

$$B \cdot (A + C) \cdot \overline{A}C$$

Distributive Law (OR)

$$B \cdot (A\overline{A}C + C\overline{A}C)$$

Distributive Law (OR)

$$B \cdot (0C + \overline{C}\overline{A}C)$$

Complement

$$B \cdot (0 + C\overline{A}C)$$

Annulment (2 pts)

$$B \cdot (0 + \overline{A}C)$$

Idempotent

$$B \cdot \overline{A}C$$

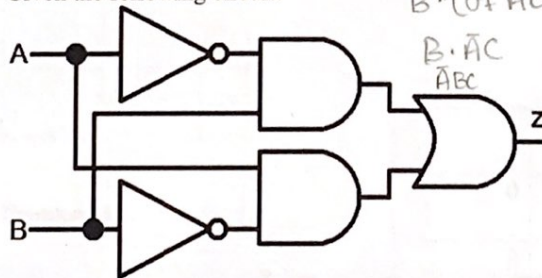
Identity

$$\overline{A}BC$$

Commutative

Problem 7

Given the following circuit:



Express Z in terms of A and B.

$$A \cdot \overline{B} + \overline{A} \cdot B$$