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Main x +

CIRCUIT ELEMENTS

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Input

Output

Gates

Decoders & Plexers

Sequential Elements

Annotation

Misc

TIMING DIAGRAM

1 cycle = 1000 Units

Properties

PROJECT PROPERTIES

Project: Untitled

Circuit: Main

Clock Time (ms): 500

Clock Enabled: ☒

Lite Mode: ☐

Edit Layout

Delete Circuit

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CIRCUIT ELEMENTS

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TESTBENCH

No Test is attached to the current circuit

Attach Test

Misc

TIMING DIAGRAM

1 cycle = 1000 Units

Properties

PROJECT PROPERTIES

Project: Untitled

Circuit: 15

Clock Time (ms): 500

Clock Enabled: ☒

Lite Mode: ☐

Edit Layout

Delete Circuit

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TIMING DIAGRAM

1 cycle = 1000 Units

X

Y

X

Y

PROPERTIES

PROJECT PROPERTIES

Project: Untitled

Circuit: 16

Clock Time (ms): 500

Clock Enabled: ☒

Lite Mode: ☐

Edit Layout

Delete Circuit

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TIMING DIAGRAM

1 cycle = 1000 Units

X

Y

X

Y

PROPERTIES

PROJECT PROPERTIES

Project: Untitled

Circuit: 17

Clock Time (ms): 500

Clock Enabled: ☒

Lite Mode: ☐

Edit Layout

Delete Circuit

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Search

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