electro-chlorination station 3_V13_SP1_V18

Project							
Name:	electro-chlorination station 3_V13_SP1_V18	**Creation time:**	04/30/2025 14:59:45	**Last change**	06/09/2025 17:38:42	**Author:**	malki.lamine
Last modified by:	Bsat	**Version:**					
Comment:							

Operating system	
Name	**Description**
Operating system	Microsoft Windows 10 Pro
Version of the operating system	10.0.19045.0
Operating system service pack	
Version of the Internet Explorer	11.3636.19041.0
Computer name	DESKTOP-L3KGCOM
User name	DESKTOP-L3KGCOM\Bsat
Installation path of the TIA Portal	C:\Program Files\Siemens\Automation\Portal V18

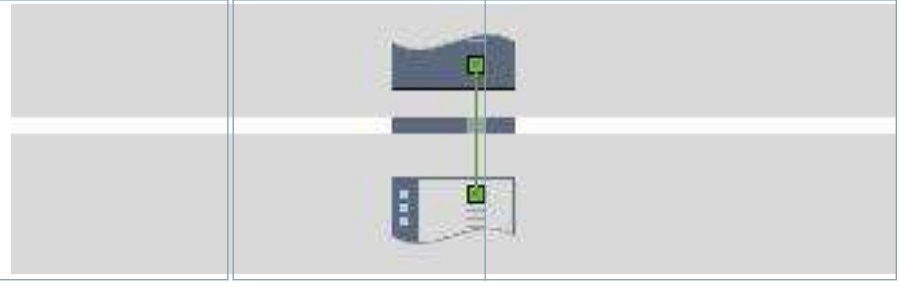
Components		
Name	**Version**	**Release**
AWB V2 - Automation License Manager Plugin V2.0 (AWB_V2)	V2.0	V02.00.00.00_01.04.00.03
AWB V2 - Software Management Plugin V2.0 (AWB_V2)	V2.0	V02.00.00.00_01.04.00.03
AWB V2 - TIA Addin V2.0 (AWB_V2)	V2.0	V02.00.00.00_01.04.00.03
AWB V2 - Central User Management Plugin V2.0 (AWB_V2)	V2.0	V02.00.00.00_01.04.00.03
AWB V2 - Host V2.1 (AWB_V2)	V2.1	02.01.00.00_01.00.00.50
TIA Portal Project Server - TIA Portal Project Server Single SetupPackage V1.1 (ProjectServer)	V1.1	V01.01.00.00_00.00.01.52
Totally Integrated Automation Portal V18 - TIA Portal Single SetupPackage V18.0 SP1 (TIAP18)	V18.0 + SP1	V18.00.01.00_42.01.00.01
Siemens Totally Integrated Automation Portal V18 - HM All Editions Single SetupPackage V18.0 + SP1 (TIAP18)	V18.0 + SP1	V18.00.01.00_42.01.00.01
Siemens Totally Integrated Automation Portal V18 - HM NoBasic Single SetupPackage V18.0 + SP1 (TIAP18)	V18.0 + SP1	V18.00.01.00_42.01.00.01
Siemens Totally Integrated Automation Portal V18 - Hardware Support Base Package 0 V18.0 + SP1 (TIAP18)	V18.0 + SP1	V18.00.01.00_35.01.00.01
Siemens Totally Integrated Automation Portal V18 - Multiuser Client Single SetupPackage V18.0 + SP1 (TIAP18)	V18.0 + SP1	V18.00.01.00_42.01.00.01
Siemens Totally Integrated Automation Portal V18 - Version Control Interface SetupPackage V18.0 + SP1 (TIAP18)	V18.0 + SP1	V18.00.01.00_42.01.00.01
Siemens Totally Integrated Automation Portal V18 - STEP 7 Safety Single SetupPackage V18.0 + SP1 (TIAP18)	V18.0 + SP1	V18.00.01.00_42.01.00.01
Siemens Totally Integrated Automation Portal V18 - STEP 7 Single SetupPackage V18.0 + SP1 (TIAP18)	V18.0 + SP1	V18.00.01.00_42.01.00.01
Siemens Totally Integrated Automation Portal V18 - Hardware Support Base Package 02 V18.0 + SP1 (TIAP18)	V18.0 + SP1	V18.00.01.00_35.01.00.01
Siemens Totally Integrated Automation Portal V18 - Hardware Support Base Package 03 V18.0 + SP1 (TIAP18)	V18.0 + SP1	V18.00.01.00_35.01.00.01
Siemens Totally Integrated Automation Portal V18 - Hardware Support Base Package 04 V18.0 + SP1 (TIAP18)	V18.0 + SP1	V18.00.01.00_35.01.00.01
Siemens Totally Integrated Automation Portal V18 - Support Base Package TO-01 V18.0 + SP1 (TIAP18)	V18.0 + SP1	V18.00.01.00_35.01.00.01
Siemens Totally Integrated Automation Portal V18 - Support Base Package TO-02 V18.0 + SP1 (TIAP18)	V18.0 + SP1	V18.00.01.00_35.01.00.01
Siemens Totally Integrated Automation Portal V18 - Hardware Support Base Package WCF-01 V18.0 + SP1 (TIAP18)	V18.0 + SP1	V18.00.01.00_35.01.00.01
Siemens Totally Integrated Automation Portal V18 - TIACOMPCHECK Single SetupPackage V18.0 + SP1 (TIAP18)	V18.0 + SP1	V18.00.01.00_42.01.00.01
Siemens Totally Integrated Automation Portal V18 - TIA Portal Security Audit Log Single SetupPackage V18.0 + SP1 (TIAP18)	V18.0 + SP1	V18.00.01.00_42.01.00.01
Siemens Totally Integrated Automation Portal V18 - TIA Portal Shim Single SetupPackage V18.0 + SP1 (TIAP18)	V18.0 + SP1	V18.00.01.00_42.01.00.01
Siemens Totally Integrated Automation Portal V18 - Simatic Single SetupPackage V18.0 + SP1 (TIAP18)	V18.0 + SP1	V18.00.01.00_42.01.00.01
Siemens Totally Integrated Automation Portal V18 - WinCC Single SetupPackage V18.0 + SP1 (TIAP18)	V18.0 + SP1	V18.00.01.00_42.01.00.01
Siemens Totally Integrated Automation Portal V18 - Openness SetupPackage V18.0 + SP1 (TIAP18)	V18.0 + SP1	V18.00.01.00_42.01.00.01
Siemens Totally Integrated Automation Portal V18 - WinCC Transfer Mandatory Single SetupPackage V18.0 + SP1 (TIAP18)	V18.0 + SP1	V18.00.01.00_42.01.00.01
User Management Component - UserManagementComponentx64 V2.10 (UMC64)	V2.10	V02.10.00.00_00.00.00.38
User Management Component - umtrayiconx64 V2.10 (UMC64)	V2.10	V02.10.00.00_00.00.00.38
WinCC Runtime Advanced V17.0 - SIMATIC WinCC Runtime Advanced V17.0 (HMIRTM_V11)	V17.0 UPD5	V17.00.00.05_04.01.00.01
WinCC Runtime Advanced V17.0 - HMIRTM Tagging Package 01 Single SetupPackage V17.0 UPD5 (HMIRTM_V11)	V17.0 UPD5	V17.00.00.05_04.01.00.01
SIMATIC S7-PCT - SIMATIC S7-PCT V3.5 SP3 (S7PCT)	V3.5 + SP3	V03.05.03.00_01.12.00.13
WinCC Runtime Professional V18 - SIMATIC WinCC Runtime V18.0 (SCADA-RT_V11)	V18.0	V07.06.18.00_01.38.00.01
WinCC Runtime Professional V18 - OPCUA_Client V1.1 + SP3 (SCADA-RT_V11)	V1.1 + SP3	V01.01.03.00_01.09.00.03
WinCC Runtime Professional V18 - SCADA Simulation Single SetupPackage V18.0 + SP1 (SCADA-RT_V11)	V18.0 + SP1	V18.00.01.00_42.01.00.01
Siemens Totally Integrated Automation Portal V18 - Simatic Single SetupPackage 32 Bit V18.0 + SP1 (TIAP18)	V18.0 + SP1	V18.00.01.00_42.01.00.01
Siemens Totally Integrated Automation Portal V18 - WinCC Single SetupPackage 32 Bit V18.0 + SP1 (TIAP18)	V18.0 + SP1	V18.00.01.00_42.01.00.01

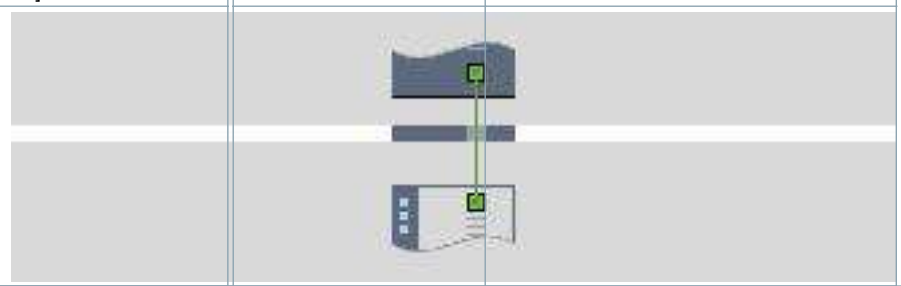
Name	Version	Release
AddinRolloutService	18.0.1.0	V18.00.01.00_42.01.00.01
SIMATIC HMI License Manager Panel Plugin (x64)	18.0.1.0	V18.00.01.00_42.01.00.01
SIMATIC WinCC Runtime Advanced Driver (x64)	18.0.1.0	V18.00.01.00_42.01.00.01
ETWEventCollector	18.0.1.0	V18.00.01.00_42.01.00.01
SIMATIC NCM FWL 64	5.6.0.3	K5.6.0.3_1.1.0.2
NCM GPRS 64	01.02.00.00	V1.2.0.0_2.1.0.1
SIMATIC PLCSIM 64	17.00.00	17.00.00.00_01.00.37.01
SIMATIC Device Drivers	9.3	09.03.03.00_01.04.00.03
TelemetryConnector	1.8.0.49	V01.08.00.49_01.00.00.00
SIMATIC HMIProvider	7.0	K07.00.03.01_01.01.00.01
SIEMENS OPC	3.9	03.09.12.00_01.07.00.01
SIMATIC HMI ProSave	18.0.1.0	V18.00.01.00_42.01.00.01
SIMATIC HMI Symbol Library	17.0.0.5	V17.00.00.05_04.01.00.01
SIMATIC HMI Touch Input	17.0.0.5	V17.00.00.05_04.01.00.01
SIMATIC Runtime Interfaces	2.1	K02.01.00.03_01.01.00.01
SIMATIC Device Drivers WoW	29.3	29.03.03.00_01.04.00.03
SIMATIC Event Database	5.6	05.06.02.03_02.01.00.01
SeCon	2.9	V02.09.00.00_01.03.00.01
SIMATIC Station Observer	K7.3.1.0	V07.03.01.00_01.01.00.14
SIMATIC SCS	V8.0.0.0	V08.00.00.00_01.09.00.10
SIMATIC WinCC Common Archiving	V8.0.0.0	V07.06.18.00_01.38.00.01
WinCC Runtime Advanced Simulator	17.0.0.0	V17.00.00.00_43.02.00.01

Products		
Name	**Version**	**Release**
TIA Administrator	V2.0	V02.00.00.00_01.04.00.03
TIA Portal Project Server	V1.1	V01.01.00.00_00.00.01.52
SIMATIC STEP 7 Prof - STEP 7 Safety - WinCC Prof	V18.0	V18.00.01.00_42.01.00.01
User Management Component	V2.10	V02.10.00.00_00.00.00.38
UMC Status Application	V2.10	V02.10.00.00_00.00.00.38
SIMATIC WinCC Runtime Advanced Simulation	V17.0 Upd5	V17.00.00.05_04.01.00.01
S7-PCT	V3.5 SP3	V03.05.03.00_01.12.00.13
SIMATIC WinCC Runtime Professional Simulation	V18.0	V18.00.01.00_42.01.00.01
Automation License Manager	V6.0 + SP9 + Upd3	06.00.09.03_01.01.00.11
S7-PLCSIM	V5.4 + SP8	V05.04.08.01_01.24.00.01
SIMATIC ProSave	V18.0	V18.00.01.00_42.01.00.01
S7-PCT	V3.5 SP3	K3.5.3.0_1.12.0.13

electro-chlorination station 3_V13_SP1_V18

PLC_1 [CPU 317-2 PN/DP]

PLC_1					
General					
Name	PLC_1	**Author**	malki.lamine	**Comment**	
Rack	0	**Slot**	2		
General\Catalog information					
Short designation	CPU 317-2 PN/DP	**Description**	Work memory 1024KB; 0.025ms/1000 instructions; PROFINET interface; S7 communication (loadable FBs/FCs); PROFINET IO controller; supports RT/IRT; 2 ports, MRP; PROFINET CBA; PROFINET CBA proxy; transport protocol TCP/IP; combined MPI/DP interface (MPI or DP master or DP slave); multi-tier configuration up to 32 modules; constant bus cycle time; routing; firmware V3.2	**Article number**	6ES7 317-2EK14-0AB0
Firmware version	V3.2		False		
General\Identification & Maintenance					
Plant designation		**Location identifier**			
MPI/DP interface [X1]\General					
Name	Interface MPI/DP_1	**Comment**			
MPI/DP interface [X1]\MPI address\Interface networked with					
Subnet:	Not networked				
MPI/DP interface [X1]\MPI address\Parameters					
Interface type:	Mpi	**Address:**	2	**Highest address:**	
Transmission speed:					
MPI/DP interface [X1]\Time-of-day synchronization\SIMATIC mode					
Type of synchronization	None	**Time interval**	None		
MPI/DP interface [X1]\Diagnostics addresses\Diagnostics addresses					
Start address	8191				
PROFINET interface [X2]\General					
Name	Interface PROFINET_1	**Comment**			
PROFINET interface [X2]\Ethernet addresses\Interface networked with					
Subnet:	PN/IE_1				
PROFINET interface [X2]\Ethernet addresses\Internet protocol version 4 (IPv4)					
IP configuration	Set IP address in the project	**IP address:**	192.168.0.1	**Subnet mask:**	255.255.255.0
Use router	False				
PROFINET interface [X2]\Ethernet addresses\PROFINET					
PROFINET device name is set directly at the device	False	**Generate PROFINET device name automatically**	True	**PROFINET device name:**	plc_1
Converted name:	plcxb1d0ed	**Device number:**	0		
PROFINET interface [X2]\Time-of-day synchronization\NTP mode					
Enable time synchronization via NTP server	False		IP addresses	**NTP Server 1**	0.0.0.0
NTP Server 2	0.0.0.0	**NTP Server 3**	0.0.0.0	**NTP Server 4**	0.0.0.0
Update interval	10s				
PROFINET interface [X2]\Operating mode					
IO controller	True	**IO system**		**Device number**	0
IO device	False				
PROFINET interface [X2]\Advanced options\Interface options					
Call the user program if communication errors occur	False	**Support device replacement without exchangeable medium**	True	**Use IEC V2.2 LLDP mode**	True
Keep-Alive connection monitoring:	30s				
PROFINET interface [X2]\Advanced options\Media redundancy					
MRP domain	mrpdomain-1	**Media redundancy role:**	Not device in the ring		
PROFINET interface [X2]\Advanced options\Real time settings\IO communication					
Send clock:	1.000ms				
PROFINET interface [X2]\Advanced options\Real time settings\Synchronization					
Sync domain:	Sync-Domain_1	**Synchronization role:**	Unsynchronized	**RT class:**	RT,IRT
PROFINET interface [X2]\Advanced options\Real time settings\Real time options					
Calculated bandwidth for cyclic IO data:	0.000ms	**Calculated bandwidth for cyclic IO data:**	0.000%		
PROFINET interface [X2]\Advanced options\Port [X2 P1]\General					
Name	Port_1	**Comment**			
PROFINET interface [X2]\Advanced options\Port [X2 P1]\Port interconnection\Local port:					
Local port:	PLC_1\Interface PROFINET_1 [X2]\Port_1 [X2 P1 R]	**Medium:**	Copper	**Cable name:**	---
					

PROFINET interface [X2]\Advanced options\Port [X2 P1]\Port interconnection\Partner port:					
	Monitoring of partner port is not possible	**Partner port:**	Any partner		
PROFINET interface [X2]\Advanced options\Port [X2 P1]\Port options\Activate					
Activate this port for use	True				
PROFINET interface [X2]\Advanced options\Port [X2 P1]\Port options\Connection					
Transmission rate / duplex:	Automatic	**Monitor**	False	**Enable autonegotiation**	True
PROFINET interface [X2]\Advanced options\Port [X2 P1]\Port options\Boundaries					
End of detection of accessible devices	False	**End of topology discovery**	False	**End of the sync domain**	False
PROFINET interface [X2]\Advanced options\Port [X2 P1]\Diagnostics addresses\Diagnostics addresses					
Start address	8189				
PROFINET interface [X2]\Advanced options\Port [X2 P2]\General					
Name	Port_2	**Comment**			
PROFINET interface [X2]\Advanced options\Port [X2 P2]\Port interconnection\Local port:					
Local port:	PLC_1\Interface PROFINET_1 [X2]\Port_2 [X2 P2 R]	**Medium:**	Copper	**Cable name:**	---
					
PROFINET interface [X2]\Advanced options\Port [X2 P2]\Port interconnection\Partner port:					
	Monitoring of partner port is not possible	**Partner port:**	Any partner		
PROFINET interface [X2]\Advanced options\Port [X2 P2]\Port options\Activate					
Activate this port for use	True				
PROFINET interface [X2]\Advanced options\Port [X2 P2]\Port options\Connection					
Transmission rate / duplex:	Automatic	**Monitor**	False	**Enable autonegotiation**	True
PROFINET interface [X2]\Advanced options\Port [X2 P2]\Port options\Boundaries					
End of detection of accessible devices	False	**End of topology discovery**	False	**End of the sync domain**	False
PROFINET interface [X2]\Advanced options\Port [X2 P2]\Diagnostics addresses\Diagnostics addresses					
Start address	8188				
PROFINET interface [X2]\Diagnostics addresses\Diagnostics addresses					
Start address	8190				
Startup					
Startup if preset configuration does not match actual configuration	True	**Startup after POWER ON**	Warm restart		
Startup\Monitoring time for					
Finished messages of the modules [100ms]	650x 100 ms	**Transfer of parameters to modules [100ms]**	100x 100 ms		
Cycle					
Cycle monitoring time [ms]	150ms	**Cycle load due to communication [%]**	20%	**Size of the process image input:**	256
Size of the process image output:	256	**OB85 call if I/O access error occurs**	No OB85 call		
Cycle\Prioritized OCM communication					
Prioritized OCM communication	False				
Clock memory					
Clock memory	False	**Memory byte**	0		

Interrupts\Time-of-day interrupts

OB number	Priority	Active	Execution	Start time
OB 10	2	False	None	1994-01-01 00:00:00.000

Interrupts\Time-delay interrupts

OB number	Priority	Process image partition(s)
OB 20	3	None
OB 21	4	None

Interrupts\Cyclic interrupts

OB number	Priority	Execution	Phase offset	Unit
OB 32	9	1000	0	ms
OB 33	10	500	0	ms
OB 34	11	200	0	ms
OB 35	12	100	0	ms

Interrupts\Hardware interrupts

OB number	Priority	Process image partition(s)
OB 40	16	None

Interrupts\Interrupts for DPV1

OB number	Priority
OB 55	2
OB 56	2
OB 57	2

Interrupts\Isochronous mode interrupts

OB number	Priority	Distributed I/O	Process image partition(s)	Delay time (ms)	Automatic setting
OB 61	25	0		0.000	True

Interrupts\Isochronous mode interrupts\OB 61					
Application cycle:	0ms	**Delay time:**	0.000ms	**Automatic setting**	True
Distributed I/O:	0				

Interrupts\Isochronous mode interrupts\OB 61\Process image partition	
PIP:	

Interrupts\Asynchronous error interrupts\	
OB number	**Priority**
OB 82	26
OB 83	26
OB 85	26
OB 86	26
OB 87	26

Diagnostics system					
Report cause of STOP	True	**Number of alarms in the diagnostics buffer**	10		

System diagnostics\General	
Activate system diagnostics for this device	False

Time of day	
Correction factor	0ms

Time of day\Synchronization on PLC					
Type of synchronization	None	**Time interval**	None		

Time of day\Synchronization on MPI					
Type of synchronization	None	**Time interval**	None		

Web server\General					
Activate web server on this module	False	**Permit access only with HTTPS**	False		

Web server\Automatic update					
Enable automatic update	False	**Update interval**	0s		

Web server\Languages		
Active	**Web server language**	**Assign project language**
False	German	None
False	English	None
False	French	None
False	Spanish	None
False	Italian	None
False	Japanese	None
False	Chinese (simplified)	None

Web server\User management	
User name	**User rights**
Everybody	

Web server\User-defined web pages					
Application name	**HTML source path**	**Default HTML page**	**Files with dynamic content**	**Web DB number**	**Fragment DB number**
		index.htm	.htm;.html	333	334

Web server\Display class of alarm	
Display class	**Active**
0	True
1	True
2	True
3	True
4	True
5	True
6	True
7	True
8	True
9	True
10	True
11	True
12	True
13	True
14	True
15	True
16	True

Retentive memory					
Number of memory bytes starting at MB 0	16	**Number of S7 timers starting at T 0**	0	**Number of S7 counters starting at C 0**	8

Protection			
Password		**Confirm password**	

Protection\	
Level of protection	No protection

Protection\ \Can be canceled with password	
Can be canceled with password	False

Connection resources					
PG communication:	1	**OP communication:**	1	**S7 basic communication:**	0
S7 communication:	0	**Maximum number of S7 connection resources:**	32		

Overview of addresses\Overview of addresses\Overview of addresses					
Inputs	True	**Outputs**	True	**Address gaps**	False
Slot	True				

Type	Addr. from	Addr. to	Module	PIP	Device name	Device number	Size	Master / IO system	Rack	Slot
I*	8191	8191	Interface MPI/DP_1	---	PLC_1 [CPU 317-2 PN/DP]	-	0 Bits	-	0	2 X1
I*	8189	8189	Port_1	---	PLC_1 [CPU 317-2 PN/DP]	-	0 Bits	-	0	2 X2 P1 R
I*	8188	8188	Port_2	---	PLC_1 [CPU 317-2 PN/DP]	-	0 Bits	-	0	2 X2 P2 R
I*	8190	8190	Interface PROFINET_1	---	PLC_1 [CPU 317-2 PN/DP]	-	0 Bits	-	0	2 X2
I	0	1	DI 16x24VDC_1	OB1-PI	PLC_1 [CPU 317-2 PN/DP]	-	2 Bytes	-	0	4
O	2	3	DO 16x24VDC/0.5A_2	OB1-PI	PLC_1 [CPU 317-2 PN/DP]	-	2 Bytes	-	0	8
O	0	1	DO 16x24VDC/0.5A_1	OB1-PI	PLC_1 [CPU 317-2 PN/DP]	-	2 Bytes	-	0	5
O	288	295	AO 4x16BIT_1	OB1-PI	PLC_1 [CPU 317-2 PN/DP]	-	8 Bytes	-	0	7
I	288	303	AI 8x16BIT_1	OB1-PI	PLC_1 [CPU 317-2 PN/DP]	-	16 Bytes	-	0	6
I	304	319	AI 8x16BIT_2	OB1-PI	PLC_1 [CPU 317-2 PN/DP]	-	16 Bytes	-	0	9
I	352	367	AI 8x16BIT_3	OB1-PI	PLC_1 [CPU 317-2 PN/DP]	-	16 Bytes	-	0	10
O	28	29	DO 16x24/48VUC_1	OB1-PI	PLC_1 [CPU 317-2 PN/DP]	-	2 Bytes	-	0	11

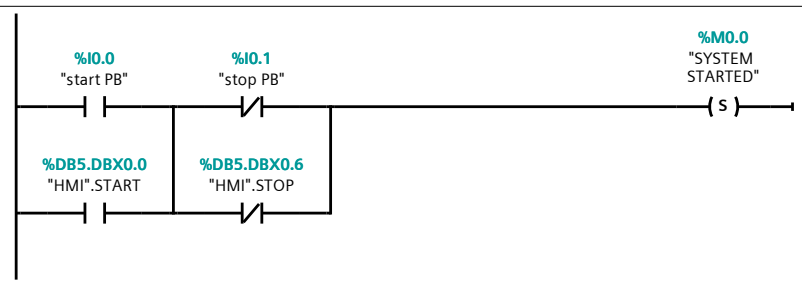
electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / Program blocks

Main [OB1]

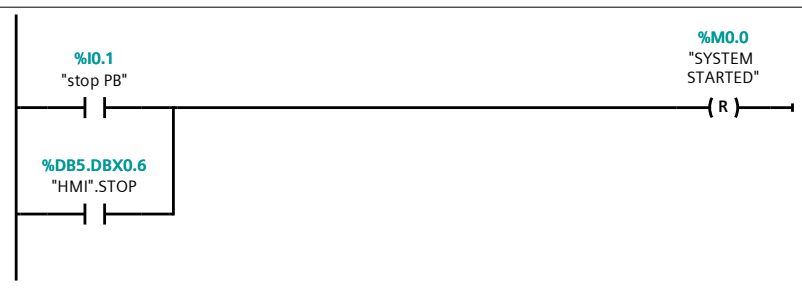
Main Properties							
General							
Name	Main	**Number**	1	**Type**	OB	**Language**	LAD
Numbering	Automatic						
Information							
Title	"Main Program Sweep (Cycle)"	**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Offset	Default value	Comment
Temp				
OB1_EV_CLASS	Byte	0.0		Bits 0-3 = 1 (Coming event), Bits 4-7 = 1 (Event class 1)
OB1_SCAN_1	Byte	1.0		1 (Cold restart scan 1 of OB 1), 3 (Scan 2-n of OB 1)
OB1_PRIORITY	Byte	2.0		Priority of OB Execution
OB1_OB_NUMBR	Byte	3.0		1 (Organization block 1, OB1)
OB1_RESERVED_1	Byte	4.0		Reserved for system
OB1_RESERVED_2	Byte	5.0		Reserved for system
OB1_PREV_CYCLE	Int	6.0		Cycle time of previous OB1 scan (milliseconds)
OB1_MIN_CYCLE	Int	8.0		Minimum cycle time of OB1 (milliseconds)
OB1_MAX_CYCLE	Int	10.0		Maximum cycle time of OB1 (milliseconds)
OB1_DATE_TIME	Date_And_Time	12.0		Date and time OB1 started
Constant				

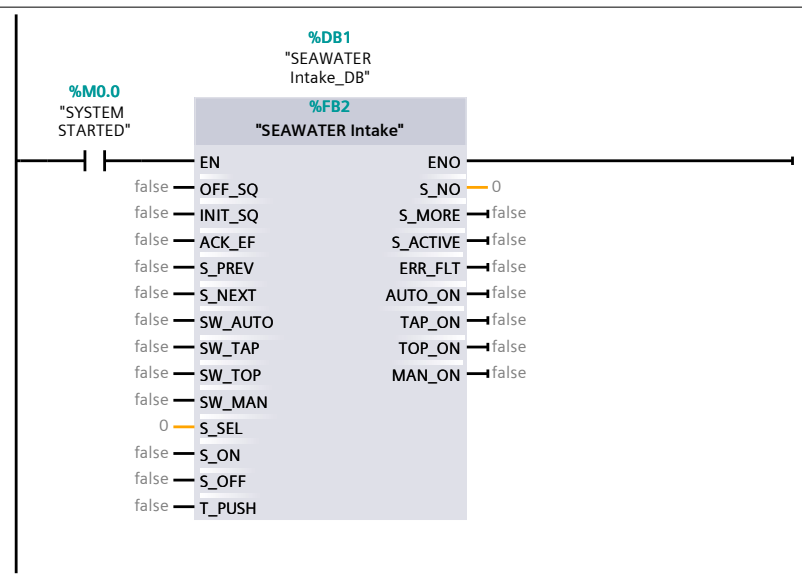
Network 1:



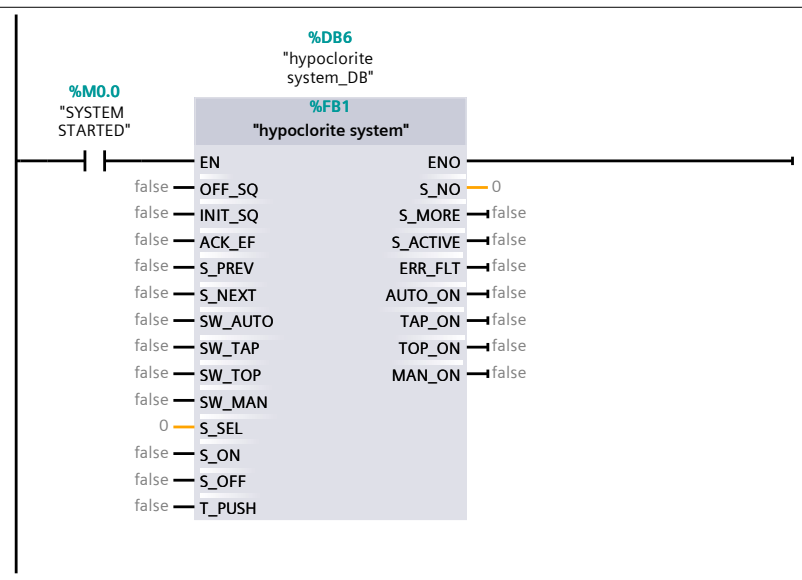
Network 2:



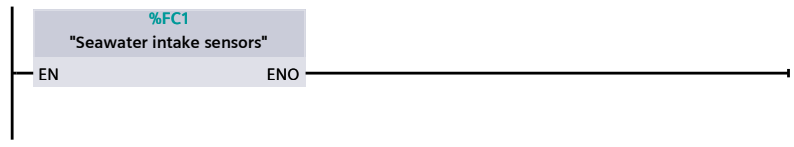
Network 3:



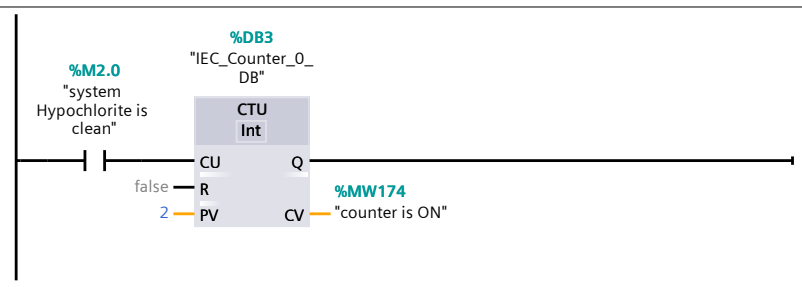
Network 4:



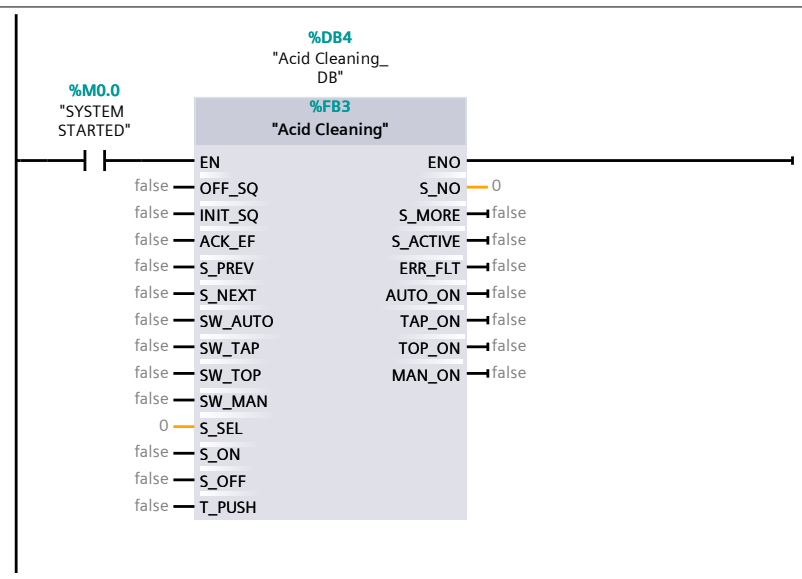
Network 5:



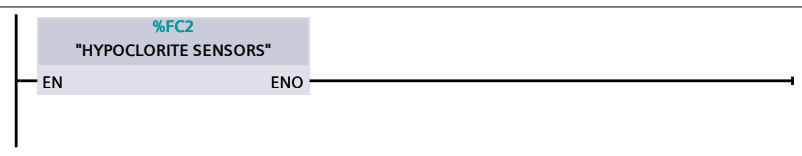
Network 6:



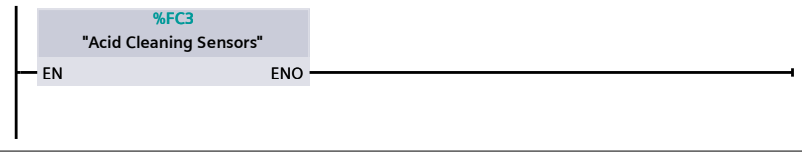
Network 7:



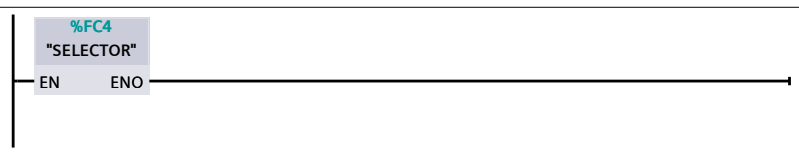
Network 8:



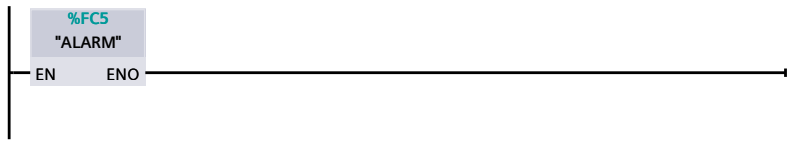
Network 9:



Network 10:



Network 11:



electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / Program blocks

hypoclorite system [FB1]

hypoclorite system Properties							
General							
Name	hypoclorite system	**Number**	1	**Type**	FB	**Language**	GRAPH
Numbering	Automatic	**Network language**	LAD				
Information							
Title		**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision				Comment
Input												
OFF_SQ	Bool	0.0	false	True	True	False	False					Désactiver le graphe
INIT_SQ	Bool	0.1	false	True	True	False	False					Mettre le graphe à l'état initial
ACK_EF	Bool	0.2	false	True	True	False	False					Acquitter toutes les erreurs et perturbations
S_PREV	Bool	0.3	false	True	True	False	False					Afficher l'étape précédente dans le paramètre S_NO
S_NEXT	Bool	0.4	false	True	True	False	False					Afficher l'étape suivante dans le paramètre S_NO
SW_AUTO	Bool	0.5	false	True	True	False	False					Mode automatique
SW_TAP	Bool	0.6	false	True	True	False	False					Mode semi-automatique/Activation de l'étape suivante avec transition
SW_TOP	Bool	0.7	false	True	True	False	False					Mode semi-automatique/Ignorer la transition
SW_MAN	Bool	1.0	false	True	True	False	False					Mode manuel
S_SEL	Int	2.0	0	True	True	False	False					Sélectionner l'étape à afficher dans S_NO
S_ON	Bool	4.0	false	True	True	False	False					Activer l'étape figurant dans le paramètre S_NO
S_OFF	Bool	4.1	false	True	True	False	False					Désactiver l'étape figurant dans le paramètre S_NO
T_PUSH	Bool	4.2	false	True	True	False	False					Validation de commutation pour transition en mode semi-automatique
Output												
S_NO	Int	6.0	0	True	True	False	False					Numéro d'étape
S_MORE	Bool	8.0	false	True	True	False	False					Il existe d'autres étapes qui peuvent être affichées dans S_NO.
S_ACTIVE	Bool	8.1	false	True	True	False	False					L'étape affichée dans le paramètre S_NO est activée
ERR_FLT	Bool	8.2	false	True	True	False	False					Erreur groupée Interlock or Supervision
AUTO_ON	Bool	8.3	false	True	True	False	False					Mode automatique activé
TAP_ON	Bool	8.4	false	True	True	False	False					Mode semi-automatique/Etape avec transition activé
TOP_ON	Bool	8.5	false	True	True	False	False					Mode semi-automatique/Ignorer la transition activé
MAN_ON	Bool	8.6	false	True	True	False	False					Mode manuel activé
InOut												
Static												
Trans1	GraphTransition	234.0		True	True	False	True					
TV	Bool	234.0	false	True	True	False	False					Transition is valid
TT	Bool	234.1	false	True	True	False	False					Transition is satisfied
TS	Bool	234.2	false	True	True	False	False					Transition switches
CF_IV	Bool	234.3	true	True	True	False	False					The CRIT_FLT entry is invalid
TNO	Int	236.0	1	True	True	False	False					Indicates the user-defined transition number
CRIT	DWord	238.0	16#0	True	True	False	False					Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	242.0	16#0	True	True	False	False					Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	246.0	16#0	True	True	False	False					Copy of CRIT if an error occurs
Trans2	GraphTransition	250.0		True	True	False	True					
TV	Bool	250.0	false	True	True	False	False					Transition is valid
TT	Bool	250.1	false	True	True	False	False					Transition is satisfied
TS	Bool	250.2	false	True	True	False	False					Transition switches
CF_IV	Bool	250.3	true	True	True	False	False					The CRIT_FLT entry is invalid

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TNO	Int	252.0	2	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	254.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	258.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	262.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans3	GraphTransition	266.0		True	True	False	True		
TV	Bool	266.0	false	True	True	False	False		Transition is valid
TT	Bool	266.1	false	True	True	False	False		Transition is satisfied
TS	Bool	266.2	false	True	True	False	False		Transition switches
CF_IV	Bool	266.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	268.0	3	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	270.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	274.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	278.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans4	GraphTransition	282.0		True	True	False	True		
TV	Bool	282.0	false	True	True	False	False		Transition is valid
TT	Bool	282.1	false	True	True	False	False		Transition is satisfied
TS	Bool	282.2	false	True	True	False	False		Transition switches
CF_IV	Bool	282.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	284.0	4	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	286.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	290.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	294.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans5	GraphTransition	298.0		True	True	False	True		
TV	Bool	298.0	false	True	True	False	False		Transition is valid
TT	Bool	298.1	false	True	True	False	False		Transition is satisfied
TS	Bool	298.2	false	True	True	False	False		Transition switches
CF_IV	Bool	298.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	300.0	5	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	302.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	306.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	310.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans6	GraphTransition	314.0		True	True	False	True		
TV	Bool	314.0	false	True	True	False	False		Transition is valid
TT	Bool	314.1	false	True	True	False	False		Transition is satisfied
TS	Bool	314.2	false	True	True	False	False		Transition switches
CF_IV	Bool	314.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	316.0	6	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	318.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	322.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	326.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans7	GraphTransition	330.0		True	True	False	True		
TV	Bool	330.0	false	True	True	False	False		Transition is valid
TT	Bool	330.1	false	True	True	False	False		Transition is satisfied
TS	Bool	330.2	false	True	True	False	False		Transition switches

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CF_IV	Bool	330.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	332.0	7	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	334.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	338.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	342.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans8	GraphTransition	346.0		True	True	False	True		
TV	Bool	346.0	false	True	True	False	False		Transition is valid
TT	Bool	346.1	false	True	True	False	False		Transition is satisfied
TS	Bool	346.2	false	True	True	False	False		Transition switches
CF_IV	Bool	346.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	348.0	8	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	350.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	354.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	358.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans9	GraphTransition	362.0		True	True	False	True		
TV	Bool	362.0	false	True	True	False	False		Transition is valid
TT	Bool	362.1	false	True	True	False	False		Transition is satisfied
TS	Bool	362.2	false	True	True	False	False		Transition switches
CF_IV	Bool	362.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	364.0	9	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	366.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	370.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	374.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans10	GraphTransition	378.0		True	True	False	True		
TV	Bool	378.0	false	True	True	False	False		Transition is valid
TT	Bool	378.1	false	True	True	False	False		Transition is satisfied
TS	Bool	378.2	false	True	True	False	False		Transition switches
CF_IV	Bool	378.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	380.0	10	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	382.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	386.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	390.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans13	GraphTransition	394.0		True	True	False	True		
TV	Bool	394.0	false	True	True	False	False		Transition is valid
TT	Bool	394.1	false	True	True	False	False		Transition is satisfied
TS	Bool	394.2	false	True	True	False	False		Transition switches
CF_IV	Bool	394.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	396.0	13	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	398.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	402.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	406.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans14	GraphTransition	410.0		True	True	False	True		
TV	Bool	410.0	false	True	True	False	False		Transition is valid
TT	Bool	410.1	false	True	True	False	False		Transition is satisfied

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TS	Bool	410.2	false	True	True	False	False		Transition switches
CF_IV	Bool	410.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	412.0	14	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	414.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	418.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	422.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans15	GraphTransition	426.0		True	True	False	True		
TV	Bool	426.0	false	True	True	False	False		Transition is valid
TT	Bool	426.1	false	True	True	False	False		Transition is satisfied
TS	Bool	426.2	false	True	True	False	False		Transition switches
CF_IV	Bool	426.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	428.0	15	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	430.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	434.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	438.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans16	GraphTransition	442.0		True	True	False	True		
TV	Bool	442.0	false	True	True	False	False		Transition is valid
TT	Bool	442.1	false	True	True	False	False		Transition is satisfied
TS	Bool	442.2	false	True	True	False	False		Transition switches
CF_IV	Bool	442.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	444.0	16	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	446.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	450.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	454.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans21	GraphTransition	458.0		True	True	False	True		
TV	Bool	458.0	false	True	True	False	False		Transition is valid
TT	Bool	458.1	false	True	True	False	False		Transition is satisfied
TS	Bool	458.2	false	True	True	False	False		Transition switches
CF_IV	Bool	458.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	460.0	21	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	462.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	466.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	470.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans22	GraphTransition	474.0		True	True	False	True		
TV	Bool	474.0	false	True	True	False	False		Transition is valid
TT	Bool	474.1	false	True	True	False	False		Transition is satisfied
TS	Bool	474.2	false	True	True	False	False		Transition switches
CF_IV	Bool	474.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	476.0	22	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	478.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	482.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	486.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans23	GraphTransition	490.0		True	True	False	True		
TV	Bool	490.0	false	True	True	False	False		Transition is valid

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TT	Bool	490.1	false	True	True	False	False		Transition is satisfied
TS	Bool	490.2	false	True	True	False	False		Transition switches
CF_IV	Bool	490.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	492.0	23	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	494.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	498.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	502.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans24	GraphTransition	506.0		True	True	False	True		
TV	Bool	506.0	false	True	True	False	False		Transition is valid
TT	Bool	506.1	false	True	True	False	False		Transition is satisfied
TS	Bool	506.2	false	True	True	False	False		Transition switches
CF_IV	Bool	506.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	508.0	24	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	510.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	514.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	518.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans25	GraphTransition	522.0		True	True	False	True		
TV	Bool	522.0	false	True	True	False	False		Transition is valid
TT	Bool	522.1	false	True	True	False	False		Transition is satisfied
TS	Bool	522.2	false	True	True	False	False		Transition switches
CF_IV	Bool	522.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	524.0	25	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	526.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	530.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	534.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans28	GraphTransition	538.0		True	True	False	True		
TV	Bool	538.0	false	True	True	False	False		Transition is valid
TT	Bool	538.1	false	True	True	False	False		Transition is satisfied
TS	Bool	538.2	false	True	True	False	False		Transition switches
CF_IV	Bool	538.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	540.0	28	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	542.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	546.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	550.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans29	GraphTransition	554.0		True	True	False	True		
TV	Bool	554.0	false	True	True	False	False		Transition is valid
TT	Bool	554.1	false	True	True	False	False		Transition is satisfied
TS	Bool	554.2	false	True	True	False	False		Transition switches
CF_IV	Bool	554.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	556.0	29	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	558.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	562.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	566.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans30	GraphTransition	570.0		True	True	False	True		

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TV	Bool	570.0	false	True	True	False	False		Transition is valid
TT	Bool	570.1	false	True	True	False	False		Transition is satisfied
TS	Bool	570.2	false	True	True	False	False		Transition switches
CF_IV	Bool	570.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	572.0	30	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	574.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	578.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	582.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans31	GraphTransition	586.0		True	True	False	True		
TV	Bool	586.0	false	True	True	False	False		Transition is valid
TT	Bool	586.1	false	True	True	False	False		Transition is satisfied
TS	Bool	586.2	false	True	True	False	False		Transition switches
CF_IV	Bool	586.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	588.0	31	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	590.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	594.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	598.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans32	GraphTransition	602.0		True	True	False	True		
TV	Bool	602.0	false	True	True	False	False		Transition is valid
TT	Bool	602.1	false	True	True	False	False		Transition is satisfied
TS	Bool	602.2	false	True	True	False	False		Transition switches
CF_IV	Bool	602.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	604.0	32	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	606.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	610.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	614.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans33	GraphTransition	618.0		True	True	False	True		
TV	Bool	618.0	false	True	True	False	False		Transition is valid
TT	Bool	618.1	false	True	True	False	False		Transition is satisfied
TS	Bool	618.2	false	True	True	False	False		Transition switches
CF_IV	Bool	618.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	620.0	33	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	622.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	626.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	630.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans34	GraphTransition	634.0		True	True	False	True		
TV	Bool	634.0	false	True	True	False	False		Transition is valid
TT	Bool	634.1	false	True	True	False	False		Transition is satisfied
TS	Bool	634.2	false	True	True	False	False		Transition switches
CF_IV	Bool	634.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	636.0	34	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	638.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	642.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	646.0	16#0	True	True	False	False		Copy of CRIT if an error occurs

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
Trans35	GraphTransition	650.0		True	True	False	True		
TV	Bool	650.0	false	True	True	False	False		Transition is valid
TT	Bool	650.1	false	True	True	False	False		Transition is satisfied
TS	Bool	650.2	false	True	True	False	False		Transition switches
CF_IV	Bool	650.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	652.0	35	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	654.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	658.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	662.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans37	GraphTransition	666.0		True	True	False	True		
TV	Bool	666.0	false	True	True	False	False		Transition is valid
TT	Bool	666.1	false	True	True	False	False		Transition is satisfied
TS	Bool	666.2	false	True	True	False	False		Transition switches
CF_IV	Bool	666.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	668.0	37	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	670.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	674.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	678.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans38	GraphTransition	682.0		True	True	False	True		
TV	Bool	682.0	false	True	True	False	False		Transition is valid
TT	Bool	682.1	false	True	True	False	False		Transition is satisfied
TS	Bool	682.2	false	True	True	False	False		Transition switches
CF_IV	Bool	682.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	684.0	38	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	686.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	690.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	694.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans40	GraphTransition	698.0		True	True	False	True		
TV	Bool	698.0	false	True	True	False	False		Transition is valid
TT	Bool	698.1	false	True	True	False	False		Transition is satisfied
TS	Bool	698.2	false	True	True	False	False		Transition switches
CF_IV	Bool	698.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	700.0	40	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	702.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	706.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	710.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans41	GraphTransition	714.0		True	True	False	True		
TV	Bool	714.0	false	True	True	False	False		Transition is valid
TT	Bool	714.1	false	True	True	False	False		Transition is satisfied
TS	Bool	714.2	false	True	True	False	False		Transition switches
CF_IV	Bool	714.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	716.0	41	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	718.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	722.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_FLT	DWord	726.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans42	GraphTransition	730.0		True	True	False	True		
TV	Bool	730.0	false	True	True	False	False		Transition is valid
TT	Bool	730.1	false	True	True	False	False		Transition is satisfied
TS	Bool	730.2	false	True	True	False	False		Transition switches
CF_IV	Bool	730.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	732.0	42	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	734.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	738.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	742.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans43	GraphTransition	746.0		True	True	False	True		
TV	Bool	746.0	false	True	True	False	False		Transition is valid
TT	Bool	746.1	false	True	True	False	False		Transition is satisfied
TS	Bool	746.2	false	True	True	False	False		Transition switches
CF_IV	Bool	746.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	748.0	43	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	750.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	754.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	758.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans44	GraphTransition	762.0		True	True	False	True		
TV	Bool	762.0	false	True	True	False	False		Transition is valid
TT	Bool	762.1	false	True	True	False	False		Transition is satisfied
TS	Bool	762.2	false	True	True	False	False		Transition switches
CF_IV	Bool	762.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	764.0	44	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	766.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	770.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	774.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans45	GraphTransition	778.0		True	True	False	True		
TV	Bool	778.0	false	True	True	False	False		Transition is valid
TT	Bool	778.1	false	True	True	False	False		Transition is satisfied
TS	Bool	778.2	false	True	True	False	False		Transition switches
CF_IV	Bool	778.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	780.0	45	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	782.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	786.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	790.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans46	GraphTransition	794.0		True	True	False	True		
TV	Bool	794.0	false	True	True	False	False		Transition is valid
TT	Bool	794.1	false	True	True	False	False		Transition is satisfied
TS	Bool	794.2	false	True	True	False	False		Transition switches
CF_IV	Bool	794.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	796.0	46	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	798.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_OLD	DWord	802.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	806.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans47	GraphTransition	810.0		True	True	False	True		
TV	Bool	810.0	false	True	True	False	False		Transition is valid
TT	Bool	810.1	false	True	True	False	False		Transition is satisfied
TS	Bool	810.2	false	True	True	False	False		Transition switches
CF_IV	Bool	810.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	812.0	47	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	814.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	818.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	822.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans48	GraphTransition	826.0		True	True	False	True		
TV	Bool	826.0	false	True	True	False	False		Transition is valid
TT	Bool	826.1	false	True	True	False	False		Transition is satisfied
TS	Bool	826.2	false	True	True	False	False		Transition switches
CF_IV	Bool	826.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	828.0	48	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	830.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	834.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	838.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans49	GraphTransition	842.0		True	True	False	True		
TV	Bool	842.0	false	True	True	False	False		Transition is valid
TT	Bool	842.1	false	True	True	False	False		Transition is satisfied
TS	Bool	842.2	false	True	True	False	False		Transition switches
CF_IV	Bool	842.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	844.0	49	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	846.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	850.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	854.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans50	GraphTransition	858.0		True	True	False	True		
TV	Bool	858.0	false	True	True	False	False		Transition is valid
TT	Bool	858.1	false	True	True	False	False		Transition is satisfied
TS	Bool	858.2	false	True	True	False	False		Transition switches
CF_IV	Bool	858.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	860.0	50	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	862.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	866.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	870.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans51	GraphTransition	874.0		True	True	False	True		
TV	Bool	874.0	false	True	True	False	False		Transition is valid
TT	Bool	874.1	false	True	True	False	False		Transition is satisfied
TS	Bool	874.2	false	True	True	False	False		Transition switches
CF_IV	Bool	874.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	876.0	51	True	True	False	False		Indicates the user-defined transition number

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT	DWord	878.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	882.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	886.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans52	GraphTransition	890.0		True	True	False	True		
TV	Bool	890.0	false	True	True	False	False		Transition is valid
TT	Bool	890.1	false	True	True	False	False		Transition is satisfied
TS	Bool	890.2	false	True	True	False	False		Transition switches
CF_IV	Bool	890.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	892.0	52	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	894.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	898.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	902.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans53	GraphTransition	906.0		True	True	False	True		
TV	Bool	906.0	false	True	True	False	False		Transition is valid
TT	Bool	906.1	false	True	True	False	False		Transition is satisfied
TS	Bool	906.2	false	True	True	False	False		Transition switches
CF_IV	Bool	906.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	908.0	53	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	910.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	914.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	918.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans54	GraphTransition	922.0		True	True	False	True		
TV	Bool	922.0	false	True	True	False	False		Transition is valid
TT	Bool	922.1	false	True	True	False	False		Transition is satisfied
TS	Bool	922.2	false	True	True	False	False		Transition switches
CF_IV	Bool	922.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	924.0	54	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	926.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	930.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	934.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans55	GraphTransition	938.0		True	True	False	True		
TV	Bool	938.0	false	True	True	False	False		Transition is valid
TT	Bool	938.1	false	True	True	False	False		Transition is satisfied
TS	Bool	938.2	false	True	True	False	False		Transition switches
CF_IV	Bool	938.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	940.0	55	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	942.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	946.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	950.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans56	GraphTransition	954.0		True	True	False	True		
TV	Bool	954.0	false	True	True	False	False		Transition is valid
TT	Bool	954.1	false	True	True	False	False		Transition is satisfied
TS	Bool	954.2	false	True	True	False	False		Transition switches
CF_IV	Bool	954.3	true	True	True	False	False		The CRIT_FLT entry is invalid

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TNO	Int	956.0	56	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	958.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	962.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	966.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans57	GraphTransition	970.0		True	True	False	True		
TV	Bool	970.0	false	True	True	False	False		Transition is valid
TT	Bool	970.1	false	True	True	False	False		Transition is satisfied
TS	Bool	970.2	false	True	True	False	False		Transition switches
CF_IV	Bool	970.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	972.0	57	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	974.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	978.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	982.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans58	GraphTransition	986.0		True	True	False	True		
TV	Bool	986.0	false	True	True	False	False		Transition is valid
TT	Bool	986.1	false	True	True	False	False		Transition is satisfied
TS	Bool	986.2	false	True	True	False	False		Transition switches
CF_IV	Bool	986.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	988.0	58	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	990.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	994.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	998.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans59	GraphTransition	1002.0		True	True	False	True		
TV	Bool	1002.0	false	True	True	False	False		Transition is valid
TT	Bool	1002.1	false	True	True	False	False		Transition is satisfied
TS	Bool	1002.2	false	True	True	False	False		Transition switches
CF_IV	Bool	1002.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1004.0	59	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1006.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1010.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1014.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans60	GraphTransition	1018.0		True	True	False	True		
TV	Bool	1018.0	false	True	True	False	False		Transition is valid
TT	Bool	1018.1	false	True	True	False	False		Transition is satisfied
TS	Bool	1018.2	false	True	True	False	False		Transition switches
CF_IV	Bool	1018.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1020.0	60	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1022.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1026.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1030.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans61	GraphTransition	1034.0		True	True	False	True		
TV	Bool	1034.0	false	True	True	False	False		Transition is valid
TT	Bool	1034.1	false	True	True	False	False		Transition is satisfied
TS	Bool	1034.2	false	True	True	False	False		Transition switches

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CF_IV	Bool	1034.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1036.0	61	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1038.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1042.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1046.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans62	GraphTransition	1050.0		True	True	False	True		
TV	Bool	1050.0	false	True	True	False	False		Transition is valid
TT	Bool	1050.1	false	True	True	False	False		Transition is satisfied
TS	Bool	1050.2	false	True	True	False	False		Transition switches
CF_IV	Bool	1050.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1052.0	62	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1054.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1058.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1062.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans63	GraphTransition	1066.0		True	True	False	True		
TV	Bool	1066.0	false	True	True	False	False		Transition is valid
TT	Bool	1066.1	false	True	True	False	False		Transition is satisfied
TS	Bool	1066.2	false	True	True	False	False		Transition switches
CF_IV	Bool	1066.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1068.0	63	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1070.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1074.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1078.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans64	GraphTransition	1082.0		True	True	False	True		
TV	Bool	1082.0	false	True	True	False	False		Transition is valid
TT	Bool	1082.1	false	True	True	False	False		Transition is satisfied
TS	Bool	1082.2	false	True	True	False	False		Transition switches
CF_IV	Bool	1082.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1084.0	64	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1086.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1090.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1094.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans65	GraphTransition	1098.0		True	True	False	True		
TV	Bool	1098.0	false	True	True	False	False		Transition is valid
TT	Bool	1098.1	false	True	True	False	False		Transition is satisfied
TS	Bool	1098.2	false	True	True	False	False		Transition switches
CF_IV	Bool	1098.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1100.0	65	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1102.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1106.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1110.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans66	GraphTransition	1114.0		True	True	False	True		
TV	Bool	1114.0	false	True	True	False	False		Transition is valid
TT	Bool	1114.1	false	True	True	False	False		Transition is satisfied

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TS	Bool	1114.2	false	True	True	False	False		Transition switches
CF_IV	Bool	1114.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1116.0	66	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1118.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1122.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1126.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans67	GraphTransition	1130.0		True	True	False	True		
TV	Bool	1130.0	false	True	True	False	False		Transition is valid
TT	Bool	1130.1	false	True	True	False	False		Transition is satisfied
TS	Bool	1130.2	false	True	True	False	False		Transition switches
CF_IV	Bool	1130.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1132.0	67	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1134.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1138.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1142.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans68	GraphTransition	1146.0		False	False	False	True		Transition structure
TV	Bool	1146.0	false	False	False	False	False		Transition is valid
TT	Bool	1146.1	false	False	False	False	False		Transition is satisfied
TS	Bool	1146.2	false	False	False	False	False		Transition switches
CF_IV	Bool	1146.3	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	1148.0	68	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	1150.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1154.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1158.0	16#0	False	False	False	False		Copy of CRIT if an error occurs
Trans69	GraphTransition	1162.0		True	True	False	True		
TV	Bool	1162.0	false	True	True	False	False		Transition is valid
TT	Bool	1162.1	false	True	True	False	False		Transition is satisfied
TS	Bool	1162.2	false	True	True	False	False		Transition switches
CF_IV	Bool	1162.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1164.0	69	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1166.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1170.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1174.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans70	GraphTransition	1178.0		True	True	False	True		
TV	Bool	1178.0	false	True	True	False	False		Transition is valid
TT	Bool	1178.1	false	True	True	False	False		Transition is satisfied
TS	Bool	1178.2	false	True	True	False	False		Transition switches
CF_IV	Bool	1178.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1180.0	70	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1182.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1186.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1190.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans71	GraphTransition	1194.0		False	False	False	True		Transition structure
TV	Bool	1194.0	false	False	False	False	False		Transition is valid

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TT	Bool	1194.1	false	False	False	False	False		Transition is satisfied
TS	Bool	1194.2	false	False	False	False	False		Transition switches
CF_IV	Bool	1194.3	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	1196.0	71	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	1198.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1202.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1206.0	16#0	False	False	False	False		Copy of CRIT if an error occurs
Trans72	GraphTransition	1210.0		True	True	False	True		
TV	Bool	1210.0	false	True	True	False	False		Transition is valid
TT	Bool	1210.1	false	True	True	False	False		Transition is satisfied
TS	Bool	1210.2	false	True	True	False	False		Transition switches
CF_IV	Bool	1210.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1212.0	72	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1214.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1218.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1222.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans73	GraphTransition	1226.0		True	True	False	True		
TV	Bool	1226.0	false	True	True	False	False		Transition is valid
TT	Bool	1226.1	false	True	True	False	False		Transition is satisfied
TS	Bool	1226.2	false	True	True	False	False		Transition switches
CF_IV	Bool	1226.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1228.0	73	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1230.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1234.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1238.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans74	GraphTransition	1242.0		True	True	False	True		
TV	Bool	1242.0	false	True	True	False	False		Transition is valid
TT	Bool	1242.1	false	True	True	False	False		Transition is satisfied
TS	Bool	1242.2	false	True	True	False	False		Transition switches
CF_IV	Bool	1242.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1244.0	74	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1246.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1250.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1254.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans75	GraphTransition	1258.0		True	True	False	True		
TV	Bool	1258.0	false	True	True	False	False		Transition is valid
TT	Bool	1258.1	false	True	True	False	False		Transition is satisfied
TS	Bool	1258.2	false	True	True	False	False		Transition switches
CF_IV	Bool	1258.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1260.0	75	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1262.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1266.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1270.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans76	GraphTransition	1274.0		True	True	False	True		

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TV	Bool	1274.0	false	True	True	False	False		Transition is valid
TT	Bool	1274.1	false	True	True	False	False		Transition is satisfied
TS	Bool	1274.2	false	True	True	False	False		Transition switches
CF_IV	Bool	1274.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1276.0	76	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1278.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1282.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1286.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans77	GraphTransition	1290.0		False	False	False	True		Transition structure
TV	Bool	1290.0	false	False	False	False	False		Transition is valid
TT	Bool	1290.1	false	False	False	False	False		Transition is satisfied
TS	Bool	1290.2	false	False	False	False	False		Transition switches
CF_IV	Bool	1290.3	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	1292.0	77	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	1294.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1298.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1302.0	16#0	False	False	False	False		Copy of CRIT if an error occurs
▼ Trans78	GraphTransition	1306.0		False	False	False	True		Transition structure
TV	Bool	1306.0	false	False	False	False	False		Transition is valid
TT	Bool	1306.1	false	False	False	False	False		Transition is satisfied
TS	Bool	1306.2	false	False	False	False	False		Transition switches
CF_IV	Bool	1306.3	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	1308.0	78	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	1310.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1314.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1318.0	16#0	False	False	False	False		Copy of CRIT if an error occurs
▼ Trans79	GraphTransition	1322.0		False	False	False	True		Transition structure
TV	Bool	1322.0	false	False	False	False	False		Transition is valid
TT	Bool	1322.1	false	False	False	False	False		Transition is satisfied
TS	Bool	1322.2	false	False	False	False	False		Transition switches
CF_IV	Bool	1322.3	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	1324.0	79	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	1326.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1330.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1334.0	16#0	False	False	False	False		Copy of CRIT if an error occurs
▼ Trans80	GraphTransition	1338.0		True	True	False	True		
TV	Bool	1338.0	false	True	True	False	False		Transition is valid
TT	Bool	1338.1	false	True	True	False	False		Transition is satisfied
TS	Bool	1338.2	false	True	True	False	False		Transition switches
CF_IV	Bool	1338.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1340.0	80	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1342.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1346.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1350.0	16#0	True	True	False	False		Copy of CRIT if an error occurs

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
Trans81	GraphTransition	1354.0		False	False	False	True		Transition structure
TV	Bool	1354.0	false	False	False	False	False		Transition is valid
TT	Bool	1354.1	false	False	False	False	False		Transition is satisfied
TS	Bool	1354.2	false	False	False	False	False		Transition switches
CF_IV	Bool	1354.3	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	1356.0	81	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	1358.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1362.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1366.0	16#0	False	False	False	False		Copy of CRIT if an error occurs
Step1	GraphStep	1402.0		True	True	False	True		
S1	Bool	1402.0	false	True	True	False	False		Step is activated
L1	Bool	1402.1	false	True	True	False	False		interlock leaving state
V1	Bool	1402.2	false	True	True	False	False		Supervision entering state
R1	Bool	1402.3	false	True	True	False	False		Reserved
A1	Bool	1402.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1402.5	false	True	True	False	False		Step is deactivated
L0	Bool	1402.6	false	True	True	False	False		Interlock entering state
V0	Bool	1402.7	false	True	True	False	False		Supervision leaving state
X	Bool	1403.0	false	True	True	False	False		Step is active
LA	Bool	1403.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1403.2	false	True	True	False	False		Supervision active
RA	Bool	1403.3	false	True	True	False	False		Reserved
AA	Bool	1403.4	false	True	True	False	False		Reserved
SS	Bool	1403.5	false	True	True	False	False		System-internal
LS	Bool	1403.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1403.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1404.0	1	True	True	False	False		User step number
T	Time	1406.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1410.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1414.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1418.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1422.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1426.0	false	True	True	False	False		System-internal
LP	Bool	1426.1	false	True	True	False	False		System-internal
LN	Bool	1426.2	false	True	True	False	False		System-internal
VP	Bool	1426.3	false	True	True	False	False		System-internal
VN	Bool	1426.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1427.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1428.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1430.0	16#0	True	True	False	False		Reserve
Step2	GraphStep	1434.0		True	True	False	True		
S1	Bool	1434.0	false	True	True	False	False		Step is activated
L1	Bool	1434.1	false	True	True	False	False		interlock leaving state
V1	Bool	1434.2	false	True	True	False	False		Supervision entering state
R1	Bool	1434.3	false	True	True	False	False		Reserved
A1	Bool	1434.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1434.5	false	True	True	False	False		Step is deactivated
L0	Bool	1434.6	false	True	True	False	False		Interlock entering state
V0	Bool	1434.7	false	True	True	False	False		Supervision leaving state
X	Bool	1435.0	false	True	True	False	False		Step is active
LA	Bool	1435.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1435.2	false	True	True	False	False		Supervision active
RA	Bool	1435.3	false	True	True	False	False		Reserved
AA	Bool	1435.4	false	True	True	False	False		Reserved
SS	Bool	1435.5	false	True	True	False	False		System-internal
LS	Bool	1435.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1435.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1436.0	2	True	True	False	False		User step number

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
T	Time	1438.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1442.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1446.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1450.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1454.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1458.0	false	True	True	False	False		System-internal
LP	Bool	1458.1	false	True	True	False	False		System-internal
LN	Bool	1458.2	false	True	True	False	False		System-internal
VP	Bool	1458.3	false	True	True	False	False		System-internal
VN	Bool	1458.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1459.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1460.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1462.0	16#0	True	True	False	False		Reserve
Step3	GraphStep	1466.0		True	True	False	True		
S1	Bool	1466.0	false	True	True	False	False		Step is activated
L1	Bool	1466.1	false	True	True	False	False		interlock leaving state
V1	Bool	1466.2	false	True	True	False	False		Supervision entering state
R1	Bool	1466.3	false	True	True	False	False		Reserved
A1	Bool	1466.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1466.5	false	True	True	False	False		Step is deactivated
L0	Bool	1466.6	false	True	True	False	False		Interlock entering state
V0	Bool	1466.7	false	True	True	False	False		Supervision leaving state
X	Bool	1467.0	false	True	True	False	False		Step is active
LA	Bool	1467.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1467.2	false	True	True	False	False		Supervision active
RA	Bool	1467.3	false	True	True	False	False		Reserved
AA	Bool	1467.4	false	True	True	False	False		Reserved
SS	Bool	1467.5	false	True	True	False	False		System-internal
LS	Bool	1467.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1467.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1468.0	3	True	True	False	False		User step number
T	Time	1470.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1474.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1478.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1482.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1486.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1490.0	false	True	True	False	False		System-internal
LP	Bool	1490.1	false	True	True	False	False		System-internal
LN	Bool	1490.2	false	True	True	False	False		System-internal
VP	Bool	1490.3	false	True	True	False	False		System-internal
VN	Bool	1490.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1491.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1492.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1494.0	16#0	True	True	False	False		Reserve
Step4	GraphStep	1498.0		True	True	False	True		
S1	Bool	1498.0	false	True	True	False	False		Step is activated
L1	Bool	1498.1	false	True	True	False	False		interlock leaving state
V1	Bool	1498.2	false	True	True	False	False		Supervision entering state
R1	Bool	1498.3	false	True	True	False	False		Reserved
A1	Bool	1498.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1498.5	false	True	True	False	False		Step is deactivated
L0	Bool	1498.6	false	True	True	False	False		Interlock entering state
V0	Bool	1498.7	false	True	True	False	False		Supervision leaving state
X	Bool	1499.0	false	True	True	False	False		Step is active
LA	Bool	1499.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1499.2	false	True	True	False	False		Supervision active
RA	Bool	1499.3	false	True	True	False	False		Reserved
AA	Bool	1499.4	false	True	True	False	False		Reserved
SS	Bool	1499.5	false	True	True	False	False		System-internal

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
LS	Bool	1499.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1499.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1500.0	4	True	True	False	False		User step number
T	Time	1502.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1506.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1510.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1514.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1518.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1522.0	false	True	True	False	False		System-internal
LP	Bool	1522.1	false	True	True	False	False		System-internal
LN	Bool	1522.2	false	True	True	False	False		System-internal
VP	Bool	1522.3	false	True	True	False	False		System-internal
VN	Bool	1522.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1523.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1524.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1526.0	16#0	True	True	False	False		Reserve
▼ Step5	GraphStep	1530.0		True	True	False	True		
S1	Bool	1530.0	false	True	True	False	False		Step is activated
L1	Bool	1530.1	false	True	True	False	False		interlock leaving state
V1	Bool	1530.2	false	True	True	False	False		Supervision entering state
R1	Bool	1530.3	false	True	True	False	False		Reserved
A1	Bool	1530.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1530.5	false	True	True	False	False		Step is deactivated
L0	Bool	1530.6	false	True	True	False	False		Interlock entering state
V0	Bool	1530.7	false	True	True	False	False		Supervision leaving state
X	Bool	1531.0	false	True	True	False	False		Step is active
LA	Bool	1531.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1531.2	false	True	True	False	False		Supervision active
RA	Bool	1531.3	false	True	True	False	False		Reserved
AA	Bool	1531.4	false	True	True	False	False		Reserved
SS	Bool	1531.5	false	True	True	False	False		System-internal
LS	Bool	1531.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1531.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1532.0	5	True	True	False	False		User step number
T	Time	1534.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1538.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1542.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1546.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1550.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1554.0	false	True	True	False	False		System-internal
LP	Bool	1554.1	false	True	True	False	False		System-internal
LN	Bool	1554.2	false	True	True	False	False		System-internal
VP	Bool	1554.3	false	True	True	False	False		System-internal
VN	Bool	1554.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1555.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1556.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1558.0	16#0	True	True	False	False		Reserve
▼ Step6	GraphStep	1562.0		True	True	False	True		
S1	Bool	1562.0	false	True	True	False	False		Step is activated
L1	Bool	1562.1	false	True	True	False	False		interlock leaving state
V1	Bool	1562.2	false	True	True	False	False		Supervision entering state
R1	Bool	1562.3	false	True	True	False	False		Reserved
A1	Bool	1562.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1562.5	false	True	True	False	False		Step is deactivated
L0	Bool	1562.6	false	True	True	False	False		Interlock entering state
V0	Bool	1562.7	false	True	True	False	False		Supervision leaving state
X	Bool	1563.0	false	True	True	False	False		Step is active

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
LA	Bool	1563.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1563.2	false	True	True	False	False		Supervision active
RA	Bool	1563.3	false	True	True	False	False		Reserved
AA	Bool	1563.4	false	True	True	False	False		Reserved
SS	Bool	1563.5	false	True	True	False	False		System-internal
LS	Bool	1563.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1563.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1564.0	6	True	True	False	False		User step number
T	Time	1566.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1570.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1574.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1578.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1582.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1586.0	false	True	True	False	False		System-internal
LP	Bool	1586.1	false	True	True	False	False		System-internal
LN	Bool	1586.2	false	True	True	False	False		System-internal
VP	Bool	1586.3	false	True	True	False	False		System-internal
VN	Bool	1586.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1587.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1588.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1590.0	16#0	True	True	False	False		Reserve
▼ Step7	GraphStep	1594.0		True	True	False	True		
S1	Bool	1594.0	false	True	True	False	False		Step is activated
L1	Bool	1594.1	false	True	True	False	False		interlock leaving state
V1	Bool	1594.2	false	True	True	False	False		Supervision entering state
R1	Bool	1594.3	false	True	True	False	False		Reserved
A1	Bool	1594.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1594.5	false	True	True	False	False		Step is deactivated
L0	Bool	1594.6	false	True	True	False	False		Interlock entering state
V0	Bool	1594.7	false	True	True	False	False		Supervision leaving state
X	Bool	1595.0	false	True	True	False	False		Step is active
LA	Bool	1595.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1595.2	false	True	True	False	False		Supervision active
RA	Bool	1595.3	false	True	True	False	False		Reserved
AA	Bool	1595.4	false	True	True	False	False		Reserved
SS	Bool	1595.5	false	True	True	False	False		System-internal
LS	Bool	1595.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1595.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1596.0	7	True	True	False	False		User step number
T	Time	1598.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1602.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1606.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1610.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1614.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1618.0	false	True	True	False	False		System-internal
LP	Bool	1618.1	false	True	True	False	False		System-internal
LN	Bool	1618.2	false	True	True	False	False		System-internal
VP	Bool	1618.3	false	True	True	False	False		System-internal
VN	Bool	1618.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1619.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1620.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1622.0	16#0	True	True	False	False		Reserve
▼ Step8	GraphStep	1626.0		True	True	False	True		
S1	Bool	1626.0	false	True	True	False	False		Step is activated
L1	Bool	1626.1	false	True	True	False	False		interlock leaving state
V1	Bool	1626.2	false	True	True	False	False		Supervision entering state
R1	Bool	1626.3	false	True	True	False	False		Reserved

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
A1	Bool	1626.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1626.5	false	True	True	False	False		Step is deactivated
L0	Bool	1626.6	false	True	True	False	False		Interlock entering state
V0	Bool	1626.7	false	True	True	False	False		Supervision leaving state
X	Bool	1627.0	false	True	True	False	False		Step is active
LA	Bool	1627.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1627.2	false	True	True	False	False		Supervision active
RA	Bool	1627.3	false	True	True	False	False		Reserved
AA	Bool	1627.4	false	True	True	False	False		Reserved
SS	Bool	1627.5	false	True	True	False	False		System-internal
LS	Bool	1627.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1627.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1628.0	8	True	True	False	False		User step number
T	Time	1630.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1634.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1638.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1642.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1646.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1650.0	false	True	True	False	False		System-internal
LP	Bool	1650.1	false	True	True	False	False		System-internal
LN	Bool	1650.2	false	True	True	False	False		System-internal
VP	Bool	1650.3	false	True	True	False	False		System-internal
VN	Bool	1650.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1651.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1652.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1654.0	16#0	True	True	False	False		Reserve
▼ Step9	GraphStep	1658.0		True	True	False	True		
S1	Bool	1658.0	false	True	True	False	False		Step is activated
L1	Bool	1658.1	false	True	True	False	False		interlock leaving state
V1	Bool	1658.2	false	True	True	False	False		Supervision entering state
R1	Bool	1658.3	false	True	True	False	False		Reserved
A1	Bool	1658.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1658.5	false	True	True	False	False		Step is deactivated
L0	Bool	1658.6	false	True	True	False	False		Interlock entering state
V0	Bool	1658.7	false	True	True	False	False		Supervision leaving state
X	Bool	1659.0	false	True	True	False	False		Step is active
LA	Bool	1659.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1659.2	false	True	True	False	False		Supervision active
RA	Bool	1659.3	false	True	True	False	False		Reserved
AA	Bool	1659.4	false	True	True	False	False		Reserved
SS	Bool	1659.5	false	True	True	False	False		System-internal
LS	Bool	1659.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1659.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1660.0	9	True	True	False	False		User step number
T	Time	1662.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1666.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1670.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1674.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1678.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1682.0	false	True	True	False	False		System-internal
LP	Bool	1682.1	false	True	True	False	False		System-internal
LN	Bool	1682.2	false	True	True	False	False		System-internal
VP	Bool	1682.3	false	True	True	False	False		System-internal
VN	Bool	1682.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1683.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1684.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1686.0	16#0	True	True	False	False		Reserve

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
Step10	GraphStep	1690.0		True	True	False	True		
S1	Bool	1690.0	false	True	True	False	False		Step is activated
L1	Bool	1690.1	false	True	True	False	False		interlock leaving state
V1	Bool	1690.2	false	True	True	False	False		Supervision entering state
R1	Bool	1690.3	false	True	True	False	False		Reserved
A1	Bool	1690.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1690.5	false	True	True	False	False		Step is deactivated
L0	Bool	1690.6	false	True	True	False	False		Interlock entering state
V0	Bool	1690.7	false	True	True	False	False		Supervision leaving state
X	Bool	1691.0	false	True	True	False	False		Step is active
LA	Bool	1691.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1691.2	false	True	True	False	False		Supervision active
RA	Bool	1691.3	false	True	True	False	False		Reserved
AA	Bool	1691.4	false	True	True	False	False		Reserved
SS	Bool	1691.5	false	True	True	False	False		System-internal
LS	Bool	1691.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1691.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1692.0	10	True	True	False	False		User step number
T	Time	1694.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1698.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1702.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1706.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1710.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1714.0	false	True	True	False	False		System-internal
LP	Bool	1714.1	false	True	True	False	False		System-internal
LN	Bool	1714.2	false	True	True	False	False		System-internal
VP	Bool	1714.3	false	True	True	False	False		System-internal
VN	Bool	1714.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1715.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1716.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1718.0	16#0	True	True	False	False		Reserve
Step11	GraphStep	1722.0		True	True	False	True		
S1	Bool	1722.0	false	True	True	False	False		Step is activated
L1	Bool	1722.1	false	True	True	False	False		interlock leaving state
V1	Bool	1722.2	false	True	True	False	False		Supervision entering state
R1	Bool	1722.3	false	True	True	False	False		Reserved
A1	Bool	1722.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1722.5	false	True	True	False	False		Step is deactivated
L0	Bool	1722.6	false	True	True	False	False		Interlock entering state
V0	Bool	1722.7	false	True	True	False	False		Supervision leaving state
X	Bool	1723.0	false	True	True	False	False		Step is active
LA	Bool	1723.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1723.2	false	True	True	False	False		Supervision active
RA	Bool	1723.3	false	True	True	False	False		Reserved
AA	Bool	1723.4	false	True	True	False	False		Reserved
SS	Bool	1723.5	false	True	True	False	False		System-internal
LS	Bool	1723.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1723.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1724.0	11	True	True	False	False		User step number
T	Time	1726.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1730.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1734.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1738.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1742.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1746.0	false	True	True	False	False		System-internal
LP	Bool	1746.1	false	True	True	False	False		System-internal
LN	Bool	1746.2	false	True	True	False	False		System-internal

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
VP	Bool	1746.3	false	True	True	False	False		System-internal
VN	Bool	1746.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1747.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1748.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1750.0	16#0	True	True	False	False		Reserve
Step12	GraphStep	1754.0		True	True	False	True		
S1	Bool	1754.0	false	True	True	False	False		Step is activated
L1	Bool	1754.1	false	True	True	False	False		interlock leaving state
V1	Bool	1754.2	false	True	True	False	False		Supervision entering state
R1	Bool	1754.3	false	True	True	False	False		Reserved
A1	Bool	1754.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1754.5	false	True	True	False	False		Step is deactivated
L0	Bool	1754.6	false	True	True	False	False		Interlock entering state
V0	Bool	1754.7	false	True	True	False	False		Supervision leaving state
X	Bool	1755.0	false	True	True	False	False		Step is active
LA	Bool	1755.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1755.2	false	True	True	False	False		Supervision active
RA	Bool	1755.3	false	True	True	False	False		Reserved
AA	Bool	1755.4	false	True	True	False	False		Reserved
SS	Bool	1755.5	false	True	True	False	False		System-internal
LS	Bool	1755.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1755.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1756.0	12	True	True	False	False		User step number
T	Time	1758.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1762.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1766.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1770.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1774.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1778.0	false	True	True	False	False		System-internal
LP	Bool	1778.1	false	True	True	False	False		System-internal
LN	Bool	1778.2	false	True	True	False	False		System-internal
VP	Bool	1778.3	false	True	True	False	False		System-internal
VN	Bool	1778.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1779.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1780.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1782.0	16#0	True	True	False	False		Reserve
Step13	GraphStep	1786.0		True	True	False	True		
S1	Bool	1786.0	false	True	True	False	False		Step is activated
L1	Bool	1786.1	false	True	True	False	False		interlock leaving state
V1	Bool	1786.2	false	True	True	False	False		Supervision entering state
R1	Bool	1786.3	false	True	True	False	False		Reserved
A1	Bool	1786.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1786.5	false	True	True	False	False		Step is deactivated
L0	Bool	1786.6	false	True	True	False	False		Interlock entering state
V0	Bool	1786.7	false	True	True	False	False		Supervision leaving state
X	Bool	1787.0	false	True	True	False	False		Step is active
LA	Bool	1787.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1787.2	false	True	True	False	False		Supervision active
RA	Bool	1787.3	false	True	True	False	False		Reserved
AA	Bool	1787.4	false	True	True	False	False		Reserved
SS	Bool	1787.5	false	True	True	False	False		System-internal
LS	Bool	1787.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1787.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1788.0	13	True	True	False	False		User step number
T	Time	1790.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1794.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1798.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1802.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_SUP	DWord	1806.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1810.0	false	True	True	False	False		System-internal
LP	Bool	1810.1	false	True	True	False	False		System-internal
LN	Bool	1810.2	false	True	True	False	False		System-internal
VP	Bool	1810.3	false	True	True	False	False		System-internal
VN	Bool	1810.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1811.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1812.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1814.0	16#0	True	True	False	False		Reserve
▼ Step15	GraphStep	1818.0		True	True	False	True		
S1	Bool	1818.0	false	True	True	False	False		Step is activated
L1	Bool	1818.1	false	True	True	False	False		interlock leaving state
V1	Bool	1818.2	false	True	True	False	False		Supervision entering state
R1	Bool	1818.3	false	True	True	False	False		Reserved
A1	Bool	1818.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1818.5	false	True	True	False	False		Step is deactivated
L0	Bool	1818.6	false	True	True	False	False		Interlock entering state
V0	Bool	1818.7	false	True	True	False	False		Supervision leaving state
X	Bool	1819.0	false	True	True	False	False		Step is active
LA	Bool	1819.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1819.2	false	True	True	False	False		Supervision active
RA	Bool	1819.3	false	True	True	False	False		Reserved
AA	Bool	1819.4	false	True	True	False	False		Reserved
SS	Bool	1819.5	false	True	True	False	False		System-internal
LS	Bool	1819.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1819.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1820.0	15	True	True	False	False		User step number
T	Time	1822.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1826.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1830.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1834.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1838.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1842.0	false	True	True	False	False		System-internal
LP	Bool	1842.1	false	True	True	False	False		System-internal
LN	Bool	1842.2	false	True	True	False	False		System-internal
VP	Bool	1842.3	false	True	True	False	False		System-internal
VN	Bool	1842.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1843.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1844.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1846.0	16#0	True	True	False	False		Reserve
▼ Step16	GraphStep	1850.0		True	True	False	True		
S1	Bool	1850.0	false	True	True	False	False		Step is activated
L1	Bool	1850.1	false	True	True	False	False		interlock leaving state
V1	Bool	1850.2	false	True	True	False	False		Supervision entering state
R1	Bool	1850.3	false	True	True	False	False		Reserved
A1	Bool	1850.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1850.5	false	True	True	False	False		Step is deactivated
L0	Bool	1850.6	false	True	True	False	False		Interlock entering state
V0	Bool	1850.7	false	True	True	False	False		Supervision leaving state
X	Bool	1851.0	false	True	True	False	False		Step is active
LA	Bool	1851.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1851.2	false	True	True	False	False		Supervision active
RA	Bool	1851.3	false	True	True	False	False		Reserved
AA	Bool	1851.4	false	True	True	False	False		Reserved
SS	Bool	1851.5	false	True	True	False	False		System-internal
LS	Bool	1851.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1851.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1852.0	16	True	True	False	False		User step number
T	Time	1854.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1858.0	T#0ms	True	True	False	False		Step activation time without disturbance

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_LOC	DWord	1862.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1866.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1870.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1874.0	false	True	True	False	False		System-internal
LP	Bool	1874.1	false	True	True	False	False		System-internal
LN	Bool	1874.2	false	True	True	False	False		System-internal
VP	Bool	1874.3	false	True	True	False	False		System-internal
VN	Bool	1874.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1875.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1876.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1878.0	16#0	True	True	False	False		Reserve
Step17	GraphStep	1882.0		True	True	False	True		
S1	Bool	1882.0	false	True	True	False	False		Step is activated
L1	Bool	1882.1	false	True	True	False	False		interlock leaving state
V1	Bool	1882.2	false	True	True	False	False		Supervision entering state
R1	Bool	1882.3	false	True	True	False	False		Reserved
A1	Bool	1882.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1882.5	false	True	True	False	False		Step is deactivated
L0	Bool	1882.6	false	True	True	False	False		Interlock entering state
V0	Bool	1882.7	false	True	True	False	False		Supervision leaving state
X	Bool	1883.0	false	True	True	False	False		Step is active
LA	Bool	1883.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1883.2	false	True	True	False	False		Supervision active
RA	Bool	1883.3	false	True	True	False	False		Reserved
AA	Bool	1883.4	false	True	True	False	False		Reserved
SS	Bool	1883.5	false	True	True	False	False		System-internal
LS	Bool	1883.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1883.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1884.0	17	True	True	False	False		User step number
T	Time	1886.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1890.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1894.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1898.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1902.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1906.0	false	True	True	False	False		System-internal
LP	Bool	1906.1	false	True	True	False	False		System-internal
LN	Bool	1906.2	false	True	True	False	False		System-internal
VP	Bool	1906.3	false	True	True	False	False		System-internal
VN	Bool	1906.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1907.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1908.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1910.0	16#0	True	True	False	False		Reserve
Step18	GraphStep	1914.0		True	True	False	True		
S1	Bool	1914.0	false	True	True	False	False		Step is activated
L1	Bool	1914.1	false	True	True	False	False		interlock leaving state
V1	Bool	1914.2	false	True	True	False	False		Supervision entering state
R1	Bool	1914.3	false	True	True	False	False		Reserved
A1	Bool	1914.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1914.5	false	True	True	False	False		Step is deactivated
L0	Bool	1914.6	false	True	True	False	False		Interlock entering state
V0	Bool	1914.7	false	True	True	False	False		Supervision leaving state
X	Bool	1915.0	false	True	True	False	False		Step is active
LA	Bool	1915.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1915.2	false	True	True	False	False		Supervision active
RA	Bool	1915.3	false	True	True	False	False		Reserved
AA	Bool	1915.4	false	True	True	False	False		Reserved
SS	Bool	1915.5	false	True	True	False	False		System-internal
LS	Bool	1915.6	true	True	True	False	False		Direct result of the programmed interlock

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
VS	Bool	1915.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1916.0	18	True	True	False	False		User step number
T	Time	1918.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1922.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1926.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1930.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1934.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1938.0	false	True	True	False	False		System-internal
LP	Bool	1938.1	false	True	True	False	False		System-internal
LN	Bool	1938.2	false	True	True	False	False		System-internal
VP	Bool	1938.3	false	True	True	False	False		System-internal
VN	Bool	1938.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1939.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1940.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1942.0	16#0	True	True	False	False		Reserve
Step19	GraphStep	1946.0		True	True	False	True		
S1	Bool	1946.0	false	True	True	False	False		Step is activated
L1	Bool	1946.1	false	True	True	False	False		interlock leaving state
V1	Bool	1946.2	false	True	True	False	False		Supervision entering state
R1	Bool	1946.3	false	True	True	False	False		Reserved
A1	Bool	1946.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1946.5	false	True	True	False	False		Step is deactivated
L0	Bool	1946.6	false	True	True	False	False		Interlock entering state
V0	Bool	1946.7	false	True	True	False	False		Supervision leaving state
X	Bool	1947.0	false	True	True	False	False		Step is active
LA	Bool	1947.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1947.2	false	True	True	False	False		Supervision active
RA	Bool	1947.3	false	True	True	False	False		Reserved
AA	Bool	1947.4	false	True	True	False	False		Reserved
SS	Bool	1947.5	false	True	True	False	False		System-internal
LS	Bool	1947.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1947.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1948.0	19	True	True	False	False		User step number
T	Time	1950.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1954.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1958.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1962.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1966.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1970.0	false	True	True	False	False		System-internal
LP	Bool	1970.1	false	True	True	False	False		System-internal
LN	Bool	1970.2	false	True	True	False	False		System-internal
VP	Bool	1970.3	false	True	True	False	False		System-internal
VN	Bool	1970.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1971.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1972.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1974.0	16#0	True	True	False	False		Reserve
Step20	GraphStep	1978.0		True	True	False	True		
S1	Bool	1978.0	false	True	True	False	False		Step is activated
L1	Bool	1978.1	false	True	True	False	False		interlock leaving state
V1	Bool	1978.2	false	True	True	False	False		Supervision entering state
R1	Bool	1978.3	false	True	True	False	False		Reserved
A1	Bool	1978.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1978.5	false	True	True	False	False		Step is deactivated
L0	Bool	1978.6	false	True	True	False	False		Interlock entering state
V0	Bool	1978.7	false	True	True	False	False		Supervision leaving state
X	Bool	1979.0	false	True	True	False	False		Step is active
LA	Bool	1979.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1979.2	false	True	True	False	False		Supervision active

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
RA	Bool	1979.3	false	True	True	False	False		Reserved
AA	Bool	1979.4	false	True	True	False	False		Reserved
SS	Bool	1979.5	false	True	True	False	False		System-internal
LS	Bool	1979.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1979.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1980.0	20	True	True	False	False		User step number
T	Time	1982.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1986.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1990.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1994.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1998.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2002.0	false	True	True	False	False		System-internal
LP	Bool	2002.1	false	True	True	False	False		System-internal
LN	Bool	2002.2	false	True	True	False	False		System-internal
VP	Bool	2002.3	false	True	True	False	False		System-internal
VN	Bool	2002.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	2003.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	2004.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	2006.0	16#0	True	True	False	False		Reserve
▼ Step21	GraphStep	2010.0		True	True	False	True		
S1	Bool	2010.0	false	True	True	False	False		Step is activated
L1	Bool	2010.1	false	True	True	False	False		interlock leaving state
V1	Bool	2010.2	false	True	True	False	False		Supervision entering state
R1	Bool	2010.3	false	True	True	False	False		Reserved
A1	Bool	2010.4	false	True	True	False	False		Error is acknowledged
S0	Bool	2010.5	false	True	True	False	False		Step is deactivated
L0	Bool	2010.6	false	True	True	False	False		Interlock entering state
V0	Bool	2010.7	false	True	True	False	False		Supervision leaving state
X	Bool	2011.0	false	True	True	False	False		Step is active
LA	Bool	2011.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	2011.2	false	True	True	False	False		Supervision active
RA	Bool	2011.3	false	True	True	False	False		Reserved
AA	Bool	2011.4	false	True	True	False	False		Reserved
SS	Bool	2011.5	false	True	True	False	False		System-internal
LS	Bool	2011.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2011.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2012.0	21	True	True	False	False		User step number
T	Time	2014.0	T#0ms	True	True	False	False		Total step activation time
U	Time	2018.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2022.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2026.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2030.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2034.0	false	True	True	False	False		System-internal
LP	Bool	2034.1	false	True	True	False	False		System-internal
LN	Bool	2034.2	false	True	True	False	False		System-internal
VP	Bool	2034.3	false	True	True	False	False		System-internal
VN	Bool	2034.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	2035.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	2036.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	2038.0	16#0	True	True	False	False		Reserve
▼ Step22	GraphStep	2042.0		True	True	False	True		
S1	Bool	2042.0	false	True	True	False	False		Step is activated
L1	Bool	2042.1	false	True	True	False	False		interlock leaving state
V1	Bool	2042.2	false	True	True	False	False		Supervision entering state
R1	Bool	2042.3	false	True	True	False	False		Reserved
A1	Bool	2042.4	false	True	True	False	False		Error is acknowledged
S0	Bool	2042.5	false	True	True	False	False		Step is deactivated

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
L0	Bool	2042.6	false	True	True	False	False		Interlock entering state
V0	Bool	2042.7	false	True	True	False	False		Supervision leaving state
X	Bool	2043.0	false	True	True	False	False		Step is active
LA	Bool	2043.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	2043.2	false	True	True	False	False		Supervision active
RA	Bool	2043.3	false	True	True	False	False		Reserved
AA	Bool	2043.4	false	True	True	False	False		Reserved
SS	Bool	2043.5	false	True	True	False	False		System-internal
LS	Bool	2043.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2043.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2044.0	22	True	True	False	False		User step number
T	Time	2046.0	T#0ms	True	True	False	False		Total step activation time
U	Time	2050.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2054.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2058.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2062.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2066.0	false	True	True	False	False		System-internal
LP	Bool	2066.1	false	True	True	False	False		System-internal
LN	Bool	2066.2	false	True	True	False	False		System-internal
VP	Bool	2066.3	false	True	True	False	False		System-internal
VN	Bool	2066.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	2067.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	2068.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	2070.0	16#0	True	True	False	False		Reserve
▼ Step23	GraphStep	2074.0		True	True	False	True		
S1	Bool	2074.0	false	True	True	False	False		Step is activated
L1	Bool	2074.1	false	True	True	False	False		interlock leaving state
V1	Bool	2074.2	false	True	True	False	False		Supervision entering state
R1	Bool	2074.3	false	True	True	False	False		Reserved
A1	Bool	2074.4	false	True	True	False	False		Error is acknowledged
S0	Bool	2074.5	false	True	True	False	False		Step is deactivated
L0	Bool	2074.6	false	True	True	False	False		Interlock entering state
V0	Bool	2074.7	false	True	True	False	False		Supervision leaving state
X	Bool	2075.0	false	True	True	False	False		Step is active
LA	Bool	2075.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	2075.2	false	True	True	False	False		Supervision active
RA	Bool	2075.3	false	True	True	False	False		Reserved
AA	Bool	2075.4	false	True	True	False	False		Reserved
SS	Bool	2075.5	false	True	True	False	False		System-internal
LS	Bool	2075.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2075.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2076.0	23	True	True	False	False		User step number
T	Time	2078.0	T#0ms	True	True	False	False		Total step activation time
U	Time	2082.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2086.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2090.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2094.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2098.0	false	True	True	False	False		System-internal
LP	Bool	2098.1	false	True	True	False	False		System-internal
LN	Bool	2098.2	false	True	True	False	False		System-internal
VP	Bool	2098.3	false	True	True	False	False		System-internal
VN	Bool	2098.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	2099.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	2100.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	2102.0	16#0	True	True	False	False		Reserve
▼ Step24	GraphStep	2106.0		True	True	False	True		
S1	Bool	2106.0	false	True	True	False	False		Step is activated

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
L1	Bool	2106.1	false	True	True	False	False		interlock leaving state
V1	Bool	2106.2	false	True	True	False	False		Supervision entering state
R1	Bool	2106.3	false	True	True	False	False		Reserved
A1	Bool	2106.4	false	True	True	False	False		Error is acknowledged
S0	Bool	2106.5	false	True	True	False	False		Step is deactivated
L0	Bool	2106.6	false	True	True	False	False		Interlock entering state
V0	Bool	2106.7	false	True	True	False	False		Supervision leaving state
X	Bool	2107.0	false	True	True	False	False		Step is active
LA	Bool	2107.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	2107.2	false	True	True	False	False		Supervision active
RA	Bool	2107.3	false	True	True	False	False		Reserved
AA	Bool	2107.4	false	True	True	False	False		Reserved
SS	Bool	2107.5	false	True	True	False	False		System-internal
LS	Bool	2107.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2107.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2108.0	24	True	True	False	False		User step number
T	Time	2110.0	T#0ms	True	True	False	False		Total step activation time
U	Time	2114.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2118.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2122.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2126.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2130.0	false	True	True	False	False		System-internal
LP	Bool	2130.1	false	True	True	False	False		System-internal
LN	Bool	2130.2	false	True	True	False	False		System-internal
VP	Bool	2130.3	false	True	True	False	False		System-internal
VN	Bool	2130.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	2131.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	2132.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	2134.0	16#0	True	True	False	False		Reserve
▼ Step25	GraphStep	2138.0		False	False	False	True		Step structure
S1	Bool	2138.0	false	False	False	False	False		Step is activated
L1	Bool	2138.1	false	False	False	False	False		interlock leaving state
V1	Bool	2138.2	false	False	False	False	False		Supervision entering state
R1	Bool	2138.3	false	False	False	False	False		Reserved
A1	Bool	2138.4	false	False	False	False	False		Error is acknowledged
S0	Bool	2138.5	false	False	False	False	False		Step is deactivated
L0	Bool	2138.6	false	False	False	False	False		Interlock entering state
V0	Bool	2138.7	false	False	False	False	False		Supervision leaving state
X	Bool	2139.0	false	False	False	False	False		Step is active
LA	Bool	2139.1	false	False	False	False	False		Interlock is not satisfied
VA	Bool	2139.2	false	False	False	False	False		Supervision active
RA	Bool	2139.3	false	False	False	False	False		Reserved
AA	Bool	2139.4	false	False	False	False	False		Reserved
SS	Bool	2139.5	false	False	False	False	False		System-internal
LS	Bool	2139.6	True	False	False	False	False		Direct result of the programmed interlock
VS	Bool	2139.7	false	False	False	False	False		Direct result of the programmed supervision
SNO	Int	2140.0	25	False	False	False	False		User step number
T	Time	2142.0	T#0ms	False	False	False	False		Total step activation time
U	Time	2146.0	T#0ms	False	False	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2150.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2154.0	16#0	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2158.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2162.0	false	False	False	False	False		System-internal
LP	Bool	2162.1	false	False	False	False	False		System-internal
LN	Bool	2162.2	false	False	False	False	False		System-internal
VP	Bool	2162.3	false	False	False	False	False		System-internal
VN	Bool	2162.4	false	False	False	False	False		System-internal

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
H_IL_ERR	Byte	2163.0	16#0	False	False	False	False		System-internal
H_SV_FLT	Byte	2164.0	16#0	False	False	False	False		System-internal
RESERVED	DWord	2166.0	16#0	False	False	False	False		Reserve
Step26	GraphStep	2170.0		True	True	False	True		
S1	Bool	2170.0	false	True	True	False	False		Step is activated
L1	Bool	2170.1	false	True	True	False	False		interlock leaving state
V1	Bool	2170.2	false	True	True	False	False		Supervision entering state
R1	Bool	2170.3	false	True	True	False	False		Reserved
A1	Bool	2170.4	false	True	True	False	False		Error is acknowledged
S0	Bool	2170.5	false	True	True	False	False		Step is deactivated
L0	Bool	2170.6	false	True	True	False	False		Interlock entering state
V0	Bool	2170.7	false	True	True	False	False		Supervision leaving state
X	Bool	2171.0	false	True	True	False	False		Step is active
LA	Bool	2171.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	2171.2	false	True	True	False	False		Supervision active
RA	Bool	2171.3	false	True	True	False	False		Reserved
AA	Bool	2171.4	false	True	True	False	False		Reserved
SS	Bool	2171.5	false	True	True	False	False		System-internal
LS	Bool	2171.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2171.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2172.0	26	True	True	False	False		User step number
T	Time	2174.0	T#0ms	True	True	False	False		Total step activation time
U	Time	2178.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2182.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2186.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2190.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2194.0	false	True	True	False	False		System-internal
LP	Bool	2194.1	false	True	True	False	False		System-internal
LN	Bool	2194.2	false	True	True	False	False		System-internal
VP	Bool	2194.3	false	True	True	False	False		System-internal
VN	Bool	2194.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	2195.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	2196.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	2198.0	16#0	True	True	False	False		Reserve
Step27	GraphStep	2202.0		True	True	False	True		
S1	Bool	2202.0	false	True	True	False	False		Step is activated
L1	Bool	2202.1	false	True	True	False	False		interlock leaving state
V1	Bool	2202.2	false	True	True	False	False		Supervision entering state
R1	Bool	2202.3	false	True	True	False	False		Reserved
A1	Bool	2202.4	false	True	True	False	False		Error is acknowledged
S0	Bool	2202.5	false	True	True	False	False		Step is deactivated
L0	Bool	2202.6	false	True	True	False	False		Interlock entering state
V0	Bool	2202.7	false	True	True	False	False		Supervision leaving state
X	Bool	2203.0	false	True	True	False	False		Step is active
LA	Bool	2203.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	2203.2	false	True	True	False	False		Supervision active
RA	Bool	2203.3	false	True	True	False	False		Reserved
AA	Bool	2203.4	false	True	True	False	False		Reserved
SS	Bool	2203.5	false	True	True	False	False		System-internal
LS	Bool	2203.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2203.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2204.0	27	True	True	False	False		User step number
T	Time	2206.0	T#0ms	True	True	False	False		Total step activation time
U	Time	2210.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2214.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2218.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2222.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
SM	Bool	2226.0	false	True	True	False	False		System-internal
LP	Bool	2226.1	false	True	True	False	False		System-internal
LN	Bool	2226.2	false	True	True	False	False		System-internal
VP	Bool	2226.3	false	True	True	False	False		System-internal
VN	Bool	2226.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	2227.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	2228.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	2230.0	16#0	True	True	False	False		Reserve
Step28	GraphStep	2234.0		True	True	False	True		
S1	Bool	2234.0	false	True	True	False	False		Step is activated
L1	Bool	2234.1	false	True	True	False	False		interlock leaving state
V1	Bool	2234.2	false	True	True	False	False		Supervision entering state
R1	Bool	2234.3	false	True	True	False	False		Reserved
A1	Bool	2234.4	false	True	True	False	False		Error is acknowledged
S0	Bool	2234.5	false	True	True	False	False		Step is deactivated
L0	Bool	2234.6	false	True	True	False	False		Interlock entering state
V0	Bool	2234.7	false	True	True	False	False		Supervision leaving state
X	Bool	2235.0	false	True	True	False	False		Step is active
LA	Bool	2235.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	2235.2	false	True	True	False	False		Supervision active
RA	Bool	2235.3	false	True	True	False	False		Reserved
AA	Bool	2235.4	false	True	True	False	False		Reserved
SS	Bool	2235.5	false	True	True	False	False		System-internal
LS	Bool	2235.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2235.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2236.0	28	True	True	False	False		User step number
T	Time	2238.0	T#0ms	True	True	False	False		Total step activation time
U	Time	2242.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2246.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2250.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2254.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2258.0	false	True	True	False	False		System-internal
LP	Bool	2258.1	false	True	True	False	False		System-internal
LN	Bool	2258.2	false	True	True	False	False		System-internal
VP	Bool	2258.3	false	True	True	False	False		System-internal
VN	Bool	2258.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	2259.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	2260.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	2262.0	16#0	True	True	False	False		Reserve
Step29	GraphStep	2266.0		True	True	False	True		
S1	Bool	2266.0	false	True	True	False	False		Step is activated
L1	Bool	2266.1	false	True	True	False	False		interlock leaving state
V1	Bool	2266.2	false	True	True	False	False		Supervision entering state
R1	Bool	2266.3	false	True	True	False	False		Reserved
A1	Bool	2266.4	false	True	True	False	False		Error is acknowledged
S0	Bool	2266.5	false	True	True	False	False		Step is deactivated
L0	Bool	2266.6	false	True	True	False	False		Interlock entering state
V0	Bool	2266.7	false	True	True	False	False		Supervision leaving state
X	Bool	2267.0	false	True	True	False	False		Step is active
LA	Bool	2267.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	2267.2	false	True	True	False	False		Supervision active
RA	Bool	2267.3	false	True	True	False	False		Reserved
AA	Bool	2267.4	false	True	True	False	False		Reserved
SS	Bool	2267.5	false	True	True	False	False		System-internal
LS	Bool	2267.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2267.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2268.0	29	True	True	False	False		User step number
T	Time	2270.0	T#0ms	True	True	False	False		Total step activation time
U	Time	2274.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2278.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_LOC_ERR	DWord	2282.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2286.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2290.0	false	True	True	False	False		System-internal
LP	Bool	2290.1	false	True	True	False	False		System-internal
LN	Bool	2290.2	false	True	True	False	False		System-internal
VP	Bool	2290.3	false	True	True	False	False		System-internal
VN	Bool	2290.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	2291.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	2292.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	2294.0	16#0	True	True	False	False		Reserve
Step30	GraphStep	2298.0		True	True	False	True		
S1	Bool	2298.0	false	True	True	False	False		Step is activated
L1	Bool	2298.1	false	True	True	False	False		interlock leaving state
V1	Bool	2298.2	false	True	True	False	False		Supervision entering state
R1	Bool	2298.3	false	True	True	False	False		Reserved
A1	Bool	2298.4	false	True	True	False	False		Error is acknowledged
S0	Bool	2298.5	false	True	True	False	False		Step is deactivated
L0	Bool	2298.6	false	True	True	False	False		Interlock entering state
V0	Bool	2298.7	false	True	True	False	False		Supervision leaving state
X	Bool	2299.0	false	True	True	False	False		Step is active
LA	Bool	2299.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	2299.2	false	True	True	False	False		Supervision active
RA	Bool	2299.3	false	True	True	False	False		Reserved
AA	Bool	2299.4	false	True	True	False	False		Reserved
SS	Bool	2299.5	false	True	True	False	False		System-internal
LS	Bool	2299.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2299.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2300.0	30	True	True	False	False		User step number
T	Time	2302.0	T#0ms	True	True	False	False		Total step activation time
U	Time	2306.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2310.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2314.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2318.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2322.0	false	True	True	False	False		System-internal
LP	Bool	2322.1	false	True	True	False	False		System-internal
LN	Bool	2322.2	false	True	True	False	False		System-internal
VP	Bool	2322.3	false	True	True	False	False		System-internal
VN	Bool	2322.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	2323.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	2324.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	2326.0	16#0	True	True	False	False		Reserve
Step31	GraphStep	2330.0		True	True	False	True		
S1	Bool	2330.0	false	True	True	False	False		Step is activated
L1	Bool	2330.1	false	True	True	False	False		interlock leaving state
V1	Bool	2330.2	false	True	True	False	False		Supervision entering state
R1	Bool	2330.3	false	True	True	False	False		Reserved
A1	Bool	2330.4	false	True	True	False	False		Error is acknowledged
S0	Bool	2330.5	false	True	True	False	False		Step is deactivated
L0	Bool	2330.6	false	True	True	False	False		Interlock entering state
V0	Bool	2330.7	false	True	True	False	False		Supervision leaving state
X	Bool	2331.0	false	True	True	False	False		Step is active
LA	Bool	2331.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	2331.2	false	True	True	False	False		Supervision active
RA	Bool	2331.3	false	True	True	False	False		Reserved
AA	Bool	2331.4	false	True	True	False	False		Reserved
SS	Bool	2331.5	false	True	True	False	False		System-internal
LS	Bool	2331.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2331.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2332.0	31	True	True	False	False		User step number
T	Time	2334.0	T#0ms	True	True	False	False		Total step activation time

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
U	Time	2338.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2342.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2346.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2350.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2354.0	false	True	True	False	False		System-internal
LP	Bool	2354.1	false	True	True	False	False		System-internal
LN	Bool	2354.2	false	True	True	False	False		System-internal
VP	Bool	2354.3	false	True	True	False	False		System-internal
VN	Bool	2354.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	2355.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	2356.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	2358.0	16#0	True	True	False	False		Reserve
Step32	GraphStep	2362.0		True	True	False	True		
S1	Bool	2362.0	false	True	True	False	False		Step is activated
L1	Bool	2362.1	false	True	True	False	False		interlock leaving state
V1	Bool	2362.2	false	True	True	False	False		Supervision entering state
R1	Bool	2362.3	false	True	True	False	False		Reserved
A1	Bool	2362.4	false	True	True	False	False		Error is acknowledged
S0	Bool	2362.5	false	True	True	False	False		Step is deactivated
L0	Bool	2362.6	false	True	True	False	False		Interlock entering state
V0	Bool	2362.7	false	True	True	False	False		Supervision leaving state
X	Bool	2363.0	false	True	True	False	False		Step is active
LA	Bool	2363.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	2363.2	false	True	True	False	False		Supervision active
RA	Bool	2363.3	false	True	True	False	False		Reserved
AA	Bool	2363.4	false	True	True	False	False		Reserved
SS	Bool	2363.5	false	True	True	False	False		System-internal
LS	Bool	2363.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2363.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2364.0	32	True	True	False	False		User step number
T	Time	2366.0	T#0ms	True	True	False	False		Total step activation time
U	Time	2370.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2374.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2378.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2382.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2386.0	false	True	True	False	False		System-internal
LP	Bool	2386.1	false	True	True	False	False		System-internal
LN	Bool	2386.2	false	True	True	False	False		System-internal
VP	Bool	2386.3	false	True	True	False	False		System-internal
VN	Bool	2386.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	2387.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	2388.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	2390.0	16#0	True	True	False	False		Reserve
Step33	GraphStep	2394.0		True	True	False	True		
S1	Bool	2394.0	false	True	True	False	False		Step is activated
L1	Bool	2394.1	false	True	True	False	False		interlock leaving state
V1	Bool	2394.2	false	True	True	False	False		Supervision entering state
R1	Bool	2394.3	false	True	True	False	False		Reserved
A1	Bool	2394.4	false	True	True	False	False		Error is acknowledged
S0	Bool	2394.5	false	True	True	False	False		Step is deactivated
L0	Bool	2394.6	false	True	True	False	False		Interlock entering state
V0	Bool	2394.7	false	True	True	False	False		Supervision leaving state
X	Bool	2395.0	false	True	True	False	False		Step is active
LA	Bool	2395.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	2395.2	false	True	True	False	False		Supervision active
RA	Bool	2395.3	false	True	True	False	False		Reserved
AA	Bool	2395.4	false	True	True	False	False		Reserved
SS	Bool	2395.5	false	True	True	False	False		System-internal

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
LS	Bool	2395.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2395.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2396.0	33	True	True	False	False		User step number
T	Time	2398.0	T#0ms	True	True	False	False		Total step activation time
U	Time	2402.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2406.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2410.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2414.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2418.0	false	True	True	False	False		System-internal
LP	Bool	2418.1	false	True	True	False	False		System-internal
LN	Bool	2418.2	false	True	True	False	False		System-internal
VP	Bool	2418.3	false	True	True	False	False		System-internal
VN	Bool	2418.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	2419.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	2420.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	2422.0	16#0	True	True	False	False		Reserve
▼ Step34	GraphStep	2426.0		True	True	False	True		
S1	Bool	2426.0	false	True	True	False	False		Step is activated
L1	Bool	2426.1	false	True	True	False	False		interlock leaving state
V1	Bool	2426.2	false	True	True	False	False		Supervision entering state
R1	Bool	2426.3	false	True	True	False	False		Reserved
A1	Bool	2426.4	false	True	True	False	False		Error is acknowledged
S0	Bool	2426.5	false	True	True	False	False		Step is deactivated
L0	Bool	2426.6	false	True	True	False	False		Interlock entering state
V0	Bool	2426.7	false	True	True	False	False		Supervision leaving state
X	Bool	2427.0	false	True	True	False	False		Step is active
LA	Bool	2427.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	2427.2	false	True	True	False	False		Supervision active
RA	Bool	2427.3	false	True	True	False	False		Reserved
AA	Bool	2427.4	false	True	True	False	False		Reserved
SS	Bool	2427.5	false	True	True	False	False		System-internal
LS	Bool	2427.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2427.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2428.0	34	True	True	False	False		User step number
T	Time	2430.0	T#0ms	True	True	False	False		Total step activation time
U	Time	2434.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2438.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2442.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2446.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2450.0	false	True	True	False	False		System-internal
LP	Bool	2450.1	false	True	True	False	False		System-internal
LN	Bool	2450.2	false	True	True	False	False		System-internal
VP	Bool	2450.3	false	True	True	False	False		System-internal
VN	Bool	2450.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	2451.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	2452.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	2454.0	16#0	True	True	False	False		Reserve
▼ Step35	GraphStep	2458.0		True	True	False	True		
S1	Bool	2458.0	false	True	True	False	False		Step is activated
L1	Bool	2458.1	false	True	True	False	False		interlock leaving state
V1	Bool	2458.2	false	True	True	False	False		Supervision entering state
R1	Bool	2458.3	false	True	True	False	False		Reserved
A1	Bool	2458.4	false	True	True	False	False		Error is acknowledged
S0	Bool	2458.5	false	True	True	False	False		Step is deactivated
L0	Bool	2458.6	false	True	True	False	False		Interlock entering state
V0	Bool	2458.7	false	True	True	False	False		Supervision leaving state
X	Bool	2459.0	false	True	True	False	False		Step is active

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
LA	Bool	2459.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	2459.2	false	True	True	False	False		Supervision active
RA	Bool	2459.3	false	True	True	False	False		Reserved
AA	Bool	2459.4	false	True	True	False	False		Reserved
SS	Bool	2459.5	false	True	True	False	False		System-internal
LS	Bool	2459.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2459.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2460.0	35	True	True	False	False		User step number
T	Time	2462.0	T#0ms	True	True	False	False		Total step activation time
U	Time	2466.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2470.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2474.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2478.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2482.0	false	True	True	False	False		System-internal
LP	Bool	2482.1	false	True	True	False	False		System-internal
LN	Bool	2482.2	false	True	True	False	False		System-internal
VP	Bool	2482.3	false	True	True	False	False		System-internal
VN	Bool	2482.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	2483.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	2484.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	2486.0	16#0	True	True	False	False		Reserve
▼ Step36	GraphStep	2490.0		True	True	False	True		
S1	Bool	2490.0	false	True	True	False	False		Step is activated
L1	Bool	2490.1	false	True	True	False	False		interlock leaving state
V1	Bool	2490.2	false	True	True	False	False		Supervision entering state
R1	Bool	2490.3	false	True	True	False	False		Reserved
A1	Bool	2490.4	false	True	True	False	False		Error is acknowledged
S0	Bool	2490.5	false	True	True	False	False		Step is deactivated
L0	Bool	2490.6	false	True	True	False	False		Interlock entering state
V0	Bool	2490.7	false	True	True	False	False		Supervision leaving state
X	Bool	2491.0	false	True	True	False	False		Step is active
LA	Bool	2491.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	2491.2	false	True	True	False	False		Supervision active
RA	Bool	2491.3	false	True	True	False	False		Reserved
AA	Bool	2491.4	false	True	True	False	False		Reserved
SS	Bool	2491.5	false	True	True	False	False		System-internal
LS	Bool	2491.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2491.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2492.0	36	True	True	False	False		User step number
T	Time	2494.0	T#0ms	True	True	False	False		Total step activation time
U	Time	2498.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2502.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2506.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2510.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2514.0	false	True	True	False	False		System-internal
LP	Bool	2514.1	false	True	True	False	False		System-internal
LN	Bool	2514.2	false	True	True	False	False		System-internal
VP	Bool	2514.3	false	True	True	False	False		System-internal
VN	Bool	2514.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	2515.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	2516.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	2518.0	16#0	True	True	False	False		Reserve
▼ Step37	GraphStep	2522.0		True	True	False	True		
S1	Bool	2522.0	false	True	True	False	False		Step is activated
L1	Bool	2522.1	false	True	True	False	False		interlock leaving state
V1	Bool	2522.2	false	True	True	False	False		Supervision entering state
R1	Bool	2522.3	false	True	True	False	False		Reserved

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
A1	Bool	2522.4	false	True	True	False	False		Error is acknowledged
S0	Bool	2522.5	false	True	True	False	False		Step is deactivated
L0	Bool	2522.6	false	True	True	False	False		Interlock entering state
V0	Bool	2522.7	false	True	True	False	False		Supervision leaving state
X	Bool	2523.0	false	True	True	False	False		Step is active
LA	Bool	2523.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	2523.2	false	True	True	False	False		Supervision active
RA	Bool	2523.3	false	True	True	False	False		Reserved
AA	Bool	2523.4	false	True	True	False	False		Reserved
SS	Bool	2523.5	false	True	True	False	False		System-internal
LS	Bool	2523.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2523.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2524.0	37	True	True	False	False		User step number
T	Time	2526.0	T#0ms	True	True	False	False		Total step activation time
U	Time	2530.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2534.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2538.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2542.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2546.0	false	True	True	False	False		System-internal
LP	Bool	2546.1	false	True	True	False	False		System-internal
LN	Bool	2546.2	false	True	True	False	False		System-internal
VP	Bool	2546.3	false	True	True	False	False		System-internal
VN	Bool	2546.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	2547.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	2548.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	2550.0	16#0	True	True	False	False		Reserve
▼ Step38	GraphStep	2554.0		True	True	False	True		
S1	Bool	2554.0	false	True	True	False	False		Step is activated
L1	Bool	2554.1	false	True	True	False	False		interlock leaving state
V1	Bool	2554.2	false	True	True	False	False		Supervision entering state
R1	Bool	2554.3	false	True	True	False	False		Reserved
A1	Bool	2554.4	false	True	True	False	False		Error is acknowledged
S0	Bool	2554.5	false	True	True	False	False		Step is deactivated
L0	Bool	2554.6	false	True	True	False	False		Interlock entering state
V0	Bool	2554.7	false	True	True	False	False		Supervision leaving state
X	Bool	2555.0	false	True	True	False	False		Step is active
LA	Bool	2555.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	2555.2	false	True	True	False	False		Supervision active
RA	Bool	2555.3	false	True	True	False	False		Reserved
AA	Bool	2555.4	false	True	True	False	False		Reserved
SS	Bool	2555.5	false	True	True	False	False		System-internal
LS	Bool	2555.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2555.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2556.0	38	True	True	False	False		User step number
T	Time	2558.0	T#0ms	True	True	False	False		Total step activation time
U	Time	2562.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2566.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2570.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2574.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2578.0	false	True	True	False	False		System-internal
LP	Bool	2578.1	false	True	True	False	False		System-internal
LN	Bool	2578.2	false	True	True	False	False		System-internal
VP	Bool	2578.3	false	True	True	False	False		System-internal
VN	Bool	2578.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	2579.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	2580.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	2582.0	16#0	True	True	False	False		Reserve

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
Step39	GraphStep	2586.0		True	True	False	True		
S1	Bool	2586.0	false	True	True	False	False		Step is activated
L1	Bool	2586.1	false	True	True	False	False		interlock leaving state
V1	Bool	2586.2	false	True	True	False	False		Supervision entering state
R1	Bool	2586.3	false	True	True	False	False		Reserved
A1	Bool	2586.4	false	True	True	False	False		Error is acknowledged
S0	Bool	2586.5	false	True	True	False	False		Step is deactivated
L0	Bool	2586.6	false	True	True	False	False		Interlock entering state
V0	Bool	2586.7	false	True	True	False	False		Supervision leaving state
X	Bool	2587.0	false	True	True	False	False		Step is active
LA	Bool	2587.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	2587.2	false	True	True	False	False		Supervision active
RA	Bool	2587.3	false	True	True	False	False		Reserved
AA	Bool	2587.4	false	True	True	False	False		Reserved
SS	Bool	2587.5	false	True	True	False	False		System-internal
LS	Bool	2587.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2587.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2588.0	39	True	True	False	False		User step number
T	Time	2590.0	T#0ms	True	True	False	False		Total step activation time
U	Time	2594.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2598.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2602.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2606.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2610.0	false	True	True	False	False		System-internal
LP	Bool	2610.1	false	True	True	False	False		System-internal
LN	Bool	2610.2	false	True	True	False	False		System-internal
VP	Bool	2610.3	false	True	True	False	False		System-internal
VN	Bool	2610.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	2611.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	2612.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	2614.0	16#0	True	True	False	False		Reserve
Step40	GraphStep	2618.0		True	True	False	True		
S1	Bool	2618.0	false	True	True	False	False		Step is activated
L1	Bool	2618.1	false	True	True	False	False		interlock leaving state
V1	Bool	2618.2	false	True	True	False	False		Supervision entering state
R1	Bool	2618.3	false	True	True	False	False		Reserved
A1	Bool	2618.4	false	True	True	False	False		Error is acknowledged
S0	Bool	2618.5	false	True	True	False	False		Step is deactivated
L0	Bool	2618.6	false	True	True	False	False		Interlock entering state
V0	Bool	2618.7	false	True	True	False	False		Supervision leaving state
X	Bool	2619.0	false	True	True	False	False		Step is active
LA	Bool	2619.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	2619.2	false	True	True	False	False		Supervision active
RA	Bool	2619.3	false	True	True	False	False		Reserved
AA	Bool	2619.4	false	True	True	False	False		Reserved
SS	Bool	2619.5	false	True	True	False	False		System-internal
LS	Bool	2619.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2619.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2620.0	40	True	True	False	False		User step number
T	Time	2622.0	T#0ms	True	True	False	False		Total step activation time
U	Time	2626.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2630.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2634.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2638.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2642.0	false	True	True	False	False		System-internal
LP	Bool	2642.1	false	True	True	False	False		System-internal
LN	Bool	2642.2	false	True	True	False	False		System-internal

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
VP	Bool	2642.3	false	True	True	False	False		System-internal
VN	Bool	2642.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	2643.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	2644.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	2646.0	16#0	True	True	False	False		Reserve
Step41	GraphStep	2650.0		False	False	False	True		Step structure
S1	Bool	2650.0	false	False	False	False	False		Step is activated
L1	Bool	2650.1	false	False	False	False	False		interlock leaving state
V1	Bool	2650.2	false	False	False	False	False		Supervision entering state
R1	Bool	2650.3	false	False	False	False	False		Reserved
A1	Bool	2650.4	false	False	False	False	False		Error is acknowledged
S0	Bool	2650.5	false	False	False	False	False		Step is deactivated
L0	Bool	2650.6	false	False	False	False	False		Interlock entering state
V0	Bool	2650.7	false	False	False	False	False		Supervision leaving state
X	Bool	2651.0	false	False	False	False	False		Step is active
LA	Bool	2651.1	false	False	False	False	False		Interlock is not satisfied
VA	Bool	2651.2	false	False	False	False	False		Supervision active
RA	Bool	2651.3	false	False	False	False	False		Reserved
AA	Bool	2651.4	false	False	False	False	False		Reserved
SS	Bool	2651.5	false	False	False	False	False		System-internal
LS	Bool	2651.6	True	False	False	False	False		Direct result of the programmed interlock
VS	Bool	2651.7	false	False	False	False	False		Direct result of the programmed supervision
SNO	Int	2652.0	41	False	False	False	False		User step number
T	Time	2654.0	T#0ms	False	False	False	False		Total step activation time
U	Time	2658.0	T#0ms	False	False	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2662.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2666.0	16#0	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2670.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2674.0	false	False	False	False	False		System-internal
LP	Bool	2674.1	false	False	False	False	False		System-internal
LN	Bool	2674.2	false	False	False	False	False		System-internal
VP	Bool	2674.3	false	False	False	False	False		System-internal
VN	Bool	2674.4	false	False	False	False	False		System-internal
H_IL_ERR	Byte	2675.0	16#0	False	False	False	False		System-internal
H_SV_FLT	Byte	2676.0	16#0	False	False	False	False		System-internal
RESERVED	DWord	2678.0	16#0	False	False	False	False		Reserve
Step42	GraphStep	2682.0		True	True	False	True		
S1	Bool	2682.0	false	True	True	False	False		Step is activated
L1	Bool	2682.1	false	True	True	False	False		interlock leaving state
V1	Bool	2682.2	false	True	True	False	False		Supervision entering state
R1	Bool	2682.3	false	True	True	False	False		Reserved
A1	Bool	2682.4	false	True	True	False	False		Error is acknowledged
S0	Bool	2682.5	false	True	True	False	False		Step is deactivated
L0	Bool	2682.6	false	True	True	False	False		Interlock entering state
V0	Bool	2682.7	false	True	True	False	False		Supervision leaving state
X	Bool	2683.0	false	True	True	False	False		Step is active
LA	Bool	2683.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	2683.2	false	True	True	False	False		Supervision active
RA	Bool	2683.3	false	True	True	False	False		Reserved
AA	Bool	2683.4	false	True	True	False	False		Reserved
SS	Bool	2683.5	false	True	True	False	False		System-internal
LS	Bool	2683.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2683.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2684.0	42	True	True	False	False		User step number
T	Time	2686.0	T#0ms	True	True	False	False		Total step activation time
U	Time	2690.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2694.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2698.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_SUP	DWord	2702.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2706.0	false	True	True	False	False		System-internal
LP	Bool	2706.1	false	True	True	False	False		System-internal
LN	Bool	2706.2	false	True	True	False	False		System-internal
VP	Bool	2706.3	false	True	True	False	False		System-internal
VN	Bool	2706.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	2707.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	2708.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	2710.0	16#0	True	True	False	False		Reserve
▼ Step43	GraphStep	2714.0		True	True	False	True		
S1	Bool	2714.0	false	True	True	False	False		Step is activated
L1	Bool	2714.1	false	True	True	False	False		interlock leaving state
V1	Bool	2714.2	false	True	True	False	False		Supervision entering state
R1	Bool	2714.3	false	True	True	False	False		Reserved
A1	Bool	2714.4	false	True	True	False	False		Error is acknowledged
S0	Bool	2714.5	false	True	True	False	False		Step is deactivated
L0	Bool	2714.6	false	True	True	False	False		Interlock entering state
V0	Bool	2714.7	false	True	True	False	False		Supervision leaving state
X	Bool	2715.0	false	True	True	False	False		Step is active
LA	Bool	2715.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	2715.2	false	True	True	False	False		Supervision active
RA	Bool	2715.3	false	True	True	False	False		Reserved
AA	Bool	2715.4	false	True	True	False	False		Reserved
SS	Bool	2715.5	false	True	True	False	False		System-internal
LS	Bool	2715.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2715.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2716.0	43	True	True	False	False		User step number
T	Time	2718.0	T#0ms	True	True	False	False		Total step activation time
U	Time	2722.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2726.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2730.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2734.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2738.0	false	True	True	False	False		System-internal
LP	Bool	2738.1	false	True	True	False	False		System-internal
LN	Bool	2738.2	false	True	True	False	False		System-internal
VP	Bool	2738.3	false	True	True	False	False		System-internal
VN	Bool	2738.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	2739.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	2740.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	2742.0	16#0	True	True	False	False		Reserve
▼ Step44	GraphStep	2746.0		True	True	False	True		
S1	Bool	2746.0	false	True	True	False	False		Step is activated
L1	Bool	2746.1	false	True	True	False	False		interlock leaving state
V1	Bool	2746.2	false	True	True	False	False		Supervision entering state
R1	Bool	2746.3	false	True	True	False	False		Reserved
A1	Bool	2746.4	false	True	True	False	False		Error is acknowledged
S0	Bool	2746.5	false	True	True	False	False		Step is deactivated
L0	Bool	2746.6	false	True	True	False	False		Interlock entering state
V0	Bool	2746.7	false	True	True	False	False		Supervision leaving state
X	Bool	2747.0	false	True	True	False	False		Step is active
LA	Bool	2747.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	2747.2	false	True	True	False	False		Supervision active
RA	Bool	2747.3	false	True	True	False	False		Reserved
AA	Bool	2747.4	false	True	True	False	False		Reserved
SS	Bool	2747.5	false	True	True	False	False		System-internal
LS	Bool	2747.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2747.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2748.0	44	True	True	False	False		User step number
T	Time	2750.0	T#0ms	True	True	False	False		Total step activation time
U	Time	2754.0	T#0ms	True	True	False	False		Step activation time without disturbance

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_LOC	DWord	2758.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2762.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2766.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2770.0	false	True	True	False	False		System-internal
LP	Bool	2770.1	false	True	True	False	False		System-internal
LN	Bool	2770.2	false	True	True	False	False		System-internal
VP	Bool	2770.3	false	True	True	False	False		System-internal
VN	Bool	2770.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	2771.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	2772.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	2774.0	16#0	True	True	False	False		Reserve
Step45	GraphStep	2778.0		True	True	False	True		
S1	Bool	2778.0	false	True	True	False	False		Step is activated
L1	Bool	2778.1	false	True	True	False	False		interlock leaving state
V1	Bool	2778.2	false	True	True	False	False		Supervision entering state
R1	Bool	2778.3	false	True	True	False	False		Reserved
A1	Bool	2778.4	false	True	True	False	False		Error is acknowledged
S0	Bool	2778.5	false	True	True	False	False		Step is deactivated
L0	Bool	2778.6	false	True	True	False	False		Interlock entering state
V0	Bool	2778.7	false	True	True	False	False		Supervision leaving state
X	Bool	2779.0	false	True	True	False	False		Step is active
LA	Bool	2779.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	2779.2	false	True	True	False	False		Supervision active
RA	Bool	2779.3	false	True	True	False	False		Reserved
AA	Bool	2779.4	false	True	True	False	False		Reserved
SS	Bool	2779.5	false	True	True	False	False		System-internal
LS	Bool	2779.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2779.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2780.0	45	True	True	False	False		User step number
T	Time	2782.0	T#0ms	True	True	False	False		Total step activation time
U	Time	2786.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2790.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2794.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2798.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2802.0	false	True	True	False	False		System-internal
LP	Bool	2802.1	false	True	True	False	False		System-internal
LN	Bool	2802.2	false	True	True	False	False		System-internal
VP	Bool	2802.3	false	True	True	False	False		System-internal
VN	Bool	2802.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	2803.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	2804.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	2806.0	16#0	True	True	False	False		Reserve
Step46	GraphStep	2810.0		True	True	False	True		
S1	Bool	2810.0	false	True	True	False	False		Step is activated
L1	Bool	2810.1	false	True	True	False	False		interlock leaving state
V1	Bool	2810.2	false	True	True	False	False		Supervision entering state
R1	Bool	2810.3	false	True	True	False	False		Reserved
A1	Bool	2810.4	false	True	True	False	False		Error is acknowledged
S0	Bool	2810.5	false	True	True	False	False		Step is deactivated
L0	Bool	2810.6	false	True	True	False	False		Interlock entering state
V0	Bool	2810.7	false	True	True	False	False		Supervision leaving state
X	Bool	2811.0	false	True	True	False	False		Step is active
LA	Bool	2811.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	2811.2	false	True	True	False	False		Supervision active
RA	Bool	2811.3	false	True	True	False	False		Reserved
AA	Bool	2811.4	false	True	True	False	False		Reserved
SS	Bool	2811.5	false	True	True	False	False		System-internal
LS	Bool	2811.6	true	True	True	False	False		Direct result of the programmed interlock

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
VS	Bool	2811.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2812.0	46	True	True	False	False		User step number
T	Time	2814.0	T#0ms	True	True	False	False		Total step activation time
U	Time	2818.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2822.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2826.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2830.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2834.0	false	True	True	False	False		System-internal
LP	Bool	2834.1	false	True	True	False	False		System-internal
LN	Bool	2834.2	false	True	True	False	False		System-internal
VP	Bool	2834.3	false	True	True	False	False		System-internal
VN	Bool	2834.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	2835.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	2836.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	2838.0	16#0	True	True	False	False		Reserve
Step47	GraphStep	2842.0		True	True	False	True		
S1	Bool	2842.0	false	True	True	False	False		Step is activated
L1	Bool	2842.1	false	True	True	False	False		interlock leaving state
V1	Bool	2842.2	false	True	True	False	False		Supervision entering state
R1	Bool	2842.3	false	True	True	False	False		Reserved
A1	Bool	2842.4	false	True	True	False	False		Error is acknowledged
S0	Bool	2842.5	false	True	True	False	False		Step is deactivated
L0	Bool	2842.6	false	True	True	False	False		Interlock entering state
V0	Bool	2842.7	false	True	True	False	False		Supervision leaving state
X	Bool	2843.0	false	True	True	False	False		Step is active
LA	Bool	2843.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	2843.2	false	True	True	False	False		Supervision active
RA	Bool	2843.3	false	True	True	False	False		Reserved
AA	Bool	2843.4	false	True	True	False	False		Reserved
SS	Bool	2843.5	false	True	True	False	False		System-internal
LS	Bool	2843.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2843.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2844.0	47	True	True	False	False		User step number
T	Time	2846.0	T#0ms	True	True	False	False		Total step activation time
U	Time	2850.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2854.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2858.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2862.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2866.0	false	True	True	False	False		System-internal
LP	Bool	2866.1	false	True	True	False	False		System-internal
LN	Bool	2866.2	false	True	True	False	False		System-internal
VP	Bool	2866.3	false	True	True	False	False		System-internal
VN	Bool	2866.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	2867.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	2868.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	2870.0	16#0	True	True	False	False		Reserve
Step48	GraphStep	2874.0		True	True	False	True		
S1	Bool	2874.0	false	True	True	False	False		Step is activated
L1	Bool	2874.1	false	True	True	False	False		interlock leaving state
V1	Bool	2874.2	false	True	True	False	False		Supervision entering state
R1	Bool	2874.3	false	True	True	False	False		Reserved
A1	Bool	2874.4	false	True	True	False	False		Error is acknowledged
S0	Bool	2874.5	false	True	True	False	False		Step is deactivated
L0	Bool	2874.6	false	True	True	False	False		Interlock entering state
V0	Bool	2874.7	false	True	True	False	False		Supervision leaving state
X	Bool	2875.0	false	True	True	False	False		Step is active
LA	Bool	2875.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	2875.2	false	True	True	False	False		Supervision active

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
RA	Bool	2875.3	false	True	True	False	False		Reserved
AA	Bool	2875.4	false	True	True	False	False		Reserved
SS	Bool	2875.5	false	True	True	False	False		System-internal
LS	Bool	2875.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2875.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2876.0	48	True	True	False	False		User step number
T	Time	2878.0	T#0ms	True	True	False	False		Total step activation time
U	Time	2882.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2886.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2890.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2894.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2898.0	false	True	True	False	False		System-internal
LP	Bool	2898.1	false	True	True	False	False		System-internal
LN	Bool	2898.2	false	True	True	False	False		System-internal
VP	Bool	2898.3	false	True	True	False	False		System-internal
VN	Bool	2898.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	2899.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	2900.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	2902.0	16#0	True	True	False	False		Reserve
▼ Step49	GraphStep	2906.0		False	False	False	True		Step structure
S1	Bool	2906.0	false	False	False	False	False		Step is activated
L1	Bool	2906.1	false	False	False	False	False		interlock leaving state
V1	Bool	2906.2	false	False	False	False	False		Supervision entering state
R1	Bool	2906.3	false	False	False	False	False		Reserved
A1	Bool	2906.4	false	False	False	False	False		Error is acknowledged
S0	Bool	2906.5	false	False	False	False	False		Step is deactivated
L0	Bool	2906.6	false	False	False	False	False		Interlock entering state
V0	Bool	2906.7	false	False	False	False	False		Supervision leaving state
X	Bool	2907.0	false	False	False	False	False		Step is active
LA	Bool	2907.1	false	False	False	False	False		Interlock is not satisfied
VA	Bool	2907.2	false	False	False	False	False		Supervision active
RA	Bool	2907.3	false	False	False	False	False		Reserved
AA	Bool	2907.4	false	False	False	False	False		Reserved
SS	Bool	2907.5	false	False	False	False	False		System-internal
LS	Bool	2907.6	True	False	False	False	False		Direct result of the programmed interlock
VS	Bool	2907.7	false	False	False	False	False		Direct result of the programmed supervision
SNO	Int	2908.0	49	False	False	False	False		User step number
T	Time	2910.0	T#0ms	False	False	False	False		Total step activation time
U	Time	2914.0	T#0ms	False	False	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2918.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2922.0	16#0	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2926.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2930.0	false	False	False	False	False		System-internal
LP	Bool	2930.1	false	False	False	False	False		System-internal
LN	Bool	2930.2	false	False	False	False	False		System-internal
VP	Bool	2930.3	false	False	False	False	False		System-internal
VN	Bool	2930.4	false	False	False	False	False		System-internal
H_IL_ERR	Byte	2931.0	16#0	False	False	False	False		System-internal
H_SV_FLT	Byte	2932.0	16#0	False	False	False	False		System-internal
RESERVED	DWord	2934.0	16#0	False	False	False	False		Reserve
▼ Step50	GraphStep	2938.0		False	False	False	True		Step structure
S1	Bool	2938.0	false	False	False	False	False		Step is activated
L1	Bool	2938.1	false	False	False	False	False		interlock leaving state
V1	Bool	2938.2	false	False	False	False	False		Supervision entering state
R1	Bool	2938.3	false	False	False	False	False		Reserved
A1	Bool	2938.4	false	False	False	False	False		Error is acknowledged
S0	Bool	2938.5	false	False	False	False	False		Step is deactivated

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
L0	Bool	2938.6	false	False	False	False	False		Interlock entering state
V0	Bool	2938.7	false	False	False	False	False		Supervision leaving state
X	Bool	2939.0	false	False	False	False	False		Step is active
LA	Bool	2939.1	false	False	False	False	False		Interlock is not satisfied
VA	Bool	2939.2	false	False	False	False	False		Supervision active
RA	Bool	2939.3	false	False	False	False	False		Reserved
AA	Bool	2939.4	false	False	False	False	False		Reserved
SS	Bool	2939.5	false	False	False	False	False		System-internal
LS	Bool	2939.6	True	False	False	False	False		Direct result of the programmed interlock
VS	Bool	2939.7	false	False	False	False	False		Direct result of the programmed supervision
SNO	Int	2940.0	50	False	False	False	False		User step number
T	Time	2942.0	T#0ms	False	False	False	False		Total step activation time
U	Time	2946.0	T#0ms	False	False	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2950.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2954.0	16#0	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2958.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2962.0	false	False	False	False	False		System-internal
LP	Bool	2962.1	false	False	False	False	False		System-internal
LN	Bool	2962.2	false	False	False	False	False		System-internal
VP	Bool	2962.3	false	False	False	False	False		System-internal
VN	Bool	2962.4	false	False	False	False	False		System-internal
H_IL_ERR	Byte	2963.0	16#0	False	False	False	False		System-internal
H_SV_FLT	Byte	2964.0	16#0	False	False	False	False		System-internal
RESERVED	DWord	2966.0	16#0	False	False	False	False		Reserve
S_DISPLAY	Int	3314.0	0	True	True	False	False		Affichage interne du paramètre de sortie S_NO
S_SEL_OLD	Int	3316.0	0	True	True	False	False		Valeur précédente dans S_SEL
S_DISPIDX	Byte	3318.0	16#0	True	True	False	False		Indice de l'étape affichée dans S_NO
T_DISPIDX	Byte	3319.0	16#0	True	True	False	False		Indice de la transition affichée dans T_NO
▼ MOP	Struct	3324.0		True	True	False	False		Mode de fonctionnement
AUTO	Bool	3324.0	true	True	True	False	False		Etat : Mode automatique
MAN	Bool	3324.1	false	True	True	False	False		Etat : Mode manuel
TAP	Bool	3324.2	false	True	True	False	False		Etat : Mode semi-automatique/Activation de l'étape suivante avec transition
TOP	Bool	3324.3	false	True	True	False	False		Etat : Mode semi-automatique/Ignorer la transition
ACK_S	Bool	3324.4	false	True	True	False	False		Requête : Acquitter l'étape figurant dans le paramètre S_NO
REG_S	Bool	3324.5	false	True	True	False	False		Requête : Enregistrer l'étape figurant dans le paramètre S_NO
T_PREV	Bool	3324.6	false	True	True	False	False		Requête : Afficher la transition valide précédente dans le paramètre T_NO
T_NEXT	Bool	3324.7	false	True	True	False	False		Requête : Afficher la transition valide suivante dans le paramètre T_NO
LOCK	Bool	3325.0	true	True	True	False	False		Etat : Traitement des Interlocks activé
SUP	Bool	3325.1	true	True	True	False	False		Etat : Traitement de Supervision activé
ACKREQ	Bool	3325.2	true	True	True	False	False		Etat : Acquittement requis
SSKIP	Bool	3325.3	false	True	True	False	False		Etat : "Ignorer les étapes" activé
OFF	Bool	3325.4	false	True	True	False	False		Requête : Désactiver toutes les étapes
INIT	Bool	3325.5	true	True	True	False	False		Requête : Mettre le graphe à l'état initial
HALT	Bool	3325.6	false	True	True	False	False		Etat : Le graphe est arrêté
TMS_HALT	Bool	3325.7	false	True	True	False	False		Etat : Toutes les temporisations internes sont arrêtées
OPS_ZERO	Bool	3326.0	false	True	True	False	False		Etat : Tous les opérandes traités avec N, L, D mis à 0
SACT_DISP	Bool	3326.1	true	True	True	False	False		Etat : N'afficher que les étapes actives
SEF_DISP	Bool	3326.2	false	True	True	False	False		Etat : Afficher uniquement les étapes avec erreurs et perturbations

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
SALL_DISP	Bool	3326.3	false	True	True	False	False		Etat : Afficher toutes les étapes
S_PREV	Bool	3326.4	false	True	True	False	False		Requête : Afficher l'étape précédente dans le paramètre S_NO
S_NEXT	Bool	3326.5	false	True	True	False	False		Requête : Afficher l'étape suivante dans le paramètre S_NO
S_SELOK	Bool	3326.6	false	True	True	False	False		Requête : Afficher dans le paramètre S_NO le numéro d'étape figurant dans S_SEL
S_ON	Bool	3326.7	false	True	True	False	False		Requête : Activer l'étape figurant dans le paramètre S_NO
S_OFF	Bool	3327.0	false	True	True	False	False		Requête : Désactiver l'étape figurant dans le paramètre S_NO
T_PUSH	Bool	3327.1	false	True	True	False	False		Requête : Validation de commutation pour transition
REG	Bool	3327.2	false	True	True	False	False		Requête : Enregistrer toutes les erreurs Interlock et Supervision
ACK	Bool	3327.3	false	True	True	False	False		Requête : Acquitter toutes les erreurs Interlock et Supervision
IL_PERM	Bool	3327.4	false	True	True	False	False		Etat : Traitement permanent de tous les Interlocks
T_PERM	Bool	3327.5	false	True	True	False	False		Etat : Traitement permanent de toutes les transitions
ILP_MAN	Bool	3327.6	false	True	True	False	False		Etat : Traitement permanent de tous les Interlocks en mode manuel
TICKS	Struct	3328.0		True	True	False	False		Périodes d'horloge
DELTA	Time	3328.0	T#0ms	True	True	False	False		Différence de temps entre deux cycles
OLD	Time	3332.0	T#0ms	True	True	False	False		Valeur du compteur de périodes 10 ms au dernier cycle
NEW	Time	3336.0	T#0ms	True	True	False	False		Valeur du compteur de périodes 10 ms dans ce cycle
SQ_FLAGS	Struct	3340.0		True	True	False	False		Mémentos de graphe
ERR_FLT	Bool	3340.0	false	True	True	False	False		Erreur groupée Interlock et Supervision
ERROR	Bool	3340.1	false	True	True	False	False		Erreur groupée Interlock
FAULT	Bool	3340.2	false	True	True	False	False		Erreur groupée Supervision
RT_FAIL	Bool	3340.3	false	True	True	False	False		Erreur à l'exécution
NO_SNO	Bool	3340.4	false	True	True	False	False		Numéro d'étape requis introuvable
NF_OFL	Bool	3340.5	false	True	True	False	False		Débordement : Trop de requêtes ON ou OFF
SA_OFL	Bool	3340.6	false	True	True	False	False		Débordement : Trop d'étapes activées
TV_OFL	Bool	3340.7	false	True	True	False	False		Débordement : Trop de transitions valides
MSG_OFL	Bool	3341.0	false	True	True	False	False		Débordement : Pas assez de ressources système pour ALARM_S
NO_SWI	Bool	3341.1	false	True	True	False	False		Pas de commutation dans ce cycle
CYC_OP	Bool	3341.2	false	True	True	False	False		Exécution cyclique du graphe après l'initialisation
AS_MSG	Bool	3341.3	true	True	True	False	False		Messages à l'exécution activés ou désactivés par instruction
AS_SEND	Bool	3341.4	false	True	True	False	False		Envoyer les messages de WR_USMSG ou les entrer uniquement dans le tampon de diagnostic
SQ_BUSY	Bool	3341.5	false	True	True	False	False		Mémento de front interne pour le traitement séquentiel
SA_BUSY	Bool	3341.6	false	True	True	False	False		Mémento de front interne pour le traitement séquentiel
AS_SIG	Bool	3341.7	false	True	True	False	False		Mémentos de front pour messages de Alarm_S et Alarm_SQ
Temp									
Constant									

Alarms

Enable alarms	True

Category	Category enabler	Display class
Error		0
Warning		0
Info		0
Category 4		0
Category 5		0

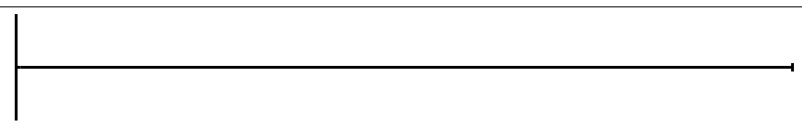
Category	Category enabler	Display class
Category 6		0
Category 7		0
Category 8		0

Category for interlocks	Error	**Subcategory 1 for interlocks**		**Subcategory 2 for interlocks**	
Category for supervisions	Error	**Subcategory 1 for supervisions**		**Subcategory 2 for supervisions**	

Symbol	Address	Type	Comment

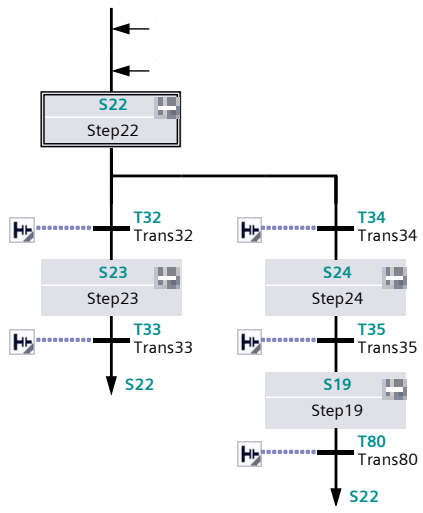
Permanent pre-instructions

1:



Sequences (5)

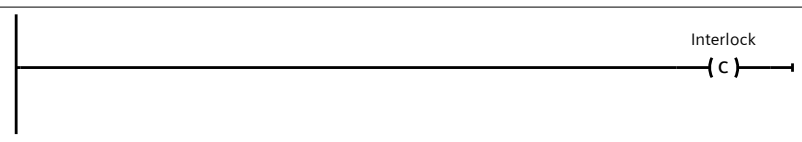
1:



S22 - [Initial step]:Step22

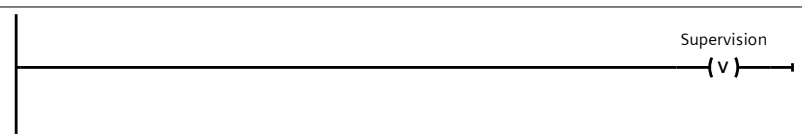
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

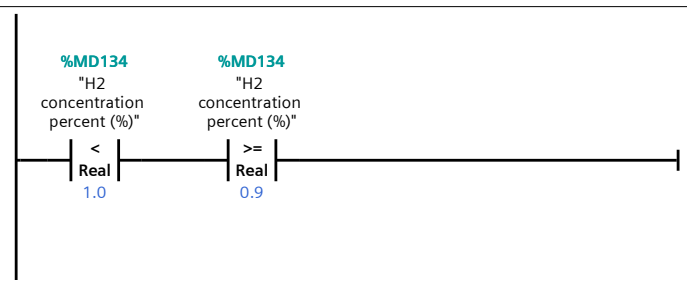
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action

T32:Trans32

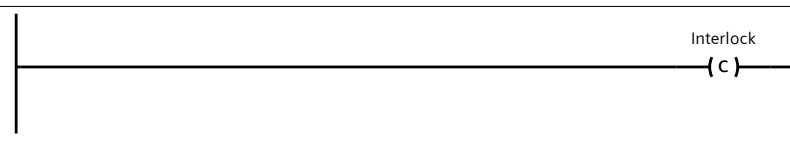


T34:Trans34

S23:Step23

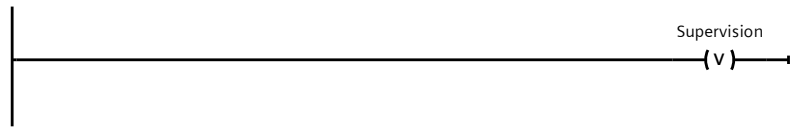
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

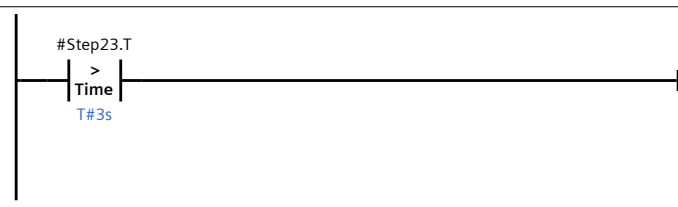
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action
		N	"H2 concentration H"

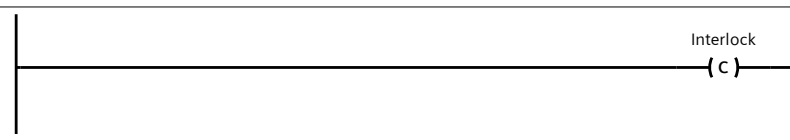
T33:Trans33



S24:Step24

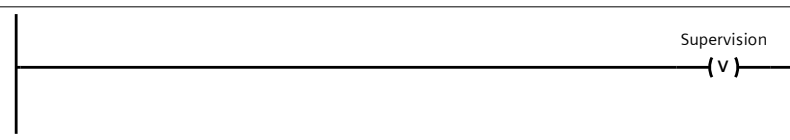
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action
		N	"H2 concentration HH"
		R	"seawater pump intake 1"
		R	"seawater pump intake 2"
		R	"filter 1"
		R	"electrolizer 1"

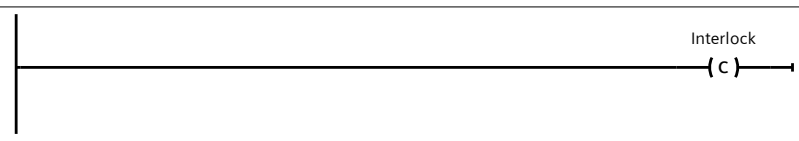
Interlock	Event	Qualifier	Action
		R	"valve to Tank"
		R	"Rectifier"
		R	"filter 2"
		R	"FILTER 1 VALVE"
		R	"FILTER 2 VALVE"
		R	"electrolizer 2"
		R	"hypochlorit production is ok"

T35:Trans35

S19:Step19

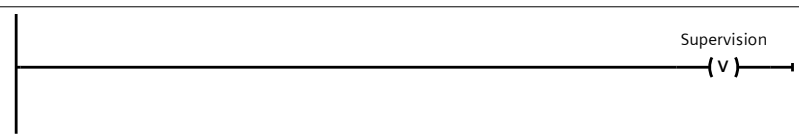
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

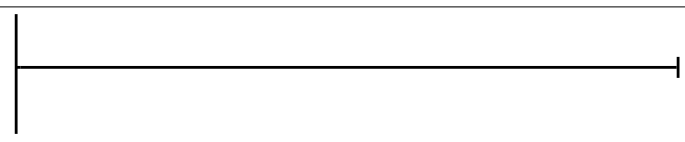
Supervision alarm	
Alarm text	



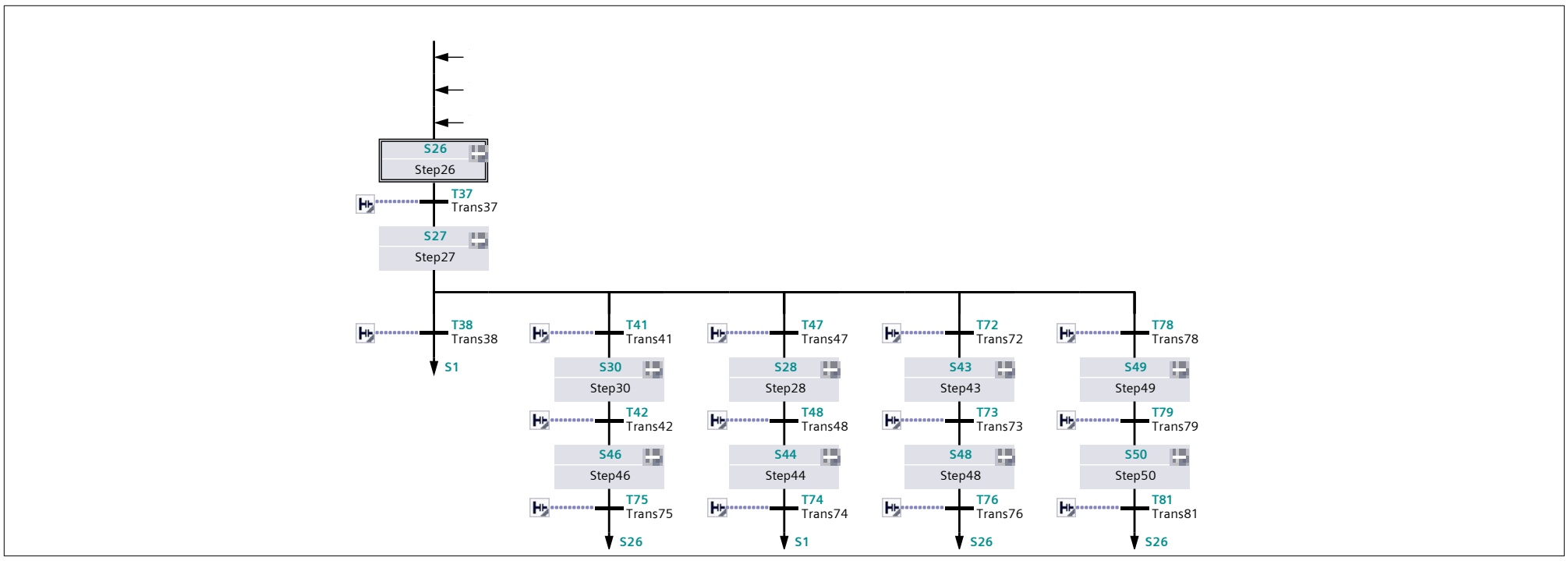
Actions:

Actions:			
Interlock	Event	Qualifier	Action
		R	"H2 blower"
	S1	ON	#Step2

T80:Trans80



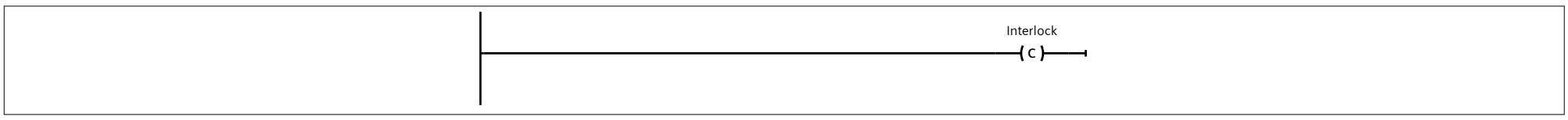
2:



S26 - [Initial step]:Step26

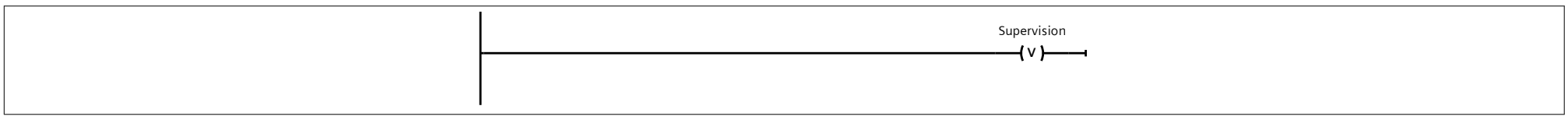
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

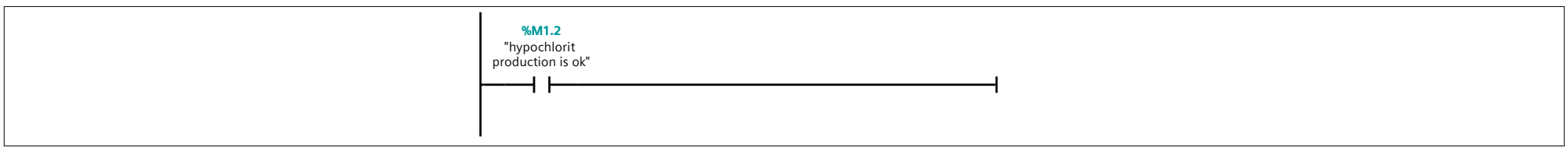
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action

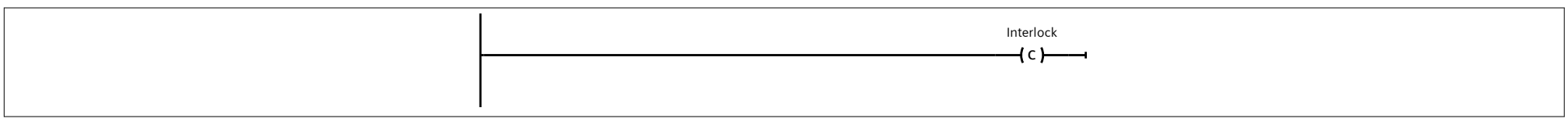
T37:Trans37



S27:Step27

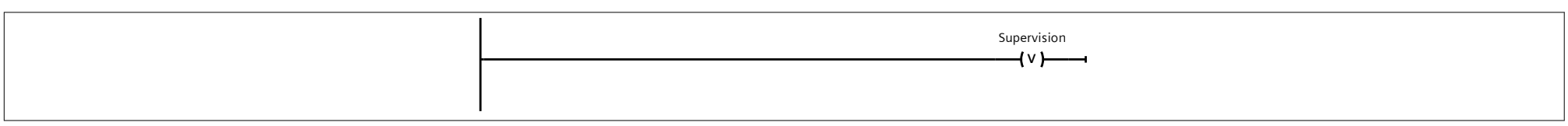
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

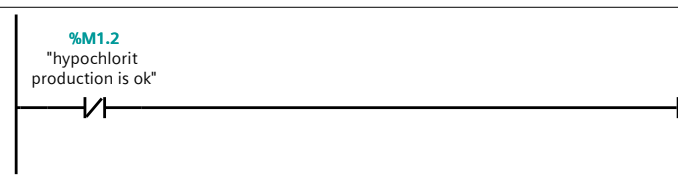
Supervision alarm	
Alarm text	



Actions:

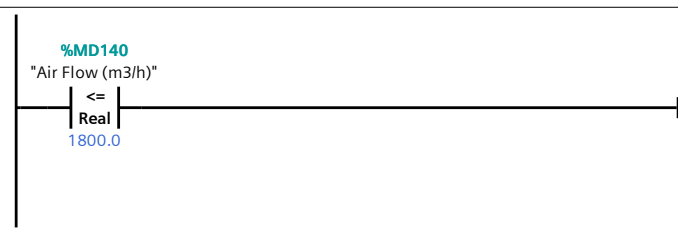
Actions:			
Interlock	Event	Qualifier	Action

T38:Trans38

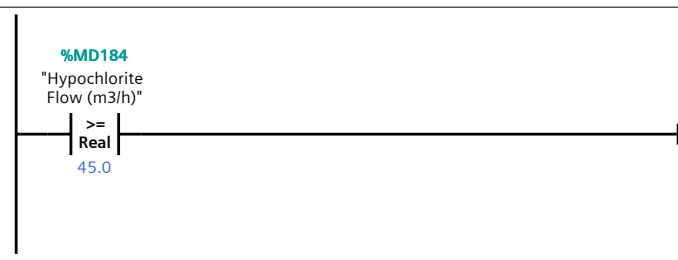


T41:Trans41

T47:Trans47



T72:Trans72

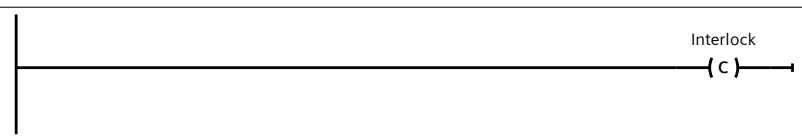


T78:Trans78

S30:Step30

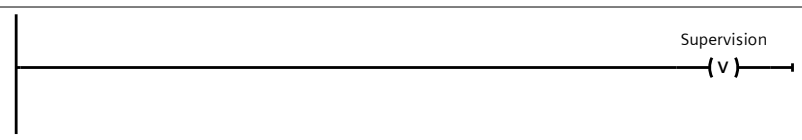
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

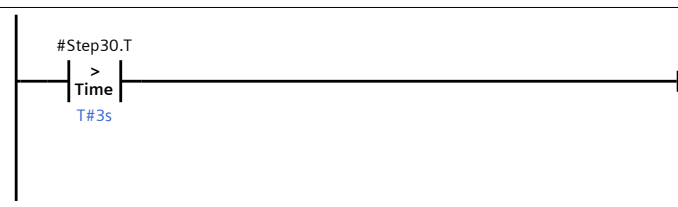
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action
		N	"Hypoclorite temperature HH"

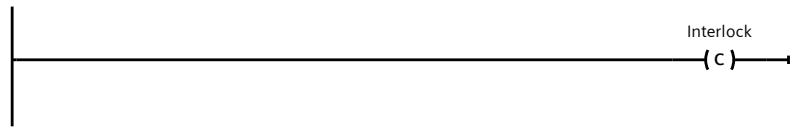
T42:Trans42



S28:Step28

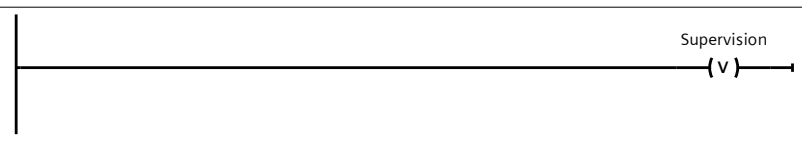
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

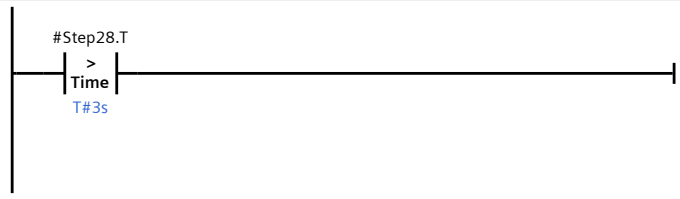
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action
		N	"air flow is not ok"

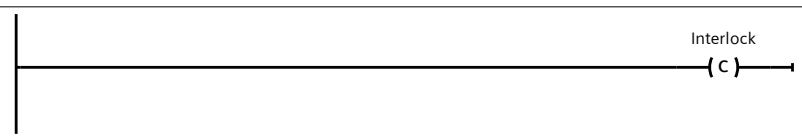
T48:Trans48



S43:Step43

Interlock -(c)-:

Interlock alarm	
Alarm text	



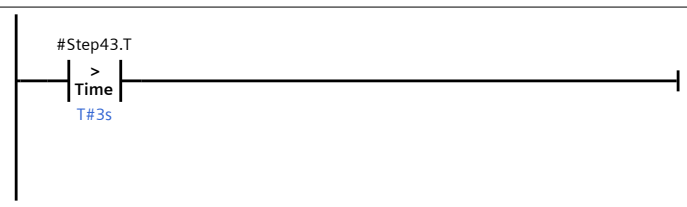
Supervision -(v)-:

Supervision alarm	
Alarm text	

Actions:

Actions:			
Interlock	Event	Qualifier	Action
		N	"Hypochlorite Flow is not ok"

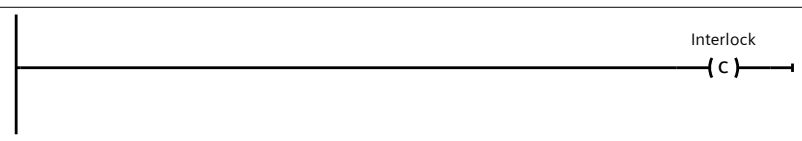
T73:Trans73



S44:Step44

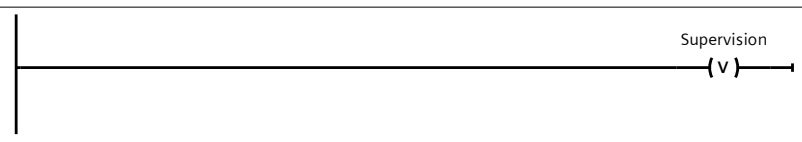
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

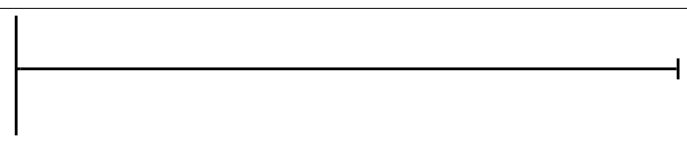
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action
		R	"Air valve"
		R	"electrolizer 1"
		R	"electrolizer 2"
		R	"filter 1"
		R	"filter 2"
		R	"FILTER 1 VALVE"
		R	"Rectifier"
		R	"seawater pump intake 1"
		R	"seawater pump intake 2"
		R	"FILTER 2 VALVE"
		R	"valve to Tank"
	S1	ON	#Step2

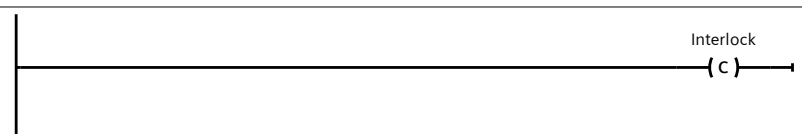
T74:Trans74



S46:Step46

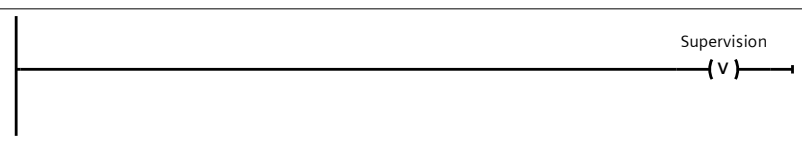
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

Supervision alarm	
Alarm text	

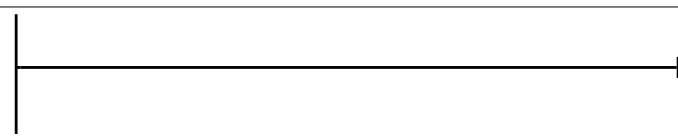


Actions:

Actions:			
Interlock	Event	Qualifier	Action
		R	"Air valve"
		R	"electrolizer 1"
		R	"electrolizer 2"
		R	"filter 1"
		R	"filter 2"
		R	"FILTER 1 VALVE"

Interlock	Event	Qualifier	Action
		R	"Rectifier"
		R	"seawater pump intake 1"
		R	"seawater pump intake 2"
		R	"FILTER 2 VALVE"
		R	"valve to Tank"
	S1	ON	#Step2

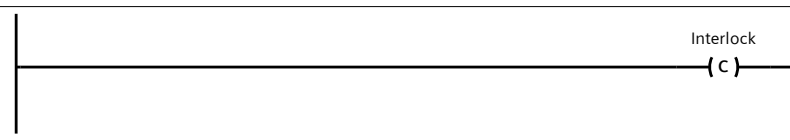
T75:Trans75



S48:Step48

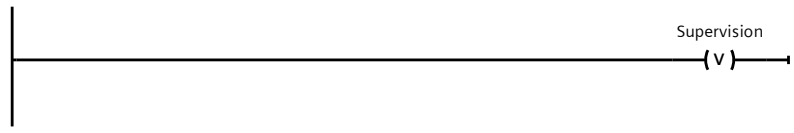
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

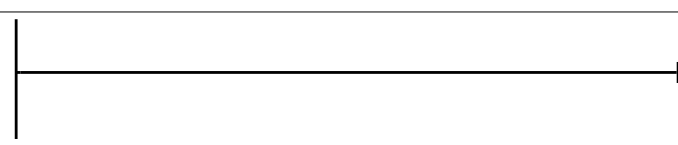
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action
		R	"Air valve"
		R	"electrolizer 1"
		R	"electrolizer 2"
		R	"filter 1"
		R	"filter 2"
		R	"FILTER 1 VALVE"
		R	"Rectifier"
		R	"seawater pump intake 1"
		R	"seawater pump intake 2"
		R	"FILTER 2 VALVE"
		R	"valve to Tank"

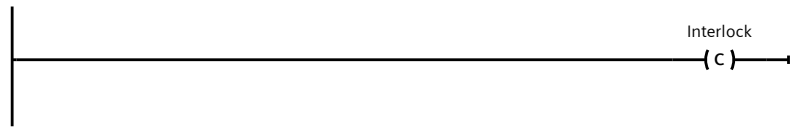
T76:Trans76



S49:Step49

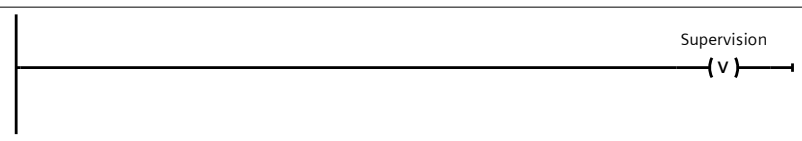
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

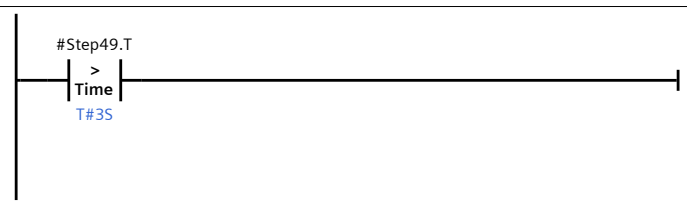
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action
		N	"seawater flow is not ok"

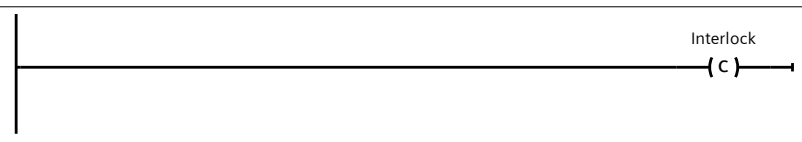
T79:Trans79



S50:Step50

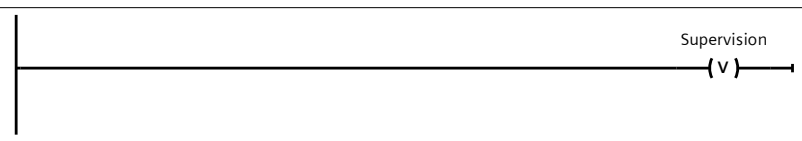
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

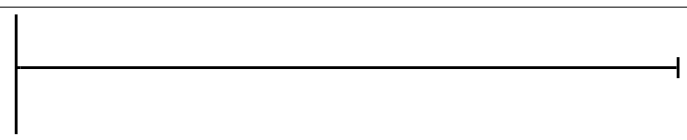
Supervision alarm	
Alarm text	



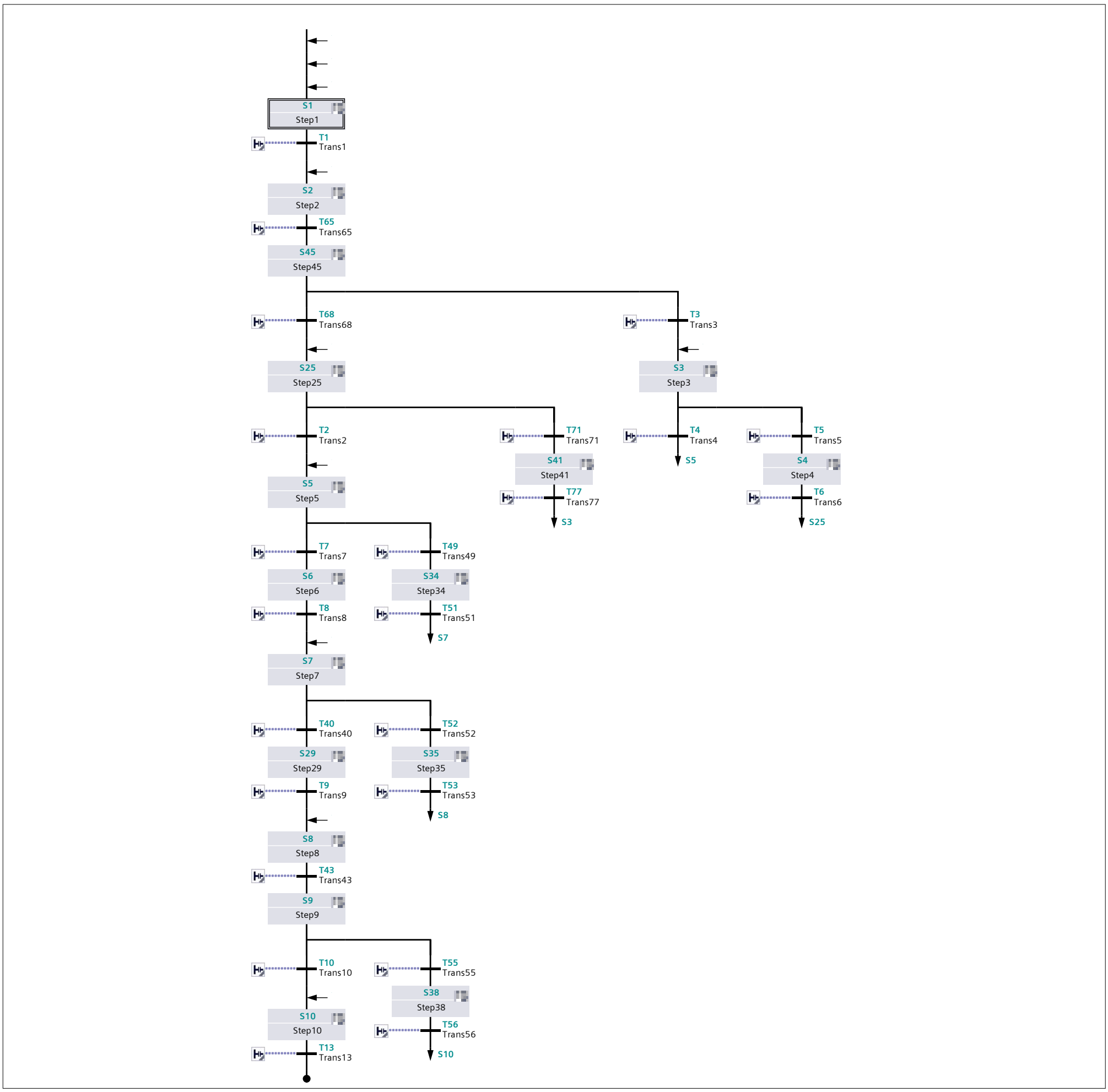
Actions:

Actions:			
Interlock	Event	Qualifier	Action
		R	"Air valve"
		R	"electrolizer 1"
		R	"electrolizer 2"
		R	"filter 1"
		R	"filter 2"
		R	"FILTER 1 VALVE"
		R	"Rectifier"
		R	"seawater pump intake 1"
		R	"seawater pump intake 2"
		R	"FILTER 2 VALVE"
		R	"valve to Tank"

T81:Trans81



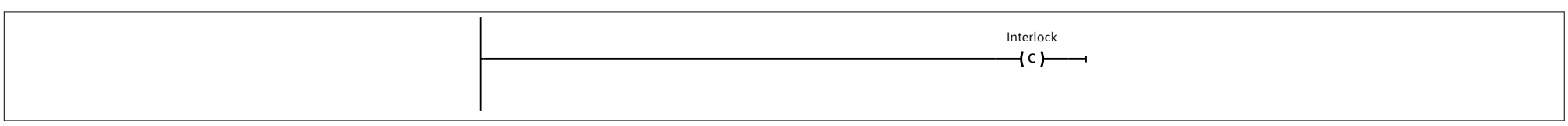
3:



S1 - [Initial step]:Step1

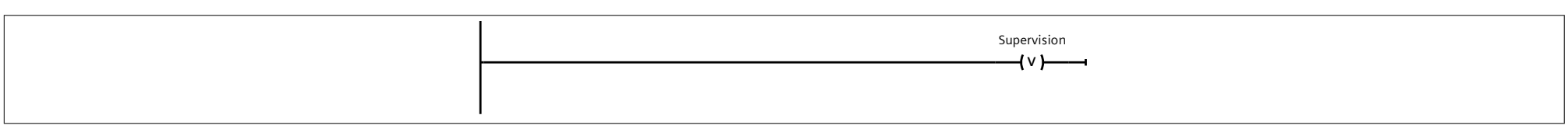
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

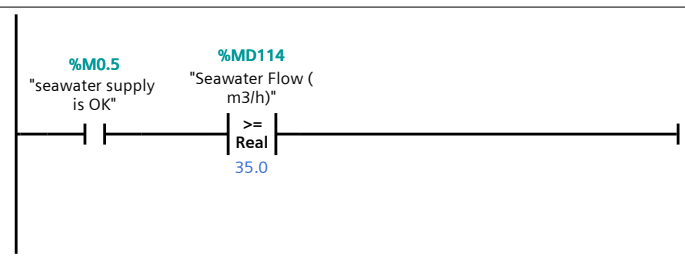
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**
		R	"seawater pump intake 1"
		R	"seawater pump intake 2"
		R	"valve to Tank"
		R	"valve of seawater pump intake 1"
		R	"valve of seawater pump intake 2"
		R	"electrolizer 1"
		R	"filter 1"
		R	"countinous injection pump 1"
		R	"countinous injection pump 2"
		R	"shocked injection pump 1"
		R	"shocked injection pump 2"
		R	"H2 blower"
		R	"Air valve"
		R	"electrolizer 2"
		R	"filter 2"
		R	"Electrolizer 1 Drain valve"
		R	"Electrolizer 2 Drain valve"
		R	"FILTER 1 VALVE"
		R	"FILTER 2 VALVE"
		R	"hypochlorit production is ok"
		R	"Hypochloration system START"
		R	"Rectifier"

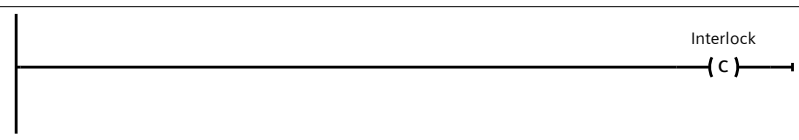
T1:Trans1



S2:Step2

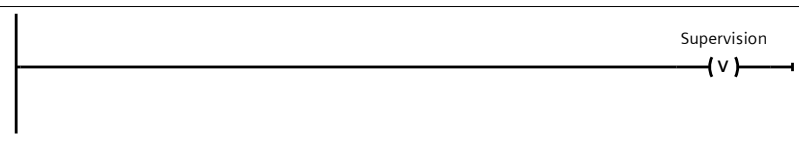
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

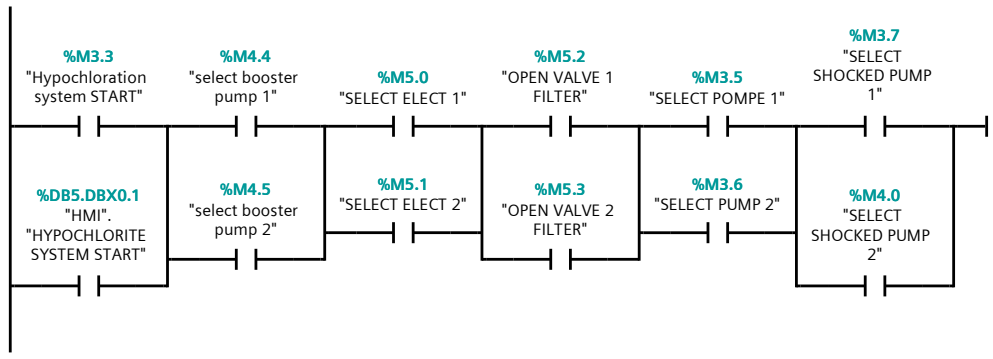
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**
		R	"Hypochloration system START"

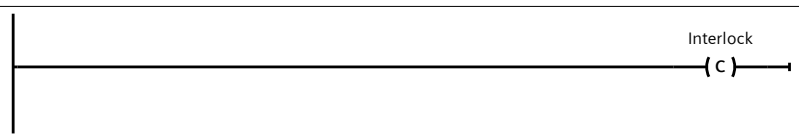
T65:Trans65



S3:Step3

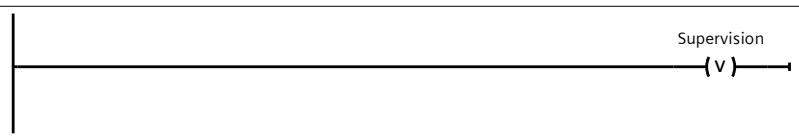
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

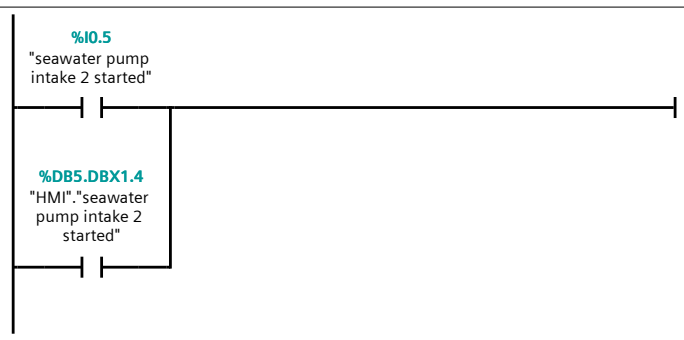
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action
		S	"seawater pump intake 2"
		S	"valve of seawater pump intake 2"
		S	"Air valve"

T4:Trans4

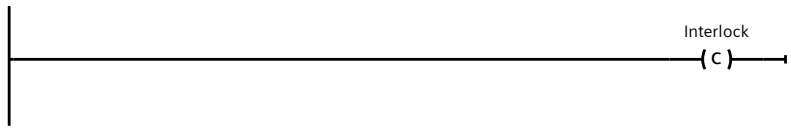


T5:Trans5

S4:Step4

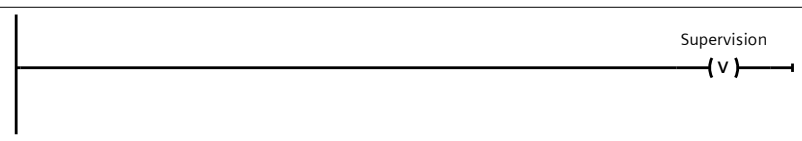
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

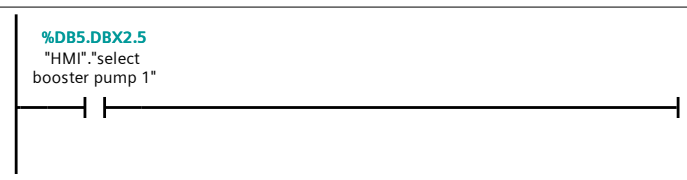
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**
		R	"seawater pump intake 2"
		R	"valve of seawater pump intake 2"
		N	"seawater pump intake 2 Fault"

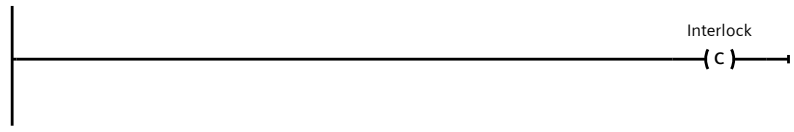
T6:Trans6



S5:Step5

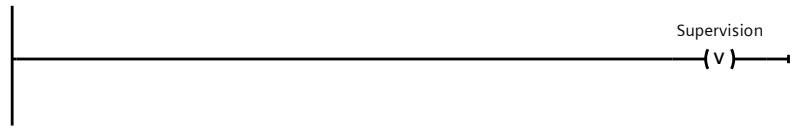
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

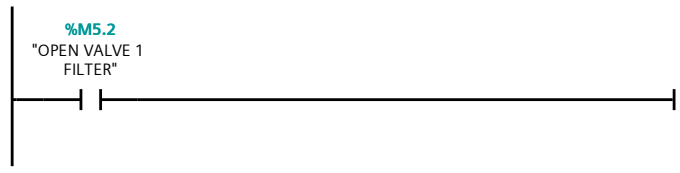
Supervision alarm	
Alarm text	



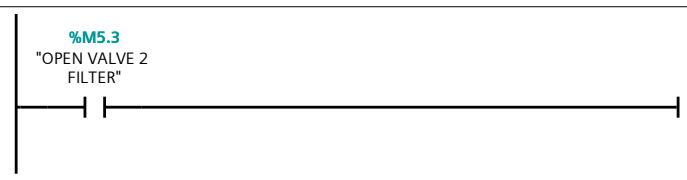
Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**

T7:Trans7



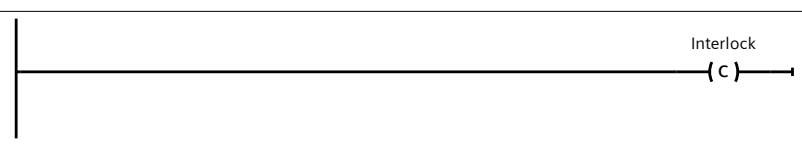
T49:Trans49



S6:Step6

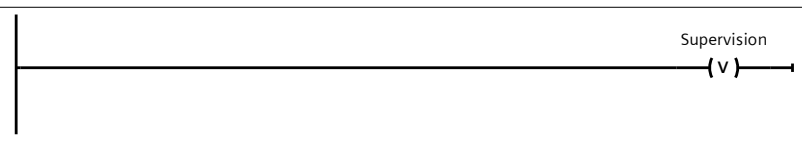
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

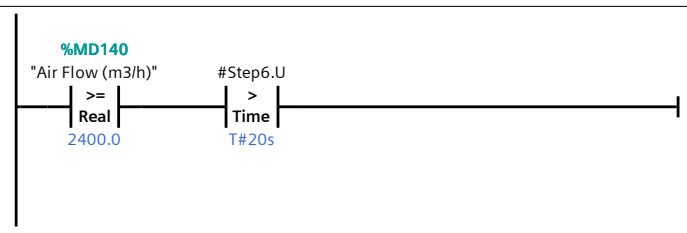
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**
		S	"filter 1"
		S	"FILTER 1 VALVE"

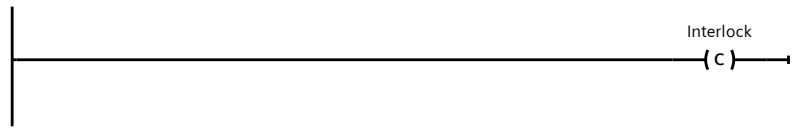
T8:Trans8



S7:Step7

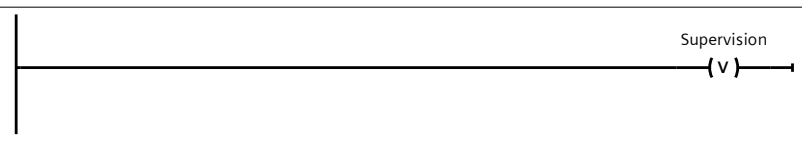
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

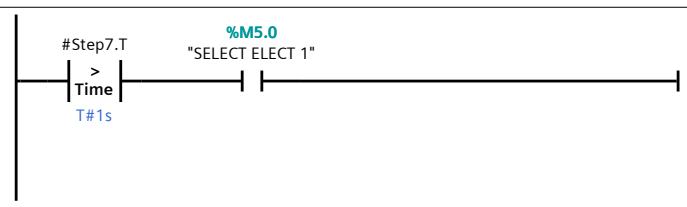
Supervision alarm	
Alarm text	



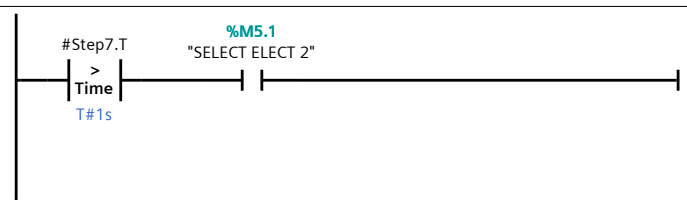
Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**
		S	"Rectifier"
		S	"H2 blower"

T40:Trans40



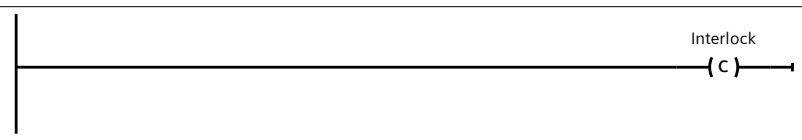
T52:Trans52



S8:Step8

Interlock -(c)-:

Interlock alarm	
Alarm text	



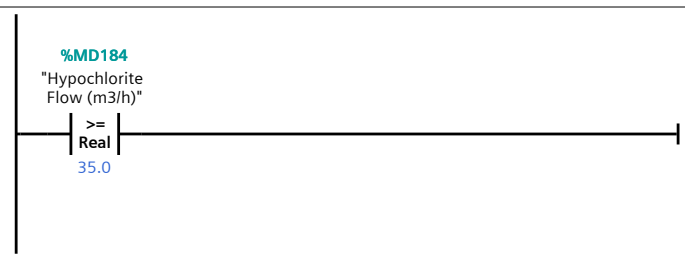
Supervision -(v)-:

Supervision alarm	
Alarm text	

Actions:

Actions:			
Interlock	Event	Qualifier	Action
		S	"desired current"

T43:Trans43



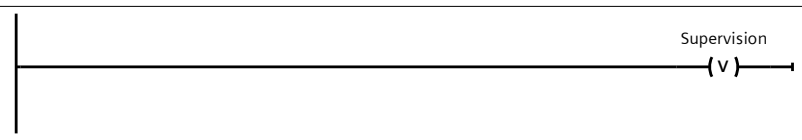
S38:Step38

Interlock -(c)-:

Interlock alarm	
Alarm text	

Supervision -(v)-:

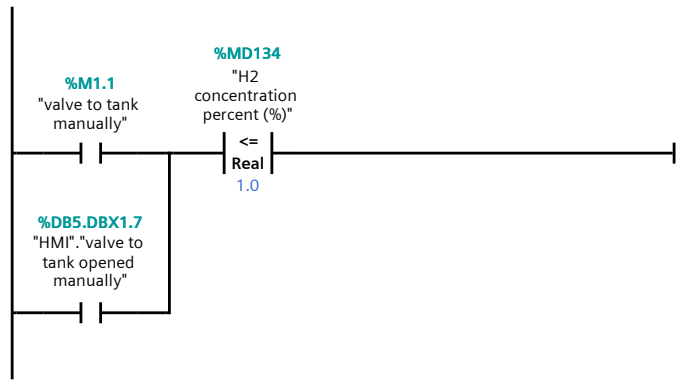
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action
		R	"valve to Tank"
		N	"valve to tank Fault"

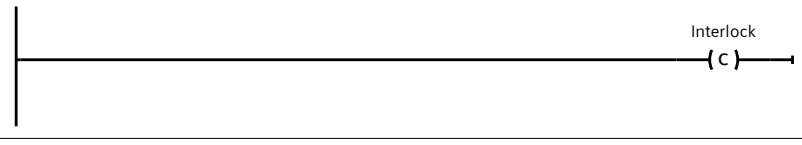
T56:Trans56



S10:Step10

Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

Supervision alarm	
Alarm text	

Actions:

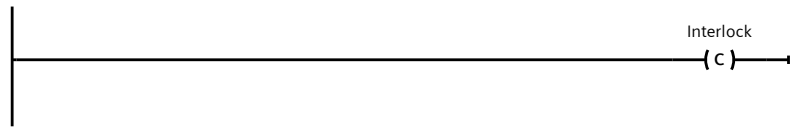
Actions:			
Interlock	Event	Qualifier	Action
		S	"valve to Tank"
		S	"hypochlorit production is ok"

T13:Trans13

S29:Step29

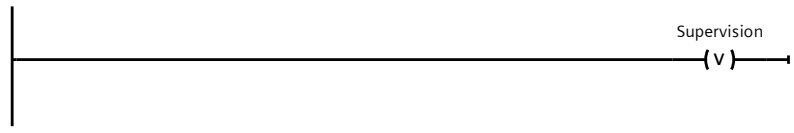
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

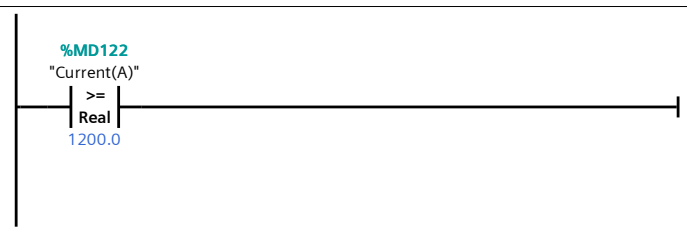
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action
		S	"electrolizer 1"

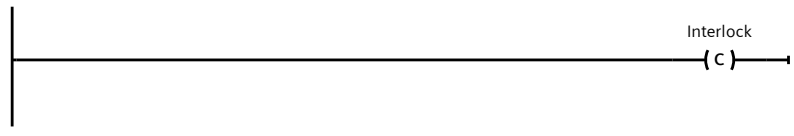
T9:Trans9



S9:Step9

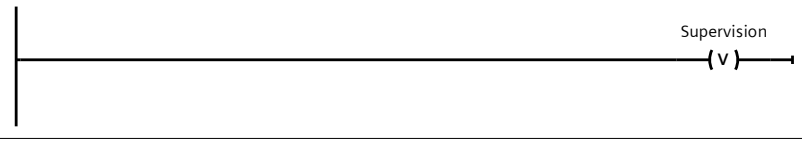
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

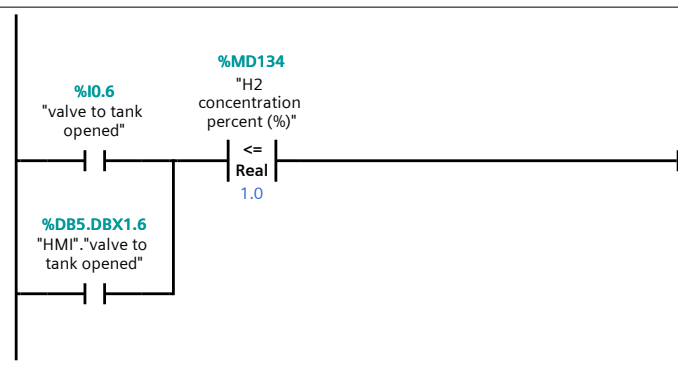
Supervision alarm	
Alarm text	



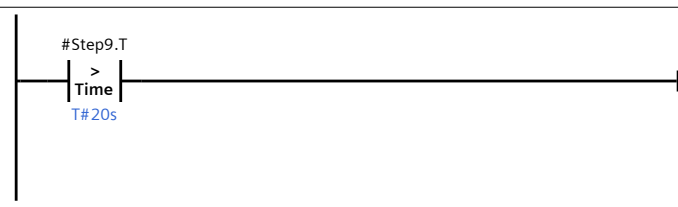
Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**
		S	"valve to Tank"

T10:Trans10



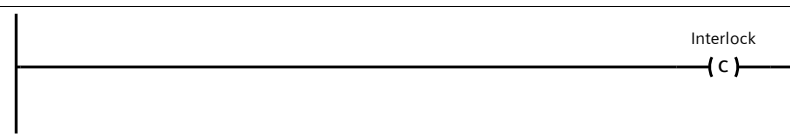
T55:Trans55



S34:Step34

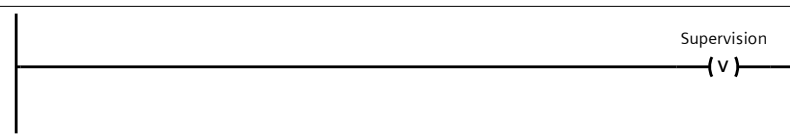
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

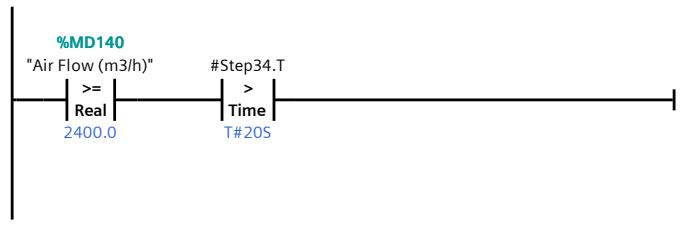
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**
		S	"filter 2"
		S	"FILTER 2 VALVE"

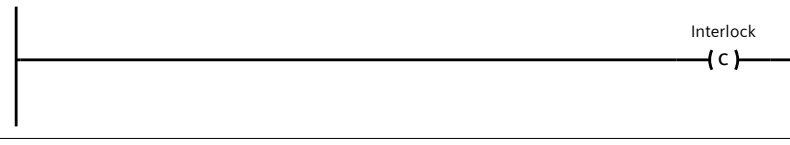
T51:Trans51



S35:Step35

Interlock -(c)-:

Interlock alarm	
Alarm text	



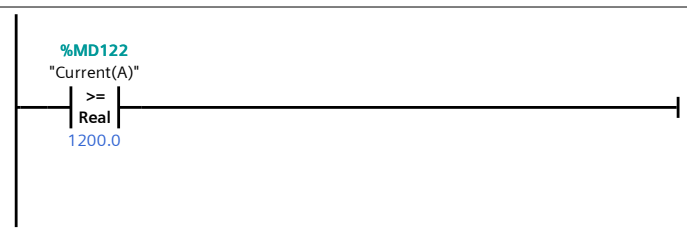
Supervision -(v)-:

Supervision alarm	
Alarm text	

Actions:

Actions:			
Interlock	Event	Qualifier	Action
		S	"electrolizer 2"

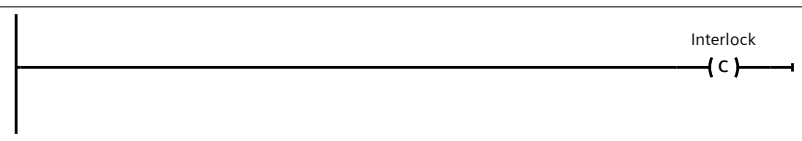
T53:Trans53



S45:Step45

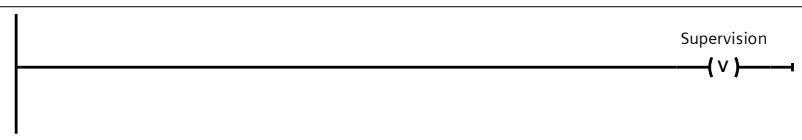
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

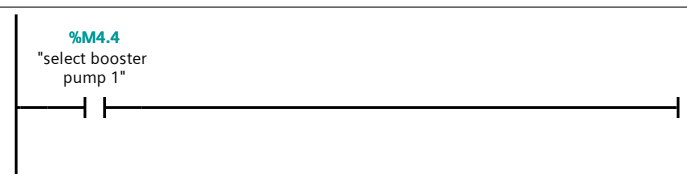
Supervision alarm	
Alarm text	



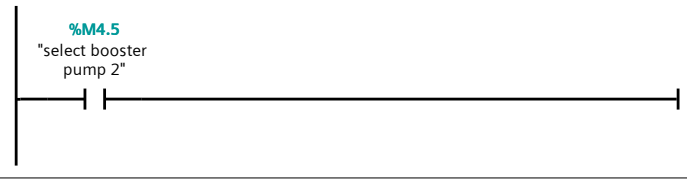
Actions:

Actions:			
Interlock	Event	Qualifier	Action
		S	"Hypochloration system START"

T68:Trans68



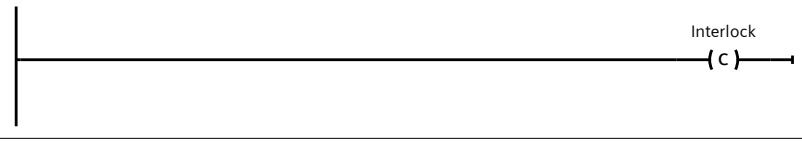
T3:Trans3



S25:Step25

Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

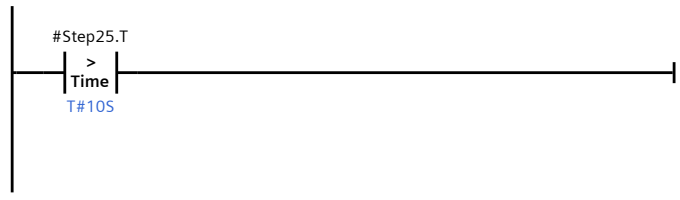
Supervision alarm	
Alarm text	

Actions:

Actions:			
Interlock	Event	Qualifier	Action
		S	"seawater pump intake 1"
		S	"valve of seawater pump intake 1"
		S	"Air valve"

T2:Trans2

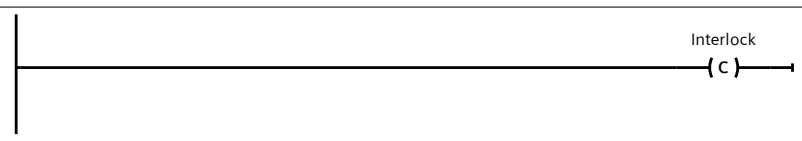
T71:Trans71



S41:Step41

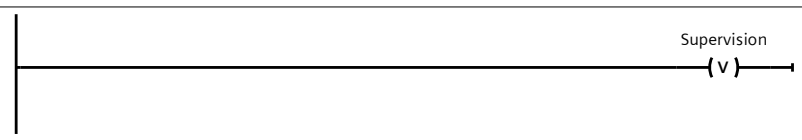
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

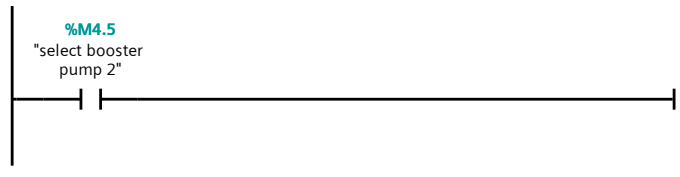
Supervision alarm	
Alarm text	



Actions:

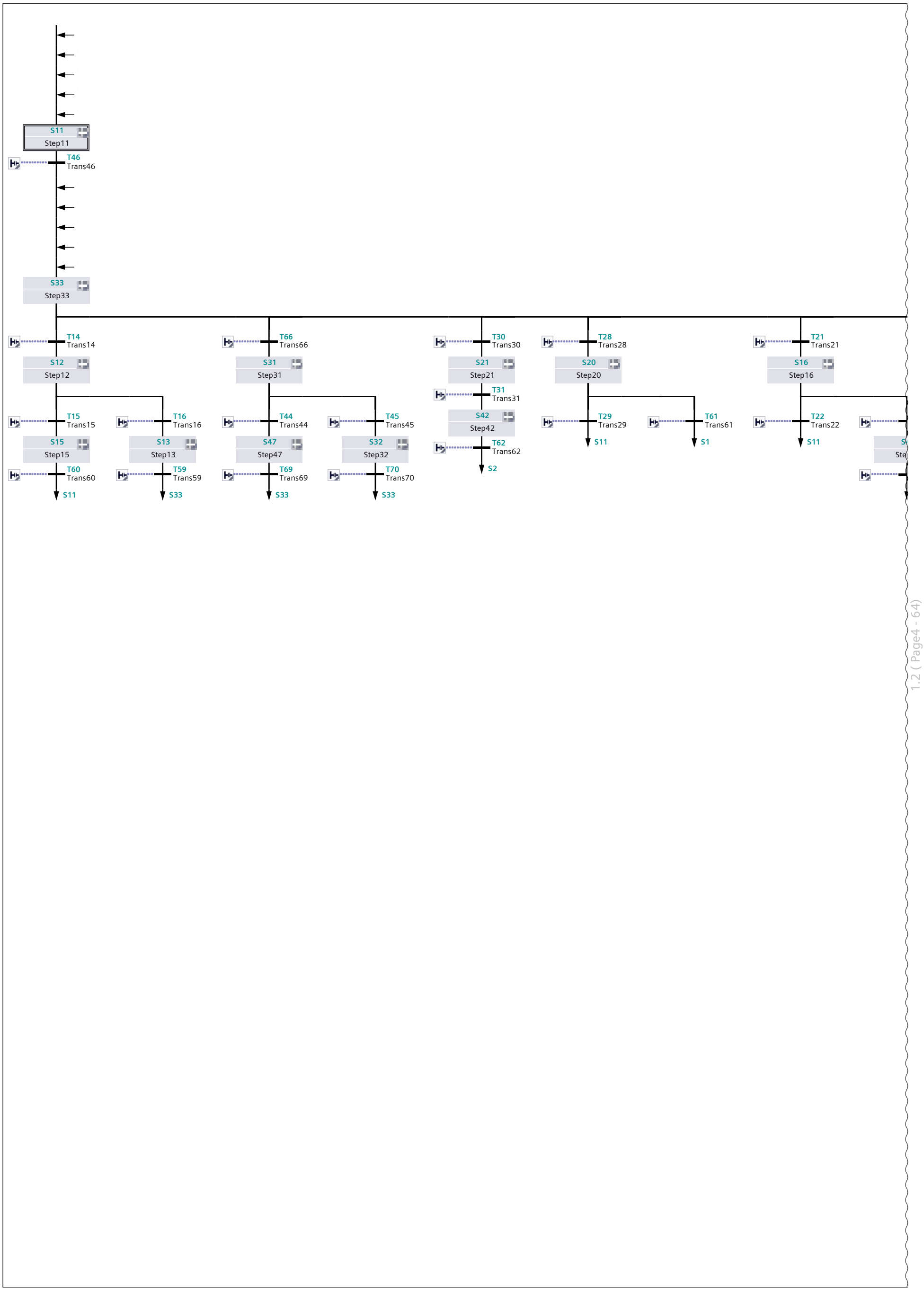
Actions:			
Interlock	Event	Qualifier	Action
		R	"valve of seawater pump intake 1"
		N	"seawater pump intake 1 Fault"
		R	"seawater pump intake 1"

T77:Trans77



4:

4: (1.1 / 1.2)


S11
Step11
T46
Trans46
S33
Step33
T14
Trans14
S12
Step12
T15
Trans15
S15
Step15
T60
Trans60
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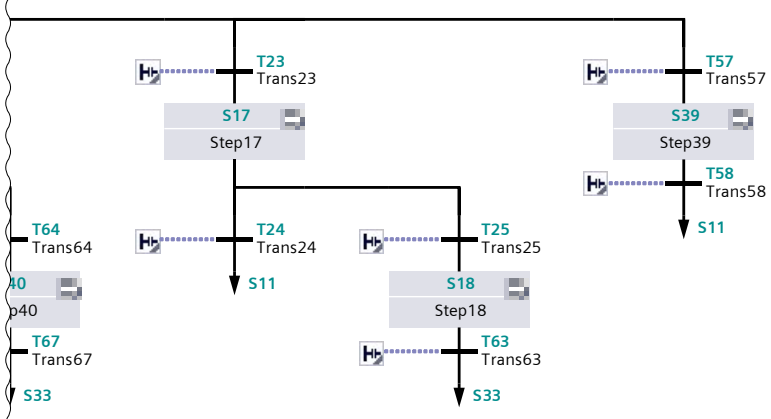
Step20
T29
Trans29
S11
T61
Trans61
S1
T21
Trans21
S16
Step16
T22
Trans22
S11
1.2 (Page4 - 64)

4: (1.2 / 1.2)

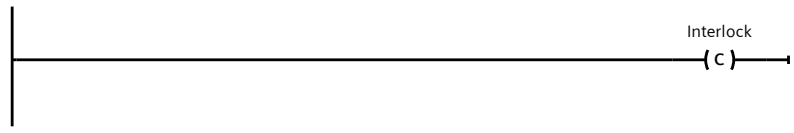


1.1 (Page4 - 63)

S11 - [Initial step]:Step11

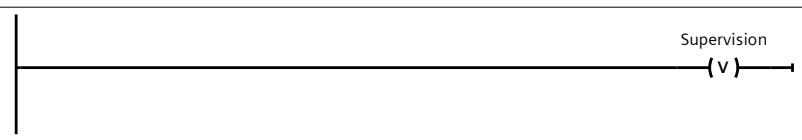
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

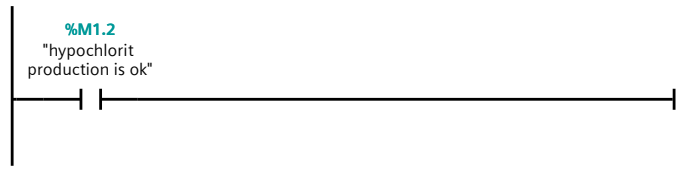
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action

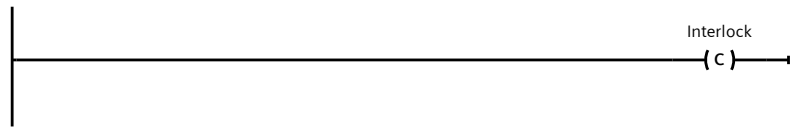
T46:Trans46



S12:Step12

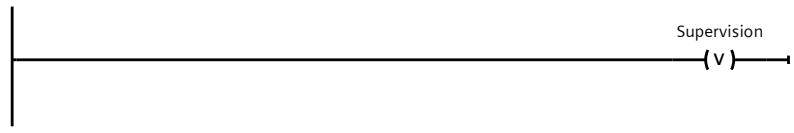
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

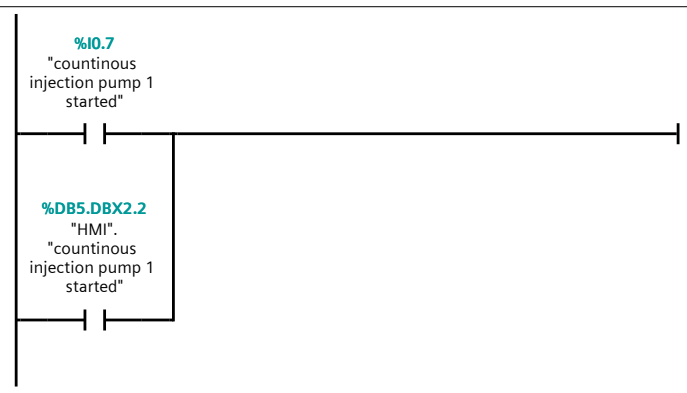
Supervision alarm	
Alarm text	



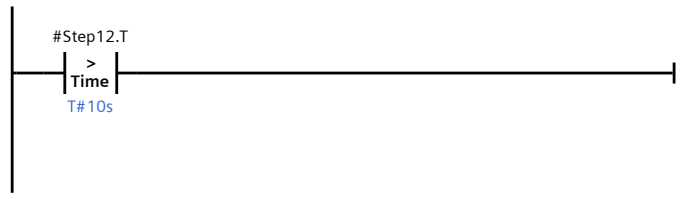
Actions:

Actions:			
Interlock	Event	Qualifier	Action
		S	"countinous injection pump 1"

T15:Trans15



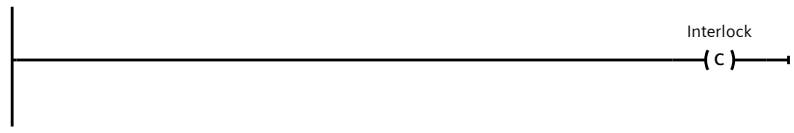
T16:Trans16



S13:Step13

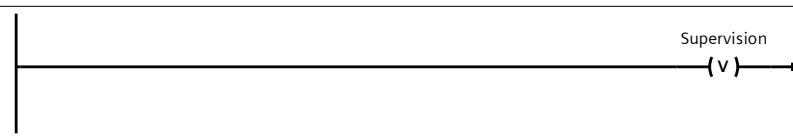
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

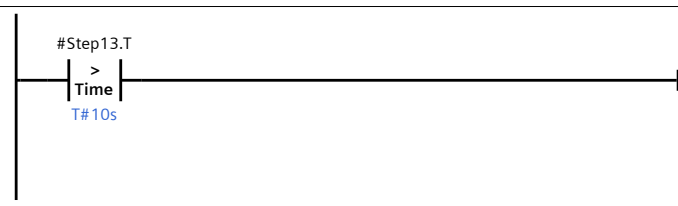
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action
		R	"countinous injection pump 1"
		N	"countinous injection pump 1 Fault"

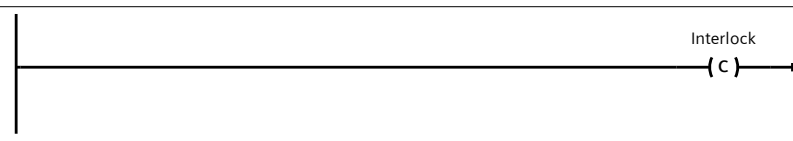
T59:Trans59



S15:Step15

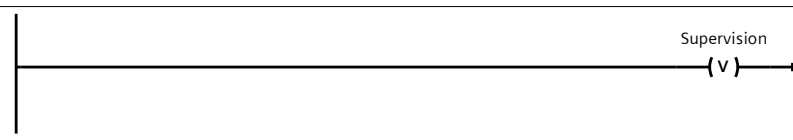
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

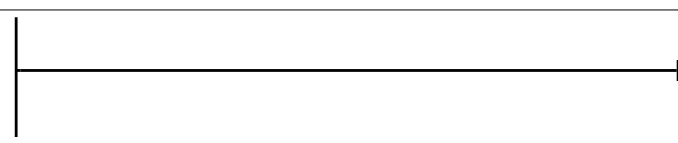
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action
		S	"countinous injection pump 1"

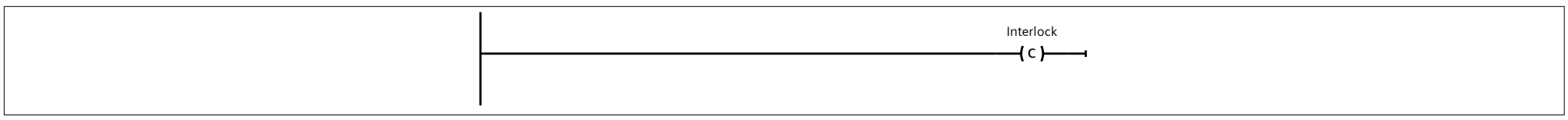
T60:Trans60



S16:Step16

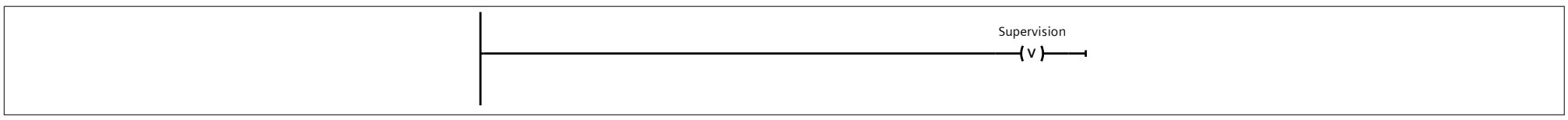
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

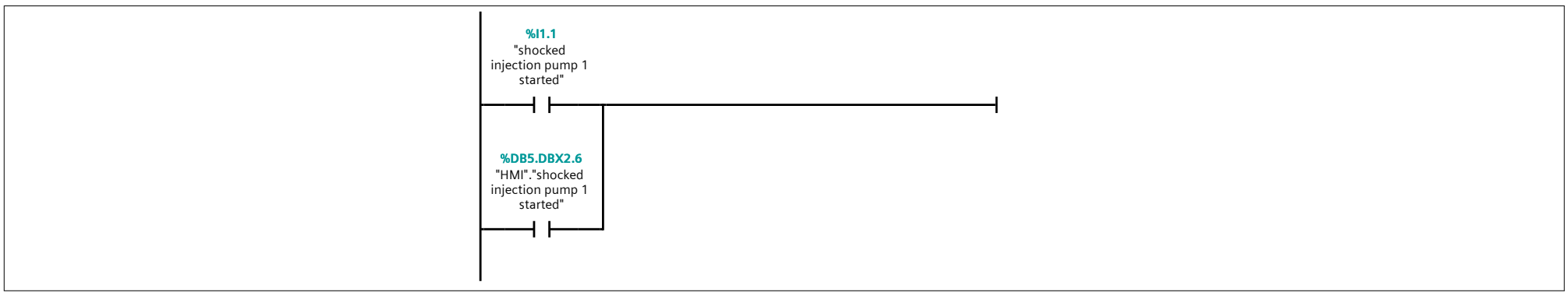
Supervision alarm	
Alarm text	



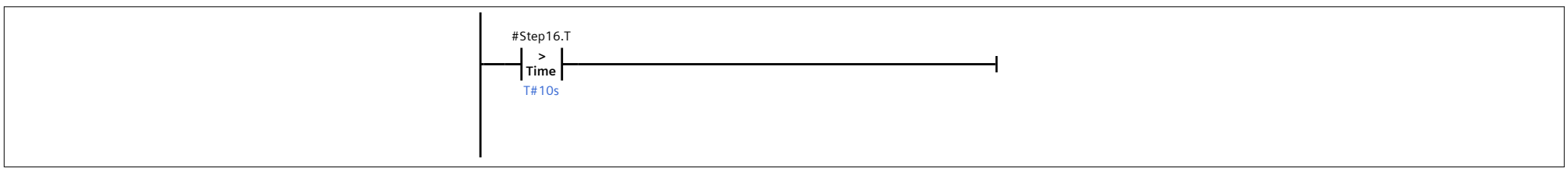
Actions:

Actions:			
Interlock	Event	Qualifier	Action
		S	"shocked injection pump 1"

T22:Trans22



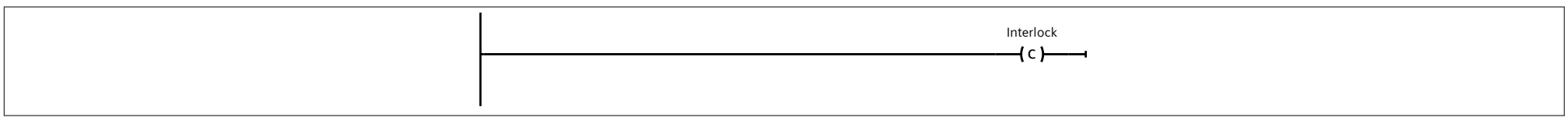
T64:Trans64



S17:Step17

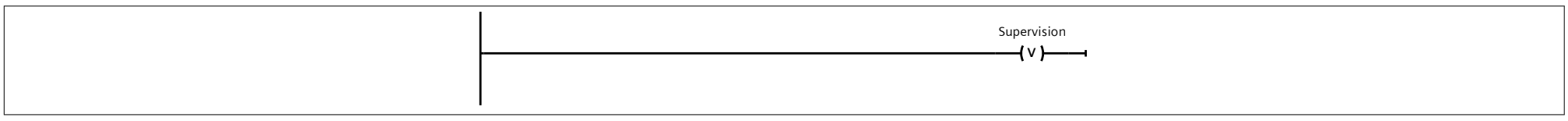
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

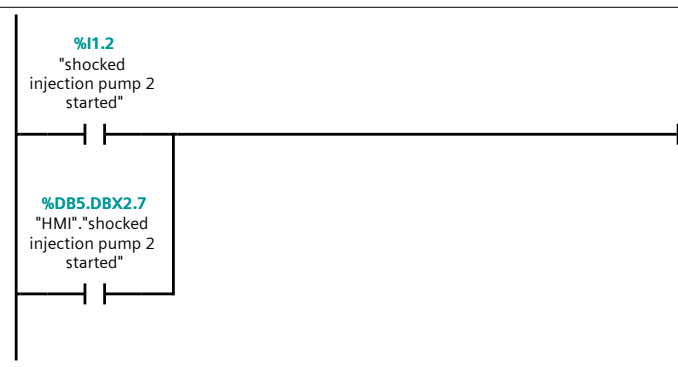
Supervision alarm	
Alarm text	



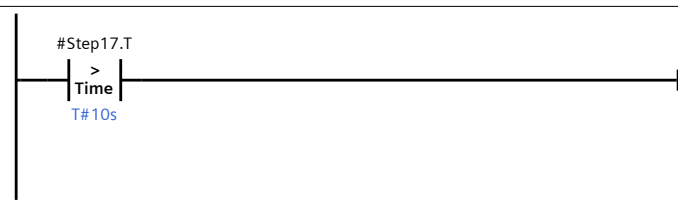
Actions:

Actions:			
Interlock	Event	Qualifier	Action
		S	"shocked injection pump 2"

T24:Trans24



T25:Trans25



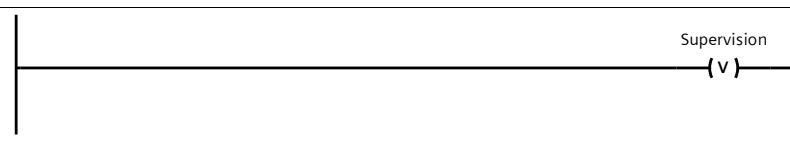
S18:Step18

Interlock -(c)-:

Interlock alarm	
Alarm text	

Supervision -(v)-:

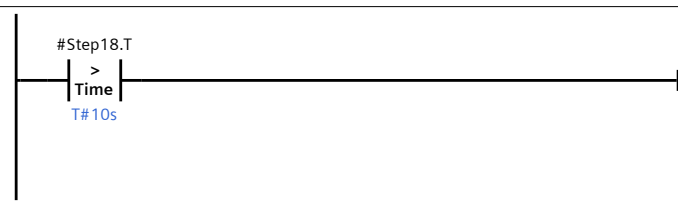
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action
		R	"shocked injection pump 2"
		N	"shocked injection pump 2 Fault"

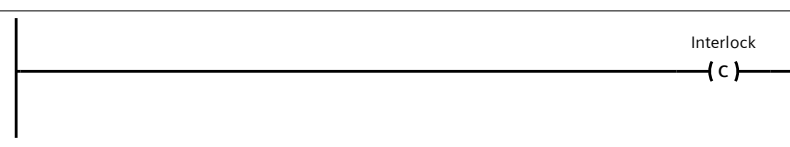
T63:Trans63



S20:Step20

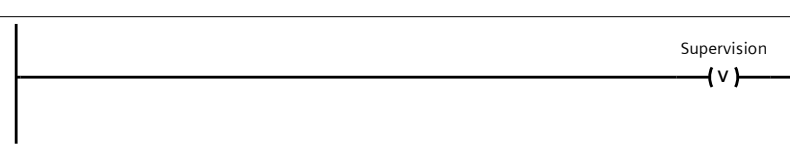
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

Supervision alarm	
Alarm text	

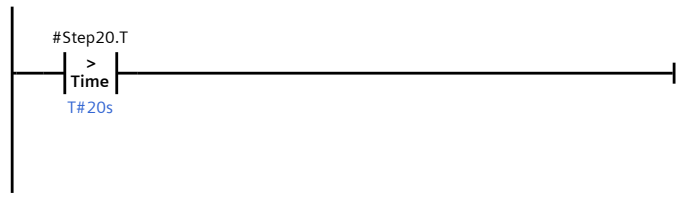


Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**
		R	"countinous injection pump 1"
		R	"countinous injection pump 2"
		N	"Tank level LL"

T29:Trans29

T61:Trans61



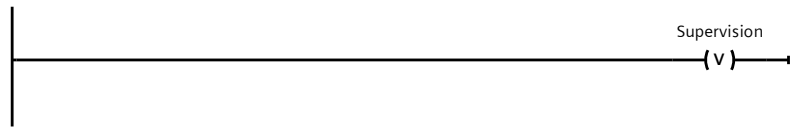
S21:Step21

Interlock -(c)-:

Interlock alarm	
Alarm text	

Supervision -(v)-:

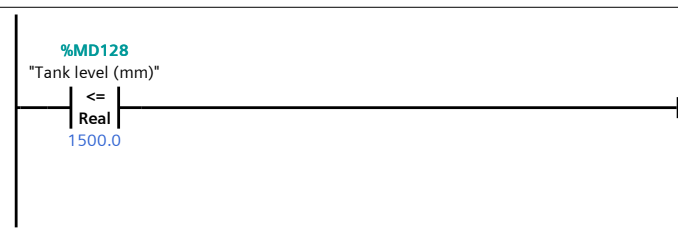
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**
		N	"Tank level HH"
		R	"valve to Tank"
		R	"electrolizer 1"
		R	"filter 1"
		R	"seawater pump intake 1"
		R	"seawater pump intake 2"
		R	"Rectifier"
		R	"electrolizer 2"
		R	"filter 2"
		R	"Before FILTER 1 VALVE"
		R	"Before FILTER 2 VALVE"
		R	"FILTER 1 VALVE"
		R	"FILTER 2 VALVE"

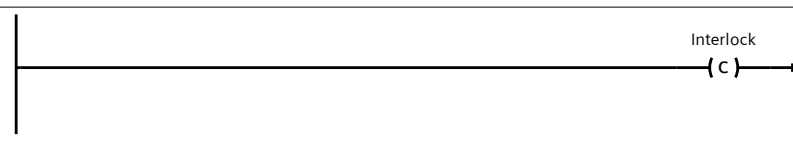
T31:Trans31



S33:Step33

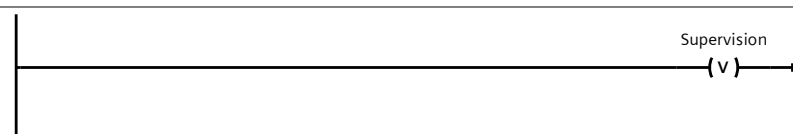
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

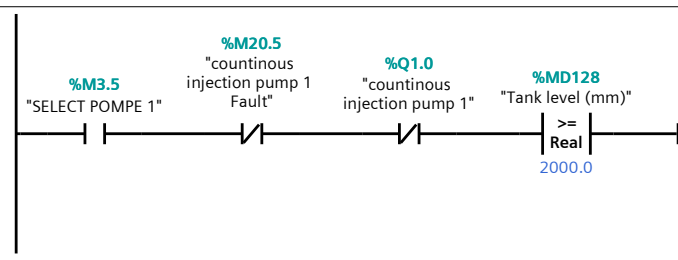
Supervision alarm	
Alarm text	



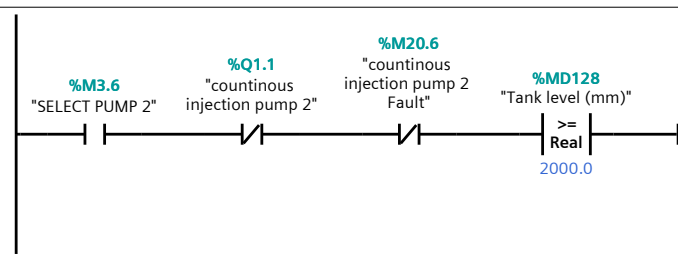
Actions:

Actions:			
Interlock	Event	Qualifier	Action

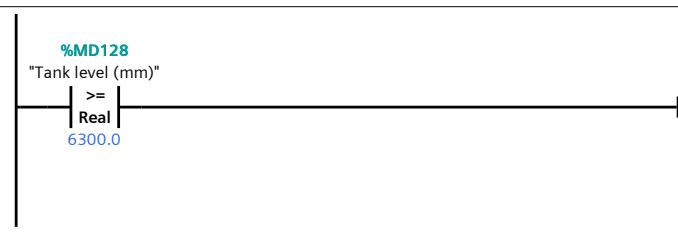
T14:Trans14



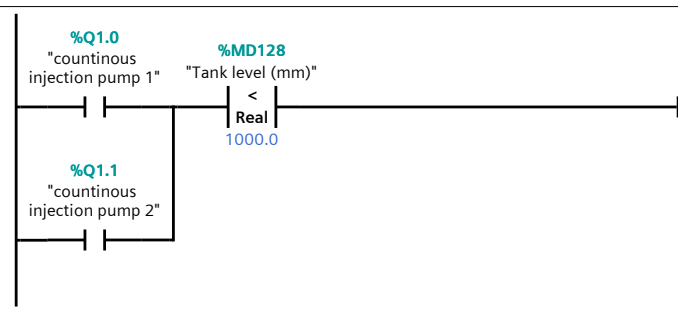
T66:Trans66



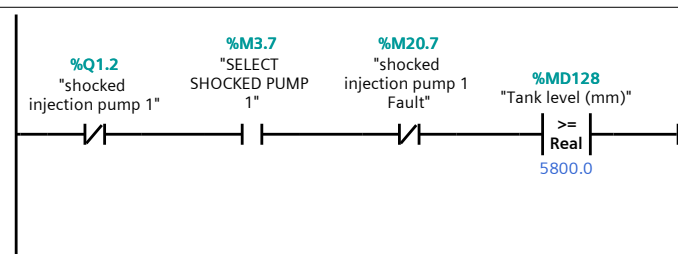
T30:Trans30



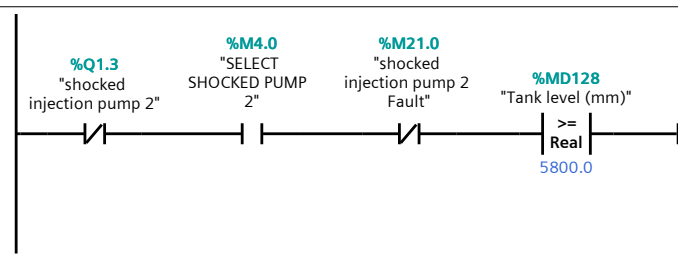
T28:Trans28



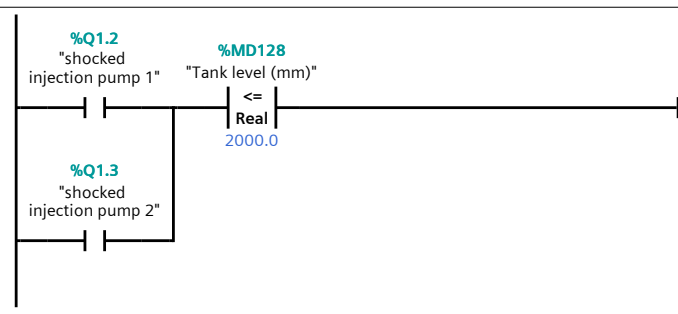
T21:Trans21



T23:Trans23



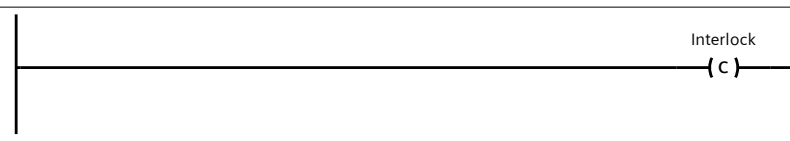
T57:Trans57



S39:Step39

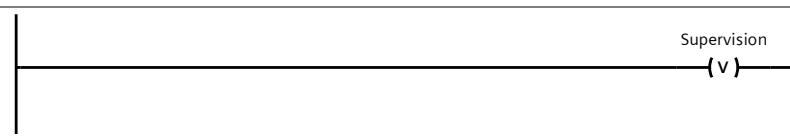
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

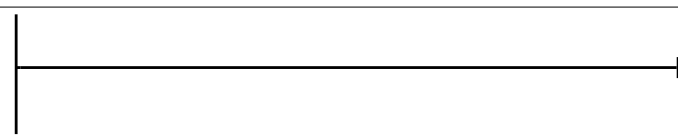
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action
		R	"shocked injection pump 1"
		R	"shocked injection pump 2"

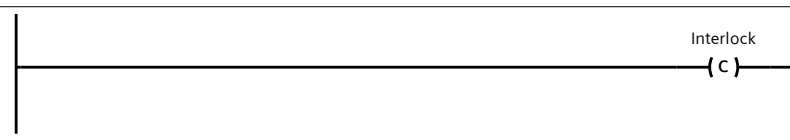
T58:Trans58



S42:Step42

Interlock -(c)-:

Interlock alarm	
Alarm text	



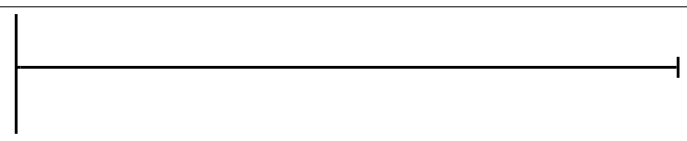
Supervision -(v)-:

Supervision alarm	
Alarm text	

Actions:

Actions:			
Interlock	Event	Qualifier	Action
		R	"countinous injection pump 1"
		R	"countinous injection pump 2"
		R	"shocked injection pump 1"
		R	"shocked injection pump 2"
	S1	ON	#Step11

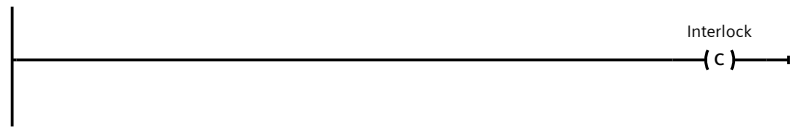
T62:Trans62



S40:Step40

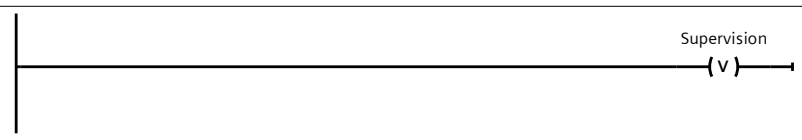
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

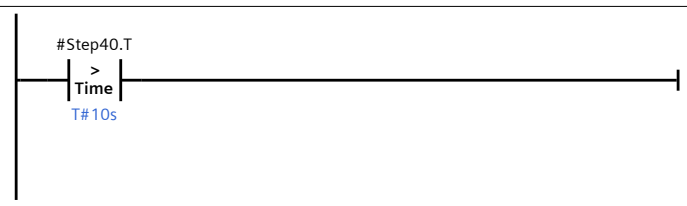
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action
		R	"shocked injection pump 1"
		N	"shocked injection pump 1 Fault"

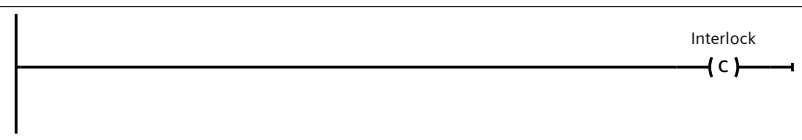
T67:Trans67



S31:Step31

Interlock -(c)-:

Interlock alarm	
Alarm text	



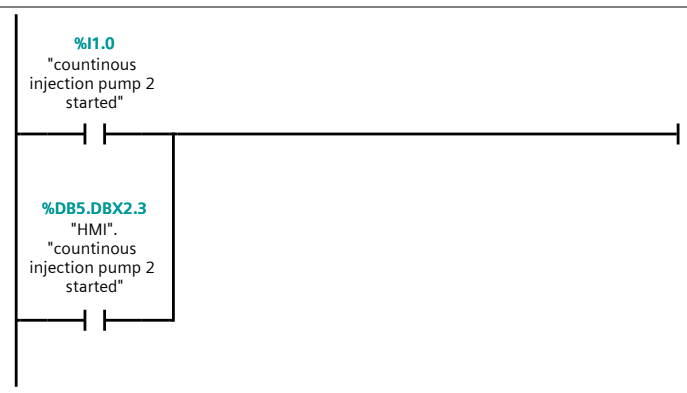
Supervision -(v)-:

Supervision alarm	
Alarm text	

Actions:

Actions:			
Interlock	Event	Qualifier	Action
		S	"countinous injection pump 2"

T44:Trans44



T45:Trans45

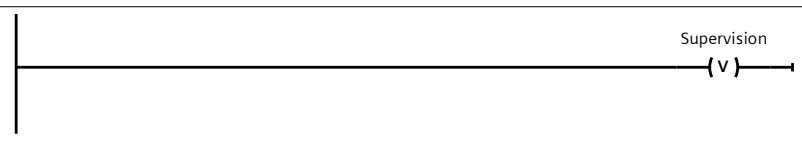
S32:Step32

Interlock -(c)-:

Interlock alarm	
Alarm text	

Supervision -(v)-:

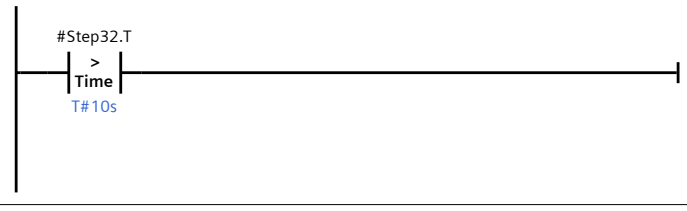
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action
		R	"countinous injection pump 2"
		N	"countinous injection pump 2 Fault"

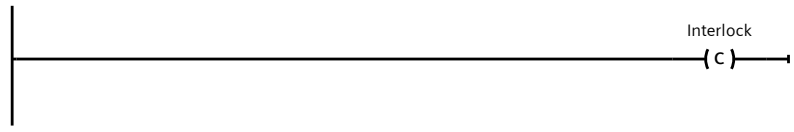
T70:Trans70



S47:Step47

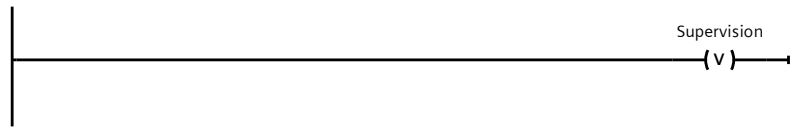
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

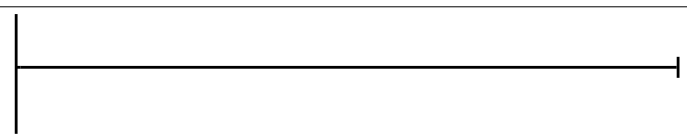
Supervision alarm	
Alarm text	



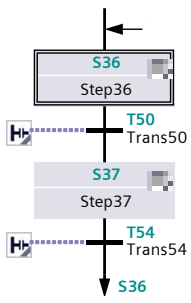
Actions:

Actions:			
Interlock	Event	Qualifier	Action
		S	"countinous injection pump 2"

T69:Trans69



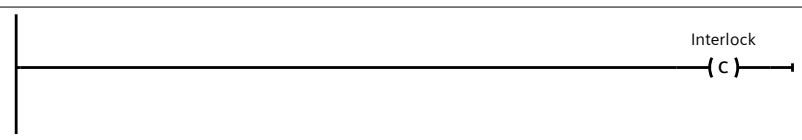
5:



S36 - [Initial step]:Step36

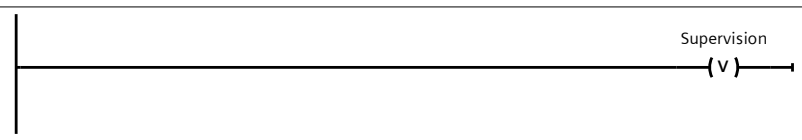
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

Supervision alarm	
Alarm text	



Actions:

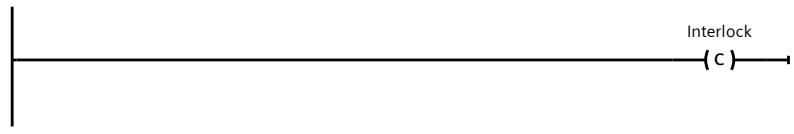
Actions:			
Interlock	Event	Qualifier	Action
	S1	ON	#Step1

T50:Trans50

S37:Step37

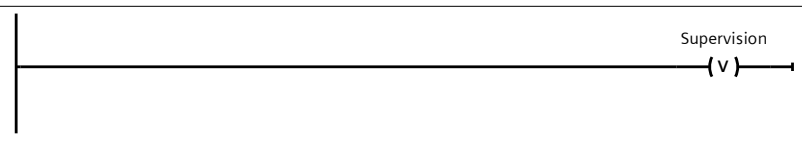
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

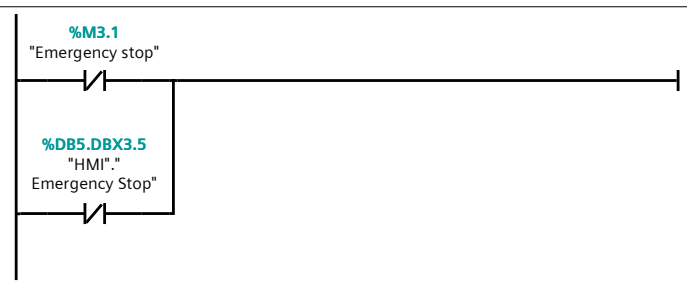
Supervision alarm	
Alarm text	



Actions:

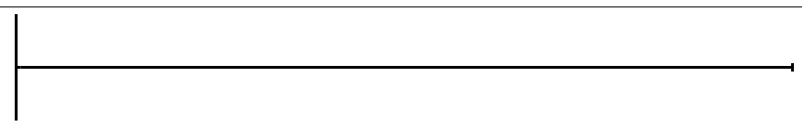
Actions:			
Interlock	Event	Qualifier	Action
	S1	OFF	S_ALL

T54:Trans54



Permanent post-instructions

1:



electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / Program blocks

SEAWATER Intake [FB2]

SEAWATER Intake Properties							
General							
Name	SEAWATER Intake	**Number**	2	**Type**	FB	**Language**	GRAPH
Numbering	Automatic	**Network language**	LAD				
Information							
Title		**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
Input									
OFF_SQ	Bool	0.0	false	True	True	False	False		Désactiver le graphe
INIT_SQ	Bool	0.1	false	True	True	False	False		Mettre le graphe à l'état initial
ACK_EF	Bool	0.2	false	True	True	False	False		Acquitter toutes les erreurs et perturbations
S_PREV	Bool	0.3	false	True	True	False	False		Afficher l'étape précédente dans le paramètre S_NO
S_NEXT	Bool	0.4	false	True	True	False	False		Afficher l'étape suivante dans le paramètre S_NO
SW_AUTO	Bool	0.5	false	True	True	False	False		Mode automatique
SW_TAP	Bool	0.6	false	True	True	False	False		Mode semi-automatique/Activation de l'étape suivante avec transition
SW_TOP	Bool	0.7	false	True	True	False	False		Mode semi-automatique/Ignorer la transition
SW_MAN	Bool	1.0	false	True	True	False	False		Mode manuel
S_SEL	Int	2.0	0	True	True	False	False		Sélectionner l'étape à afficher dans S_NO
S_ON	Bool	4.0	false	True	True	False	False		Activer l'étape figurant dans le paramètre S_NO
S_OFF	Bool	4.1	false	True	True	False	False		Désactiver l'étape figurant dans le paramètre S_NO
T_PUSH	Bool	4.2	false	True	True	False	False		Validation de commutation pour transition en mode semi-automatique
Output									
S_NO	Int	6.0	0	True	True	False	False		Numéro d'étape
S_MORE	Bool	8.0	false	True	True	False	False		Il existe d'autres étapes qui peuvent être affichées dans S_NO.
S_ACTIVE	Bool	8.1	false	True	True	False	False		L'étape affichée dans le paramètre S_NO est activée
ERR_FLT	Bool	8.2	false	True	True	False	False		Erreur groupée Interlock or Supervision
AUTO_ON	Bool	8.3	false	True	True	False	False		Mode automatique activé
TAP_ON	Bool	8.4	false	True	True	False	False		Mode semi-automatique/Etape avec transition activé
TOP_ON	Bool	8.5	false	True	True	False	False		Mode semi-automatique/Ignorer la transition activé
MAN_ON	Bool	8.6	false	True	True	False	False		Mode manuel activé
InOut									
Static									
Trans1	GraphTransition	70.0		True	True	False	True		
TV	Bool	70.0	false	True	True	False	False		Transition is valid
TT	Bool	70.1	false	True	True	False	False		Transition is satisfied
TS	Bool	70.2	false	True	True	False	False		Transition switches
CF_IV	Bool	70.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	72.0	13	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	74.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	78.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	82.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans7	GraphTransition	86.0		True	True	False	True		
TV	Bool	86.0	false	True	True	False	False		Transition is valid
TT	Bool	86.1	false	True	True	False	False		Transition is satisfied
TS	Bool	86.2	false	True	True	False	False		Transition switches
CF_IV	Bool	86.3	true	True	True	False	False		The CRIT_FLT entry is invalid

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TNO	Int	88.0	18	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	90.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	94.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	98.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans13	GraphTransition	102.0		True	True	False	True		
TV	Bool	102.0	false	True	True	False	False		Transition is valid
TT	Bool	102.1	false	True	True	False	False		Transition is satisfied
TS	Bool	102.2	false	True	True	False	False		Transition switches
CF_IV	Bool	102.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	104.0	19	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	106.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	110.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	114.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans26	GraphTransition	118.0		True	True	False	True		
TV	Bool	118.0	false	True	True	False	False		Transition is valid
TT	Bool	118.1	false	True	True	False	False		Transition is satisfied
TS	Bool	118.2	false	True	True	False	False		Transition switches
CF_IV	Bool	118.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	120.0	26	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	122.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	126.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	130.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans27	GraphTransition	134.0		True	True	False	True		
TV	Bool	134.0	false	True	True	False	False		Transition is valid
TT	Bool	134.1	false	True	True	False	False		Transition is satisfied
TS	Bool	134.2	false	True	True	False	False		Transition switches
CF_IV	Bool	134.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	136.0	27	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	138.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	142.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	146.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans28	GraphTransition	150.0		True	True	False	True		
TV	Bool	150.0	false	True	True	False	False		Transition is valid
TT	Bool	150.1	false	True	True	False	False		Transition is satisfied
TS	Bool	150.2	false	True	True	False	False		Transition switches
CF_IV	Bool	150.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	152.0	28	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	154.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	158.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	162.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans29	GraphTransition	166.0		True	True	False	True		
TV	Bool	166.0	false	True	True	False	False		Transition is valid
TT	Bool	166.1	false	True	True	False	False		Transition is satisfied
TS	Bool	166.2	false	True	True	False	False		Transition switches

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CF_IV	Bool	166.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	168.0	29	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	170.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	174.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	178.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans30	GraphTransition	182.0		True	True	False	True		
TV	Bool	182.0	false	True	True	False	False		Transition is valid
TT	Bool	182.1	false	True	True	False	False		Transition is satisfied
TS	Bool	182.2	false	True	True	False	False		Transition switches
CF_IV	Bool	182.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	184.0	30	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	186.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	190.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	194.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans31	GraphTransition	198.0		True	True	False	True		
TV	Bool	198.0	false	True	True	False	False		Transition is valid
TT	Bool	198.1	false	True	True	False	False		Transition is satisfied
TS	Bool	198.2	false	True	True	False	False		Transition switches
CF_IV	Bool	198.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	200.0	31	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	202.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	206.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	210.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans32	GraphTransition	214.0		True	True	False	True		
TV	Bool	214.0	false	True	True	False	False		Transition is valid
TT	Bool	214.1	false	True	True	False	False		Transition is satisfied
TS	Bool	214.2	false	True	True	False	False		Transition switches
CF_IV	Bool	214.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	216.0	32	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	218.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	222.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	226.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans33	GraphTransition	230.0		True	True	False	True		
TV	Bool	230.0	false	True	True	False	False		Transition is valid
TT	Bool	230.1	false	True	True	False	False		Transition is satisfied
TS	Bool	230.2	false	True	True	False	False		Transition switches
CF_IV	Bool	230.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	232.0	33	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	234.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	238.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	242.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans34	GraphTransition	246.0		True	True	False	True		
TV	Bool	246.0	false	True	True	False	False		Transition is valid
TT	Bool	246.1	false	True	True	False	False		Transition is satisfied

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TS	Bool	246.2	false	True	True	False	False		Transition switches
CF_IV	Bool	246.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	248.0	34	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	250.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	254.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	258.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans35	GraphTransition	262.0		True	True	False	True		
TV	Bool	262.0	false	True	True	False	False		Transition is valid
TT	Bool	262.1	false	True	True	False	False		Transition is satisfied
TS	Bool	262.2	false	True	True	False	False		Transition switches
CF_IV	Bool	262.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	264.0	35	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	266.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	270.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	274.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans36	GraphTransition	278.0		True	True	False	True		
TV	Bool	278.0	false	True	True	False	False		Transition is valid
TT	Bool	278.1	false	True	True	False	False		Transition is satisfied
TS	Bool	278.2	false	True	True	False	False		Transition switches
CF_IV	Bool	278.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	280.0	36	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	282.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	286.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	290.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans37	GraphTransition	294.0		True	True	False	True		
TV	Bool	294.0	false	True	True	False	False		Transition is valid
TT	Bool	294.1	false	True	True	False	False		Transition is satisfied
TS	Bool	294.2	false	True	True	False	False		Transition switches
CF_IV	Bool	294.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	296.0	37	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	298.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	302.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	306.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans38	GraphTransition	310.0		True	True	False	True		
TV	Bool	310.0	false	True	True	False	False		Transition is valid
TT	Bool	310.1	false	True	True	False	False		Transition is satisfied
TS	Bool	310.2	false	True	True	False	False		Transition switches
CF_IV	Bool	310.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	312.0	38	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	314.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	318.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	322.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans39	GraphTransition	326.0		True	True	False	True		
TV	Bool	326.0	false	True	True	False	False		Transition is valid

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TT	Bool	326.1	false	True	True	False	False		Transition is satisfied
TS	Bool	326.2	false	True	True	False	False		Transition switches
CF_IV	Bool	326.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	328.0	39	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	330.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	334.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	338.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans41	GraphTransition	342.0		False	False	False	True		Transition structure
TV	Bool	342.0	false	False	False	False	False		Transition is valid
TT	Bool	342.1	false	False	False	False	False		Transition is satisfied
TS	Bool	342.2	false	False	False	False	False		Transition switches
CF_IV	Bool	342.3	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	344.0	41	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	346.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	350.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	354.0	16#0	False	False	False	False		Copy of CRIT if an error occurs
Trans42	GraphTransition	358.0		False	False	False	True		Transition structure
TV	Bool	358.0	false	False	False	False	False		Transition is valid
TT	Bool	358.1	false	False	False	False	False		Transition is satisfied
TS	Bool	358.2	false	False	False	False	False		Transition switches
CF_IV	Bool	358.3	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	360.0	42	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	362.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	366.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	370.0	16#0	False	False	False	False		Copy of CRIT if an error occurs
Trans43	GraphTransition	374.0		False	False	False	True		Transition structure
TV	Bool	374.0	false	False	False	False	False		Transition is valid
TT	Bool	374.1	false	False	False	False	False		Transition is satisfied
TS	Bool	374.2	false	False	False	False	False		Transition switches
CF_IV	Bool	374.3	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	376.0	43	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	378.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	382.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	386.0	16#0	False	False	False	False		Copy of CRIT if an error occurs
Trans44	GraphTransition	390.0		False	False	False	True		Transition structure
TV	Bool	390.0	false	False	False	False	False		Transition is valid
TT	Bool	390.1	false	False	False	False	False		Transition is satisfied
TS	Bool	390.2	false	False	False	False	False		Transition switches
CF_IV	Bool	390.3	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	392.0	44	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	394.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	398.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	402.0	16#0	False	False	False	False		Copy of CRIT if an error occurs
Trans45	GraphTransition	406.0		False	False	False	True		Transition structure

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TV	Bool	406.0	false	False	False	False	False		Transition is valid
TT	Bool	406.1	false	False	False	False	False		Transition is satisfied
TS	Bool	406.2	false	False	False	False	False		Transition switches
CF_IV	Bool	406.3	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	408.0	45	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	410.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	414.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	418.0	16#0	False	False	False	False		Copy of CRIT if an error occurs
▼ Trans46	GraphTransition	422.0		False	False	False	True		Transition structure
TV	Bool	422.0	false	False	False	False	False		Transition is valid
TT	Bool	422.1	false	False	False	False	False		Transition is satisfied
TS	Bool	422.2	false	False	False	False	False		Transition switches
CF_IV	Bool	422.3	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	424.0	46	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	426.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	430.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	434.0	16#0	False	False	False	False		Copy of CRIT if an error occurs
▼ Trans47	GraphTransition	438.0		False	False	False	True		Transition structure
TV	Bool	438.0	false	False	False	False	False		Transition is valid
TT	Bool	438.1	false	False	False	False	False		Transition is satisfied
TS	Bool	438.2	false	False	False	False	False		Transition switches
CF_IV	Bool	438.3	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	440.0	47	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	442.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	446.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	450.0	16#0	False	False	False	False		Copy of CRIT if an error occurs
▼ Trans48	GraphTransition	454.0		False	False	False	True		Transition structure
TV	Bool	454.0	false	False	False	False	False		Transition is valid
TT	Bool	454.1	false	False	False	False	False		Transition is satisfied
TS	Bool	454.2	false	False	False	False	False		Transition switches
CF_IV	Bool	454.3	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	456.0	48	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	458.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	462.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	466.0	16#0	False	False	False	False		Copy of CRIT if an error occurs
▼ Step1	GraphStep	502.0		True	True	False	True		
S1	Bool	502.0	false	True	True	False	False		Step is activated
L1	Bool	502.1	false	True	True	False	False		interlock leaving state
V1	Bool	502.2	false	True	True	False	False		Supervision entering state
R1	Bool	502.3	false	True	True	False	False		Reserved
A1	Bool	502.4	false	True	True	False	False		Error is acknowledged
S0	Bool	502.5	false	True	True	False	False		Step is deactivated
L0	Bool	502.6	false	True	True	False	False		Interlock entering state
V0	Bool	502.7	false	True	True	False	False		Supervision leaving state
X	Bool	503.0	false	True	True	False	False		Step is active
LA	Bool	503.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	503.2	false	True	True	False	False		Supervision active
RA	Bool	503.3	false	True	True	False	False		Reserved
AA	Bool	503.4	false	True	True	False	False		Reserved
SS	Bool	503.5	false	True	True	False	False		System-internal

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
LS	Bool	503.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	503.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	504.0	1	True	True	False	False		User step number
T	Time	506.0	T#0ms	True	True	False	False		Total step activation time
U	Time	510.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	514.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	518.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	522.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	526.0	false	True	True	False	False		System-internal
LP	Bool	526.1	false	True	True	False	False		System-internal
LN	Bool	526.2	false	True	True	False	False		System-internal
VP	Bool	526.3	false	True	True	False	False		System-internal
VN	Bool	526.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	527.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	528.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	530.0	16#0	True	True	False	False		Reserve
▼ Step7	GraphStep	534.0		True	True	False	True		
S1	Bool	534.0	false	True	True	False	False		Step is activated
L1	Bool	534.1	false	True	True	False	False		interlock leaving state
V1	Bool	534.2	false	True	True	False	False		Supervision entering state
R1	Bool	534.3	false	True	True	False	False		Reserved
A1	Bool	534.4	false	True	True	False	False		Error is acknowledged
S0	Bool	534.5	false	True	True	False	False		Step is deactivated
L0	Bool	534.6	false	True	True	False	False		Interlock entering state
V0	Bool	534.7	false	True	True	False	False		Supervision leaving state
X	Bool	535.0	false	True	True	False	False		Step is active
LA	Bool	535.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	535.2	false	True	True	False	False		Supervision active
RA	Bool	535.3	false	True	True	False	False		Reserved
AA	Bool	535.4	false	True	True	False	False		Reserved
SS	Bool	535.5	false	True	True	False	False		System-internal
LS	Bool	535.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	535.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	536.0	7	True	True	False	False		User step number
T	Time	538.0	T#0ms	True	True	False	False		Total step activation time
U	Time	542.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	546.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	550.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	554.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	558.0	false	True	True	False	False		System-internal
LP	Bool	558.1	false	True	True	False	False		System-internal
LN	Bool	558.2	false	True	True	False	False		System-internal
VP	Bool	558.3	false	True	True	False	False		System-internal
VN	Bool	558.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	559.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	560.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	562.0	16#0	True	True	False	False		Reserve
▼ Step10	GraphStep	566.0		True	True	False	True		
S1	Bool	566.0	false	True	True	False	False		Step is activated
L1	Bool	566.1	false	True	True	False	False		interlock leaving state
V1	Bool	566.2	false	True	True	False	False		Supervision entering state
R1	Bool	566.3	false	True	True	False	False		Reserved
A1	Bool	566.4	false	True	True	False	False		Error is acknowledged
S0	Bool	566.5	false	True	True	False	False		Step is deactivated
L0	Bool	566.6	false	True	True	False	False		Interlock entering state
V0	Bool	566.7	false	True	True	False	False		Supervision leaving state
X	Bool	567.0	false	True	True	False	False		Step is active

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
LA	Bool	567.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	567.2	false	True	True	False	False		Supervision active
RA	Bool	567.3	false	True	True	False	False		Reserved
AA	Bool	567.4	false	True	True	False	False		Reserved
SS	Bool	567.5	false	True	True	False	False		System-internal
LS	Bool	567.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	567.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	568.0	10	True	True	False	False		User step number
T	Time	570.0	T#0ms	True	True	False	False		Total step activation time
U	Time	574.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	578.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	582.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	586.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	590.0	false	True	True	False	False		System-internal
LP	Bool	590.1	false	True	True	False	False		System-internal
LN	Bool	590.2	false	True	True	False	False		System-internal
VP	Bool	590.3	false	True	True	False	False		System-internal
VN	Bool	590.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	591.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	592.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	594.0	16#0	True	True	False	False		Reserve
▼ Step11	GraphStep	598.0		True	True	False	True		
S1	Bool	598.0	false	True	True	False	False		Step is activated
L1	Bool	598.1	false	True	True	False	False		interlock leaving state
V1	Bool	598.2	false	True	True	False	False		Supervision entering state
R1	Bool	598.3	false	True	True	False	False		Reserved
A1	Bool	598.4	false	True	True	False	False		Error is acknowledged
S0	Bool	598.5	false	True	True	False	False		Step is deactivated
L0	Bool	598.6	false	True	True	False	False		Interlock entering state
V0	Bool	598.7	false	True	True	False	False		Supervision leaving state
X	Bool	599.0	false	True	True	False	False		Step is active
LA	Bool	599.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	599.2	false	True	True	False	False		Supervision active
RA	Bool	599.3	false	True	True	False	False		Reserved
AA	Bool	599.4	false	True	True	False	False		Reserved
SS	Bool	599.5	false	True	True	False	False		System-internal
LS	Bool	599.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	599.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	600.0	11	True	True	False	False		User step number
T	Time	602.0	T#0ms	True	True	False	False		Total step activation time
U	Time	606.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	610.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	614.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	618.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	622.0	false	True	True	False	False		System-internal
LP	Bool	622.1	false	True	True	False	False		System-internal
LN	Bool	622.2	false	True	True	False	False		System-internal
VP	Bool	622.3	false	True	True	False	False		System-internal
VN	Bool	622.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	623.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	624.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	626.0	16#0	True	True	False	False		Reserve
▼ Step12	GraphStep	630.0		True	True	False	True		
S1	Bool	630.0	false	True	True	False	False		Step is activated
L1	Bool	630.1	false	True	True	False	False		interlock leaving state
V1	Bool	630.2	false	True	True	False	False		Supervision entering state
R1	Bool	630.3	false	True	True	False	False		Reserved

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
A1	Bool	630.4	false	True	True	False	False		Error is acknowledged
S0	Bool	630.5	false	True	True	False	False		Step is deactivated
L0	Bool	630.6	false	True	True	False	False		Interlock entering state
V0	Bool	630.7	false	True	True	False	False		Supervision leaving state
X	Bool	631.0	false	True	True	False	False		Step is active
LA	Bool	631.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	631.2	false	True	True	False	False		Supervision active
RA	Bool	631.3	false	True	True	False	False		Reserved
AA	Bool	631.4	false	True	True	False	False		Reserved
SS	Bool	631.5	false	True	True	False	False		System-internal
LS	Bool	631.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	631.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	632.0	12	True	True	False	False		User step number
T	Time	634.0	T#0ms	True	True	False	False		Total step activation time
U	Time	638.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	642.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	646.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	650.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	654.0	false	True	True	False	False		System-internal
LP	Bool	654.1	false	True	True	False	False		System-internal
LN	Bool	654.2	false	True	True	False	False		System-internal
VP	Bool	654.3	false	True	True	False	False		System-internal
VN	Bool	654.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	655.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	656.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	658.0	16#0	True	True	False	False		Reserve
▼ Step13	GraphStep	662.0		True	True	False	True		
S1	Bool	662.0	false	True	True	False	False		Step is activated
L1	Bool	662.1	false	True	True	False	False		interlock leaving state
V1	Bool	662.2	false	True	True	False	False		Supervision entering state
R1	Bool	662.3	false	True	True	False	False		Reserved
A1	Bool	662.4	false	True	True	False	False		Error is acknowledged
S0	Bool	662.5	false	True	True	False	False		Step is deactivated
L0	Bool	662.6	false	True	True	False	False		Interlock entering state
V0	Bool	662.7	false	True	True	False	False		Supervision leaving state
X	Bool	663.0	false	True	True	False	False		Step is active
LA	Bool	663.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	663.2	false	True	True	False	False		Supervision active
RA	Bool	663.3	false	True	True	False	False		Reserved
AA	Bool	663.4	false	True	True	False	False		Reserved
SS	Bool	663.5	false	True	True	False	False		System-internal
LS	Bool	663.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	663.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	664.0	13	True	True	False	False		User step number
T	Time	666.0	T#0ms	True	True	False	False		Total step activation time
U	Time	670.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	674.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	678.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	682.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	686.0	false	True	True	False	False		System-internal
LP	Bool	686.1	false	True	True	False	False		System-internal
LN	Bool	686.2	false	True	True	False	False		System-internal
VP	Bool	686.3	false	True	True	False	False		System-internal
VN	Bool	686.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	687.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	688.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	690.0	16#0	True	True	False	False		Reserve

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
Step14	GraphStep	694.0		True	True	False	True		
S1	Bool	694.0	false	True	True	False	False		Step is activated
L1	Bool	694.1	false	True	True	False	False		interlock leaving state
V1	Bool	694.2	false	True	True	False	False		Supervision entering state
R1	Bool	694.3	false	True	True	False	False		Reserved
A1	Bool	694.4	false	True	True	False	False		Error is acknowledged
S0	Bool	694.5	false	True	True	False	False		Step is deactivated
L0	Bool	694.6	false	True	True	False	False		Interlock entering state
V0	Bool	694.7	false	True	True	False	False		Supervision leaving state
X	Bool	695.0	false	True	True	False	False		Step is active
LA	Bool	695.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	695.2	false	True	True	False	False		Supervision active
RA	Bool	695.3	false	True	True	False	False		Reserved
AA	Bool	695.4	false	True	True	False	False		Reserved
SS	Bool	695.5	false	True	True	False	False		System-internal
LS	Bool	695.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	695.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	696.0	14	True	True	False	False		User step number
T	Time	698.0	T#0ms	True	True	False	False		Total step activation time
U	Time	702.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	706.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	710.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	714.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	718.0	false	True	True	False	False		System-internal
LP	Bool	718.1	false	True	True	False	False		System-internal
LN	Bool	718.2	false	True	True	False	False		System-internal
VP	Bool	718.3	false	True	True	False	False		System-internal
VN	Bool	718.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	719.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	720.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	722.0	16#0	True	True	False	False		Reserve
Step15	GraphStep	726.0		True	True	False	True		
S1	Bool	726.0	false	True	True	False	False		Step is activated
L1	Bool	726.1	false	True	True	False	False		interlock leaving state
V1	Bool	726.2	false	True	True	False	False		Supervision entering state
R1	Bool	726.3	false	True	True	False	False		Reserved
A1	Bool	726.4	false	True	True	False	False		Error is acknowledged
S0	Bool	726.5	false	True	True	False	False		Step is deactivated
L0	Bool	726.6	false	True	True	False	False		Interlock entering state
V0	Bool	726.7	false	True	True	False	False		Supervision leaving state
X	Bool	727.0	false	True	True	False	False		Step is active
LA	Bool	727.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	727.2	false	True	True	False	False		Supervision active
RA	Bool	727.3	false	True	True	False	False		Reserved
AA	Bool	727.4	false	True	True	False	False		Reserved
SS	Bool	727.5	false	True	True	False	False		System-internal
LS	Bool	727.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	727.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	728.0	15	True	True	False	False		User step number
T	Time	730.0	T#0ms	True	True	False	False		Total step activation time
U	Time	734.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	738.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	742.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	746.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	750.0	false	True	True	False	False		System-internal
LP	Bool	750.1	false	True	True	False	False		System-internal
LN	Bool	750.2	false	True	True	False	False		System-internal

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
VP	Bool	750.3	false	True	True	False	False		System-internal
VN	Bool	750.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	751.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	752.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	754.0	16#0	True	True	False	False		Reserve
Step16	GraphStep	758.0		True	True	False	True		
S1	Bool	758.0	false	True	True	False	False		Step is activated
L1	Bool	758.1	false	True	True	False	False		interlock leaving state
V1	Bool	758.2	false	True	True	False	False		Supervision entering state
R1	Bool	758.3	false	True	True	False	False		Reserved
A1	Bool	758.4	false	True	True	False	False		Error is acknowledged
S0	Bool	758.5	false	True	True	False	False		Step is deactivated
L0	Bool	758.6	false	True	True	False	False		Interlock entering state
V0	Bool	758.7	false	True	True	False	False		Supervision leaving state
X	Bool	759.0	false	True	True	False	False		Step is active
LA	Bool	759.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	759.2	false	True	True	False	False		Supervision active
RA	Bool	759.3	false	True	True	False	False		Reserved
AA	Bool	759.4	false	True	True	False	False		Reserved
SS	Bool	759.5	false	True	True	False	False		System-internal
LS	Bool	759.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	759.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	760.0	16	True	True	False	False		User step number
T	Time	762.0	T#0ms	True	True	False	False		Total step activation time
U	Time	766.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	770.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	774.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	778.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	782.0	false	True	True	False	False		System-internal
LP	Bool	782.1	false	True	True	False	False		System-internal
LN	Bool	782.2	false	True	True	False	False		System-internal
VP	Bool	782.3	false	True	True	False	False		System-internal
VN	Bool	782.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	783.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	784.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	786.0	16#0	True	True	False	False		Reserve
Step17	GraphStep	790.0		True	True	False	True		
S1	Bool	790.0	false	True	True	False	False		Step is activated
L1	Bool	790.1	false	True	True	False	False		interlock leaving state
V1	Bool	790.2	false	True	True	False	False		Supervision entering state
R1	Bool	790.3	false	True	True	False	False		Reserved
A1	Bool	790.4	false	True	True	False	False		Error is acknowledged
S0	Bool	790.5	false	True	True	False	False		Step is deactivated
L0	Bool	790.6	false	True	True	False	False		Interlock entering state
V0	Bool	790.7	false	True	True	False	False		Supervision leaving state
X	Bool	791.0	false	True	True	False	False		Step is active
LA	Bool	791.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	791.2	false	True	True	False	False		Supervision active
RA	Bool	791.3	false	True	True	False	False		Reserved
AA	Bool	791.4	false	True	True	False	False		Reserved
SS	Bool	791.5	false	True	True	False	False		System-internal
LS	Bool	791.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	791.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	792.0	17	True	True	False	False		User step number
T	Time	794.0	T#0ms	True	True	False	False		Total step activation time
U	Time	798.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	802.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	806.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_SUP	DWord	810.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	814.0	false	True	True	False	False		System-internal
LP	Bool	814.1	false	True	True	False	False		System-internal
LN	Bool	814.2	false	True	True	False	False		System-internal
VP	Bool	814.3	false	True	True	False	False		System-internal
VN	Bool	814.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	815.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	816.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	818.0	16#0	True	True	False	False		Reserve
▼ Step18	GraphStep	822.0		True	True	False	True		
S1	Bool	822.0	false	True	True	False	False		Step is activated
L1	Bool	822.1	false	True	True	False	False		interlock leaving state
V1	Bool	822.2	false	True	True	False	False		Supervision entering state
R1	Bool	822.3	false	True	True	False	False		Reserved
A1	Bool	822.4	false	True	True	False	False		Error is acknowledged
S0	Bool	822.5	false	True	True	False	False		Step is deactivated
L0	Bool	822.6	false	True	True	False	False		Interlock entering state
V0	Bool	822.7	false	True	True	False	False		Supervision leaving state
X	Bool	823.0	false	True	True	False	False		Step is active
LA	Bool	823.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	823.2	false	True	True	False	False		Supervision active
RA	Bool	823.3	false	True	True	False	False		Reserved
AA	Bool	823.4	false	True	True	False	False		Reserved
SS	Bool	823.5	false	True	True	False	False		System-internal
LS	Bool	823.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	823.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	824.0	18	True	True	False	False		User step number
T	Time	826.0	T#0ms	True	True	False	False		Total step activation time
U	Time	830.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	834.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	838.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	842.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	846.0	false	True	True	False	False		System-internal
LP	Bool	846.1	false	True	True	False	False		System-internal
LN	Bool	846.2	false	True	True	False	False		System-internal
VP	Bool	846.3	false	True	True	False	False		System-internal
VN	Bool	846.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	847.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	848.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	850.0	16#0	True	True	False	False		Reserve
▼ Step19	GraphStep	854.0		True	True	False	True		
S1	Bool	854.0	false	True	True	False	False		Step is activated
L1	Bool	854.1	false	True	True	False	False		interlock leaving state
V1	Bool	854.2	false	True	True	False	False		Supervision entering state
R1	Bool	854.3	false	True	True	False	False		Reserved
A1	Bool	854.4	false	True	True	False	False		Error is acknowledged
S0	Bool	854.5	false	True	True	False	False		Step is deactivated
L0	Bool	854.6	false	True	True	False	False		Interlock entering state
V0	Bool	854.7	false	True	True	False	False		Supervision leaving state
X	Bool	855.0	false	True	True	False	False		Step is active
LA	Bool	855.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	855.2	false	True	True	False	False		Supervision active
RA	Bool	855.3	false	True	True	False	False		Reserved
AA	Bool	855.4	false	True	True	False	False		Reserved
SS	Bool	855.5	false	True	True	False	False		System-internal
LS	Bool	855.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	855.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	856.0	19	True	True	False	False		User step number
T	Time	858.0	T#0ms	True	True	False	False		Total step activation time
U	Time	862.0	T#0ms	True	True	False	False		Step activation time without disturbance

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_LOC	DWord	866.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	870.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	874.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	878.0	false	True	True	False	False		System-internal
LP	Bool	878.1	false	True	True	False	False		System-internal
LN	Bool	878.2	false	True	True	False	False		System-internal
VP	Bool	878.3	false	True	True	False	False		System-internal
VN	Bool	878.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	879.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	880.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	882.0	16#0	True	True	False	False		Reserve
▼ Step20	GraphStep	886.0		True	True	False	True		
S1	Bool	886.0	false	True	True	False	False		Step is activated
L1	Bool	886.1	false	True	True	False	False		interlock leaving state
V1	Bool	886.2	false	True	True	False	False		Supervision entering state
R1	Bool	886.3	false	True	True	False	False		Reserved
A1	Bool	886.4	false	True	True	False	False		Error is acknowledged
S0	Bool	886.5	false	True	True	False	False		Step is deactivated
L0	Bool	886.6	false	True	True	False	False		Interlock entering state
V0	Bool	886.7	false	True	True	False	False		Supervision leaving state
X	Bool	887.0	false	True	True	False	False		Step is active
LA	Bool	887.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	887.2	false	True	True	False	False		Supervision active
RA	Bool	887.3	false	True	True	False	False		Reserved
AA	Bool	887.4	false	True	True	False	False		Reserved
SS	Bool	887.5	false	True	True	False	False		System-internal
LS	Bool	887.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	887.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	888.0	20	True	True	False	False		User step number
T	Time	890.0	T#0ms	True	True	False	False		Total step activation time
U	Time	894.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	898.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	902.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	906.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	910.0	false	True	True	False	False		System-internal
LP	Bool	910.1	false	True	True	False	False		System-internal
LN	Bool	910.2	false	True	True	False	False		System-internal
VP	Bool	910.3	false	True	True	False	False		System-internal
VN	Bool	910.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	911.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	912.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	914.0	16#0	True	True	False	False		Reserve
▼ Step21	GraphStep	918.0		True	True	False	True		
S1	Bool	918.0	false	True	True	False	False		Step is activated
L1	Bool	918.1	false	True	True	False	False		interlock leaving state
V1	Bool	918.2	false	True	True	False	False		Supervision entering state
R1	Bool	918.3	false	True	True	False	False		Reserved
A1	Bool	918.4	false	True	True	False	False		Error is acknowledged
S0	Bool	918.5	false	True	True	False	False		Step is deactivated
L0	Bool	918.6	false	True	True	False	False		Interlock entering state
V0	Bool	918.7	false	True	True	False	False		Supervision leaving state
X	Bool	919.0	false	True	True	False	False		Step is active
LA	Bool	919.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	919.2	false	True	True	False	False		Supervision active
RA	Bool	919.3	false	True	True	False	False		Reserved
AA	Bool	919.4	false	True	True	False	False		Reserved
SS	Bool	919.5	false	True	True	False	False		System-internal
LS	Bool	919.6	true	True	True	False	False		Direct result of the programmed interlock

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
VS	Bool	919.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	920.0	21	True	True	False	False		User step number
T	Time	922.0	T#0ms	True	True	False	False		Total step activation time
U	Time	926.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	930.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	934.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	938.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	942.0	false	True	True	False	False		System-internal
LP	Bool	942.1	false	True	True	False	False		System-internal
LN	Bool	942.2	false	True	True	False	False		System-internal
VP	Bool	942.3	false	True	True	False	False		System-internal
VN	Bool	942.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	943.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	944.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	946.0	16#0	True	True	False	False		Reserve
Step22	GraphStep	950.0		True	True	False	True		
S1	Bool	950.0	false	True	True	False	False		Step is activated
L1	Bool	950.1	false	True	True	False	False		interlock leaving state
V1	Bool	950.2	false	True	True	False	False		Supervision entering state
R1	Bool	950.3	false	True	True	False	False		Reserved
A1	Bool	950.4	false	True	True	False	False		Error is acknowledged
S0	Bool	950.5	false	True	True	False	False		Step is deactivated
L0	Bool	950.6	false	True	True	False	False		Interlock entering state
V0	Bool	950.7	false	True	True	False	False		Supervision leaving state
X	Bool	951.0	false	True	True	False	False		Step is active
LA	Bool	951.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	951.2	false	True	True	False	False		Supervision active
RA	Bool	951.3	false	True	True	False	False		Reserved
AA	Bool	951.4	false	True	True	False	False		Reserved
SS	Bool	951.5	false	True	True	False	False		System-internal
LS	Bool	951.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	951.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	952.0	22	True	True	False	False		User step number
T	Time	954.0	T#0ms	True	True	False	False		Total step activation time
U	Time	958.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	962.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	966.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	970.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	974.0	false	True	True	False	False		System-internal
LP	Bool	974.1	false	True	True	False	False		System-internal
LN	Bool	974.2	false	True	True	False	False		System-internal
VP	Bool	974.3	false	True	True	False	False		System-internal
VN	Bool	974.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	975.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	976.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	978.0	16#0	True	True	False	False		Reserve
Step23	GraphStep	982.0		False	False	False	True		Step structure
S1	Bool	982.0	false	False	False	False	False		Step is activated
L1	Bool	982.1	false	False	False	False	False		interlock leaving state
V1	Bool	982.2	false	False	False	False	False		Supervision entering state
R1	Bool	982.3	false	False	False	False	False		Reserved
A1	Bool	982.4	false	False	False	False	False		Error is acknowledged
S0	Bool	982.5	false	False	False	False	False		Step is deactivated
L0	Bool	982.6	false	False	False	False	False		Interlock entering state
V0	Bool	982.7	false	False	False	False	False		Supervision leaving state
X	Bool	983.0	false	False	False	False	False		Step is active
LA	Bool	983.1	false	False	False	False	False		Interlock is not satisfied
VA	Bool	983.2	false	False	False	False	False		Supervision active

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
RA	Bool	983.3	false	False	False	False	False		Reserved
AA	Bool	983.4	false	False	False	False	False		Reserved
SS	Bool	983.5	false	False	False	False	False		System-internal
LS	Bool	983.6	True	False	False	False	False		Direct result of the programmed interlock
VS	Bool	983.7	false	False	False	False	False		Direct result of the programmed supervision
SNO	Int	984.0	23	False	False	False	False		User step number
T	Time	986.0	T#0ms	False	False	False	False		Total step activation time
U	Time	990.0	T#0ms	False	False	False	False		Step activation time without disturbance
CRIT_LOC	DWord	994.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	998.0	16#0	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1002.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1006.0	false	False	False	False	False		System-internal
LP	Bool	1006.1	false	False	False	False	False		System-internal
LN	Bool	1006.2	false	False	False	False	False		System-internal
VP	Bool	1006.3	false	False	False	False	False		System-internal
VN	Bool	1006.4	false	False	False	False	False		System-internal
H_IL_ERR	Byte	1007.0	16#0	False	False	False	False		System-internal
H_SV_FLT	Byte	1008.0	16#0	False	False	False	False		System-internal
RESERVED	DWord	1010.0	16#0	False	False	False	False		Reserve
▼ Step24	GraphStep	1014.0		False	False	False	True		Step structure
S1	Bool	1014.0	false	False	False	False	False		Step is activated
L1	Bool	1014.1	false	False	False	False	False		interlock leaving state
V1	Bool	1014.2	false	False	False	False	False		Supervision entering state
R1	Bool	1014.3	false	False	False	False	False		Reserved
A1	Bool	1014.4	false	False	False	False	False		Error is acknowledged
S0	Bool	1014.5	false	False	False	False	False		Step is deactivated
L0	Bool	1014.6	false	False	False	False	False		Interlock entering state
V0	Bool	1014.7	false	False	False	False	False		Supervision leaving state
X	Bool	1015.0	false	False	False	False	False		Step is active
LA	Bool	1015.1	false	False	False	False	False		Interlock is not satisfied
VA	Bool	1015.2	false	False	False	False	False		Supervision active
RA	Bool	1015.3	false	False	False	False	False		Reserved
AA	Bool	1015.4	false	False	False	False	False		Reserved
SS	Bool	1015.5	false	False	False	False	False		System-internal
LS	Bool	1015.6	True	False	False	False	False		Direct result of the programmed interlock
VS	Bool	1015.7	false	False	False	False	False		Direct result of the programmed supervision
SNO	Int	1016.0	24	False	False	False	False		User step number
T	Time	1018.0	T#0ms	False	False	False	False		Total step activation time
U	Time	1022.0	T#0ms	False	False	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1026.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1030.0	16#0	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1034.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1038.0	false	False	False	False	False		System-internal
LP	Bool	1038.1	false	False	False	False	False		System-internal
LN	Bool	1038.2	false	False	False	False	False		System-internal
VP	Bool	1038.3	false	False	False	False	False		System-internal
VN	Bool	1038.4	false	False	False	False	False		System-internal
H_IL_ERR	Byte	1039.0	16#0	False	False	False	False		System-internal
H_SV_FLT	Byte	1040.0	16#0	False	False	False	False		System-internal
RESERVED	DWord	1042.0	16#0	False	False	False	False		Reserve
▼ Step25	GraphStep	1046.0		False	False	False	True		Step structure
S1	Bool	1046.0	false	False	False	False	False		Step is activated
L1	Bool	1046.1	false	False	False	False	False		interlock leaving state
V1	Bool	1046.2	false	False	False	False	False		Supervision entering state
R1	Bool	1046.3	false	False	False	False	False		Reserved
A1	Bool	1046.4	false	False	False	False	False		Error is acknowledged
S0	Bool	1046.5	false	False	False	False	False		Step is deactivated

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
L0	Bool	1046.6	false	False	False	False	False		Interlock entering state
V0	Bool	1046.7	false	False	False	False	False		Supervision leaving state
X	Bool	1047.0	false	False	False	False	False		Step is active
LA	Bool	1047.1	false	False	False	False	False		Interlock is not satisfied
VA	Bool	1047.2	false	False	False	False	False		Supervision active
RA	Bool	1047.3	false	False	False	False	False		Reserved
AA	Bool	1047.4	false	False	False	False	False		Reserved
SS	Bool	1047.5	false	False	False	False	False		System-internal
LS	Bool	1047.6	True	False	False	False	False		Direct result of the programmed interlock
VS	Bool	1047.7	false	False	False	False	False		Direct result of the programmed supervision
SNO	Int	1048.0	25	False	False	False	False		User step number
T	Time	1050.0	T#0ms	False	False	False	False		Total step activation time
U	Time	1054.0	T#0ms	False	False	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1058.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1062.0	16#0	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1066.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1070.0	false	False	False	False	False		System-internal
LP	Bool	1070.1	false	False	False	False	False		System-internal
LN	Bool	1070.2	false	False	False	False	False		System-internal
VP	Bool	1070.3	false	False	False	False	False		System-internal
VN	Bool	1070.4	false	False	False	False	False		System-internal
H_IL_ERR	Byte	1071.0	16#0	False	False	False	False		System-internal
H_SV_FLT	Byte	1072.0	16#0	False	False	False	False		System-internal
RESERVED	DWord	1074.0	16#0	False	False	False	False		Reserve
▼ Step26	GraphStep	1078.0		False	False	False	True		Step structure
S1	Bool	1078.0	false	False	False	False	False		Step is activated
L1	Bool	1078.1	false	False	False	False	False		interlock leaving state
V1	Bool	1078.2	false	False	False	False	False		Supervision entering state
R1	Bool	1078.3	false	False	False	False	False		Reserved
A1	Bool	1078.4	false	False	False	False	False		Error is acknowledged
S0	Bool	1078.5	false	False	False	False	False		Step is deactivated
L0	Bool	1078.6	false	False	False	False	False		Interlock entering state
V0	Bool	1078.7	false	False	False	False	False		Supervision leaving state
X	Bool	1079.0	false	False	False	False	False		Step is active
LA	Bool	1079.1	false	False	False	False	False		Interlock is not satisfied
VA	Bool	1079.2	false	False	False	False	False		Supervision active
RA	Bool	1079.3	false	False	False	False	False		Reserved
AA	Bool	1079.4	false	False	False	False	False		Reserved
SS	Bool	1079.5	false	False	False	False	False		System-internal
LS	Bool	1079.6	True	False	False	False	False		Direct result of the programmed interlock
VS	Bool	1079.7	false	False	False	False	False		Direct result of the programmed supervision
SNO	Int	1080.0	26	False	False	False	False		User step number
T	Time	1082.0	T#0ms	False	False	False	False		Total step activation time
U	Time	1086.0	T#0ms	False	False	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1090.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1094.0	16#0	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1098.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1102.0	false	False	False	False	False		System-internal
LP	Bool	1102.1	false	False	False	False	False		System-internal
LN	Bool	1102.2	false	False	False	False	False		System-internal
VP	Bool	1102.3	false	False	False	False	False		System-internal
VN	Bool	1102.4	false	False	False	False	False		System-internal
H_IL_ERR	Byte	1103.0	16#0	False	False	False	False		System-internal
H_SV_FLT	Byte	1104.0	16#0	False	False	False	False		System-internal
RESERVED	DWord	1106.0	16#0	False	False	False	False		Reserve
▼ Step27	GraphStep	1110.0		False	False	False	True		Step structure
S1	Bool	1110.0	false	False	False	False	False		Step is activated

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
L1	Bool	1110.1	false	False	False	False	False		interlock leaving state
V1	Bool	1110.2	false	False	False	False	False		Supervision entering state
R1	Bool	1110.3	false	False	False	False	False		Reserved
A1	Bool	1110.4	false	False	False	False	False		Error is acknowledged
S0	Bool	1110.5	false	False	False	False	False		Step is deactivated
L0	Bool	1110.6	false	False	False	False	False		Interlock entering state
V0	Bool	1110.7	false	False	False	False	False		Supervision leaving state
X	Bool	1111.0	false	False	False	False	False		Step is active
LA	Bool	1111.1	false	False	False	False	False		Interlock is not satisfied
VA	Bool	1111.2	false	False	False	False	False		Supervision active
RA	Bool	1111.3	false	False	False	False	False		Reserved
AA	Bool	1111.4	false	False	False	False	False		Reserved
SS	Bool	1111.5	false	False	False	False	False		System-internal
LS	Bool	1111.6	True	False	False	False	False		Direct result of the programmed interlock
VS	Bool	1111.7	false	False	False	False	False		Direct result of the programmed supervision
SNO	Int	1112.0	27	False	False	False	False		User step number
T	Time	1114.0	T#0ms	False	False	False	False		Total step activation time
U	Time	1118.0	T#0ms	False	False	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1122.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1126.0	16#0	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1130.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1134.0	false	False	False	False	False		System-internal
LP	Bool	1134.1	false	False	False	False	False		System-internal
LN	Bool	1134.2	false	False	False	False	False		System-internal
VP	Bool	1134.3	false	False	False	False	False		System-internal
VN	Bool	1134.4	false	False	False	False	False		System-internal
H_IL_ERR	Byte	1135.0	16#0	False	False	False	False		System-internal
H_SV_FLT	Byte	1136.0	16#0	False	False	False	False		System-internal
RESERVED	DWord	1138.0	16#0	False	False	False	False		Reserve
▼ Step28	GraphStep	1142.0		False	False	False	True		Step structure
S1	Bool	1142.0	false	False	False	False	False		Step is activated
L1	Bool	1142.1	false	False	False	False	False		interlock leaving state
V1	Bool	1142.2	false	False	False	False	False		Supervision entering state
R1	Bool	1142.3	false	False	False	False	False		Reserved
A1	Bool	1142.4	false	False	False	False	False		Error is acknowledged
S0	Bool	1142.5	false	False	False	False	False		Step is deactivated
L0	Bool	1142.6	false	False	False	False	False		Interlock entering state
V0	Bool	1142.7	false	False	False	False	False		Supervision leaving state
X	Bool	1143.0	false	False	False	False	False		Step is active
LA	Bool	1143.1	false	False	False	False	False		Interlock is not satisfied
VA	Bool	1143.2	false	False	False	False	False		Supervision active
RA	Bool	1143.3	false	False	False	False	False		Reserved
AA	Bool	1143.4	false	False	False	False	False		Reserved
SS	Bool	1143.5	false	False	False	False	False		System-internal
LS	Bool	1143.6	True	False	False	False	False		Direct result of the programmed interlock
VS	Bool	1143.7	false	False	False	False	False		Direct result of the programmed supervision
SNO	Int	1144.0	28	False	False	False	False		User step number
T	Time	1146.0	T#0ms	False	False	False	False		Total step activation time
U	Time	1150.0	T#0ms	False	False	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1154.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1158.0	16#0	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1162.0	16#0	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1166.0	false	False	False	False	False		System-internal
LP	Bool	1166.1	false	False	False	False	False		System-internal
LN	Bool	1166.2	false	False	False	False	False		System-internal
VP	Bool	1166.3	false	False	False	False	False		System-internal
VN	Bool	1166.4	false	False	False	False	False		System-internal

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
H_IL_ERR	Byte	1167.0	16#0	False	False	False	False		System-internal
H_SV_FLT	Byte	1168.0	16#0	False	False	False	False		System-internal
RESERVED	DWord	1170.0	16#0	False	False	False	False		Reserve
S_DISPLAY	Int	1350.0	0	True	True	False	False		Affichage interne du paramètre de sortie S_NO
S_SEL_OLD	Int	1352.0	0	True	True	False	False		Valeur précédente dans S_SEL
S_DISPIDX	Byte	1354.0	16#0	True	True	False	False		Indice de l'étape affichée dans S_NO
T_DISPIDX	Byte	1355.0	16#0	True	True	False	False		Indice de la transition affichée dans T_NO
MOP	Struct	1360.0		True	True	False	False		Mode de fonctionnement
AUTO	Bool	1360.0	true	True	True	False	False		Etat : Mode automatique
MAN	Bool	1360.1	false	True	True	False	False		Etat : Mode manuel
TAP	Bool	1360.2	false	True	True	False	False		Etat : Mode semi-automatique/Activation de l'étape suivante avec transition
TOP	Bool	1360.3	false	True	True	False	False		Etat : Mode semi-automatique/Ignorer la transition
ACK_S	Bool	1360.4	false	True	True	False	False		Requête : Acquitter l'étape figurant dans le paramètre S_NO
REG_S	Bool	1360.5	false	True	True	False	False		Requête : Enregistrer l'étape figurant dans le paramètre S_NO
T_PREV	Bool	1360.6	false	True	True	False	False		Requête : Afficher la transition valide précédente dans le paramètre T_NO
T_NEXT	Bool	1360.7	false	True	True	False	False		Requête : Afficher la transition valide suivante dans le paramètre T_NO
LOCK	Bool	1361.0	true	True	True	False	False		Etat : Traitement des Interlocks activé
SUP	Bool	1361.1	true	True	True	False	False		Etat : Traitement de Supervision activé
ACKREQ	Bool	1361.2	true	True	True	False	False		Etat : Acquittement requis
SSKIP	Bool	1361.3	false	True	True	False	False		Etat : "Ignorer les étapes" activé
OFF	Bool	1361.4	false	True	True	False	False		Requête : Désactiver toutes les étapes
INIT	Bool	1361.5	true	True	True	False	False		Requête : Mettre le graphe à l'état initial
HALT	Bool	1361.6	false	True	True	False	False		Etat : Le graphe est arrêté
TMS_HALT	Bool	1361.7	false	True	True	False	False		Etat : Toutes les temporisations internes sont arrêtées
OPS_ZERO	Bool	1362.0	false	True	True	False	False		Etat : Tous les opérandes traités avec N, L, D mis à 0
SACT_DISP	Bool	1362.1	true	True	True	False	False		Etat : N'afficher que les étapes actives
SEF_DISP	Bool	1362.2	false	True	True	False	False		Etat : Afficher uniquement les étapes avec erreurs et perturbations
SALL_DISP	Bool	1362.3	false	True	True	False	False		Etat : Afficher toutes les étapes
S_PREV	Bool	1362.4	false	True	True	False	False		Requête : Afficher l'étape précédente dans le paramètre S_NO
S_NEXT	Bool	1362.5	false	True	True	False	False		Requête : Afficher l'étape suivante dans le paramètre S_NO
S_SELOK	Bool	1362.6	false	True	True	False	False		Requête : Afficher dans le paramètre S_NO le numéro d'étape figurant dans S_SEL
S_ON	Bool	1362.7	false	True	True	False	False		Requête : Activer l'étape figurant dans le paramètre S_NO
S_OFF	Bool	1363.0	false	True	True	False	False		Requête : Désactiver l'étape figurant dans le paramètre S_NO
T_PUSH	Bool	1363.1	false	True	True	False	False		Requête : Validation de commutation pour transition
REG	Bool	1363.2	false	True	True	False	False		Requête : Enregistrer toutes les erreurs Interlock et Supervision
ACK	Bool	1363.3	false	True	True	False	False		Requête : Acquitter toutes les erreurs Interlock et Supervision
IL_PERM	Bool	1363.4	false	True	True	False	False		Etat : Traitement permanent de tous les Interlocks
T_PERM	Bool	1363.5	false	True	True	False	False		Etat : Traitement permanent de toutes les transitions
ILP_MAN	Bool	1363.6	false	True	True	False	False		Etat : Traitement permanent de tous les Interlocks en mode manuel
TICKS	Struct	1364.0		True	True	False	False		Périodes d'horloge
DELTA	Time	1364.0	T#0ms	True	True	False	False		Différence de temps entre deux cycles

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
OLD	Time	1368.0	T#0ms	True	True	False	False		Valeur du compteur de périodes 10 ms au dernier cycle
NEW	Time	1372.0	T#0ms	True	True	False	False		Valeur du compteur de périodes 10 ms dans ce cycle
SQ_FLAGS	Struct	1376.0		True	True	False	False		Mémentos de graphe
ERR_FLT	Bool	1376.0	false	True	True	False	False		Erreur groupée Interlock et Supervision
ERROR	Bool	1376.1	false	True	True	False	False		Erreur groupée Interlock
FAULT	Bool	1376.2	false	True	True	False	False		Erreur groupée Supervision
RT_FAIL	Bool	1376.3	false	True	True	False	False		Erreur à l'exécution
NO_SNO	Bool	1376.4	false	True	True	False	False		Numéro d'étape requis introuvable
NF_OFL	Bool	1376.5	false	True	True	False	False		Débordement : Trop de requêtes ON ou OFF
SA_OFL	Bool	1376.6	false	True	True	False	False		Débordement : Trop d'étapes activées
TV_OFL	Bool	1376.7	false	True	True	False	False		Débordement : Trop de transitions valides
MSG_OFL	Bool	1377.0	false	True	True	False	False		Débordement : Pas assez de ressources système pour ALARM_S
NO_SWI	Bool	1377.1	false	True	True	False	False		Pas de commutation dans ce cycle
CYC_OP	Bool	1377.2	false	True	True	False	False		Exécution cyclique du graphe après l'initialisation
AS_MSG	Bool	1377.3	true	True	True	False	False		Messages à l'exécution activés ou désactivés par instruction
AS_SEND	Bool	1377.4	false	True	True	False	False		Envoyer les messages de WR_USMSG ou les entrer uniquement dans le tampon de diagnostic
SQ_BUSY	Bool	1377.5	false	True	True	False	False		Mémento de front interne pour le traitement séquentiel
SA_BUSY	Bool	1377.6	false	True	True	False	False		Mémento de front interne pour le traitement séquentiel
AS_SIG	Bool	1377.7	false	True	True	False	False		Mémentos de front pour messages de Alarm_S et Alarm_SQ
Temp									
Constant									

Alarms

Enable alarms	True

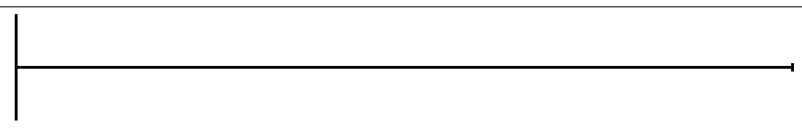
Category	Category enabler	Display class
Error		0
Warning		0
Info		0
Category 4		0
Category 5		0
Category 6		0
Category 7		0
Category 8		0

Category for interlocks	Error	**Subcategory 1 for interlocks**		**Subcategory 2 for interlocks**	
Category for supervisions	Error	**Subcategory 1 for supervisions**		**Subcategory 2 for supervisions**	

Symbol	Address	Type	Comment

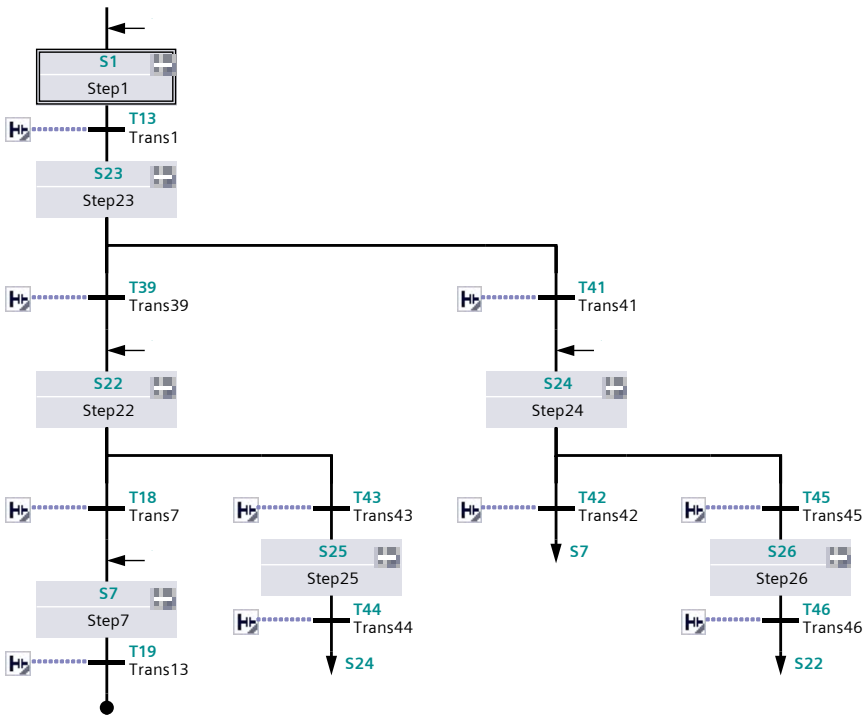
Permanent pre-instructions

1 :



Sequences (5)

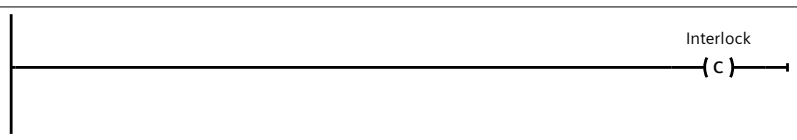
1 :



S1 - [Initial step]:Step1

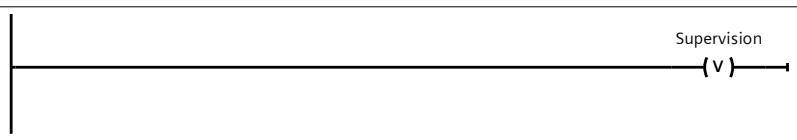
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

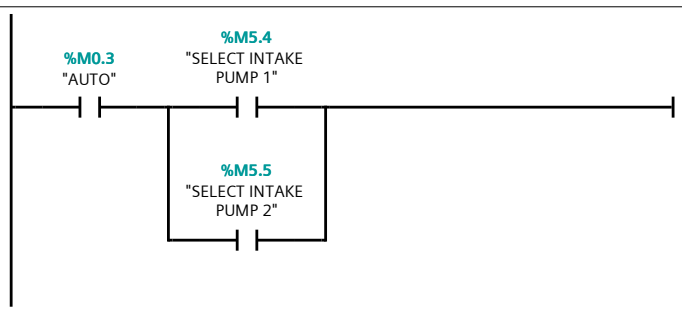
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action
		R	"BAR SCREEN Filter"
		R	"Rotating Drum Screaan Filter"
		R	"Seawater Pump 1 To Elech-Station"
		R	"Seawater Pump 2 To Elech-Station"
		R	"Seawater supply valve 1"
		R	"Seawater supply valve 2"

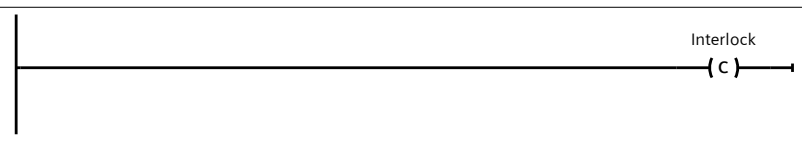
T13:Trans1



S24:Step24

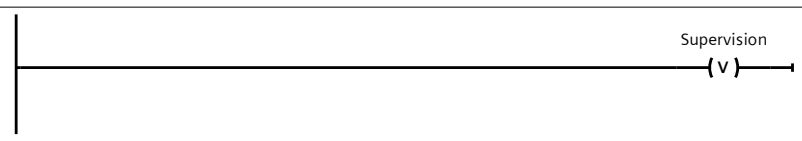
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

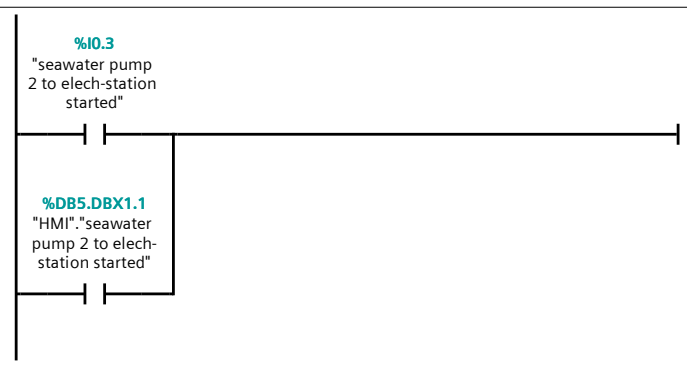
Supervision alarm	
Alarm text	



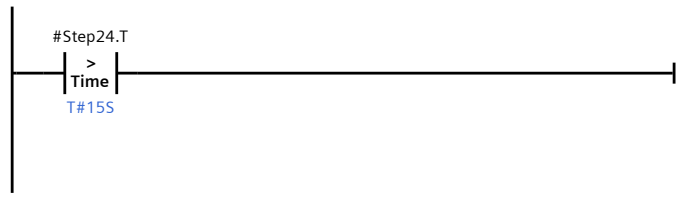
Actions:

Actions:			
Interlock	Event	Qualifier	Action
		S	"Seawater Pump 2 To Elech-Station"
		S	"Seawater supply valve 2"

T42:Trans42



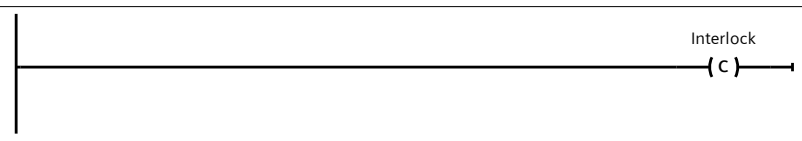
T45:Trans45



S25:Step25

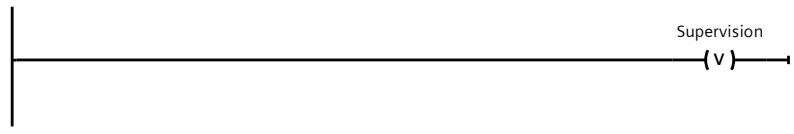
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

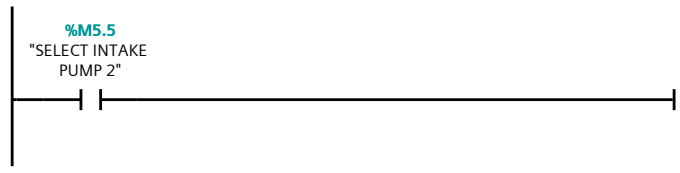
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action
		R	"Seawater Pump 1 To Elech-Station"
		R	"Seawater supply valve 1"
		N	"seawater pump 2 FAULT"

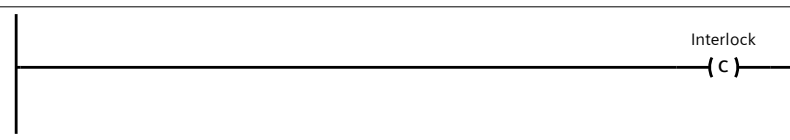
T44:Trans44



S7:Step7

Interlock -(c)-:

Interlock alarm	
Alarm text	



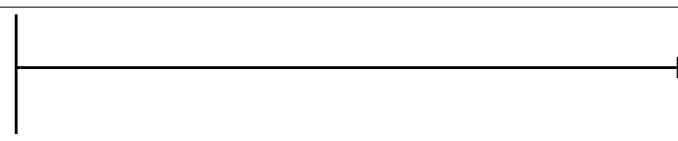
Supervision -(v)-:

Supervision alarm	
Alarm text	

Actions:

Actions:			
Interlock	Event	Qualifier	Action
		S	"seawater supply is OK"

T19:Trans13



S22:Step22

Interlock -(c)-:

Interlock alarm	
Alarm text	

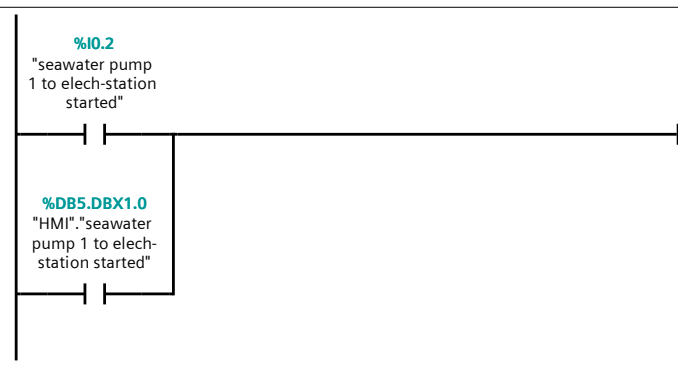
Supervision -(v)-:

Supervision alarm	
Alarm text	

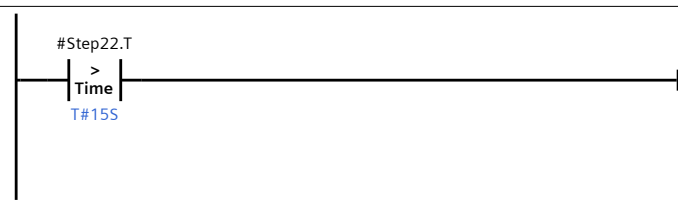
Actions:

Actions:			
Interlock	Event	Qualifier	Action
		S	"Seawater Pump 1 To Elech-Station"
		S	"Seawater supply valve 1"

T18:Trans7



T43:Trans43



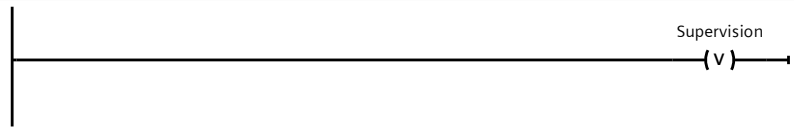
S23:Step23

Interlock -(c)-:

Interlock alarm	
Alarm text	

Supervision -(v)-:

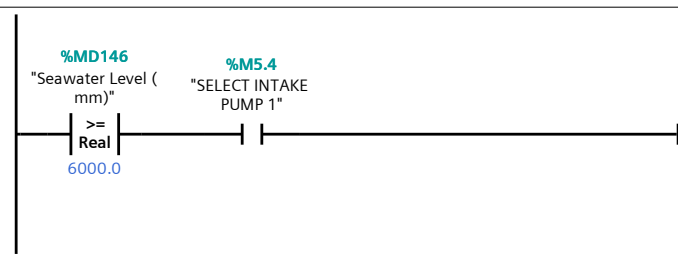
Supervision alarm	
Alarm text	



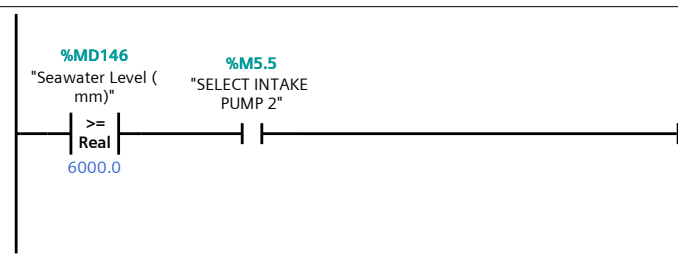
Actions:

Actions:			
Interlock	Event	Qualifier	Action

T39:Trans39



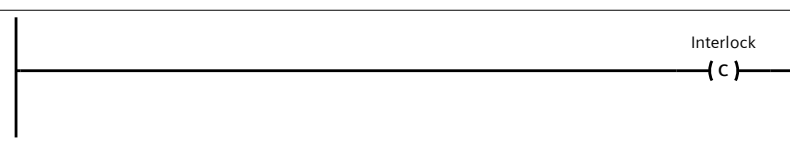
T41:Trans41



S26:Step26

Interlock -(c)-:

Interlock alarm	
Alarm text	



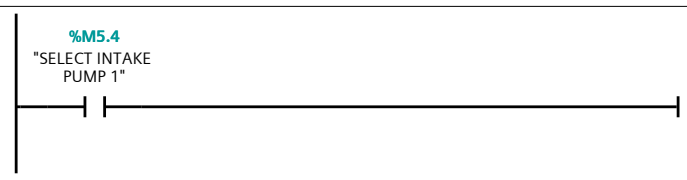
Supervision -(v)-:

Supervision alarm	
Alarm text	

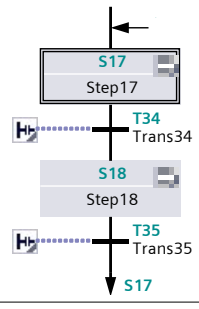
Actions:

Actions:			
Interlock	Event	Qualifier	Action
		R	"Seawater Pump 2 To Elech-Station"
		R	"Seawater supply valve 2"
		N	"seawater pump 2 FAULT"

T46:Trans46



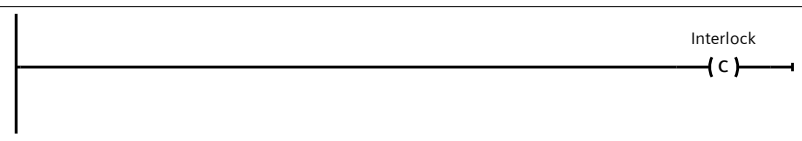
2:



S17 - [Initial step]:Step17

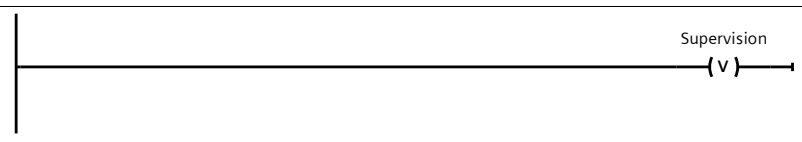
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

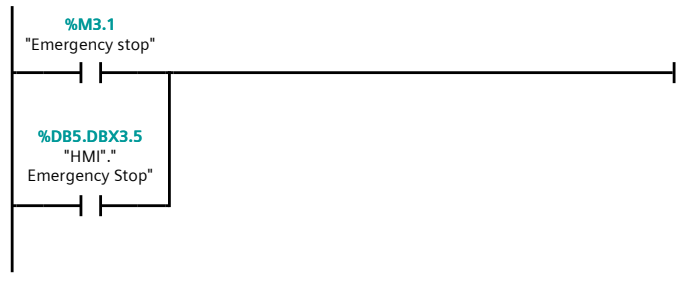
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action
	S1	ON	#Step1

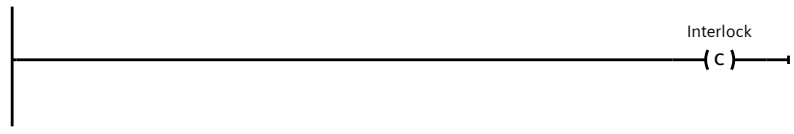
T34:Trans34



S18:Step18

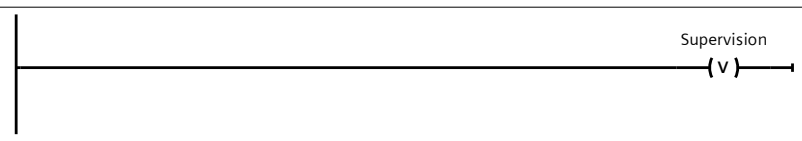
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

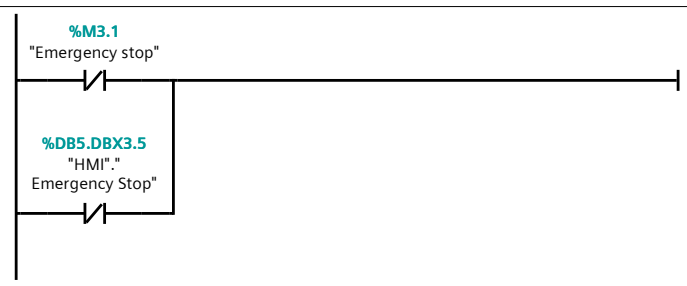
Supervision alarm	
Alarm text	



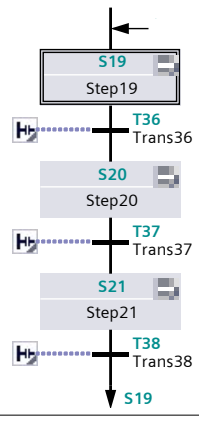
Actions:

Actions:			
Interlock	Event	Qualifier	Action
	S1	OFF	S_ALL

T35:Trans35



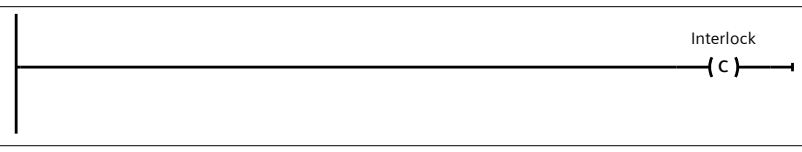
3:



S19 - [Initial step]:Step19

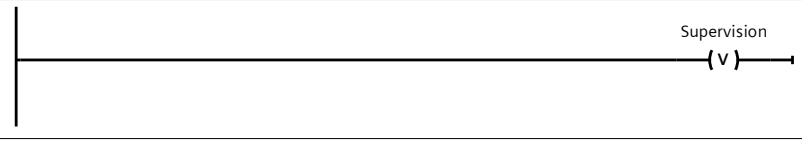
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

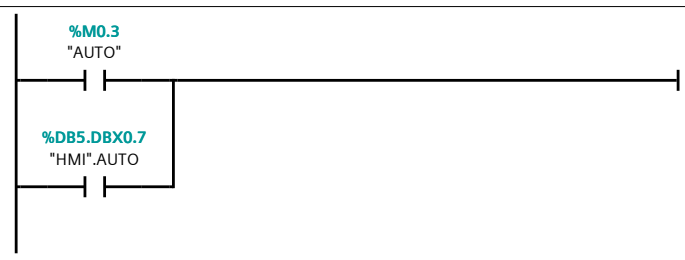
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**
		R	"washing pomps started"
		R	"washing filters valve"
		R	"washing supply valve"

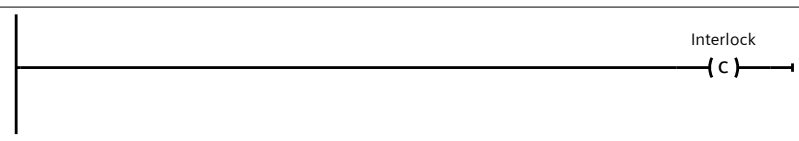
T36:Trans36



S20:Step20

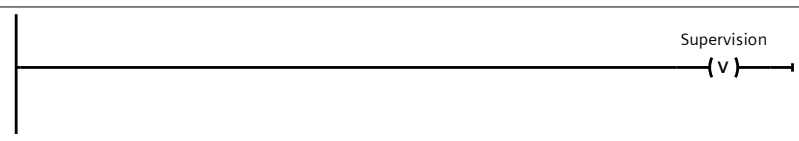
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

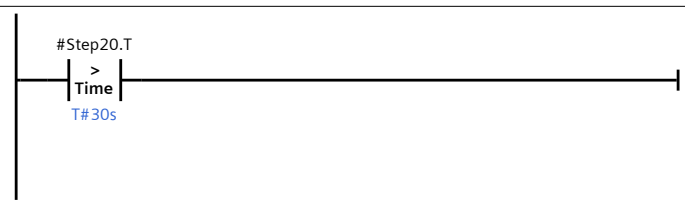
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**
		S	"AUTO"

T37:Trans37



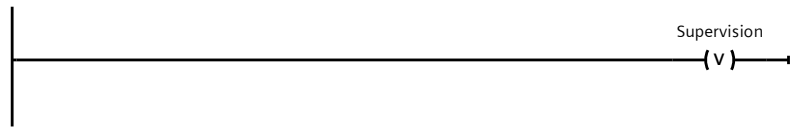
S21:Step21

Interlock -(c)-:

Interlock alarm	
Alarm text	

Supervision -(v)-:

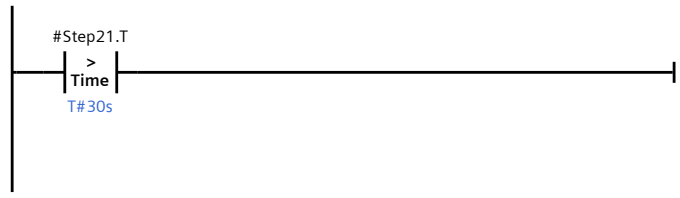
Supervision alarm	
Alarm text	



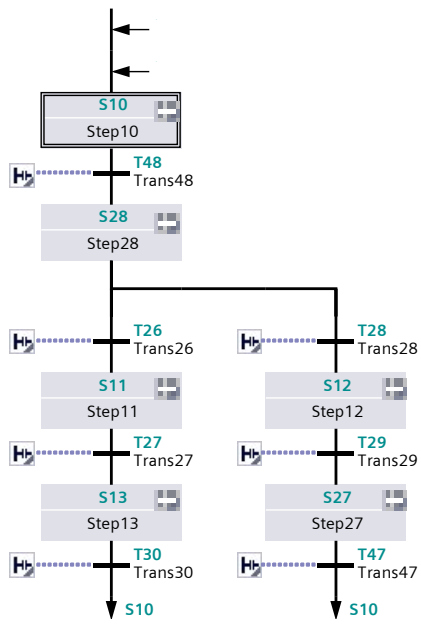
Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**
		S	"washing pomps started"
		S	"washing filters valve"
		S	"washing supply valve"

T38:Trans38



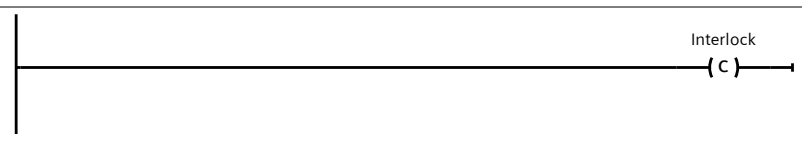
4:



S10 - [Initial step]:Step10

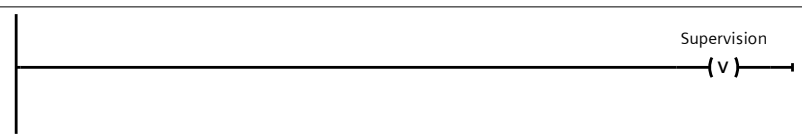
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

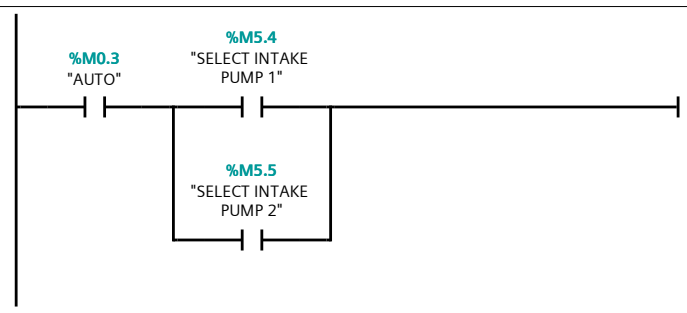
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**

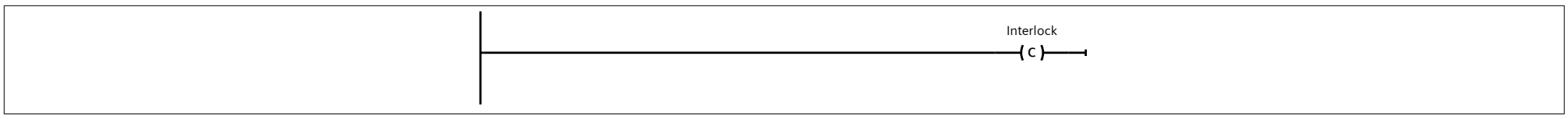
T48:Trans48



S11:Step11

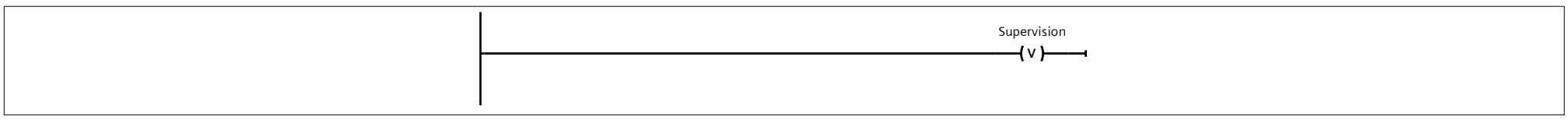
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

Supervision alarm	
Alarm text	



Actions:

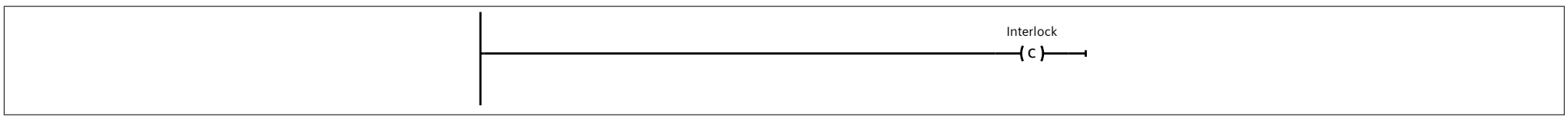
Actions:			
Interlock	Event	Qualifier	Action
		S	"BAR SCREEN Filter"

T27:Trans27

S12:Step12

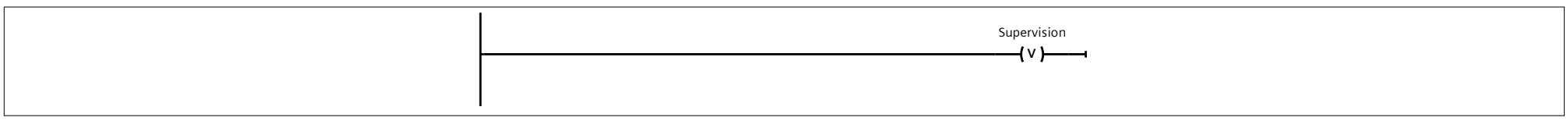
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

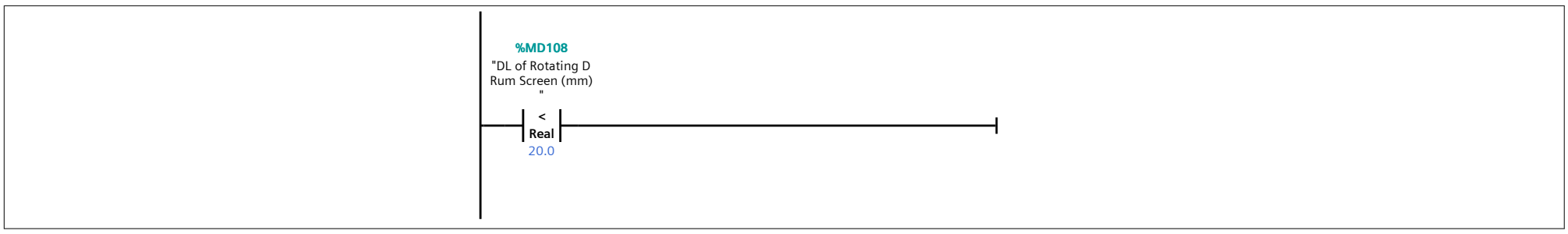
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action
		S	"Rotating Drum Screaan Filter"

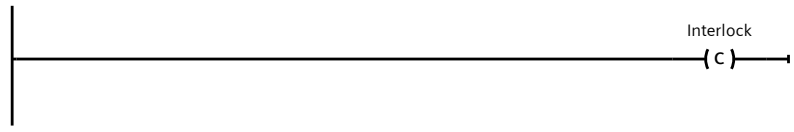
T29:Trans29



S13:Step13

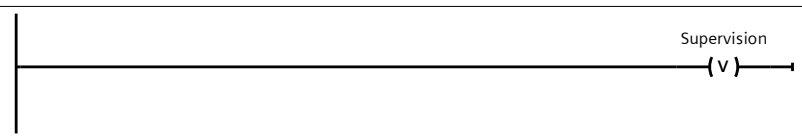
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

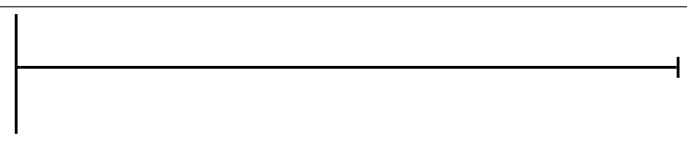
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action
		R	"BAR SCREEN Filter"

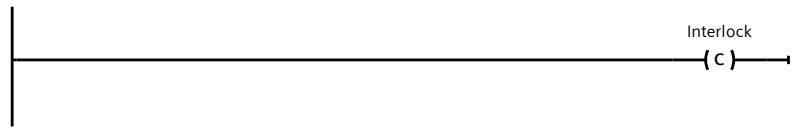
T30:Trans30



S27:Step27

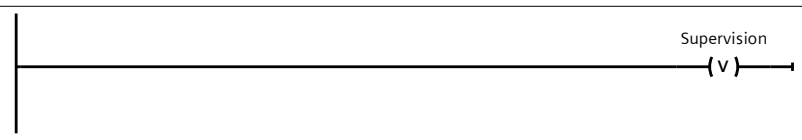
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

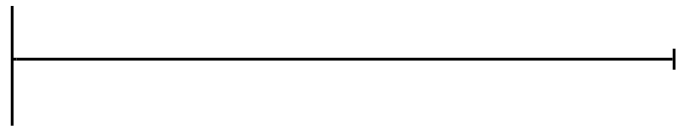
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action
		R	"Rotating Drum Screaan Filter"

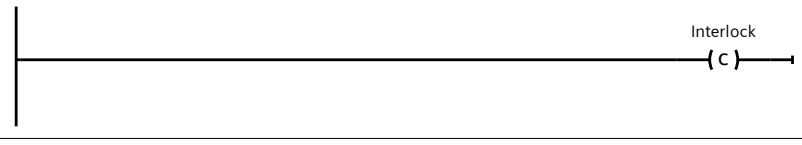
T47:Trans47



S28:Step28

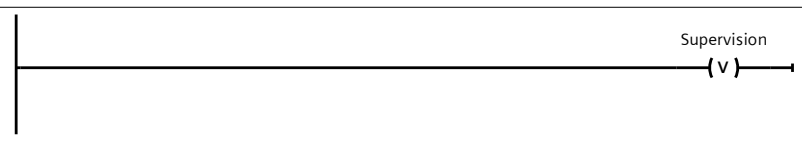
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

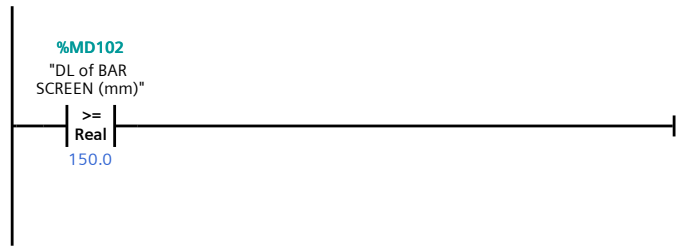
Supervision alarm	
Alarm text	



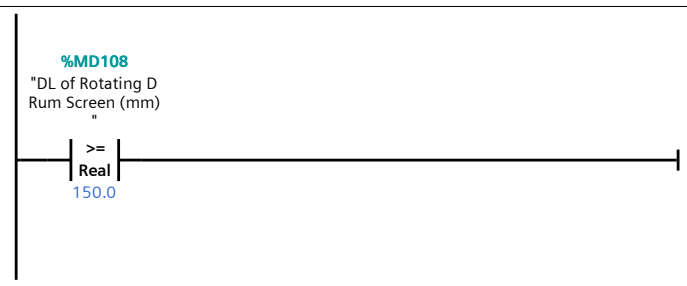
Actions:

Actions:			
Interlock	Event	Qualifier	Action

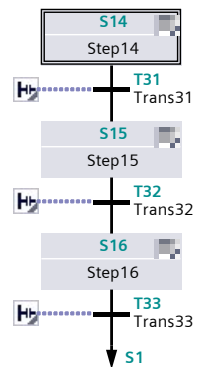
T26:Trans26



T28:Trans28



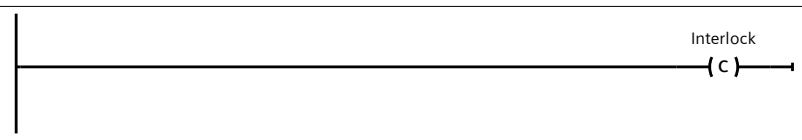
5:



S14 - [Initial step]:Step14

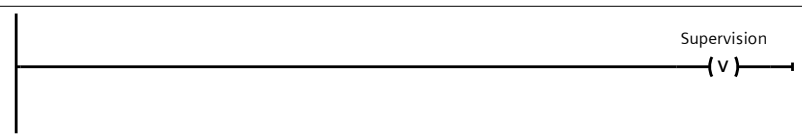
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

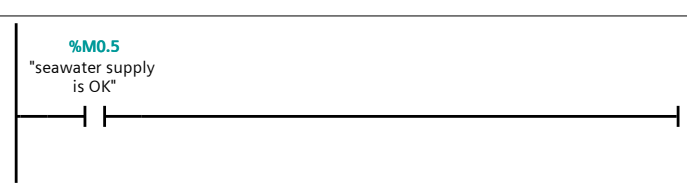
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action

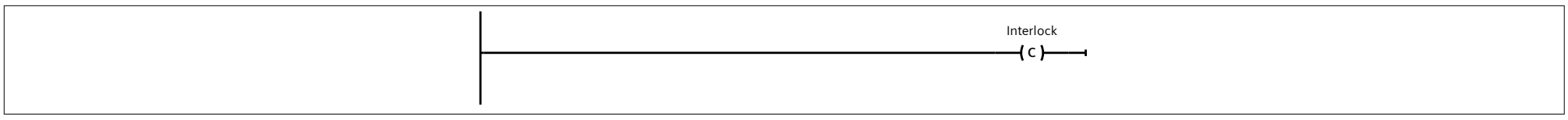
T31:Trans31



S15:Step15

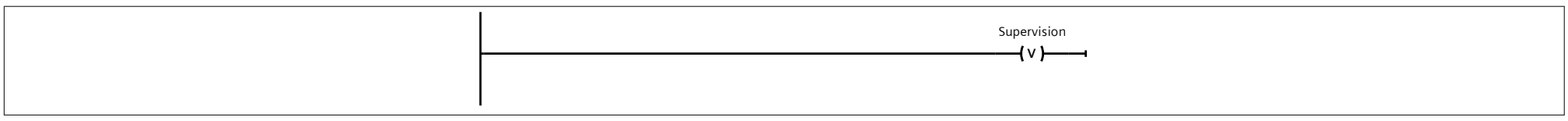
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

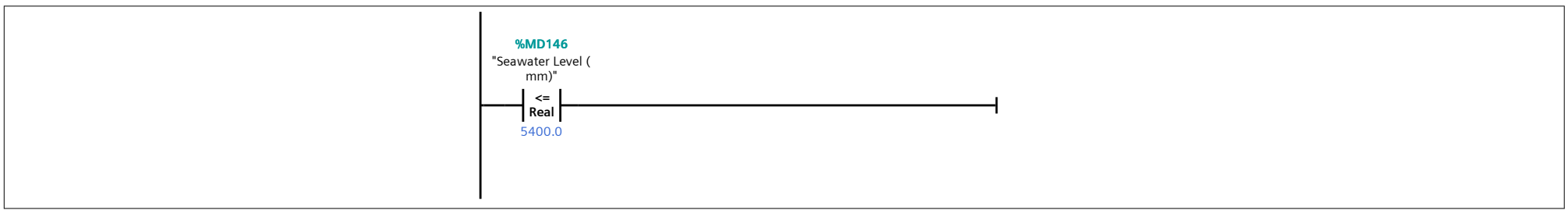
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action

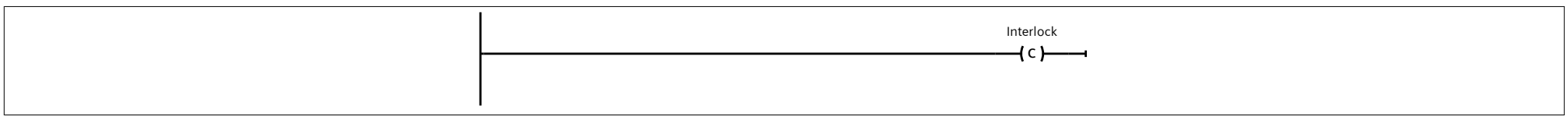
T32:Trans32



S16:Step16

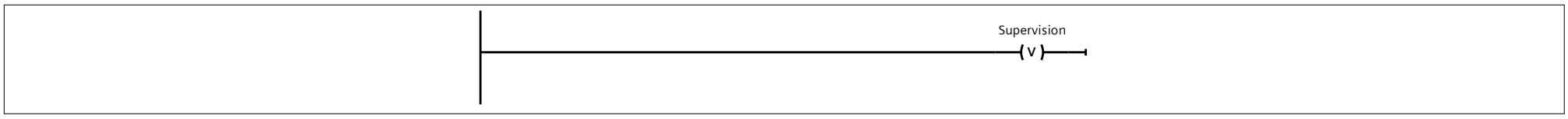
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

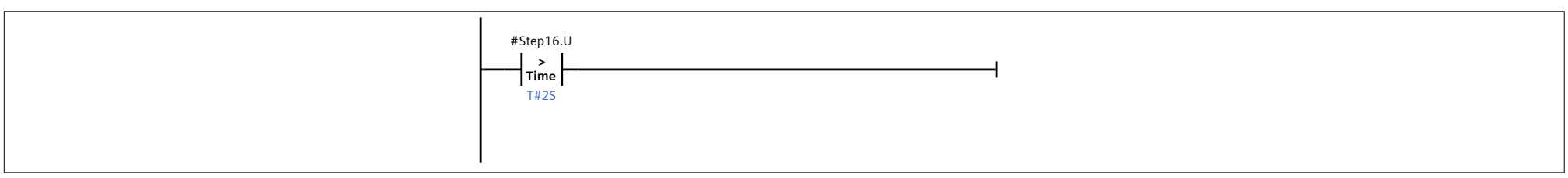
Supervision alarm	
Alarm text	



Actions:

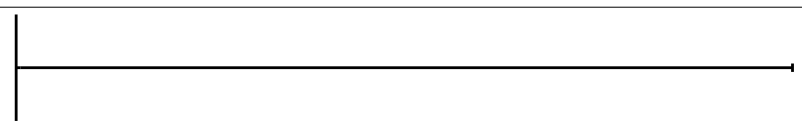
Actions:			
Interlock	Event	Qualifier	Action
		N	"seawater level LL"
		R	"Seawater Pump 1 To Elech-Station"
		R	"Seawater Pump 2 To Elech-Station"

T33:Trans33



Permanent post-instructions

1:



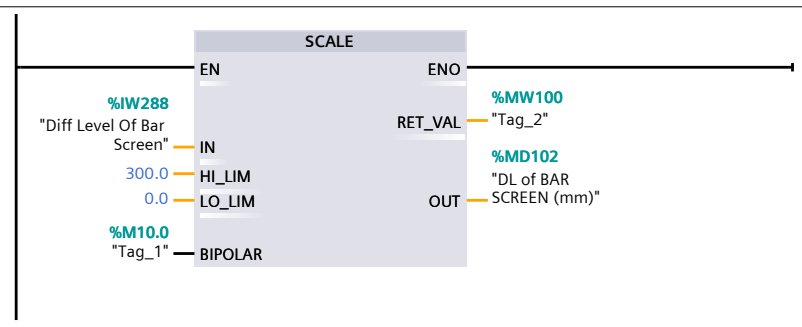
electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / Program blocks

Seawater intake sensors [FC1]

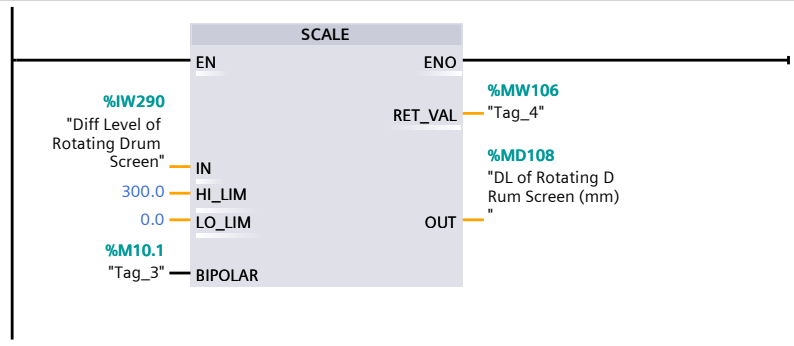
Seawater intake sensors Properties							
General							
Name	Seawater intake sensors	**Number**	1	**Type**	FC	**Language**	LAD
Numbering	Automatic						
Information							
Title		**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Offset	Default value	Comment
Input				
Output				
InOut				
Temp				
Constant				
Return				
Seawater intake sensors	Void			

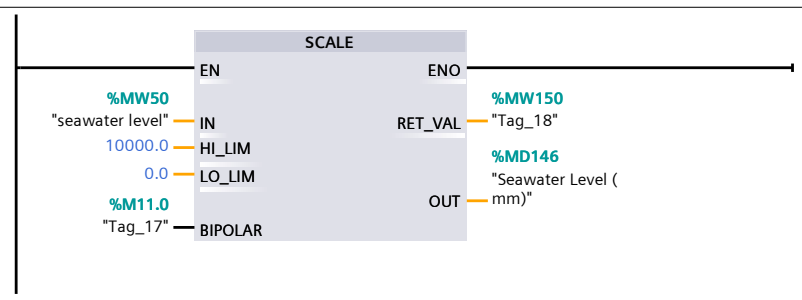
Network 1:



Network 2:



Network 3:



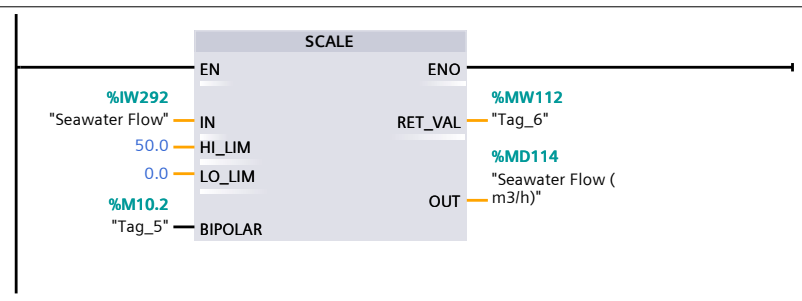
electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / Program blocks

HYPOCLORITE SENSORS [FC2]

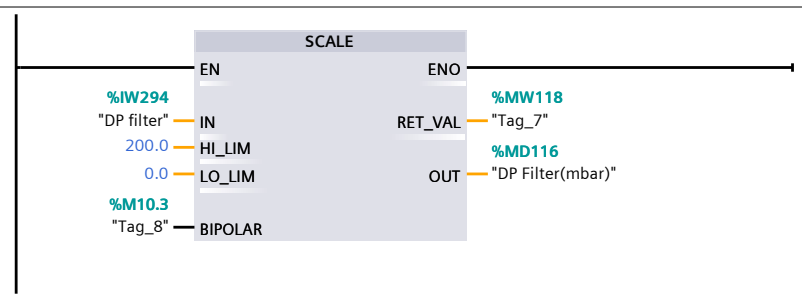
HYPOCLORITE SENSORS Properties							
General							
Name	HYPOCLORITE SENSORS	**Number**	2	**Type**	FC	**Language**	LAD
Numbering	Automatic						
Information							
Title		**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Offset	Default value	Comment
Input				
Output				
InOut				
Temp				
Constant				
Return				
HYPOCLORITE SENSORS	Void			

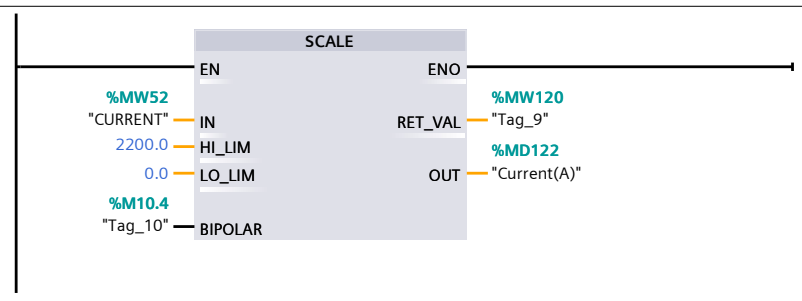
Network 1:



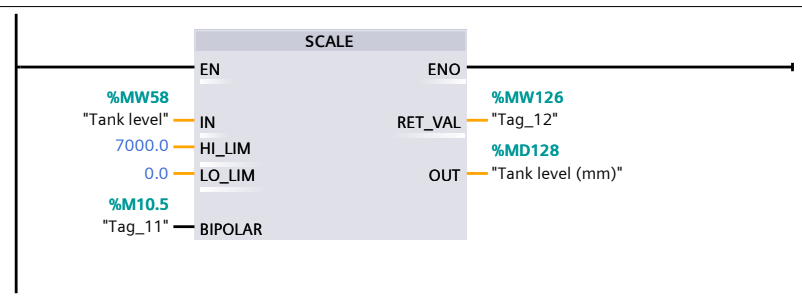
Network 2:



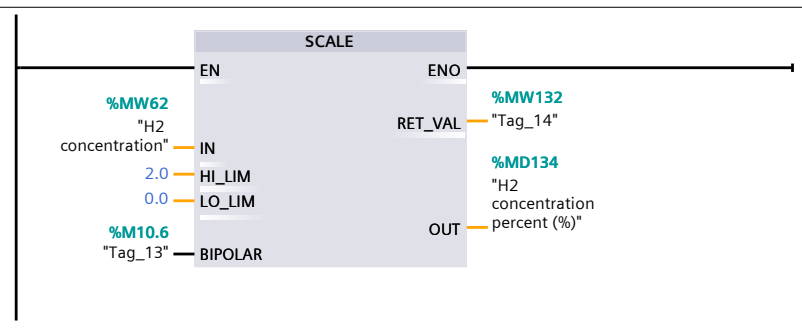
Network 3:



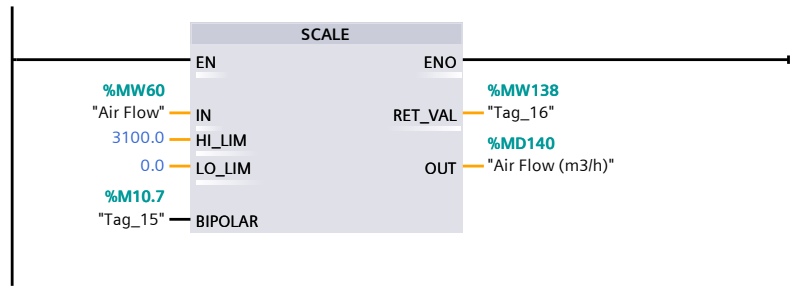
Network 4:



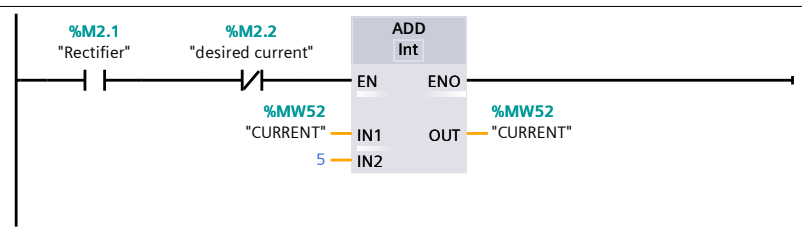
Network 5:



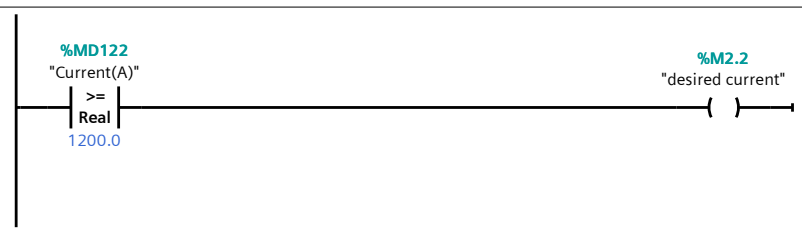
Network 6:



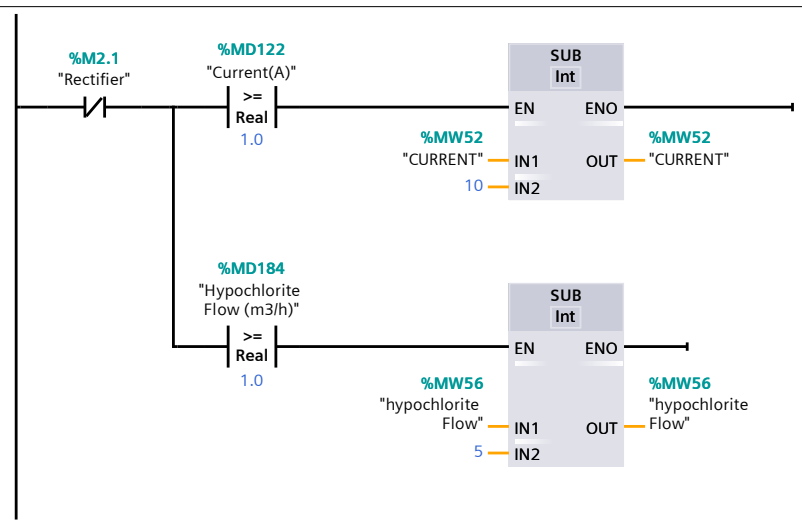
Network 7:



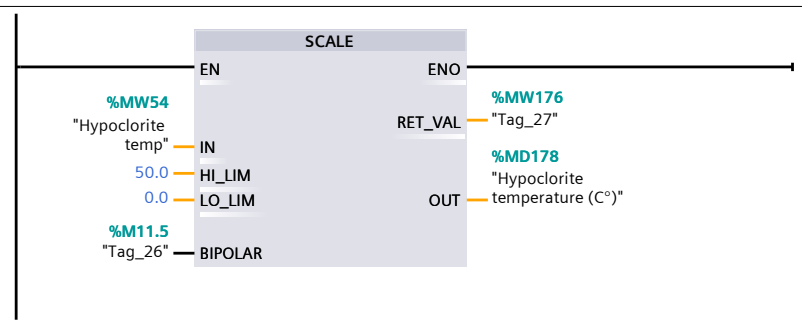
Network 8:



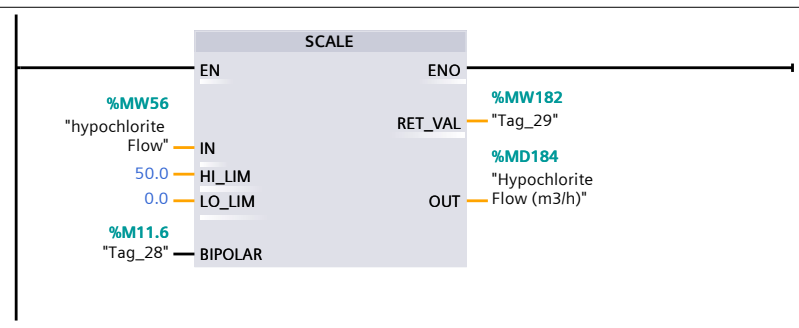
Network 9:



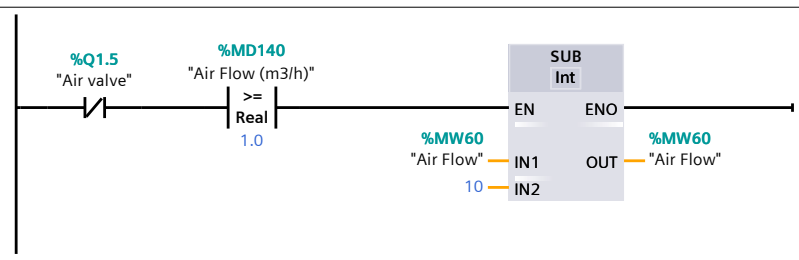
Network 10:



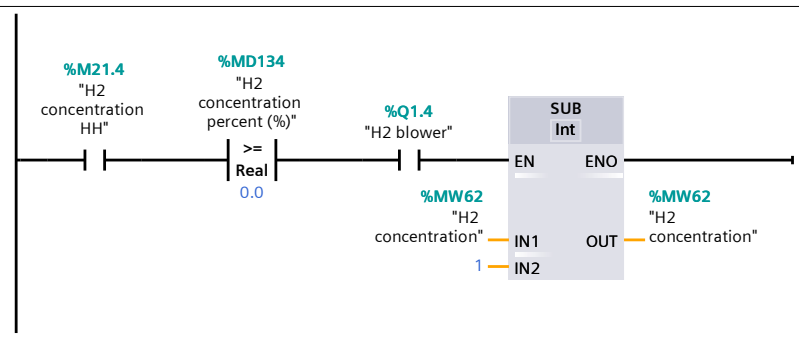
Network 11:



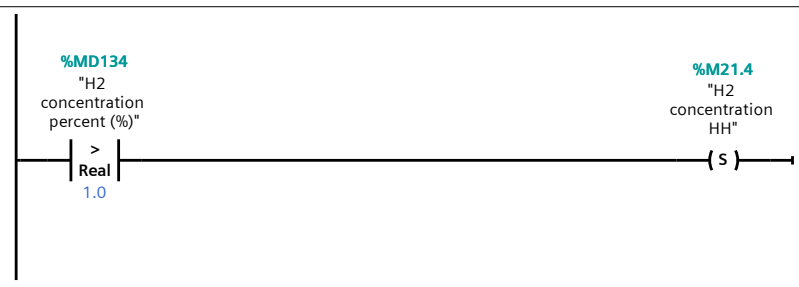
Network 12:



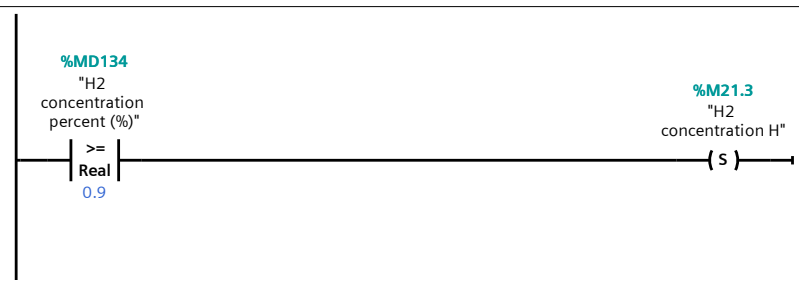
Network 13:



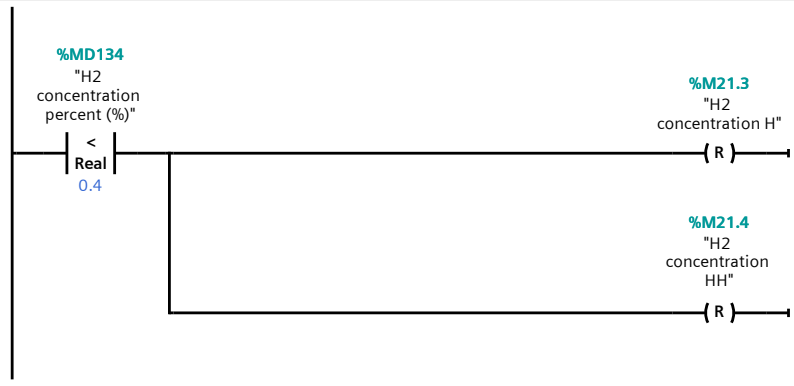
Network 14:



Network 15:



Network 16:



electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / Program blocks

Acid Cleaning [FB3]

Acid Cleaning Properties							
General							
Name	Acid Cleaning	**Number**	3	**Type**	FB	**Language**	GRAPH
Numbering	Automatic	**Network language**	LAD				
Information							
Title		**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
Input									
OFF_SQ	Bool	0.0	false	True	True	False	False		Désactiver le graphe
INIT_SQ	Bool	0.1	false	True	True	False	False		Mettre le graphe à l'état initial
ACK_EF	Bool	0.2	false	True	True	False	False		Acquitter toutes les erreurs et perturbations
S_PREV	Bool	0.3	false	True	True	False	False		Afficher l'étape précédente dans le paramètre S_NO
S_NEXT	Bool	0.4	false	True	True	False	False		Afficher l'étape suivante dans le paramètre S_NO
SW_AUTO	Bool	0.5	false	True	True	False	False		Mode automatique
SW_TAP	Bool	0.6	false	True	True	False	False		Mode semi-automatique/Activation de l'étape suivante avec transition
SW_TOP	Bool	0.7	false	True	True	False	False		Mode semi-automatique/Ignorer la transition
SW_MAN	Bool	1.0	false	True	True	False	False		Mode manuel
S_SEL	Int	2.0	0	True	True	False	False		Sélectionner l'étape à afficher dans S_NO
S_ON	Bool	4.0	false	True	True	False	False		Activer l'étape figurant dans le paramètre S_NO
S_OFF	Bool	4.1	false	True	True	False	False		Désactiver l'étape figurant dans le paramètre S_NO
T_PUSH	Bool	4.2	false	True	True	False	False		Validation de commutation pour transition en mode semi-automatique
Output									
S_NO	Int	6.0	0	True	True	False	False		Numéro d'étape
S_MORE	Bool	8.0	false	True	True	False	False		Il existe d'autres étapes qui peuvent être affichées dans S_NO.
S_ACTIVE	Bool	8.1	false	True	True	False	False		L'étape affichée dans le paramètre S_NO est activée
ERR_FLT	Bool	8.2	false	True	True	False	False		Erreur groupée Interlock or Supervision
AUTO_ON	Bool	8.3	false	True	True	False	False		Mode automatique activé
TAP_ON	Bool	8.4	false	True	True	False	False		Mode semi-automatique/Etape avec transition activé
TOP_ON	Bool	8.5	false	True	True	False	False		Mode semi-automatique/Ignorer la transition activé
MAN_ON	Bool	8.6	false	True	True	False	False		Mode manuel activé
InOut									
Static									
Trans1	GraphTransition	80.0		True	True	False	True		
TV	Bool	80.0	false	True	True	False	False		Transition is valid
TT	Bool	80.1	false	True	True	False	False		Transition is satisfied
TS	Bool	80.2	false	True	True	False	False		Transition switches
CF_IV	Bool	80.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	82.0	1	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	84.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	88.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	92.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans2	GraphTransition	96.0		True	True	False	True		
TV	Bool	96.0	false	True	True	False	False		Transition is valid
TT	Bool	96.1	false	True	True	False	False		Transition is satisfied
TS	Bool	96.2	false	True	True	False	False		Transition switches
CF_IV	Bool	96.3	true	True	True	False	False		The CRIT_FLT entry is invalid

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TNO	Int	98.0	2	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	100.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	104.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	108.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans3	GraphTransition	112.0		True	True	False	True		
TV	Bool	112.0	false	True	True	False	False		Transition is valid
TT	Bool	112.1	false	True	True	False	False		Transition is satisfied
TS	Bool	112.2	false	True	True	False	False		Transition switches
CF_IV	Bool	112.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	114.0	3	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	116.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	120.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	124.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans4	GraphTransition	128.0		True	True	False	True		
TV	Bool	128.0	false	True	True	False	False		Transition is valid
TT	Bool	128.1	false	True	True	False	False		Transition is satisfied
TS	Bool	128.2	false	True	True	False	False		Transition switches
CF_IV	Bool	128.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	130.0	4	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	132.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	136.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	140.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans5	GraphTransition	144.0		True	True	False	True		
TV	Bool	144.0	false	True	True	False	False		Transition is valid
TT	Bool	144.1	false	True	True	False	False		Transition is satisfied
TS	Bool	144.2	false	True	True	False	False		Transition switches
CF_IV	Bool	144.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	146.0	5	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	148.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	152.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	156.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans6	GraphTransition	160.0		True	True	False	True		
TV	Bool	160.0	false	True	True	False	False		Transition is valid
TT	Bool	160.1	false	True	True	False	False		Transition is satisfied
TS	Bool	160.2	false	True	True	False	False		Transition switches
CF_IV	Bool	160.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	162.0	6	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	164.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	168.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	172.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans7	GraphTransition	176.0		True	True	False	True		
TV	Bool	176.0	false	True	True	False	False		Transition is valid
TT	Bool	176.1	false	True	True	False	False		Transition is satisfied
TS	Bool	176.2	false	True	True	False	False		Transition switches

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CF_IV	Bool	176.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	178.0	7	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	180.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	184.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	188.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans8	GraphTransition	192.0		True	True	False	True		
TV	Bool	192.0	false	True	True	False	False		Transition is valid
TT	Bool	192.1	false	True	True	False	False		Transition is satisfied
TS	Bool	192.2	false	True	True	False	False		Transition switches
CF_IV	Bool	192.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	194.0	8	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	196.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	200.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	204.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans9	GraphTransition	208.0		True	True	False	True		
TV	Bool	208.0	false	True	True	False	False		Transition is valid
TT	Bool	208.1	false	True	True	False	False		Transition is satisfied
TS	Bool	208.2	false	True	True	False	False		Transition switches
CF_IV	Bool	208.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	210.0	9	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	212.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	216.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	220.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans10	GraphTransition	224.0		True	True	False	True		
TV	Bool	224.0	false	True	True	False	False		Transition is valid
TT	Bool	224.1	false	True	True	False	False		Transition is satisfied
TS	Bool	224.2	false	True	True	False	False		Transition switches
CF_IV	Bool	224.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	226.0	10	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	228.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	232.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	236.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans11	GraphTransition	240.0		True	True	False	True		
TV	Bool	240.0	false	True	True	False	False		Transition is valid
TT	Bool	240.1	false	True	True	False	False		Transition is satisfied
TS	Bool	240.2	false	True	True	False	False		Transition switches
CF_IV	Bool	240.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	242.0	11	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	244.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	248.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	252.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans12	GraphTransition	256.0		True	True	False	True		
TV	Bool	256.0	false	True	True	False	False		Transition is valid
TT	Bool	256.1	false	True	True	False	False		Transition is satisfied

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TS	Bool	256.2	false	True	True	False	False		Transition switches
CF_IV	Bool	256.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	258.0	12	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	260.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	264.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	268.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans13	GraphTransition	272.0		True	True	False	True		
TV	Bool	272.0	false	True	True	False	False		Transition is valid
TT	Bool	272.1	false	True	True	False	False		Transition is satisfied
TS	Bool	272.2	false	True	True	False	False		Transition switches
CF_IV	Bool	272.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	274.0	13	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	276.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	280.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	284.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans14	GraphTransition	288.0		True	True	False	True		
TV	Bool	288.0	false	True	True	False	False		Transition is valid
TT	Bool	288.1	false	True	True	False	False		Transition is satisfied
TS	Bool	288.2	false	True	True	False	False		Transition switches
CF_IV	Bool	288.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	290.0	14	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	292.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	296.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	300.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans15	GraphTransition	304.0		True	True	False	True		
TV	Bool	304.0	false	True	True	False	False		Transition is valid
TT	Bool	304.1	false	True	True	False	False		Transition is satisfied
TS	Bool	304.2	false	True	True	False	False		Transition switches
CF_IV	Bool	304.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	306.0	15	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	308.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	312.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	316.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans18	GraphTransition	320.0		True	True	False	True		
TV	Bool	320.0	false	True	True	False	False		Transition is valid
TT	Bool	320.1	false	True	True	False	False		Transition is satisfied
TS	Bool	320.2	false	True	True	False	False		Transition switches
CF_IV	Bool	320.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	322.0	18	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	324.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	328.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	332.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans19	GraphTransition	336.0		True	True	False	True		
TV	Bool	336.0	false	True	True	False	False		Transition is valid

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TT	Bool	336.1	false	True	True	False	False		Transition is satisfied
TS	Bool	336.2	false	True	True	False	False		Transition switches
CF_IV	Bool	336.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	338.0	19	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	340.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	344.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	348.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans20	GraphTransition	352.0		True	True	False	True		
TV	Bool	352.0	false	True	True	False	False		Transition is valid
TT	Bool	352.1	false	True	True	False	False		Transition is satisfied
TS	Bool	352.2	false	True	True	False	False		Transition switches
CF_IV	Bool	352.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	354.0	20	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	356.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	360.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	364.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans21	GraphTransition	368.0		True	True	False	True		
TV	Bool	368.0	false	True	True	False	False		Transition is valid
TT	Bool	368.1	false	True	True	False	False		Transition is satisfied
TS	Bool	368.2	false	True	True	False	False		Transition switches
CF_IV	Bool	368.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	370.0	21	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	372.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	376.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	380.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans22	GraphTransition	384.0		True	True	False	True		
TV	Bool	384.0	false	True	True	False	False		Transition is valid
TT	Bool	384.1	false	True	True	False	False		Transition is satisfied
TS	Bool	384.2	false	True	True	False	False		Transition switches
CF_IV	Bool	384.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	386.0	22	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	388.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	392.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	396.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans23	GraphTransition	400.0		True	True	False	True		
TV	Bool	400.0	false	True	True	False	False		Transition is valid
TT	Bool	400.1	false	True	True	False	False		Transition is satisfied
TS	Bool	400.2	false	True	True	False	False		Transition switches
CF_IV	Bool	400.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	402.0	23	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	404.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	408.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	412.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans24	GraphTransition	416.0		True	True	False	True		

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TV	Bool	416.0	false	True	True	False	False		Transition is valid
TT	Bool	416.1	false	True	True	False	False		Transition is satisfied
TS	Bool	416.2	false	True	True	False	False		Transition switches
CF_IV	Bool	416.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	418.0	24	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	420.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	424.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	428.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans25	GraphTransition	432.0		True	True	False	True		
TV	Bool	432.0	false	True	True	False	False		Transition is valid
TT	Bool	432.1	false	True	True	False	False		Transition is satisfied
TS	Bool	432.2	false	True	True	False	False		Transition switches
CF_IV	Bool	432.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	434.0	25	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	436.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	440.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	444.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans26	GraphTransition	448.0		True	True	False	True		
TV	Bool	448.0	false	True	True	False	False		Transition is valid
TT	Bool	448.1	false	True	True	False	False		Transition is satisfied
TS	Bool	448.2	false	True	True	False	False		Transition switches
CF_IV	Bool	448.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	450.0	26	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	452.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	456.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	460.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans27	GraphTransition	464.0		True	True	False	True		
TV	Bool	464.0	false	True	True	False	False		Transition is valid
TT	Bool	464.1	false	True	True	False	False		Transition is satisfied
TS	Bool	464.2	false	True	True	False	False		Transition switches
CF_IV	Bool	464.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	466.0	27	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	468.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	472.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	476.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans28	GraphTransition	480.0		True	True	False	True		
TV	Bool	480.0	false	True	True	False	False		Transition is valid
TT	Bool	480.1	false	True	True	False	False		Transition is satisfied
TS	Bool	480.2	false	True	True	False	False		Transition switches
CF_IV	Bool	480.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	482.0	28	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	484.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	488.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	492.0	16#0	True	True	False	False		Copy of CRIT if an error occurs

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
Trans29	GraphTransition	496.0		True	True	False	True		
TV	Bool	496.0	false	True	True	False	False		Transition is valid
TT	Bool	496.1	false	True	True	False	False		Transition is satisfied
TS	Bool	496.2	false	True	True	False	False		Transition switches
CF_IV	Bool	496.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	498.0	29	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	500.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	504.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	508.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans30	GraphTransition	512.0		True	True	False	True		
TV	Bool	512.0	false	True	True	False	False		Transition is valid
TT	Bool	512.1	false	True	True	False	False		Transition is satisfied
TS	Bool	512.2	false	True	True	False	False		Transition switches
CF_IV	Bool	512.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	514.0	30	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	516.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	520.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	524.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans33	GraphTransition	528.0		True	True	False	True		
TV	Bool	528.0	false	True	True	False	False		Transition is valid
TT	Bool	528.1	false	True	True	False	False		Transition is satisfied
TS	Bool	528.2	false	True	True	False	False		Transition switches
CF_IV	Bool	528.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	530.0	33	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	532.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	536.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	540.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans34	GraphTransition	544.0		True	True	False	True		
TV	Bool	544.0	false	True	True	False	False		Transition is valid
TT	Bool	544.1	false	True	True	False	False		Transition is satisfied
TS	Bool	544.2	false	True	True	False	False		Transition switches
CF_IV	Bool	544.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	546.0	34	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	548.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	552.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	556.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans35	GraphTransition	560.0		True	True	False	True		
TV	Bool	560.0	false	True	True	False	False		Transition is valid
TT	Bool	560.1	false	True	True	False	False		Transition is satisfied
TS	Bool	560.2	false	True	True	False	False		Transition switches
CF_IV	Bool	560.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	562.0	35	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	564.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	568.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle

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CRIT_FLT	DWord	572.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Trans37	GraphTransition	576.0		True	True	False	True		
TV	Bool	576.0	false	True	True	False	False		Transition is valid
TT	Bool	576.1	false	True	True	False	False		Transition is satisfied
TS	Bool	576.2	false	True	True	False	False		Transition switches
CF_IV	Bool	576.3	true	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	578.0	37	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	580.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	584.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	588.0	16#0	True	True	False	False		Copy of CRIT if an error occurs
Step1	GraphStep	624.0		True	True	False	True		
S1	Bool	624.0	false	True	True	False	False		Step is activated
L1	Bool	624.1	false	True	True	False	False		interlock leaving state
V1	Bool	624.2	false	True	True	False	False		Supervision entering state
R1	Bool	624.3	false	True	True	False	False		Reserved
A1	Bool	624.4	false	True	True	False	False		Error is acknowledged
S0	Bool	624.5	false	True	True	False	False		Step is deactivated
L0	Bool	624.6	false	True	True	False	False		Interlock entering state
V0	Bool	624.7	false	True	True	False	False		Supervision leaving state
X	Bool	625.0	false	True	True	False	False		Step is active
LA	Bool	625.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	625.2	false	True	True	False	False		Supervision active
RA	Bool	625.3	false	True	True	False	False		Reserved
AA	Bool	625.4	false	True	True	False	False		Reserved
SS	Bool	625.5	false	True	True	False	False		System-internal
LS	Bool	625.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	625.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	626.0	1	True	True	False	False		User step number
T	Time	628.0	T#0ms	True	True	False	False		Total step activation time
U	Time	632.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	636.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	640.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	644.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	648.0	false	True	True	False	False		System-internal
LP	Bool	648.1	false	True	True	False	False		System-internal
LN	Bool	648.2	false	True	True	False	False		System-internal
VP	Bool	648.3	false	True	True	False	False		System-internal
VN	Bool	648.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	649.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	650.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	652.0	16#0	True	True	False	False		Reserve
Step2	GraphStep	656.0		True	True	False	True		
S1	Bool	656.0	false	True	True	False	False		Step is activated
L1	Bool	656.1	false	True	True	False	False		interlock leaving state
V1	Bool	656.2	false	True	True	False	False		Supervision entering state
R1	Bool	656.3	false	True	True	False	False		Reserved
A1	Bool	656.4	false	True	True	False	False		Error is acknowledged
S0	Bool	656.5	false	True	True	False	False		Step is deactivated
L0	Bool	656.6	false	True	True	False	False		Interlock entering state
V0	Bool	656.7	false	True	True	False	False		Supervision leaving state
X	Bool	657.0	false	True	True	False	False		Step is active
LA	Bool	657.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	657.2	false	True	True	False	False		Supervision active
RA	Bool	657.3	false	True	True	False	False		Reserved
AA	Bool	657.4	false	True	True	False	False		Reserved
SS	Bool	657.5	false	True	True	False	False		System-internal
LS	Bool	657.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	657.7	false	True	True	False	False		Direct result of the programmed supervision

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
SNO	Int	658.0	2	True	True	False	False		User step number
T	Time	660.0	T#0ms	True	True	False	False		Total step activation time
U	Time	664.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	668.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	672.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	676.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	680.0	false	True	True	False	False		System-internal
LP	Bool	680.1	false	True	True	False	False		System-internal
LN	Bool	680.2	false	True	True	False	False		System-internal
VP	Bool	680.3	false	True	True	False	False		System-internal
VN	Bool	680.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	681.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	682.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	684.0	16#0	True	True	False	False		Reserve
Step3	GraphStep	688.0		True	True	False	True		
S1	Bool	688.0	false	True	True	False	False		Step is activated
L1	Bool	688.1	false	True	True	False	False		interlock leaving state
V1	Bool	688.2	false	True	True	False	False		Supervision entering state
R1	Bool	688.3	false	True	True	False	False		Reserved
A1	Bool	688.4	false	True	True	False	False		Error is acknowledged
S0	Bool	688.5	false	True	True	False	False		Step is deactivated
L0	Bool	688.6	false	True	True	False	False		Interlock entering state
V0	Bool	688.7	false	True	True	False	False		Supervision leaving state
X	Bool	689.0	false	True	True	False	False		Step is active
LA	Bool	689.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	689.2	false	True	True	False	False		Supervision active
RA	Bool	689.3	false	True	True	False	False		Reserved
AA	Bool	689.4	false	True	True	False	False		Reserved
SS	Bool	689.5	false	True	True	False	False		System-internal
LS	Bool	689.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	689.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	690.0	3	True	True	False	False		User step number
T	Time	692.0	T#0ms	True	True	False	False		Total step activation time
U	Time	696.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	700.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	704.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	708.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	712.0	false	True	True	False	False		System-internal
LP	Bool	712.1	false	True	True	False	False		System-internal
LN	Bool	712.2	false	True	True	False	False		System-internal
VP	Bool	712.3	false	True	True	False	False		System-internal
VN	Bool	712.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	713.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	714.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	716.0	16#0	True	True	False	False		Reserve
Step4	GraphStep	720.0		True	True	False	True		
S1	Bool	720.0	false	True	True	False	False		Step is activated
L1	Bool	720.1	false	True	True	False	False		interlock leaving state
V1	Bool	720.2	false	True	True	False	False		Supervision entering state
R1	Bool	720.3	false	True	True	False	False		Reserved
A1	Bool	720.4	false	True	True	False	False		Error is acknowledged
S0	Bool	720.5	false	True	True	False	False		Step is deactivated
L0	Bool	720.6	false	True	True	False	False		Interlock entering state
V0	Bool	720.7	false	True	True	False	False		Supervision leaving state
X	Bool	721.0	false	True	True	False	False		Step is active
LA	Bool	721.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	721.2	false	True	True	False	False		Supervision active
RA	Bool	721.3	false	True	True	False	False		Reserved
AA	Bool	721.4	false	True	True	False	False		Reserved

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
SS	Bool	721.5	false	True	True	False	False		System-internal
LS	Bool	721.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	721.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	722.0	4	True	True	False	False		User step number
T	Time	724.0	T#0ms	True	True	False	False		Total step activation time
U	Time	728.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	732.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	736.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	740.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	744.0	false	True	True	False	False		System-internal
LP	Bool	744.1	false	True	True	False	False		System-internal
LN	Bool	744.2	false	True	True	False	False		System-internal
VP	Bool	744.3	false	True	True	False	False		System-internal
VN	Bool	744.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	745.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	746.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	748.0	16#0	True	True	False	False		Reserve
Step5	GraphStep	752.0		True	True	False	True		
S1	Bool	752.0	false	True	True	False	False		Step is activated
L1	Bool	752.1	false	True	True	False	False		interlock leaving state
V1	Bool	752.2	false	True	True	False	False		Supervision entering state
R1	Bool	752.3	false	True	True	False	False		Reserved
A1	Bool	752.4	false	True	True	False	False		Error is acknowledged
S0	Bool	752.5	false	True	True	False	False		Step is deactivated
L0	Bool	752.6	false	True	True	False	False		Interlock entering state
V0	Bool	752.7	false	True	True	False	False		Supervision leaving state
X	Bool	753.0	false	True	True	False	False		Step is active
LA	Bool	753.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	753.2	false	True	True	False	False		Supervision active
RA	Bool	753.3	false	True	True	False	False		Reserved
AA	Bool	753.4	false	True	True	False	False		Reserved
SS	Bool	753.5	false	True	True	False	False		System-internal
LS	Bool	753.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	753.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	754.0	5	True	True	False	False		User step number
T	Time	756.0	T#0ms	True	True	False	False		Total step activation time
U	Time	760.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	764.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	768.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	772.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	776.0	false	True	True	False	False		System-internal
LP	Bool	776.1	false	True	True	False	False		System-internal
LN	Bool	776.2	false	True	True	False	False		System-internal
VP	Bool	776.3	false	True	True	False	False		System-internal
VN	Bool	776.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	777.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	778.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	780.0	16#0	True	True	False	False		Reserve
Step6	GraphStep	784.0		True	True	False	True		
S1	Bool	784.0	false	True	True	False	False		Step is activated
L1	Bool	784.1	false	True	True	False	False		interlock leaving state
V1	Bool	784.2	false	True	True	False	False		Supervision entering state
R1	Bool	784.3	false	True	True	False	False		Reserved
A1	Bool	784.4	false	True	True	False	False		Error is acknowledged
S0	Bool	784.5	false	True	True	False	False		Step is deactivated
L0	Bool	784.6	false	True	True	False	False		Interlock entering state
V0	Bool	784.7	false	True	True	False	False		Supervision leaving state

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
X	Bool	785.0	false	True	True	False	False		Step is active
LA	Bool	785.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	785.2	false	True	True	False	False		Supervision active
RA	Bool	785.3	false	True	True	False	False		Reserved
AA	Bool	785.4	false	True	True	False	False		Reserved
SS	Bool	785.5	false	True	True	False	False		System-internal
LS	Bool	785.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	785.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	786.0	6	True	True	False	False		User step number
T	Time	788.0	T#0ms	True	True	False	False		Total step activation time
U	Time	792.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	796.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	800.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	804.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	808.0	false	True	True	False	False		System-internal
LP	Bool	808.1	false	True	True	False	False		System-internal
LN	Bool	808.2	false	True	True	False	False		System-internal
VP	Bool	808.3	false	True	True	False	False		System-internal
VN	Bool	808.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	809.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	810.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	812.0	16#0	True	True	False	False		Reserve
▼ Step7	GraphStep	816.0		True	True	False	True		
S1	Bool	816.0	false	True	True	False	False		Step is activated
L1	Bool	816.1	false	True	True	False	False		interlock leaving state
V1	Bool	816.2	false	True	True	False	False		Supervision entering state
R1	Bool	816.3	false	True	True	False	False		Reserved
A1	Bool	816.4	false	True	True	False	False		Error is acknowledged
S0	Bool	816.5	false	True	True	False	False		Step is deactivated
L0	Bool	816.6	false	True	True	False	False		Interlock entering state
V0	Bool	816.7	false	True	True	False	False		Supervision leaving state
X	Bool	817.0	false	True	True	False	False		Step is active
LA	Bool	817.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	817.2	false	True	True	False	False		Supervision active
RA	Bool	817.3	false	True	True	False	False		Reserved
AA	Bool	817.4	false	True	True	False	False		Reserved
SS	Bool	817.5	false	True	True	False	False		System-internal
LS	Bool	817.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	817.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	818.0	7	True	True	False	False		User step number
T	Time	820.0	T#0ms	True	True	False	False		Total step activation time
U	Time	824.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	828.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	832.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	836.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	840.0	false	True	True	False	False		System-internal
LP	Bool	840.1	false	True	True	False	False		System-internal
LN	Bool	840.2	false	True	True	False	False		System-internal
VP	Bool	840.3	false	True	True	False	False		System-internal
VN	Bool	840.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	841.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	842.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	844.0	16#0	True	True	False	False		Reserve
▼ Step8	GraphStep	848.0		True	True	False	True		
S1	Bool	848.0	false	True	True	False	False		Step is activated
L1	Bool	848.1	false	True	True	False	False		interlock leaving state
V1	Bool	848.2	false	True	True	False	False		Supervision entering state

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
R1	Bool	848.3	false	True	True	False	False		Reserved
A1	Bool	848.4	false	True	True	False	False		Error is acknowledged
S0	Bool	848.5	false	True	True	False	False		Step is deactivated
L0	Bool	848.6	false	True	True	False	False		Interlock entering state
V0	Bool	848.7	false	True	True	False	False		Supervision leaving state
X	Bool	849.0	false	True	True	False	False		Step is active
LA	Bool	849.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	849.2	false	True	True	False	False		Supervision active
RA	Bool	849.3	false	True	True	False	False		Reserved
AA	Bool	849.4	false	True	True	False	False		Reserved
SS	Bool	849.5	false	True	True	False	False		System-internal
LS	Bool	849.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	849.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	850.0	8	True	True	False	False		User step number
T	Time	852.0	T#0ms	True	True	False	False		Total step activation time
U	Time	856.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	860.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	864.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	868.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	872.0	false	True	True	False	False		System-internal
LP	Bool	872.1	false	True	True	False	False		System-internal
LN	Bool	872.2	false	True	True	False	False		System-internal
VP	Bool	872.3	false	True	True	False	False		System-internal
VN	Bool	872.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	873.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	874.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	876.0	16#0	True	True	False	False		Reserve
▼ Step9	GraphStep	880.0		True	True	False	True		
S1	Bool	880.0	false	True	True	False	False		Step is activated
L1	Bool	880.1	false	True	True	False	False		interlock leaving state
V1	Bool	880.2	false	True	True	False	False		Supervision entering state
R1	Bool	880.3	false	True	True	False	False		Reserved
A1	Bool	880.4	false	True	True	False	False		Error is acknowledged
S0	Bool	880.5	false	True	True	False	False		Step is deactivated
L0	Bool	880.6	false	True	True	False	False		Interlock entering state
V0	Bool	880.7	false	True	True	False	False		Supervision leaving state
X	Bool	881.0	false	True	True	False	False		Step is active
LA	Bool	881.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	881.2	false	True	True	False	False		Supervision active
RA	Bool	881.3	false	True	True	False	False		Reserved
AA	Bool	881.4	false	True	True	False	False		Reserved
SS	Bool	881.5	false	True	True	False	False		System-internal
LS	Bool	881.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	881.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	882.0	9	True	True	False	False		User step number
T	Time	884.0	T#0ms	True	True	False	False		Total step activation time
U	Time	888.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	892.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	896.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	900.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	904.0	false	True	True	False	False		System-internal
LP	Bool	904.1	false	True	True	False	False		System-internal
LN	Bool	904.2	false	True	True	False	False		System-internal
VP	Bool	904.3	false	True	True	False	False		System-internal
VN	Bool	904.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	905.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	906.0	16#0	True	True	False	False		System-internal

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
RESERVED	DWord	908.0	16#0	True	True	False	False		Reserve
Step10	GraphStep	912.0		True	True	False	True		
S1	Bool	912.0	false	True	True	False	False		Step is activated
L1	Bool	912.1	false	True	True	False	False		interlock leaving state
V1	Bool	912.2	false	True	True	False	False		Supervision entering state
R1	Bool	912.3	false	True	True	False	False		Reserved
A1	Bool	912.4	false	True	True	False	False		Error is acknowledged
S0	Bool	912.5	false	True	True	False	False		Step is deactivated
L0	Bool	912.6	false	True	True	False	False		Interlock entering state
V0	Bool	912.7	false	True	True	False	False		Supervision leaving state
X	Bool	913.0	false	True	True	False	False		Step is active
LA	Bool	913.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	913.2	false	True	True	False	False		Supervision active
RA	Bool	913.3	false	True	True	False	False		Reserved
AA	Bool	913.4	false	True	True	False	False		Reserved
SS	Bool	913.5	false	True	True	False	False		System-internal
LS	Bool	913.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	913.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	914.0	10	True	True	False	False		User step number
T	Time	916.0	T#0ms	True	True	False	False		Total step activation time
U	Time	920.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	924.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	928.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	932.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	936.0	false	True	True	False	False		System-internal
LP	Bool	936.1	false	True	True	False	False		System-internal
LN	Bool	936.2	false	True	True	False	False		System-internal
VP	Bool	936.3	false	True	True	False	False		System-internal
VN	Bool	936.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	937.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	938.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	940.0	16#0	True	True	False	False		Reserve
Step11	GraphStep	944.0		True	True	False	True		
S1	Bool	944.0	false	True	True	False	False		Step is activated
L1	Bool	944.1	false	True	True	False	False		interlock leaving state
V1	Bool	944.2	false	True	True	False	False		Supervision entering state
R1	Bool	944.3	false	True	True	False	False		Reserved
A1	Bool	944.4	false	True	True	False	False		Error is acknowledged
S0	Bool	944.5	false	True	True	False	False		Step is deactivated
L0	Bool	944.6	false	True	True	False	False		Interlock entering state
V0	Bool	944.7	false	True	True	False	False		Supervision leaving state
X	Bool	945.0	false	True	True	False	False		Step is active
LA	Bool	945.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	945.2	false	True	True	False	False		Supervision active
RA	Bool	945.3	false	True	True	False	False		Reserved
AA	Bool	945.4	false	True	True	False	False		Reserved
SS	Bool	945.5	false	True	True	False	False		System-internal
LS	Bool	945.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	945.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	946.0	11	True	True	False	False		User step number
T	Time	948.0	T#0ms	True	True	False	False		Total step activation time
U	Time	952.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	956.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	960.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	964.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	968.0	false	True	True	False	False		System-internal
LP	Bool	968.1	false	True	True	False	False		System-internal

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
LN	Bool	968.2	false	True	True	False	False		System-internal
VP	Bool	968.3	false	True	True	False	False		System-internal
VN	Bool	968.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	969.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	970.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	972.0	16#0	True	True	False	False		Reserve
▼ Step12	GraphStep	976.0		True	True	False	True		
S1	Bool	976.0	false	True	True	False	False		Step is activated
L1	Bool	976.1	false	True	True	False	False		interlock leaving state
V1	Bool	976.2	false	True	True	False	False		Supervision entering state
R1	Bool	976.3	false	True	True	False	False		Reserved
A1	Bool	976.4	false	True	True	False	False		Error is acknowledged
S0	Bool	976.5	false	True	True	False	False		Step is deactivated
L0	Bool	976.6	false	True	True	False	False		Interlock entering state
V0	Bool	976.7	false	True	True	False	False		Supervision leaving state
X	Bool	977.0	false	True	True	False	False		Step is active
LA	Bool	977.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	977.2	false	True	True	False	False		Supervision active
RA	Bool	977.3	false	True	True	False	False		Reserved
AA	Bool	977.4	false	True	True	False	False		Reserved
SS	Bool	977.5	false	True	True	False	False		System-internal
LS	Bool	977.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	977.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	978.0	12	True	True	False	False		User step number
T	Time	980.0	T#0ms	True	True	False	False		Total step activation time
U	Time	984.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	988.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	992.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	996.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1000.0	false	True	True	False	False		System-internal
LP	Bool	1000.1	false	True	True	False	False		System-internal
LN	Bool	1000.2	false	True	True	False	False		System-internal
VP	Bool	1000.3	false	True	True	False	False		System-internal
VN	Bool	1000.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1001.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1002.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1004.0	16#0	True	True	False	False		Reserve
▼ Step13	GraphStep	1008.0		True	True	False	True		
S1	Bool	1008.0	false	True	True	False	False		Step is activated
L1	Bool	1008.1	false	True	True	False	False		interlock leaving state
V1	Bool	1008.2	false	True	True	False	False		Supervision entering state
R1	Bool	1008.3	false	True	True	False	False		Reserved
A1	Bool	1008.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1008.5	false	True	True	False	False		Step is deactivated
L0	Bool	1008.6	false	True	True	False	False		Interlock entering state
V0	Bool	1008.7	false	True	True	False	False		Supervision leaving state
X	Bool	1009.0	false	True	True	False	False		Step is active
LA	Bool	1009.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1009.2	false	True	True	False	False		Supervision active
RA	Bool	1009.3	false	True	True	False	False		Reserved
AA	Bool	1009.4	false	True	True	False	False		Reserved
SS	Bool	1009.5	false	True	True	False	False		System-internal
LS	Bool	1009.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1009.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1010.0	13	True	True	False	False		User step number
T	Time	1012.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1016.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1020.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1024.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_SUP	DWord	1028.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1032.0	false	True	True	False	False		System-internal
LP	Bool	1032.1	false	True	True	False	False		System-internal
LN	Bool	1032.2	false	True	True	False	False		System-internal
VP	Bool	1032.3	false	True	True	False	False		System-internal
VN	Bool	1032.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1033.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1034.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1036.0	16#0	True	True	False	False		Reserve
▼ Step15	GraphStep	1040.0		True	True	False	True		
S1	Bool	1040.0	false	True	True	False	False		Step is activated
L1	Bool	1040.1	false	True	True	False	False		interlock leaving state
V1	Bool	1040.2	false	True	True	False	False		Supervision entering state
R1	Bool	1040.3	false	True	True	False	False		Reserved
A1	Bool	1040.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1040.5	false	True	True	False	False		Step is deactivated
L0	Bool	1040.6	false	True	True	False	False		Interlock entering state
V0	Bool	1040.7	false	True	True	False	False		Supervision leaving state
X	Bool	1041.0	false	True	True	False	False		Step is active
LA	Bool	1041.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1041.2	false	True	True	False	False		Supervision active
RA	Bool	1041.3	false	True	True	False	False		Reserved
AA	Bool	1041.4	false	True	True	False	False		Reserved
SS	Bool	1041.5	false	True	True	False	False		System-internal
LS	Bool	1041.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1041.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1042.0	15	True	True	False	False		User step number
T	Time	1044.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1048.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1052.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1056.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1060.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1064.0	false	True	True	False	False		System-internal
LP	Bool	1064.1	false	True	True	False	False		System-internal
LN	Bool	1064.2	false	True	True	False	False		System-internal
VP	Bool	1064.3	false	True	True	False	False		System-internal
VN	Bool	1064.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1065.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1066.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1068.0	16#0	True	True	False	False		Reserve
▼ Step16	GraphStep	1072.0		True	True	False	True		
S1	Bool	1072.0	false	True	True	False	False		Step is activated
L1	Bool	1072.1	false	True	True	False	False		interlock leaving state
V1	Bool	1072.2	false	True	True	False	False		Supervision entering state
R1	Bool	1072.3	false	True	True	False	False		Reserved
A1	Bool	1072.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1072.5	false	True	True	False	False		Step is deactivated
L0	Bool	1072.6	false	True	True	False	False		Interlock entering state
V0	Bool	1072.7	false	True	True	False	False		Supervision leaving state
X	Bool	1073.0	false	True	True	False	False		Step is active
LA	Bool	1073.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1073.2	false	True	True	False	False		Supervision active
RA	Bool	1073.3	false	True	True	False	False		Reserved
AA	Bool	1073.4	false	True	True	False	False		Reserved
SS	Bool	1073.5	false	True	True	False	False		System-internal
LS	Bool	1073.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1073.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1074.0	16	True	True	False	False		User step number
T	Time	1076.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1080.0	T#0ms	True	True	False	False		Step activation time without disturbance

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_LOC	DWord	1084.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1088.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1092.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1096.0	false	True	True	False	False		System-internal
LP	Bool	1096.1	false	True	True	False	False		System-internal
LN	Bool	1096.2	false	True	True	False	False		System-internal
VP	Bool	1096.3	false	True	True	False	False		System-internal
VN	Bool	1096.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1097.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1098.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1100.0	16#0	True	True	False	False		Reserve
Step17	GraphStep	1104.0		True	True	False	True		
S1	Bool	1104.0	false	True	True	False	False		Step is activated
L1	Bool	1104.1	false	True	True	False	False		interlock leaving state
V1	Bool	1104.2	false	True	True	False	False		Supervision entering state
R1	Bool	1104.3	false	True	True	False	False		Reserved
A1	Bool	1104.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1104.5	false	True	True	False	False		Step is deactivated
L0	Bool	1104.6	false	True	True	False	False		Interlock entering state
V0	Bool	1104.7	false	True	True	False	False		Supervision leaving state
X	Bool	1105.0	false	True	True	False	False		Step is active
LA	Bool	1105.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1105.2	false	True	True	False	False		Supervision active
RA	Bool	1105.3	false	True	True	False	False		Reserved
AA	Bool	1105.4	false	True	True	False	False		Reserved
SS	Bool	1105.5	false	True	True	False	False		System-internal
LS	Bool	1105.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1105.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1106.0	17	True	True	False	False		User step number
T	Time	1108.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1112.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1116.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1120.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1124.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1128.0	false	True	True	False	False		System-internal
LP	Bool	1128.1	false	True	True	False	False		System-internal
LN	Bool	1128.2	false	True	True	False	False		System-internal
VP	Bool	1128.3	false	True	True	False	False		System-internal
VN	Bool	1128.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1129.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1130.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1132.0	16#0	True	True	False	False		Reserve
Step18	GraphStep	1136.0		True	True	False	True		
S1	Bool	1136.0	false	True	True	False	False		Step is activated
L1	Bool	1136.1	false	True	True	False	False		interlock leaving state
V1	Bool	1136.2	false	True	True	False	False		Supervision entering state
R1	Bool	1136.3	false	True	True	False	False		Reserved
A1	Bool	1136.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1136.5	false	True	True	False	False		Step is deactivated
L0	Bool	1136.6	false	True	True	False	False		Interlock entering state
V0	Bool	1136.7	false	True	True	False	False		Supervision leaving state
X	Bool	1137.0	false	True	True	False	False		Step is active
LA	Bool	1137.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1137.2	false	True	True	False	False		Supervision active
RA	Bool	1137.3	false	True	True	False	False		Reserved
AA	Bool	1137.4	false	True	True	False	False		Reserved
SS	Bool	1137.5	false	True	True	False	False		System-internal
LS	Bool	1137.6	true	True	True	False	False		Direct result of the programmed interlock

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
VS	Bool	1137.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1138.0	18	True	True	False	False		User step number
T	Time	1140.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1144.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1148.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1152.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1156.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1160.0	false	True	True	False	False		System-internal
LP	Bool	1160.1	false	True	True	False	False		System-internal
LN	Bool	1160.2	false	True	True	False	False		System-internal
VP	Bool	1160.3	false	True	True	False	False		System-internal
VN	Bool	1160.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1161.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1162.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1164.0	16#0	True	True	False	False		Reserve
Step19	GraphStep	1168.0		True	True	False	True		
S1	Bool	1168.0	false	True	True	False	False		Step is activated
L1	Bool	1168.1	false	True	True	False	False		interlock leaving state
V1	Bool	1168.2	false	True	True	False	False		Supervision entering state
R1	Bool	1168.3	false	True	True	False	False		Reserved
A1	Bool	1168.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1168.5	false	True	True	False	False		Step is deactivated
L0	Bool	1168.6	false	True	True	False	False		Interlock entering state
V0	Bool	1168.7	false	True	True	False	False		Supervision leaving state
X	Bool	1169.0	false	True	True	False	False		Step is active
LA	Bool	1169.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1169.2	false	True	True	False	False		Supervision active
RA	Bool	1169.3	false	True	True	False	False		Reserved
AA	Bool	1169.4	false	True	True	False	False		Reserved
SS	Bool	1169.5	false	True	True	False	False		System-internal
LS	Bool	1169.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1169.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1170.0	19	True	True	False	False		User step number
T	Time	1172.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1176.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1180.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1184.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1188.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1192.0	false	True	True	False	False		System-internal
LP	Bool	1192.1	false	True	True	False	False		System-internal
LN	Bool	1192.2	false	True	True	False	False		System-internal
VP	Bool	1192.3	false	True	True	False	False		System-internal
VN	Bool	1192.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1193.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1194.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1196.0	16#0	True	True	False	False		Reserve
Step20	GraphStep	1200.0		True	True	False	True		
S1	Bool	1200.0	false	True	True	False	False		Step is activated
L1	Bool	1200.1	false	True	True	False	False		interlock leaving state
V1	Bool	1200.2	false	True	True	False	False		Supervision entering state
R1	Bool	1200.3	false	True	True	False	False		Reserved
A1	Bool	1200.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1200.5	false	True	True	False	False		Step is deactivated
L0	Bool	1200.6	false	True	True	False	False		Interlock entering state
V0	Bool	1200.7	false	True	True	False	False		Supervision leaving state
X	Bool	1201.0	false	True	True	False	False		Step is active
LA	Bool	1201.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1201.2	false	True	True	False	False		Supervision active

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
RA	Bool	1201.3	false	True	True	False	False		Reserved
AA	Bool	1201.4	false	True	True	False	False		Reserved
SS	Bool	1201.5	false	True	True	False	False		System-internal
LS	Bool	1201.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1201.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1202.0	20	True	True	False	False		User step number
T	Time	1204.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1208.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1212.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1216.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1220.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1224.0	false	True	True	False	False		System-internal
LP	Bool	1224.1	false	True	True	False	False		System-internal
LN	Bool	1224.2	false	True	True	False	False		System-internal
VP	Bool	1224.3	false	True	True	False	False		System-internal
VN	Bool	1224.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1225.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1226.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1228.0	16#0	True	True	False	False		Reserve
▼ Step21	GraphStep	1232.0		True	True	False	True		
S1	Bool	1232.0	false	True	True	False	False		Step is activated
L1	Bool	1232.1	false	True	True	False	False		interlock leaving state
V1	Bool	1232.2	false	True	True	False	False		Supervision entering state
R1	Bool	1232.3	false	True	True	False	False		Reserved
A1	Bool	1232.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1232.5	false	True	True	False	False		Step is deactivated
L0	Bool	1232.6	false	True	True	False	False		Interlock entering state
V0	Bool	1232.7	false	True	True	False	False		Supervision leaving state
X	Bool	1233.0	false	True	True	False	False		Step is active
LA	Bool	1233.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1233.2	false	True	True	False	False		Supervision active
RA	Bool	1233.3	false	True	True	False	False		Reserved
AA	Bool	1233.4	false	True	True	False	False		Reserved
SS	Bool	1233.5	false	True	True	False	False		System-internal
LS	Bool	1233.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1233.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1234.0	21	True	True	False	False		User step number
T	Time	1236.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1240.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1244.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1248.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1252.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1256.0	false	True	True	False	False		System-internal
LP	Bool	1256.1	false	True	True	False	False		System-internal
LN	Bool	1256.2	false	True	True	False	False		System-internal
VP	Bool	1256.3	false	True	True	False	False		System-internal
VN	Bool	1256.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1257.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1258.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1260.0	16#0	True	True	False	False		Reserve
▼ Step22	GraphStep	1264.0		True	True	False	True		
S1	Bool	1264.0	false	True	True	False	False		Step is activated
L1	Bool	1264.1	false	True	True	False	False		interlock leaving state
V1	Bool	1264.2	false	True	True	False	False		Supervision entering state
R1	Bool	1264.3	false	True	True	False	False		Reserved
A1	Bool	1264.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1264.5	false	True	True	False	False		Step is deactivated

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
L0	Bool	1264.6	false	True	True	False	False		Interlock entering state
V0	Bool	1264.7	false	True	True	False	False		Supervision leaving state
X	Bool	1265.0	false	True	True	False	False		Step is active
LA	Bool	1265.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1265.2	false	True	True	False	False		Supervision active
RA	Bool	1265.3	false	True	True	False	False		Reserved
AA	Bool	1265.4	false	True	True	False	False		Reserved
SS	Bool	1265.5	false	True	True	False	False		System-internal
LS	Bool	1265.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1265.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1266.0	22	True	True	False	False		User step number
T	Time	1268.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1272.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1276.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1280.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1284.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1288.0	false	True	True	False	False		System-internal
LP	Bool	1288.1	false	True	True	False	False		System-internal
LN	Bool	1288.2	false	True	True	False	False		System-internal
VP	Bool	1288.3	false	True	True	False	False		System-internal
VN	Bool	1288.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1289.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1290.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1292.0	16#0	True	True	False	False		Reserve
▼ Step23	GraphStep	1296.0		True	True	False	True		
S1	Bool	1296.0	false	True	True	False	False		Step is activated
L1	Bool	1296.1	false	True	True	False	False		interlock leaving state
V1	Bool	1296.2	false	True	True	False	False		Supervision entering state
R1	Bool	1296.3	false	True	True	False	False		Reserved
A1	Bool	1296.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1296.5	false	True	True	False	False		Step is deactivated
L0	Bool	1296.6	false	True	True	False	False		Interlock entering state
V0	Bool	1296.7	false	True	True	False	False		Supervision leaving state
X	Bool	1297.0	false	True	True	False	False		Step is active
LA	Bool	1297.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1297.2	false	True	True	False	False		Supervision active
RA	Bool	1297.3	false	True	True	False	False		Reserved
AA	Bool	1297.4	false	True	True	False	False		Reserved
SS	Bool	1297.5	false	True	True	False	False		System-internal
LS	Bool	1297.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1297.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1298.0	23	True	True	False	False		User step number
T	Time	1300.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1304.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1308.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1312.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1316.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1320.0	false	True	True	False	False		System-internal
LP	Bool	1320.1	false	True	True	False	False		System-internal
LN	Bool	1320.2	false	True	True	False	False		System-internal
VP	Bool	1320.3	false	True	True	False	False		System-internal
VN	Bool	1320.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1321.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1322.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1324.0	16#0	True	True	False	False		Reserve
▼ Step24	GraphStep	1328.0		True	True	False	True		
S1	Bool	1328.0	false	True	True	False	False		Step is activated

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
L1	Bool	1328.1	false	True	True	False	False		interlock leaving state
V1	Bool	1328.2	false	True	True	False	False		Supervision entering state
R1	Bool	1328.3	false	True	True	False	False		Reserved
A1	Bool	1328.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1328.5	false	True	True	False	False		Step is deactivated
L0	Bool	1328.6	false	True	True	False	False		Interlock entering state
V0	Bool	1328.7	false	True	True	False	False		Supervision leaving state
X	Bool	1329.0	false	True	True	False	False		Step is active
LA	Bool	1329.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1329.2	false	True	True	False	False		Supervision active
RA	Bool	1329.3	false	True	True	False	False		Reserved
AA	Bool	1329.4	false	True	True	False	False		Reserved
SS	Bool	1329.5	false	True	True	False	False		System-internal
LS	Bool	1329.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1329.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1330.0	24	True	True	False	False		User step number
T	Time	1332.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1336.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1340.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1344.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1348.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1352.0	false	True	True	False	False		System-internal
LP	Bool	1352.1	false	True	True	False	False		System-internal
LN	Bool	1352.2	false	True	True	False	False		System-internal
VP	Bool	1352.3	false	True	True	False	False		System-internal
VN	Bool	1352.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1353.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1354.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1356.0	16#0	True	True	False	False		Reserve
▼ Step26	GraphStep	1360.0		True	True	False	True		
S1	Bool	1360.0	false	True	True	False	False		Step is activated
L1	Bool	1360.1	false	True	True	False	False		interlock leaving state
V1	Bool	1360.2	false	True	True	False	False		Supervision entering state
R1	Bool	1360.3	false	True	True	False	False		Reserved
A1	Bool	1360.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1360.5	false	True	True	False	False		Step is deactivated
L0	Bool	1360.6	false	True	True	False	False		Interlock entering state
V0	Bool	1360.7	false	True	True	False	False		Supervision leaving state
X	Bool	1361.0	false	True	True	False	False		Step is active
LA	Bool	1361.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1361.2	false	True	True	False	False		Supervision active
RA	Bool	1361.3	false	True	True	False	False		Reserved
AA	Bool	1361.4	false	True	True	False	False		Reserved
SS	Bool	1361.5	false	True	True	False	False		System-internal
LS	Bool	1361.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1361.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1362.0	26	True	True	False	False		User step number
T	Time	1364.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1368.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1372.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1376.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1380.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1384.0	false	True	True	False	False		System-internal
LP	Bool	1384.1	false	True	True	False	False		System-internal
LN	Bool	1384.2	false	True	True	False	False		System-internal
VP	Bool	1384.3	false	True	True	False	False		System-internal
VN	Bool	1384.4	false	True	True	False	False		System-internal

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
H_IL_ERR	Byte	1385.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1386.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1388.0	16#0	True	True	False	False		Reserve
▼ Step27	GraphStep	1392.0		True	True	False	True		
S1	Bool	1392.0	false	True	True	False	False		Step is activated
L1	Bool	1392.1	false	True	True	False	False		interlock leaving state
V1	Bool	1392.2	false	True	True	False	False		Supervision entering state
R1	Bool	1392.3	false	True	True	False	False		Reserved
A1	Bool	1392.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1392.5	false	True	True	False	False		Step is deactivated
L0	Bool	1392.6	false	True	True	False	False		Interlock entering state
V0	Bool	1392.7	false	True	True	False	False		Supervision leaving state
X	Bool	1393.0	false	True	True	False	False		Step is active
LA	Bool	1393.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1393.2	false	True	True	False	False		Supervision active
RA	Bool	1393.3	false	True	True	False	False		Reserved
AA	Bool	1393.4	false	True	True	False	False		Reserved
SS	Bool	1393.5	false	True	True	False	False		System-internal
LS	Bool	1393.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1393.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1394.0	27	True	True	False	False		User step number
T	Time	1396.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1400.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1404.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1408.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1412.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1416.0	false	True	True	False	False		System-internal
LP	Bool	1416.1	false	True	True	False	False		System-internal
LN	Bool	1416.2	false	True	True	False	False		System-internal
VP	Bool	1416.3	false	True	True	False	False		System-internal
VN	Bool	1416.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1417.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1418.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1420.0	16#0	True	True	False	False		Reserve
▼ Step28	GraphStep	1424.0		True	True	False	True		
S1	Bool	1424.0	false	True	True	False	False		Step is activated
L1	Bool	1424.1	false	True	True	False	False		interlock leaving state
V1	Bool	1424.2	false	True	True	False	False		Supervision entering state
R1	Bool	1424.3	false	True	True	False	False		Reserved
A1	Bool	1424.4	false	True	True	False	False		Error is acknowledged
S0	Bool	1424.5	false	True	True	False	False		Step is deactivated
L0	Bool	1424.6	false	True	True	False	False		Interlock entering state
V0	Bool	1424.7	false	True	True	False	False		Supervision leaving state
X	Bool	1425.0	false	True	True	False	False		Step is active
LA	Bool	1425.1	false	True	True	False	False		Interlock is not satisfied
VA	Bool	1425.2	false	True	True	False	False		Supervision active
RA	Bool	1425.3	false	True	True	False	False		Reserved
AA	Bool	1425.4	false	True	True	False	False		Reserved
SS	Bool	1425.5	false	True	True	False	False		System-internal
LS	Bool	1425.6	true	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1425.7	false	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1426.0	28	True	True	False	False		User step number
T	Time	1428.0	T#0ms	True	True	False	False		Total step activation time
U	Time	1432.0	T#0ms	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1436.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1440.0	16#0	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1444.0	16#0	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle

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SM	Bool	1448.0	false	True	True	False	False		System-internal
LP	Bool	1448.1	false	True	True	False	False		System-internal
LN	Bool	1448.2	false	True	True	False	False		System-internal
VP	Bool	1448.3	false	True	True	False	False		System-internal
VN	Bool	1448.4	false	True	True	False	False		System-internal
H_IL_ERR	Byte	1449.0	16#0	True	True	False	False		System-internal
H_SV_FLT	Byte	1450.0	16#0	True	True	False	False		System-internal
RESERVED	DWord	1452.0	16#0	True	True	False	False		Reserve
S_DISPLAY	Int	1642.0	0	True	True	False	False		Affichage interne du paramètre de sortie S_NO
S_SEL_OLD	Int	1644.0	0	True	True	False	False		Valeur précédente dans S_SEL
S_DISPIDX	Byte	1646.0	16#0	True	True	False	False		Indice de l'étape affichée dans S_NO
T_DISPIDX	Byte	1647.0	16#0	True	True	False	False		Indice de la transition affichée dans T_NO
MOP	Struct	1652.0		True	True	False	False		Mode de fonctionnement
AUTO	Bool	1652.0	true	True	True	False	False		Etat : Mode automatique
MAN	Bool	1652.1	false	True	True	False	False		Etat : Mode manuel
TAP	Bool	1652.2	false	True	True	False	False		Etat : Mode semi-automatique/Activation de l'étape suivante avec transition
TOP	Bool	1652.3	false	True	True	False	False		Etat : Mode semi-automatique/Ignorer la transition
ACK_S	Bool	1652.4	false	True	True	False	False		Requête : Acquitter l'étape figurant dans le paramètre S_NO
REG_S	Bool	1652.5	false	True	True	False	False		Requête : Enregistrer l'étape figurant dans le paramètre S_NO
T_PREV	Bool	1652.6	false	True	True	False	False		Requête : Afficher la transition valide précédente dans le paramètre T_NO
T_NEXT	Bool	1652.7	false	True	True	False	False		Requête : Afficher la transition valide suivante dans le paramètre T_NO
LOCK	Bool	1653.0	true	True	True	False	False		Etat : Traitement des Interlocks activé
SUP	Bool	1653.1	true	True	True	False	False		Etat : Traitement de Supervision activé
ACKREQ	Bool	1653.2	true	True	True	False	False		Etat : Acquittement requis
SSKIP	Bool	1653.3	false	True	True	False	False		Etat : "Ignorer les étapes" activé
OFF	Bool	1653.4	false	True	True	False	False		Requête : Désactiver toutes les étapes
INIT	Bool	1653.5	true	True	True	False	False		Requête : Mettre le graphe à l'état initial
HALT	Bool	1653.6	false	True	True	False	False		Etat : Le graphe est arrêté
TMS_HALT	Bool	1653.7	false	True	True	False	False		Etat : Toutes les temporisations internes sont arrêtées
OPS_ZERO	Bool	1654.0	false	True	True	False	False		Etat : Tous les opérandes traités avec N, L, D mis à 0
SACT_DISP	Bool	1654.1	true	True	True	False	False		Etat : N'afficher que les étapes actives
SEF_DISP	Bool	1654.2	false	True	True	False	False		Etat : Afficher uniquement les étapes avec erreurs et perturbations
SALL_DISP	Bool	1654.3	false	True	True	False	False		Etat : Afficher toutes les étapes
S_PREV	Bool	1654.4	false	True	True	False	False		Requête : Afficher l'étape précédente dans le paramètre S_NO
S_NEXT	Bool	1654.5	false	True	True	False	False		Requête : Afficher l'étape suivante dans le paramètre S_NO
S_SELOK	Bool	1654.6	false	True	True	False	False		Requête : Afficher dans le paramètre S_NO le numéro d'étape figurant dans S_SEL
S_ON	Bool	1654.7	false	True	True	False	False		Requête : Activer l'étape figurant dans le paramètre S_NO
S_OFF	Bool	1655.0	false	True	True	False	False		Requête : Désactiver l'étape figurant dans le paramètre S_NO
T_PUSH	Bool	1655.1	false	True	True	False	False		Requête : Validation de commutation pour transition
REG	Bool	1655.2	false	True	True	False	False		Requête : Enregistrer toutes les erreurs Interlock et Supervision
ACK	Bool	1655.3	false	True	True	False	False		Requête : Acquitter toutes les erreurs Interlock et Supervision
IL_PERM	Bool	1655.4	false	True	True	False	False		Etat : Traitement permanent de tous les Interlocks
T_PERM	Bool	1655.5	false	True	True	False	False		Etat : Traitement permanent de toutes les transitions

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ILP_MAN	Bool	1655.6	false	True	True	False	False		Etat : Traitement permanent de tous les Interlocks en mode manuel
TICKS	Struct	1656.0		True	True	False	False		Périodes d'horloge
DELTA	Time	1656.0	T#0ms	True	True	False	False		Différence de temps entre deux cycles
OLD	Time	1660.0	T#0ms	True	True	False	False		Valeur du compteur de périodes 10 ms au dernier cycle
NEW	Time	1664.0	T#0ms	True	True	False	False		Valeur du compteur de périodes 10 ms dans ce cycle
SQ_FLAGS	Struct	1668.0		True	True	False	False		Mémentos de graphe
ERR_FLT	Bool	1668.0	false	True	True	False	False		Erreur groupée Interlock et Supervision
ERROR	Bool	1668.1	false	True	True	False	False		Erreur groupée Interlock
FAULT	Bool	1668.2	false	True	True	False	False		Erreur groupée Supervision
RT_FAIL	Bool	1668.3	false	True	True	False	False		Erreur à l'exécution
NO_SNO	Bool	1668.4	false	True	True	False	False		Numéro d'étape requis introuvable
NF_OFL	Bool	1668.5	false	True	True	False	False		Débordement : Trop de requêtes ON ou OFF
SA_OFL	Bool	1668.6	false	True	True	False	False		Débordement : Trop d'étapes activées
TV_OFL	Bool	1668.7	false	True	True	False	False		Débordement : Trop de transitions valides
MSG_OFL	Bool	1669.0	false	True	True	False	False		Débordement : Pas assez de ressources système pour ALARM_S
NO_SWI	Bool	1669.1	false	True	True	False	False		Pas de commutation dans ce cycle
CYC_OP	Bool	1669.2	false	True	True	False	False		Exécution cyclique du graphe après l'initialisation
AS_MSG	Bool	1669.3	true	True	True	False	False		Messages à l'exécution activés ou désactivés par instruction
AS_SEND	Bool	1669.4	false	True	True	False	False		Envoyer les messages de WR_USMSG ou les entrer uniquement dans le tampon de diagnostic
SQ_BUSY	Bool	1669.5	false	True	True	False	False		Mémento de front interne pour le traitement séquentiel
SA_BUSY	Bool	1669.6	false	True	True	False	False		Mémento de front interne pour le traitement séquentiel
AS_SIG	Bool	1669.7	false	True	True	False	False		Mémentos de front pour messages de Alarm_S et Alarm_SQ
Temp									
Constant									

Alarms

Enable alarms	True

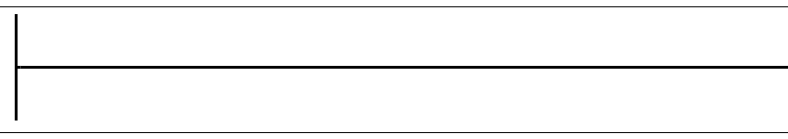
Category	Category enabler	Display class
Error		0
Warning		0
Info		0
Category 4		0
Category 5		0
Category 6		0
Category 7		0
Category 8		0

Category for interlocks	Error	**Subcategory 1 for interlocks**		**Subcategory 2 for interlocks**	
Category for supervisions	Error	**Subcategory 1 for supervisions**		**Subcategory 2 for supervisions**	

Symbol	Address	Type	Comment

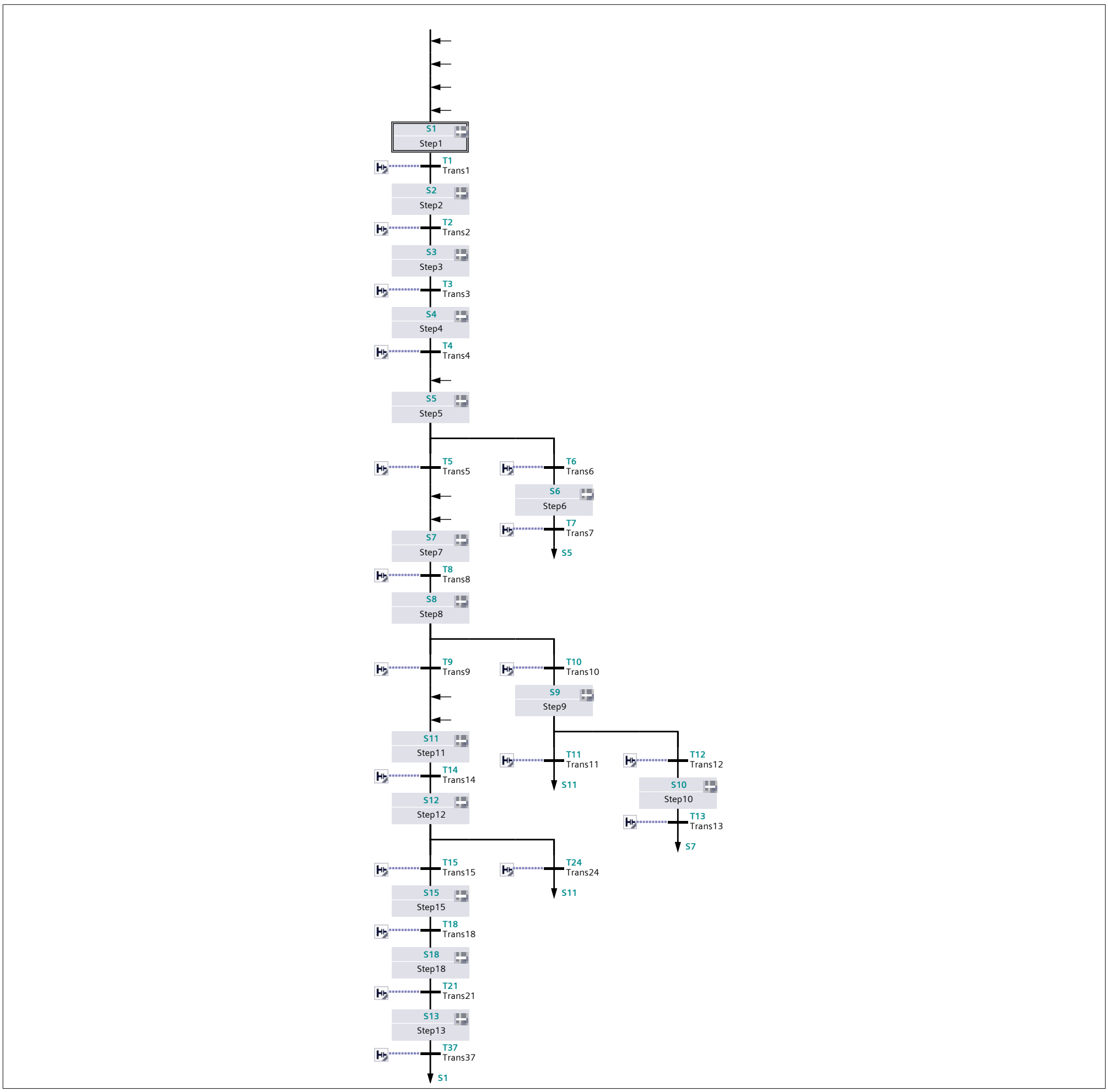
Permanent pre-instructions

1 :



Sequences (4)

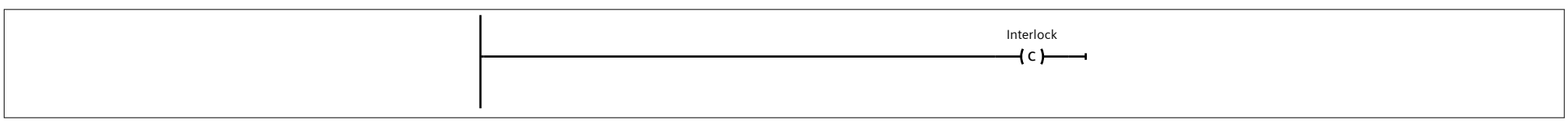
1 :



S1 - [Initial step]:Step1

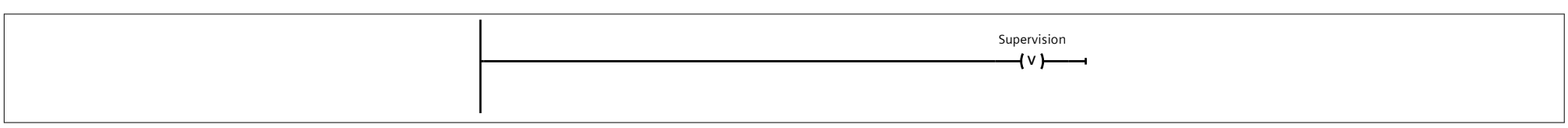
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

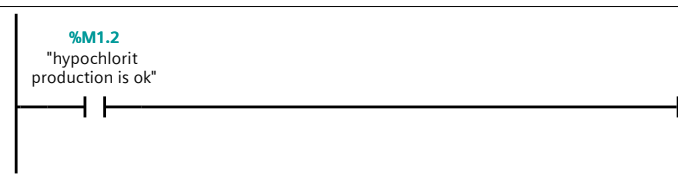
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**
		R	"Acid cleaning pump 1"
		R	"Acid cleaning pump 2"
		R	"Acid return Valve"
		R	"Acid supply valve"
		R	"Acid Tank Drain valve"
		R	"Acid transfer Pump"
		R	"Electrolizer 1 Drain valve"
		R	"Service water valve"
		R	"system Hypochlorite is clean"
		R	"Electrolizer 2 Drain valve"

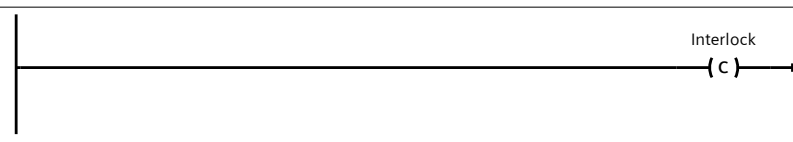
T1:Trans1



S2:Step2

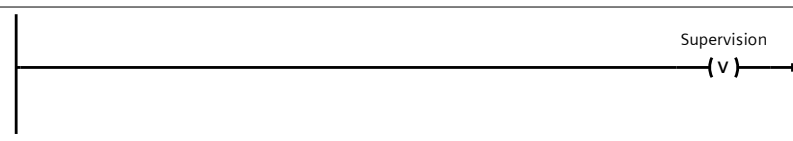
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

Supervision alarm	
Alarm text	



Actions:

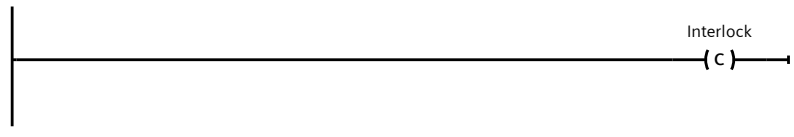
Actions:			
Interlock	**Event**	**Qualifier**	**Action**
		S	"hypochlorit production is ok"

T2:Trans2

S3:Step3

Interlock -(c)-:

Interlock alarm	
Alarm text	



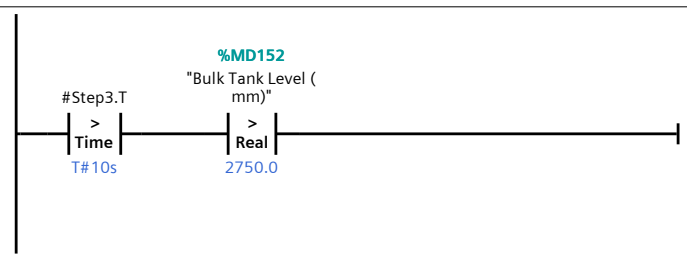
Supervision -(v)-:

Supervision alarm	
Alarm text	

Actions:

Actions:			
Interlock	Event	Qualifier	Action
		R	"hypochlorit production is ok"
		S	"Electrolizer 1 Drain valve"
		S	"Acid Tank Drain valve"
		S	"Electrolizer 2 Drain valve"
		S	"acid cleaning system started"

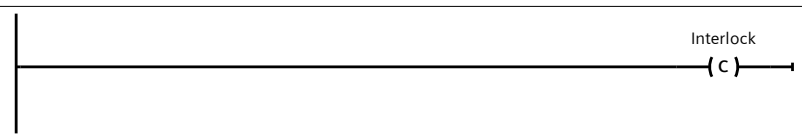
T3:Trans3



S4:Step4

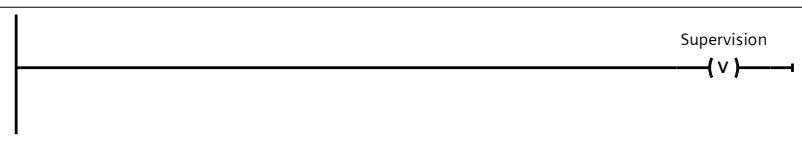
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

Supervision alarm	
Alarm text	



Actions:

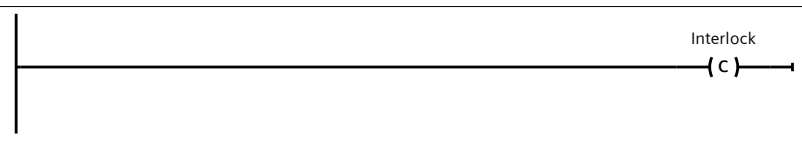
Actions:			
Interlock	Event	Qualifier	Action
		R	"Electrolizer 1 Drain valve"
		S	"Acid transfer Pump"
		R	"Electrolizer 2 Drain valve"
		R	"Acid Tank Drain valve"

T4:Trans4

S5:Step5

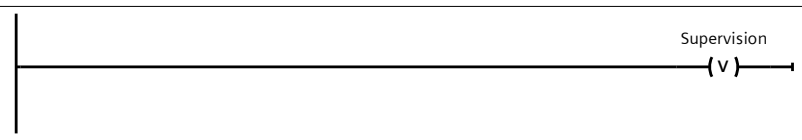
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

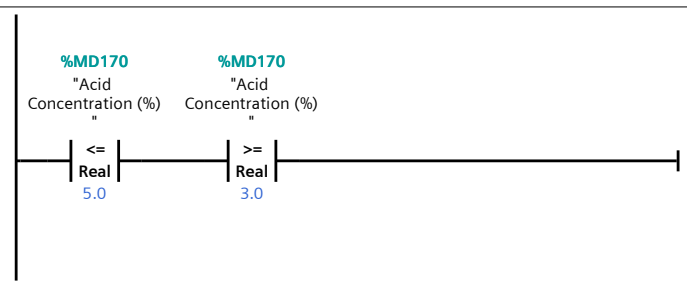
Supervision alarm	
Alarm text	



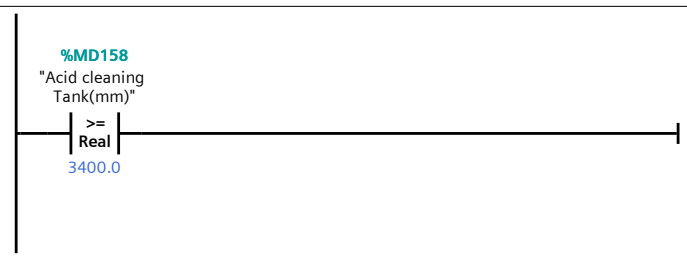
Actions:

Actions:			
Interlock	Event	Qualifier	Action
		R	"Acid transfer Pump"
		S	"Service water valve"
		R	"Acid Tank Drain valve"

T5:Trans5



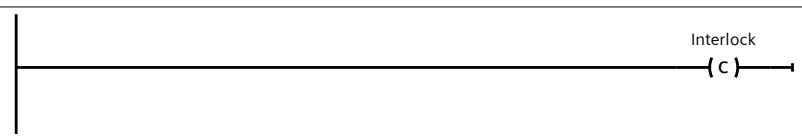
T6:Trans6



S6:Step6

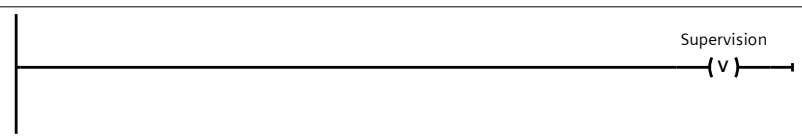
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

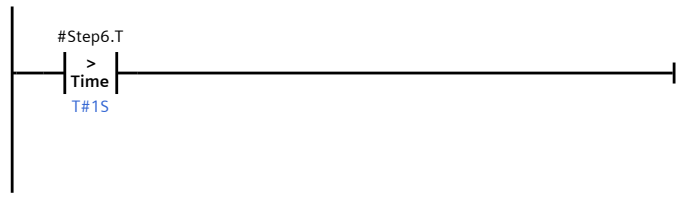
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action
		S	"Acid Tank Drain valve"

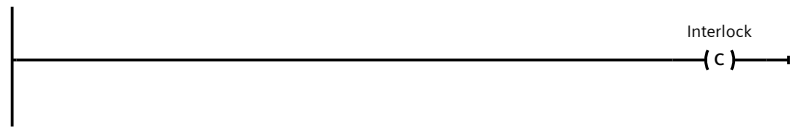
T7:Trans7



S7:Step7

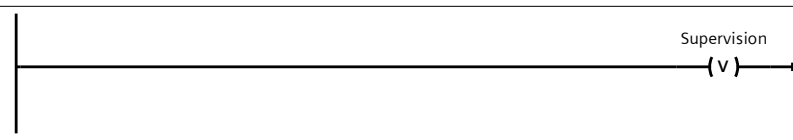
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

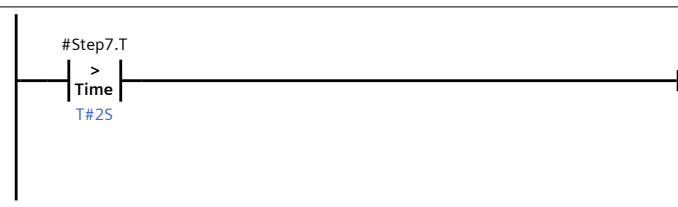
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action
		R	"Service water valve"
		S	"Acid return Valve"
		S	"Acid supply valve"
		R	"system Hypochlorite is clean"

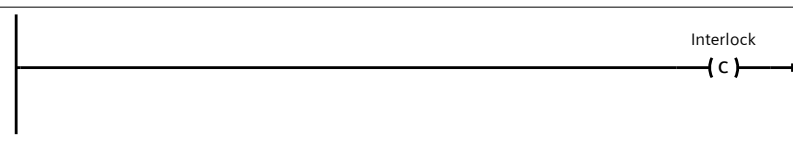
T8:Trans8



S8:Step8

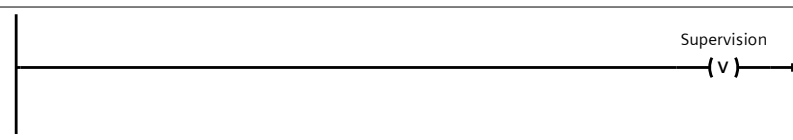
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

Supervision alarm	
Alarm text	

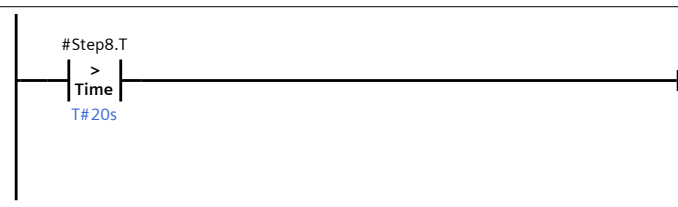


Actions:

Actions:			
Interlock	Event	Qualifier	Action
		S	"Acid cleaning pump 1"

T9:Trans9

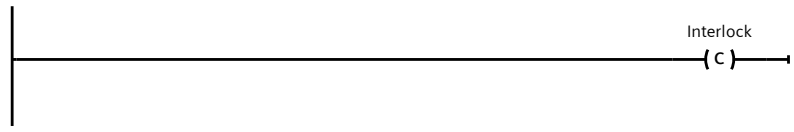
T10:Trans10



S9:Step9

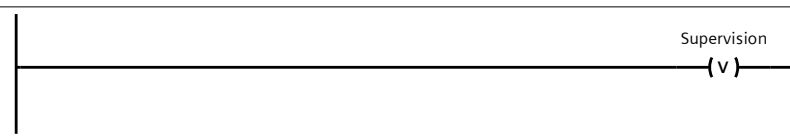
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

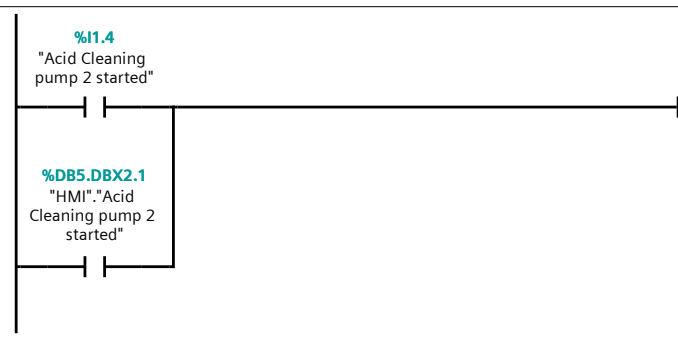
Supervision alarm	
Alarm text	



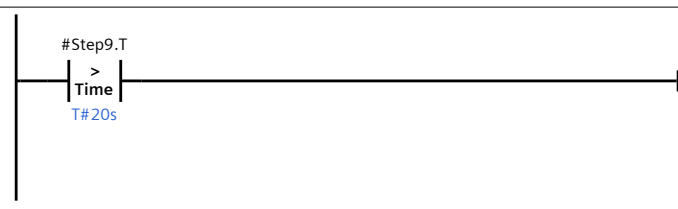
Actions:

Actions:			
Interlock	Event	Qualifier	Action
		R	"Acid cleaning pump 1"
		N	"Acid Cleaning pump 1 Fault"
		S	"Acid cleaning pump 2"

T11:Trans11



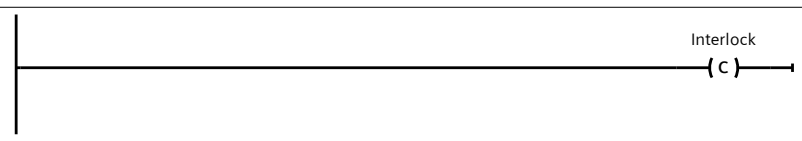
T12:Trans12



S10:Step10

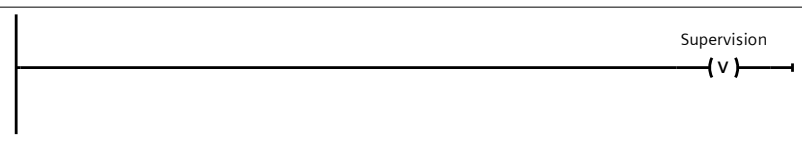
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

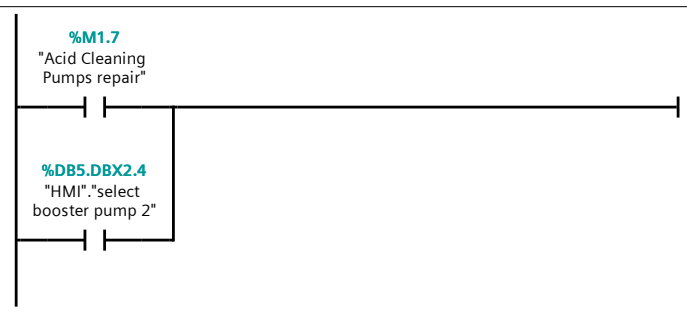
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action
		R	"Acid cleaning pump 2"
		N	"Acid Cleaning pump 2 Fault"
		R	"Acid return Valve"
		R	"Acid supply valve"

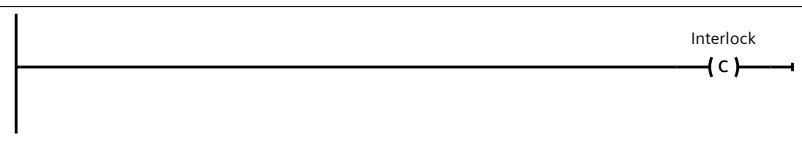
T13:Trans13



S11:Step11

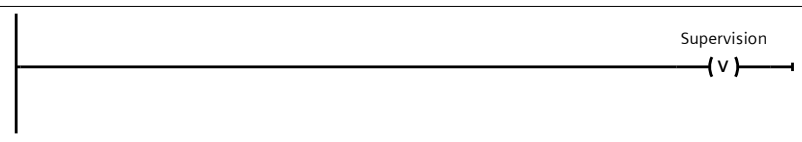
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

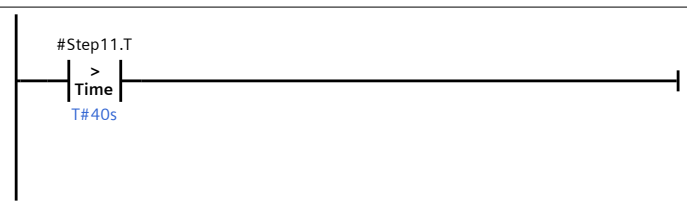
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action

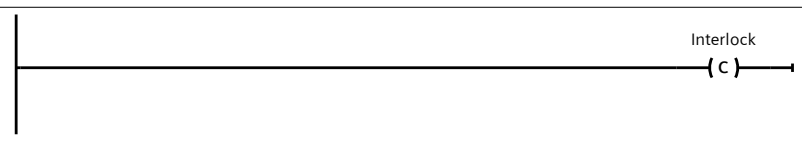
T14:Trans14



S12:Step12

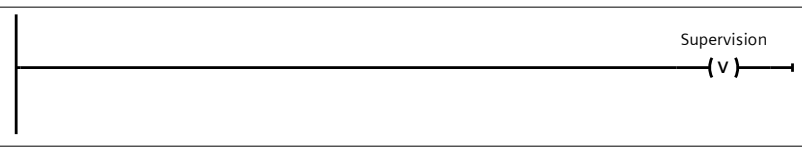
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

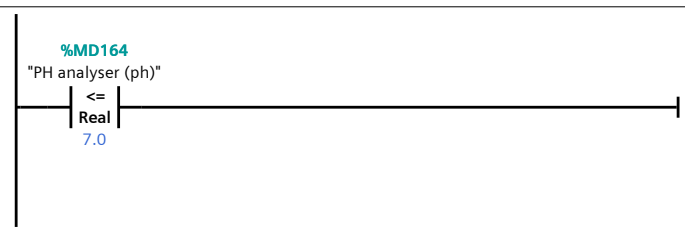
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action

T15:Trans15

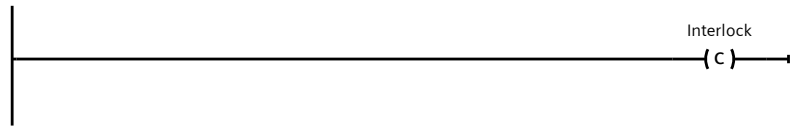


T24:Trans24

S18:Step18

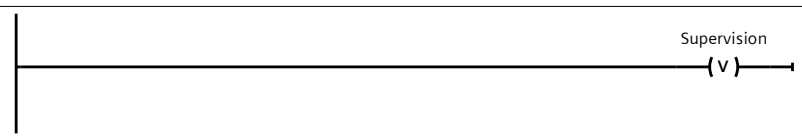
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

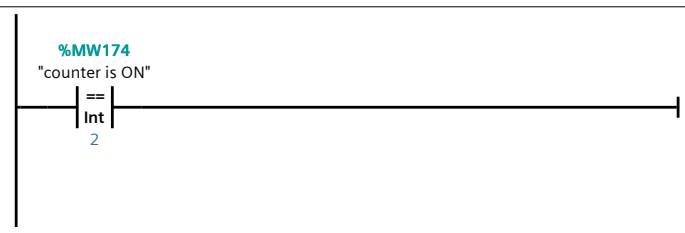
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action
		S	"system Hypochlorite is clean"

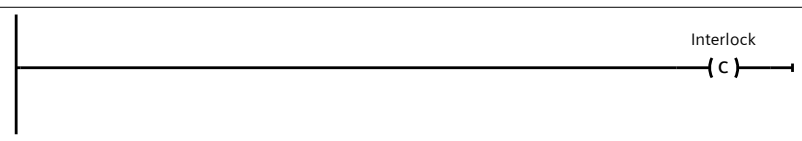
T21:Trans21



S15:Step15

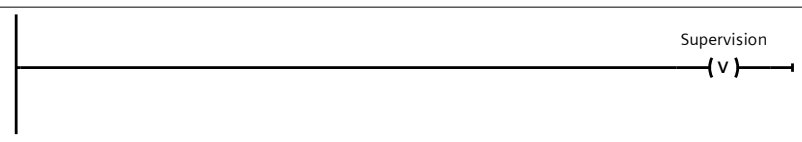
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

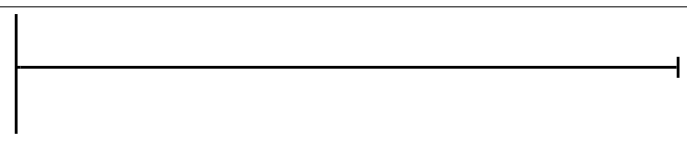
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action
		R	"Acid cleaning pump 1"
		R	"Acid cleaning pump 2"
		R	"Acid return Valve"
		R	"Acid supply valve"
		S	"Electrolizer 1 Drain valve"
		S	"Acid Tank Drain valve"
		S	"Electrolizer 2 Drain valve"

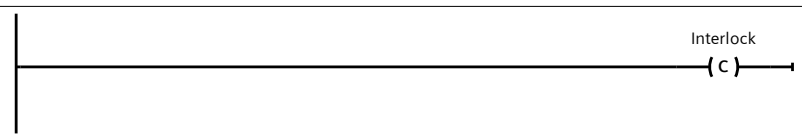
T18:Trans18



S13:Step13

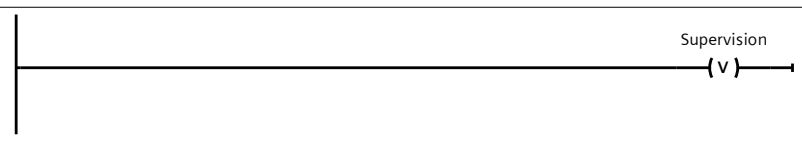
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

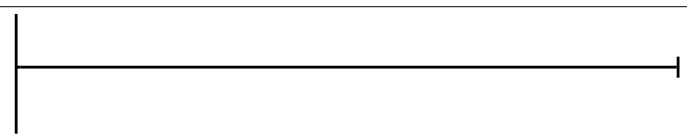
Supervision alarm	
Alarm text	



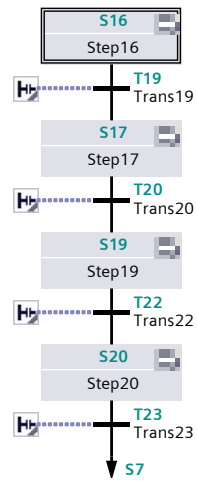
Actions:

Actions:			
Interlock	Event	Qualifier	Action
		S	"water cleaning is done"

T37:Trans37



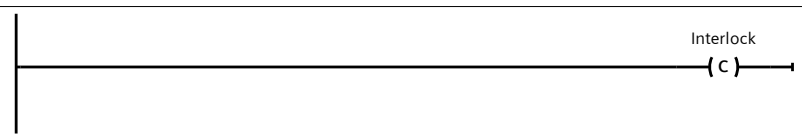
2:



S16 - [Initial step]:Step16

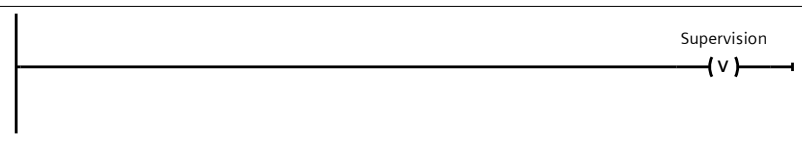
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

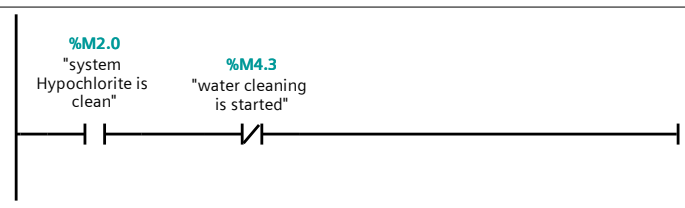
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action

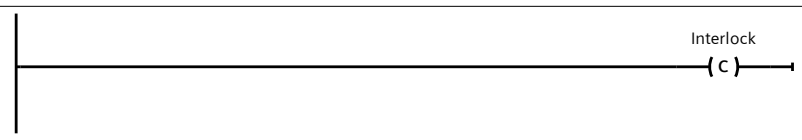
T19:Trans19



S17:Step17

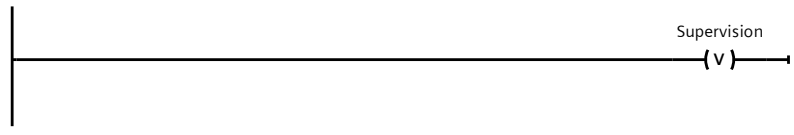
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

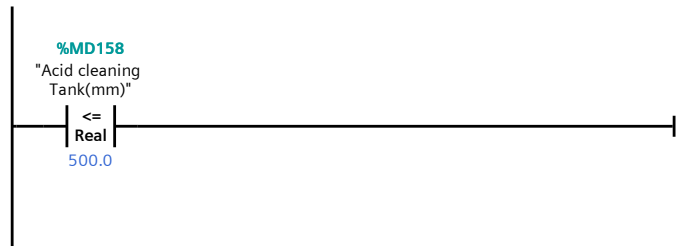
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action
		S	"Acid Tank Drain valve"

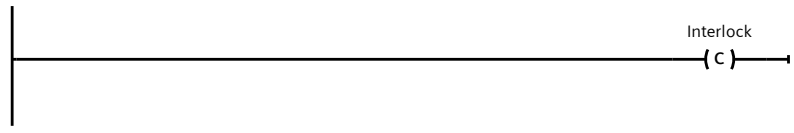
T20:Trans20



S19:Step19

Interlock -(c)-:

Interlock alarm	
Alarm text	



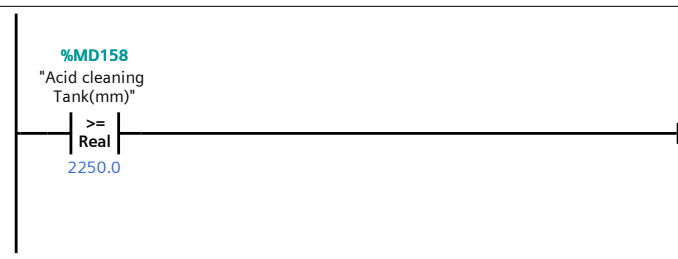
Supervision -(v)-:

Supervision alarm	
Alarm text	

Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**
		R	"Acid Tank Drain valve"
		R	"Electrolizer 1 Drain valve"
		S	"Service water valve"
		R	"Electrolizer 2 Drain valve"
		S	"FILTER 1 VALVE"
		S	"FILTER 2 VALVE"
		N	"Acid tank level LL"

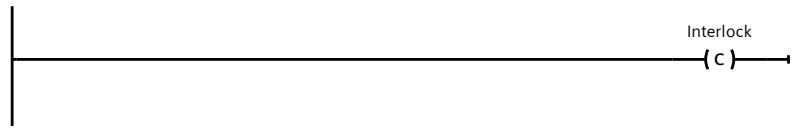
T22:Trans22



S20:Step20

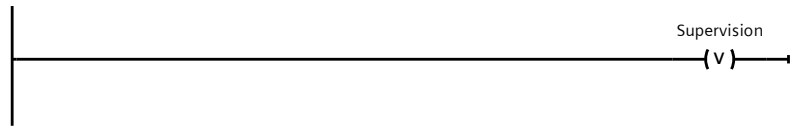
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

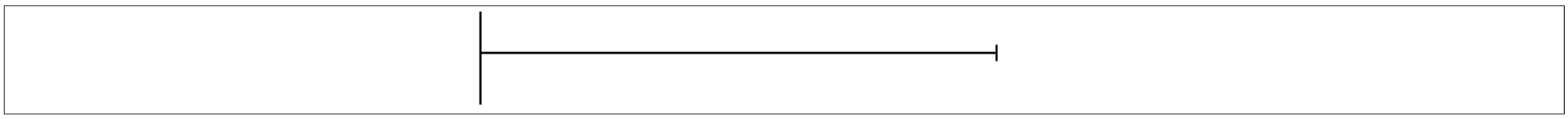
Supervision alarm	
Alarm text	



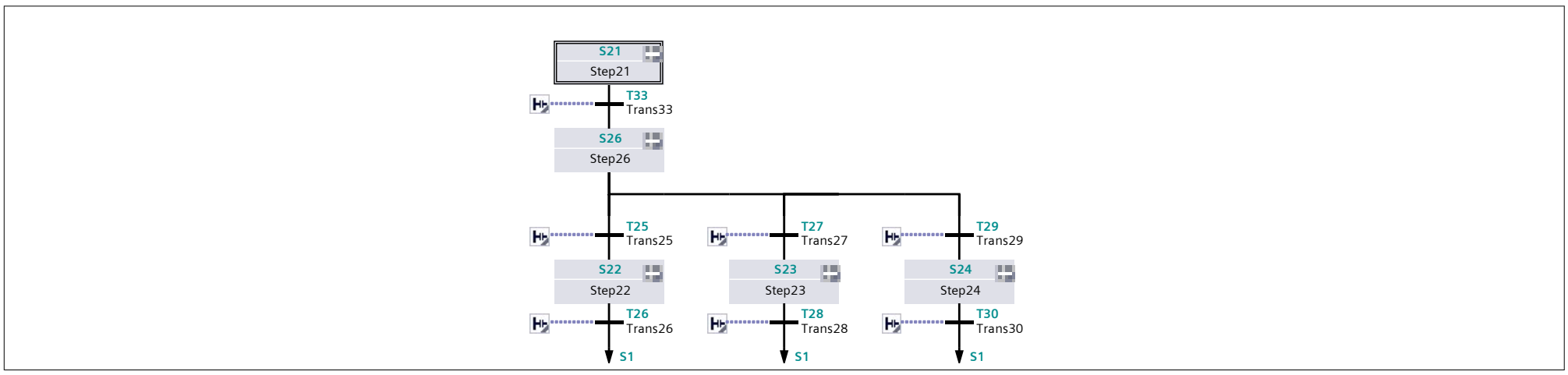
Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**
		R	"Service water valve"
	S1	ON	#Step16
		S	"water cleaning is started"

T23:Trans23



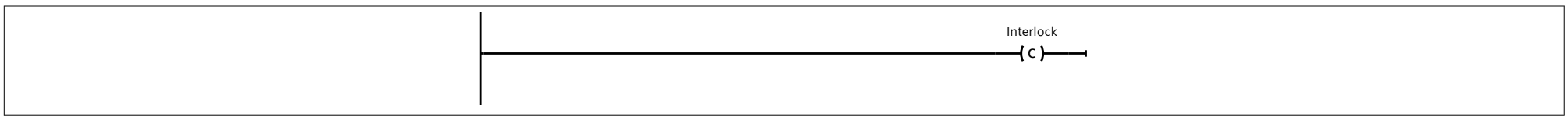
3:



S21 - [Initial step]:Step21

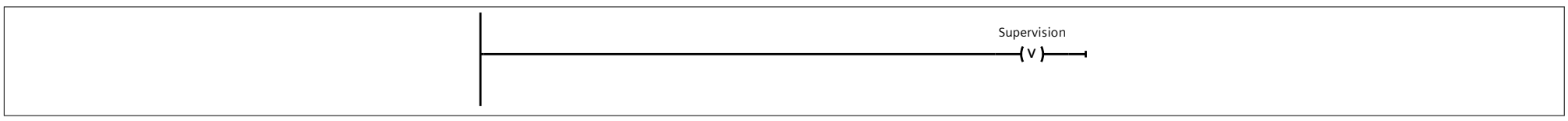
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

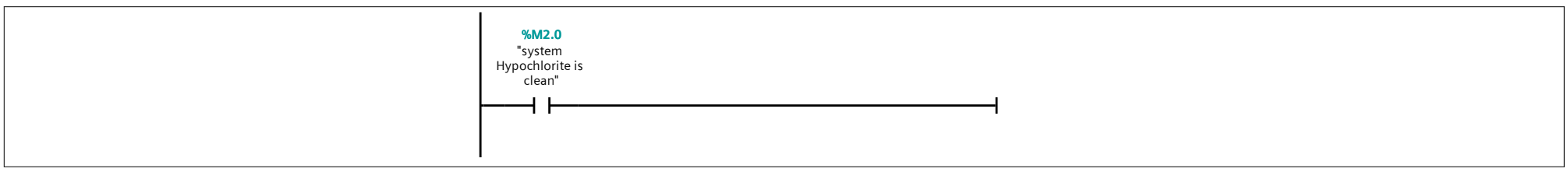
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**

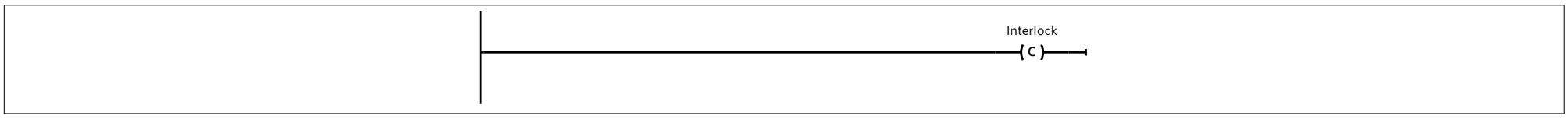
T33:Trans33



S22:Step22

Interlock -(c)-:

Interlock alarm	
Alarm text	



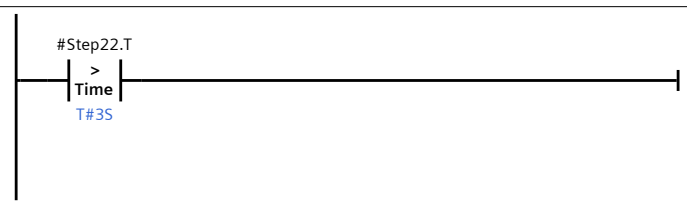
Supervision -(v)-:

Supervision alarm	
Alarm text	

Actions:

Actions:			
Interlock	Event	Qualifier	Action
		N	"Acid tank level HH"
	S1	ON	#Step21

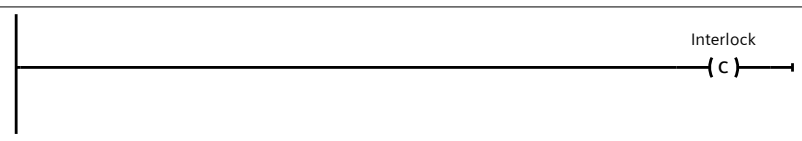
T26:Trans26



S23:Step23

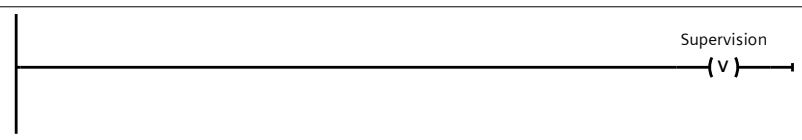
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

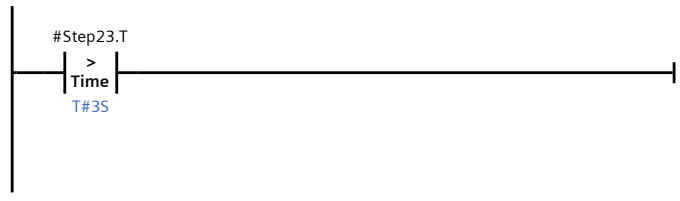
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	Event	Qualifier	Action
		N	"Bulk Acid tank level HH"
	S1	ON	#Step21

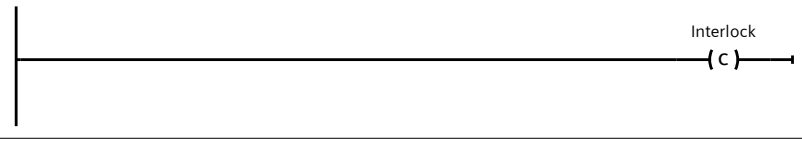
T28:Trans28



S24:Step24

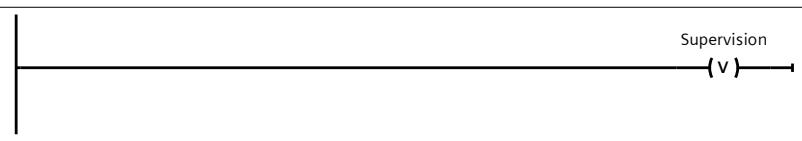
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

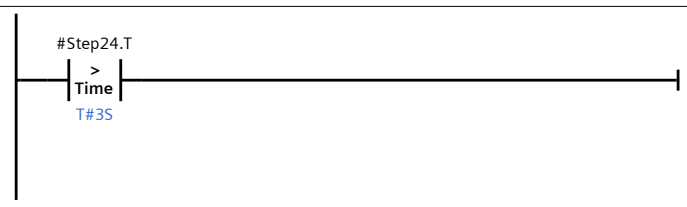
Supervision alarm	
Alarm text	



Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**
		N	"Bulk Tank Level LL"
	S1	ON	#Step21

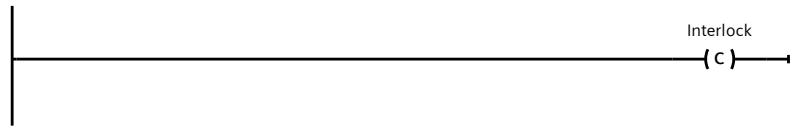
T30:Trans30



S26:Step26

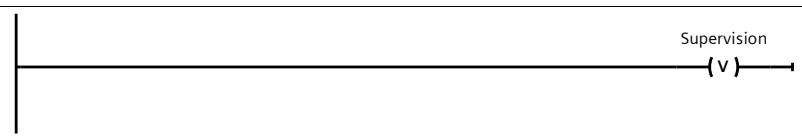
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

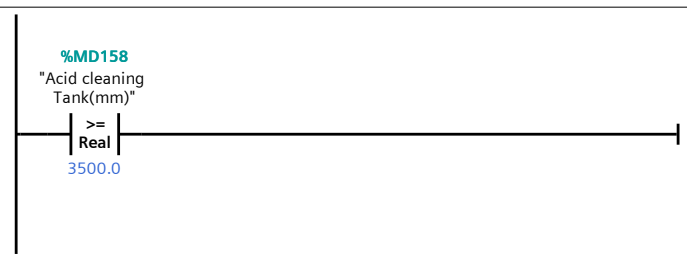
Supervision alarm	
Alarm text	



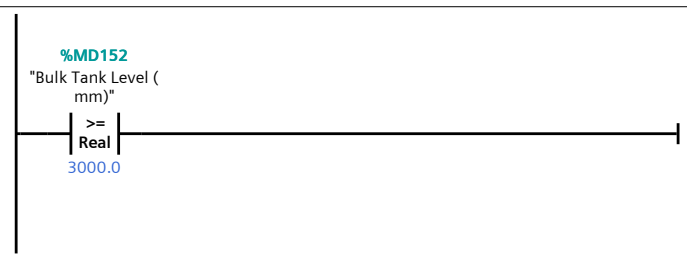
Actions:

Actions:			
Interlock	**Event**	**Qualifier**	**Action**

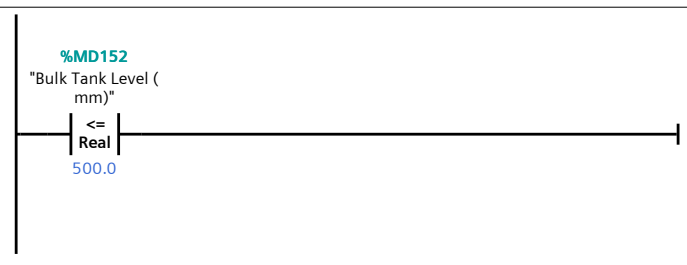
T25:Trans25



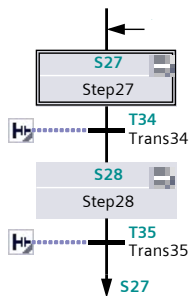
T27:Trans27



T29:Trans29



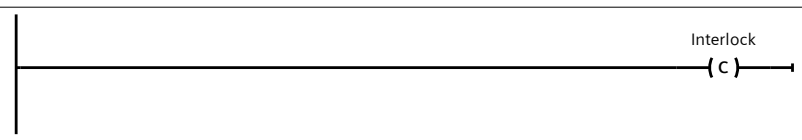
4:



S27 - [Initial step]:Step27

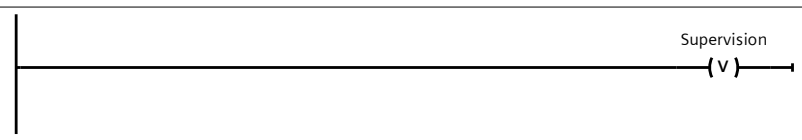
Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

Supervision alarm	
Alarm text	



Actions:

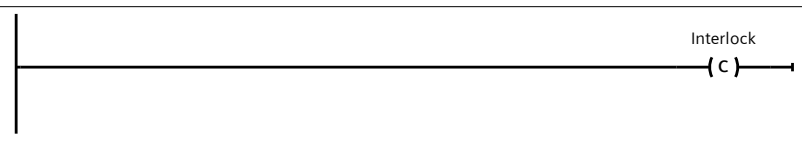
Actions:			
Interlock	Event	Qualifier	Action
	S1	ON	#Step1

T34:Trans34

S28:Step28

Interlock -(c)-:

Interlock alarm	
Alarm text	



Supervision -(v)-:

Supervision alarm	
Alarm text	

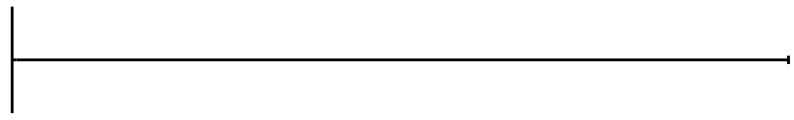
Actions:

Actions:			
Interlock	Event	Qualifier	Action
	S1	OFF	S_ALL

T35:Trans35

Permanent post-instructions

1:



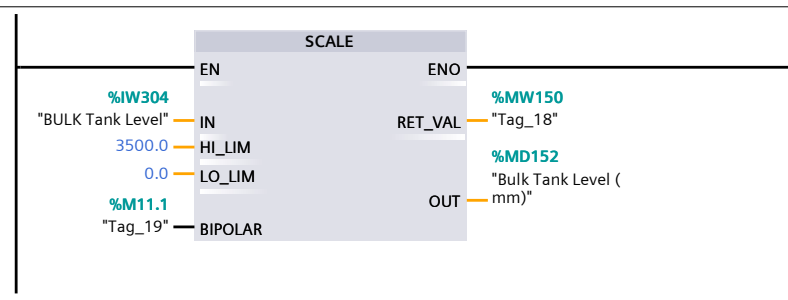
electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / Program blocks

Acid Cleaning Sensors [FC3]

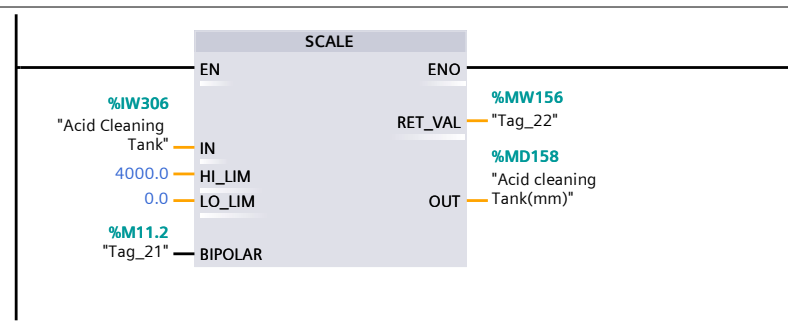
Acid Cleaning Sensors Properties							
General							
Name	Acid Cleaning Sensors	**Number**	3	**Type**	FC	**Language**	LAD
Numbering	Automatic						
Information							
Title		**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Offset	Default value	Comment
Input				
Output				
InOut				
Temp				
Constant				
Return				
Acid Cleaning Sensors	Void			

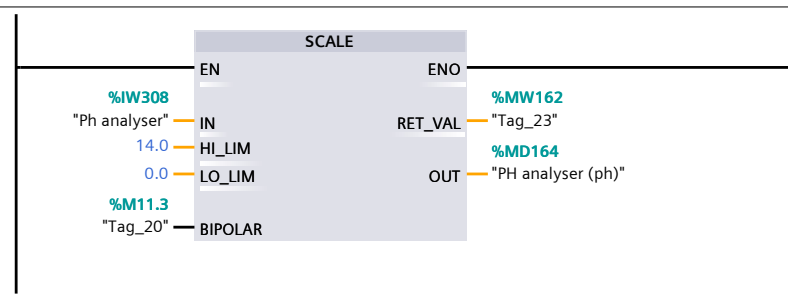
Network 1:



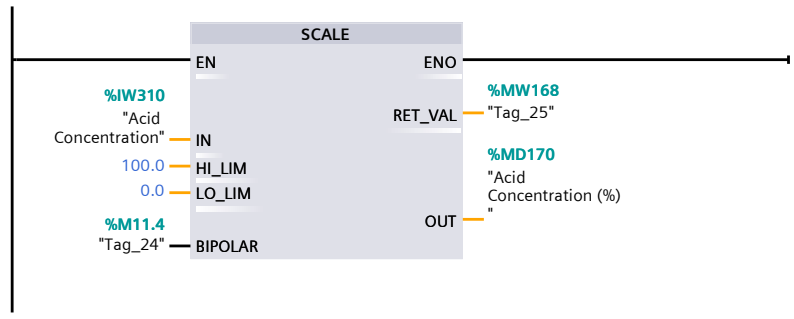
Network 2:



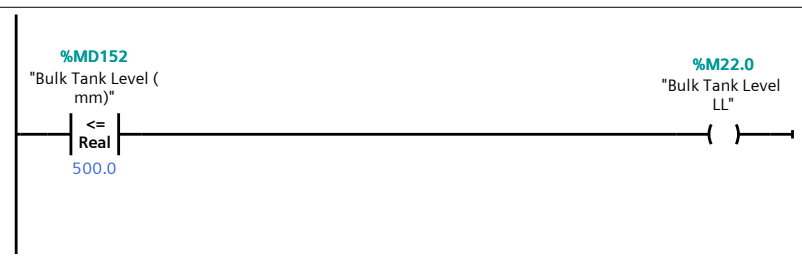
Network 3:



Network 4:

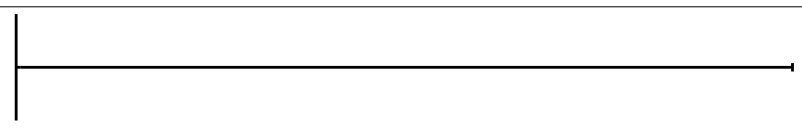


Network 5:

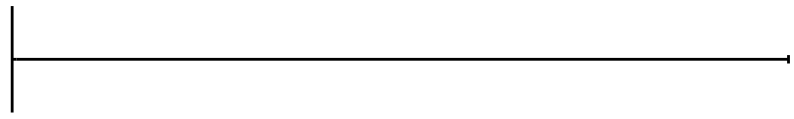


Network 6:

Network 7:



Network 8:



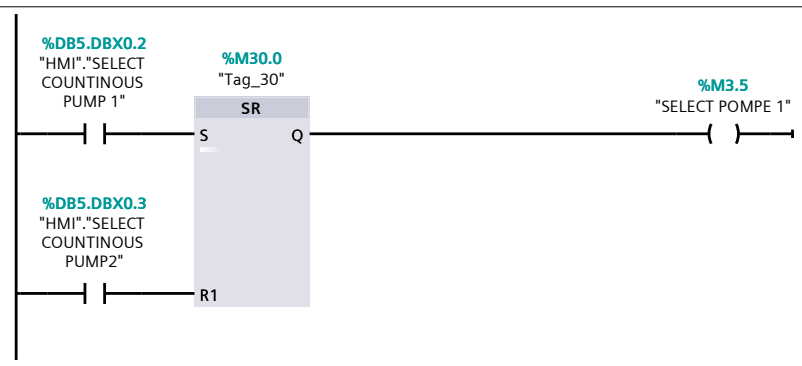
electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / Program blocks

SELECTOR [FC4]

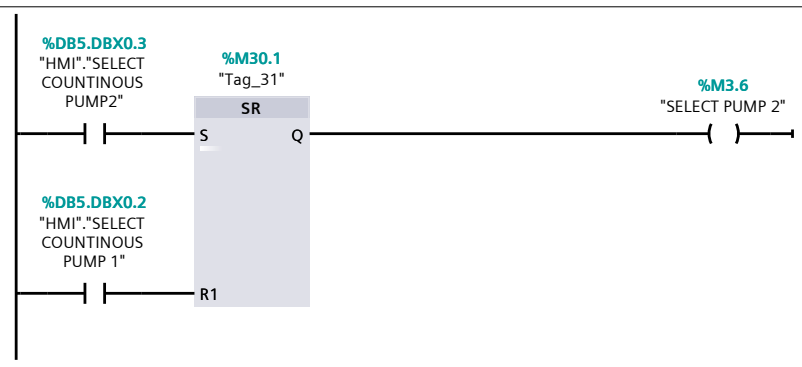
SELECTOR Properties							
General							
Name	SELECTOR	**Number**	4	**Type**	FC	**Language**	LAD
Numbering	Automatic						
Information							
Title		**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Offset	Default value	Comment
Input				
Output				
InOut				
Temp				
Constant				
Return				
SELECTOR	Void			

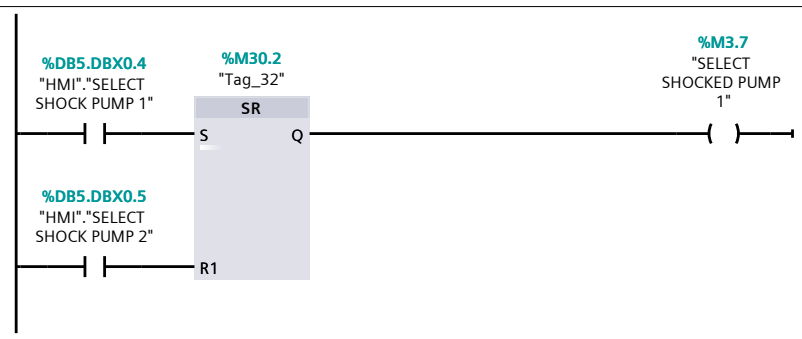
Network 1:



Network 2:



Network 3:



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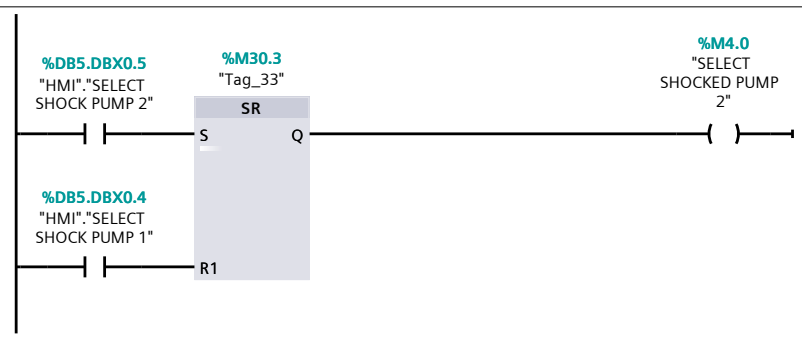

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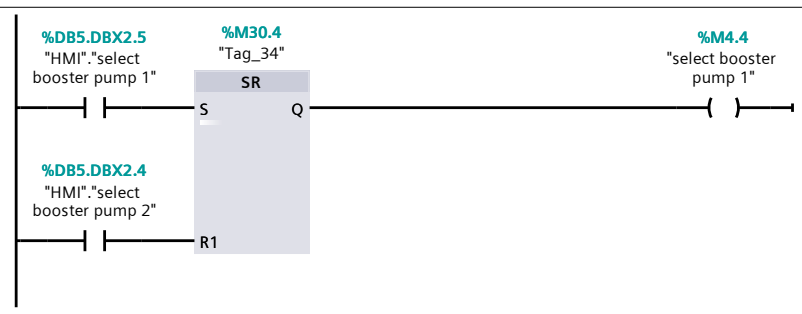

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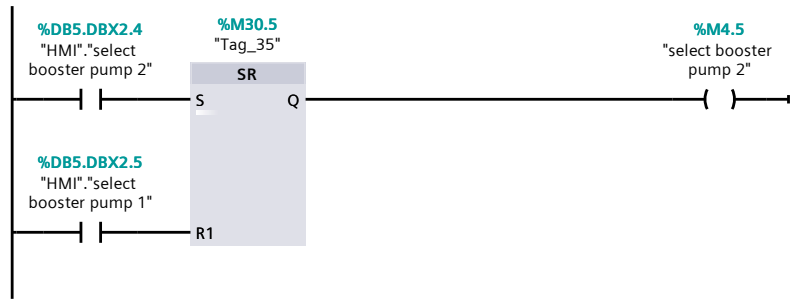
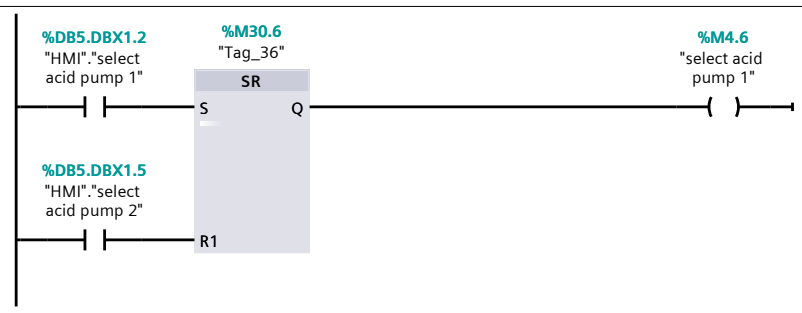


Network 7:



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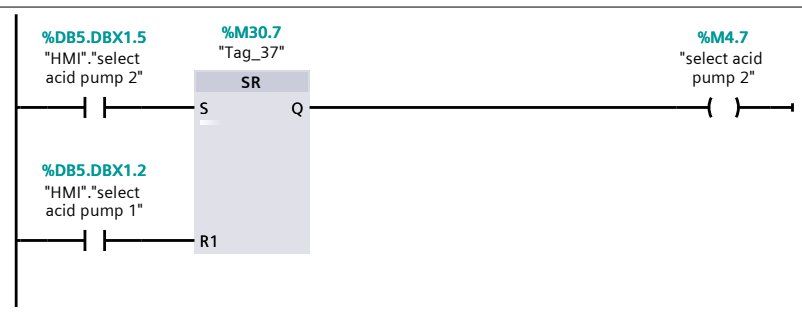
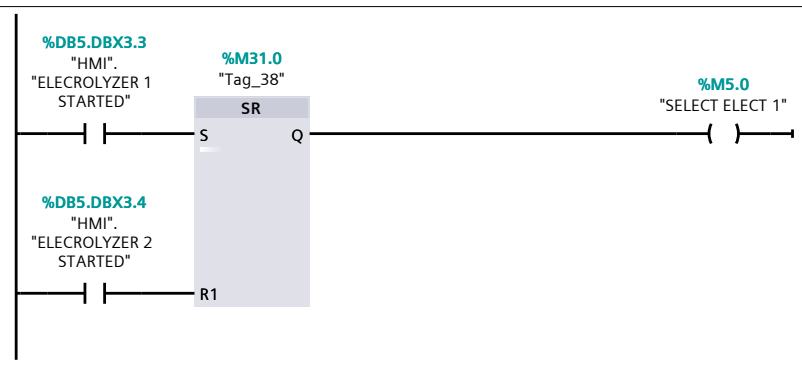


Network 9:



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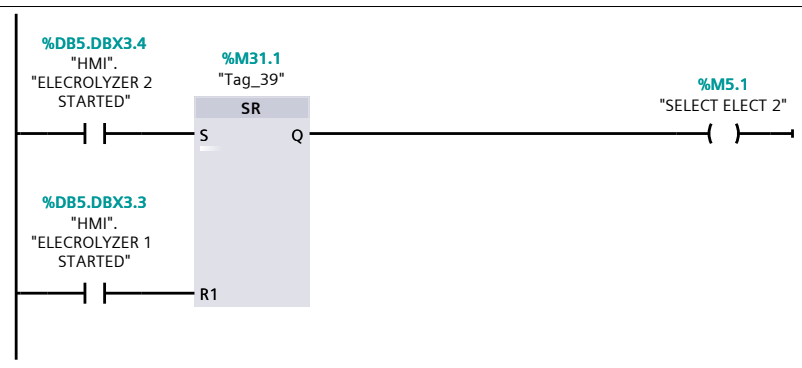
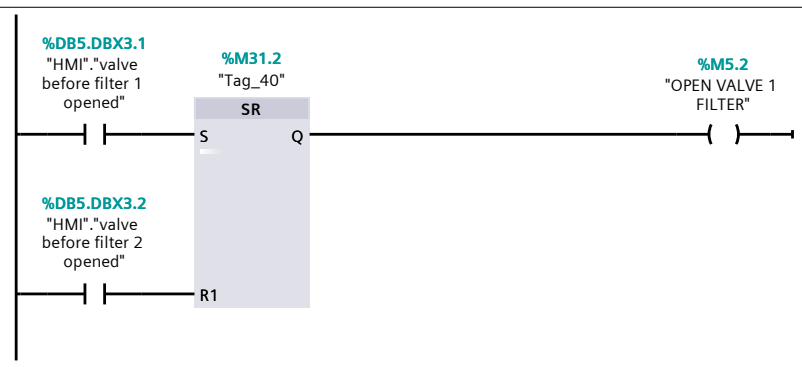
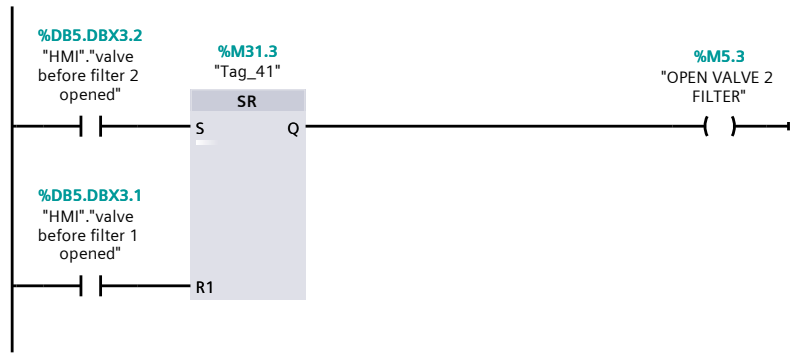


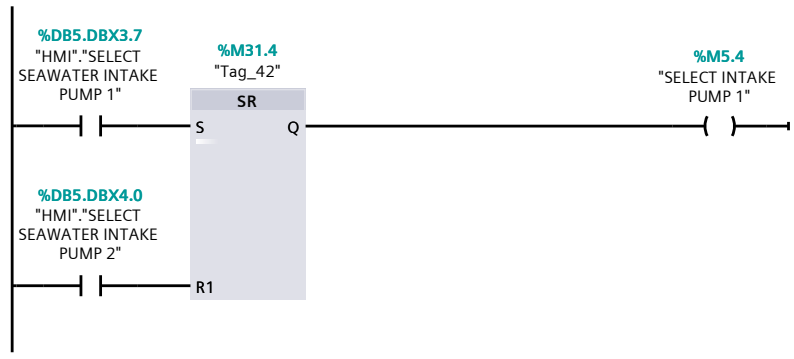
Network 11:



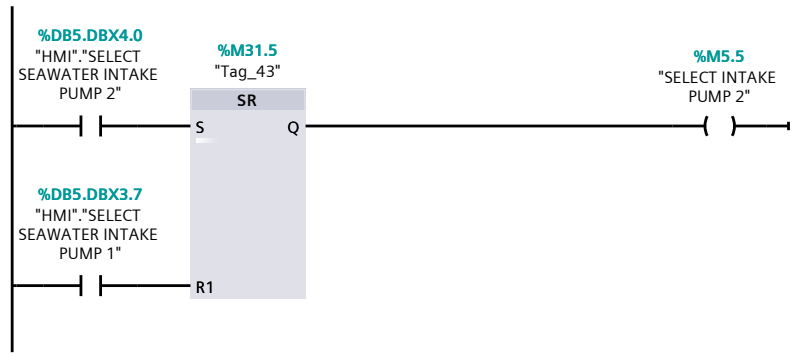
Network 12:



Network 13:



Network 14:



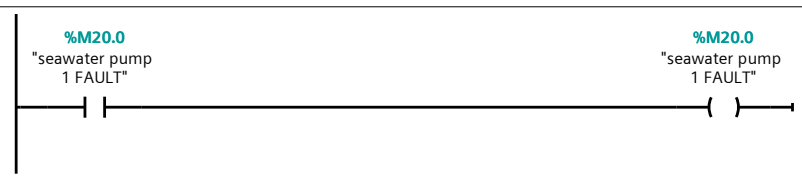
electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / Program blocks

ALARM [FC5]

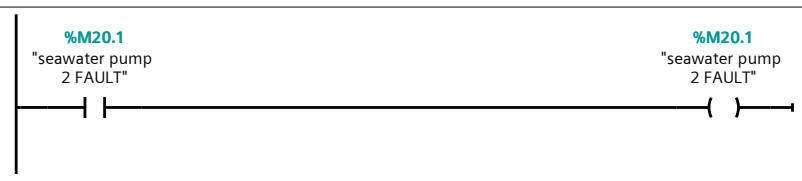
ALARM Properties							
General							
Name	ALARM	**Number**	5	**Type**	FC	**Language**	LAD
Numbering	Automatic						
Information							
Title		**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Offset	Default value	Comment
Input				
Output				
InOut				
Temp				
Constant				
▼ Return				
ALARM	Void			

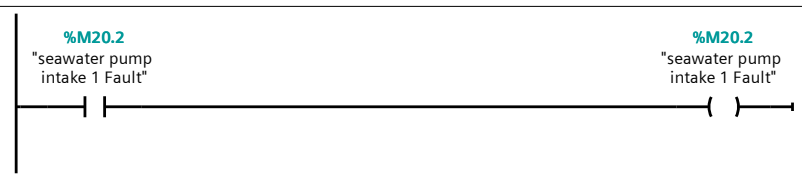
Network 1:



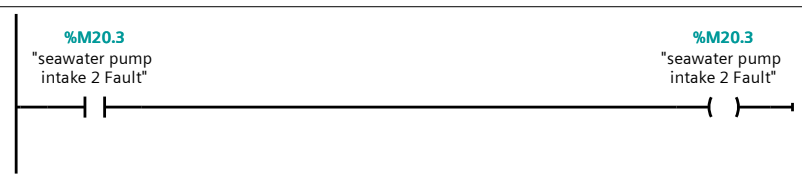
Network 2:



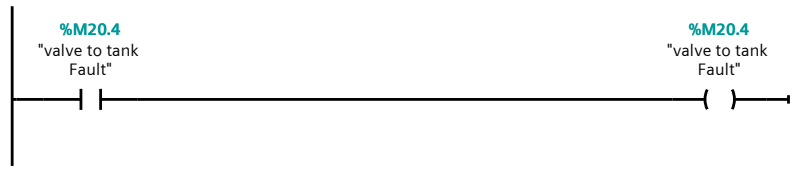
Network 3:



Network 4:



Network 5:



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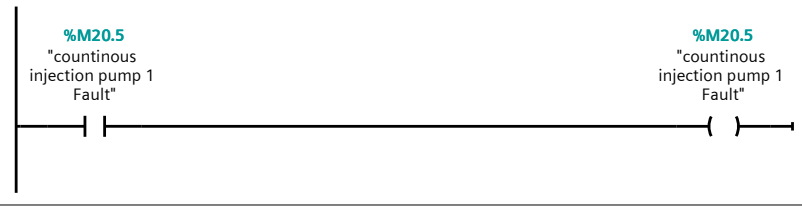
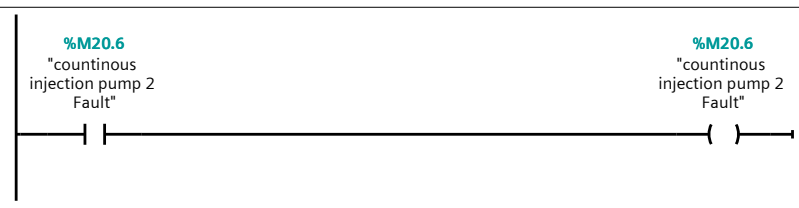
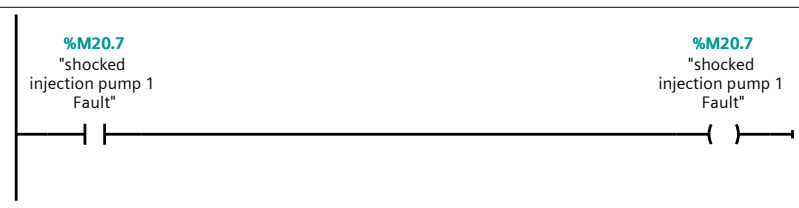


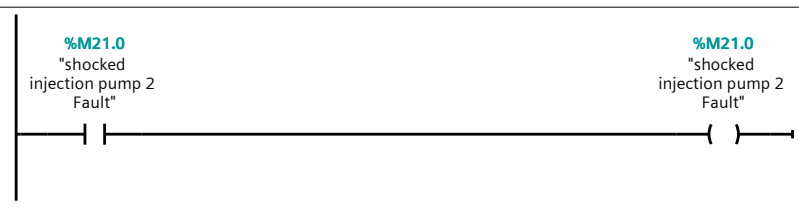
Network 7:



Network 8:



Network 9:



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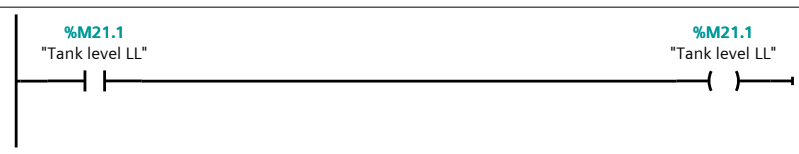

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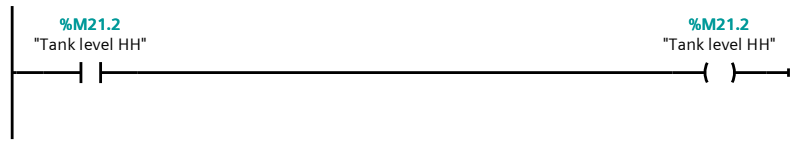
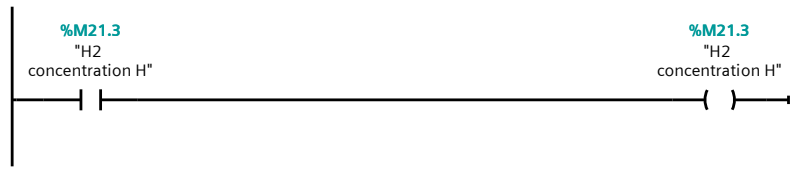


Network 12:



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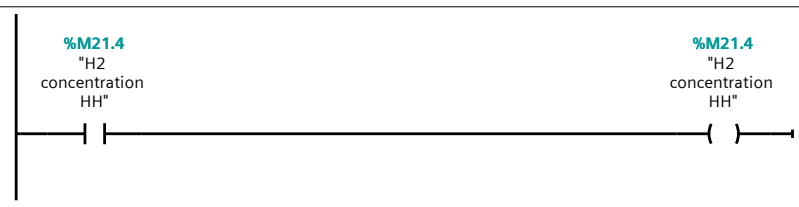
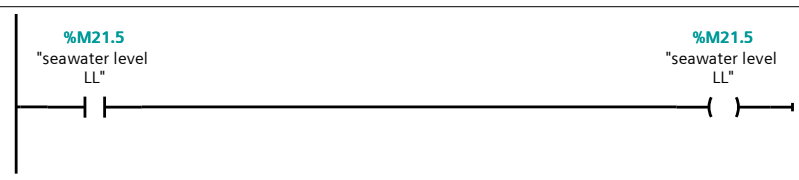


Network 14:



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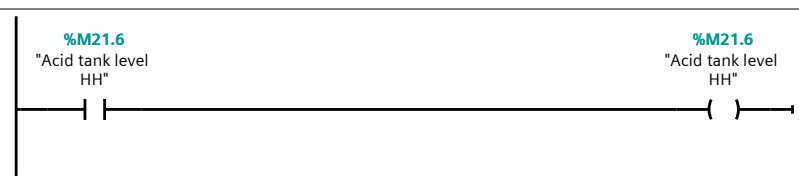

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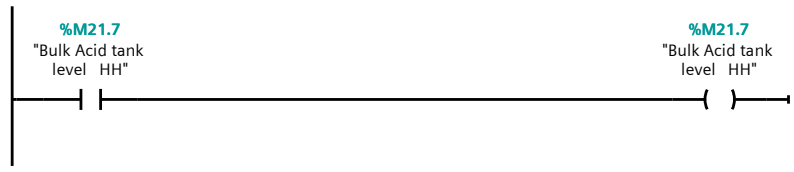

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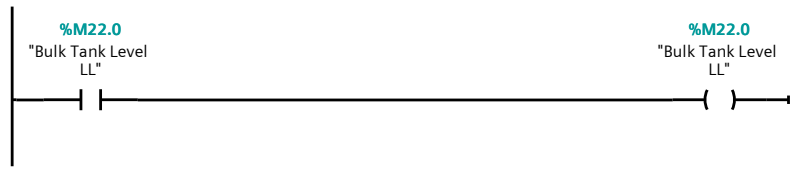

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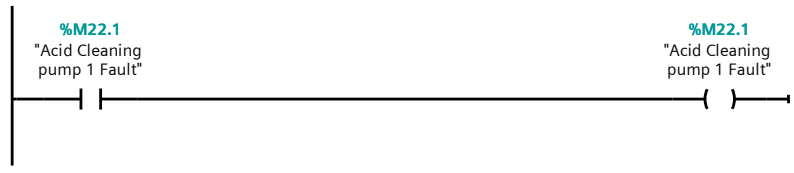

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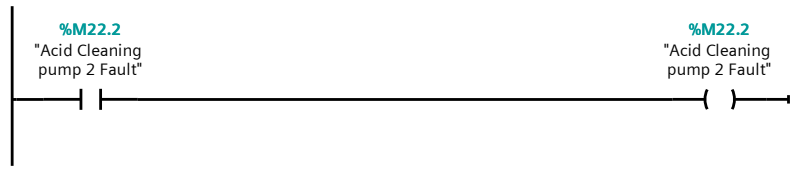

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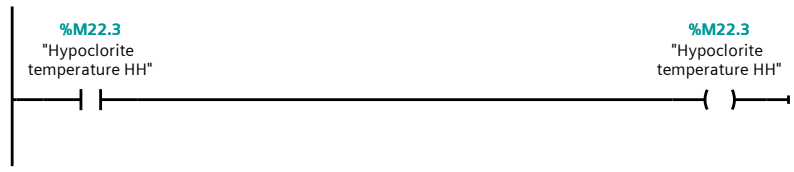
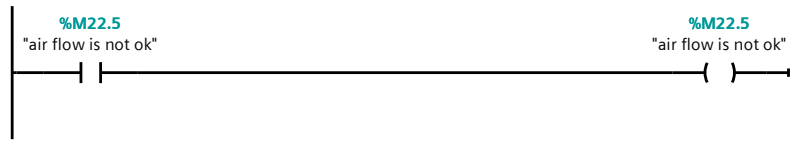


Network 21:

Network 22:



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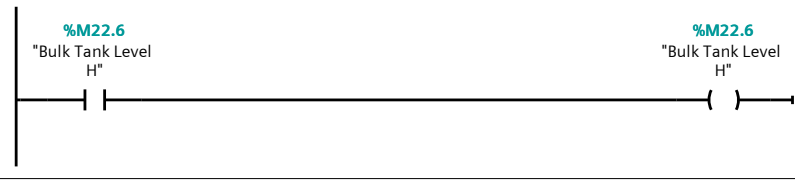

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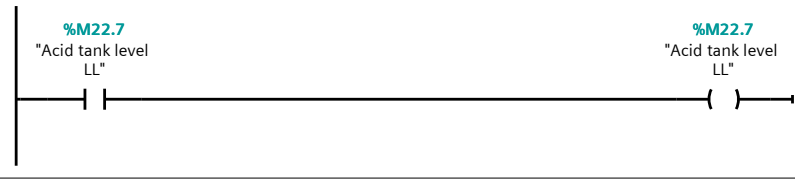

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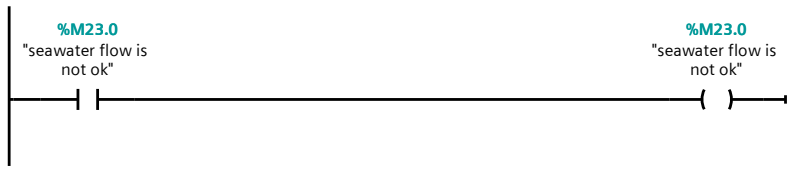
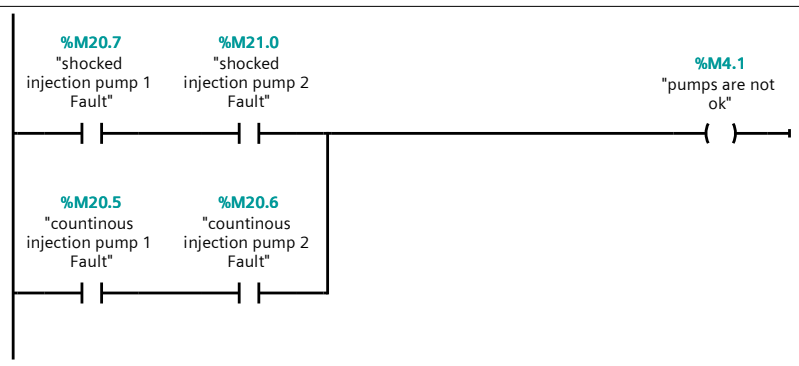


Network 26:



electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / Program blocks

SEAWATER Intake_DB [DB1]

SEAWATER Intake_DB Properties							
General							
Name	SEAWATER Intake_DB	**Number**	1	**Type**	DB	**Language**	DB
Numbering	Automatic						
Information							
Title		**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
Input										
OFF_SQ	Bool	0.0	false	True	True	True	False	False		Désactiver le graphe
INIT_SQ	Bool	0.1	false	True	True	True	False	False		Mettre le graphe à l'état initial
ACK_EF	Bool	0.2	false	True	True	True	False	False		Acquitter toutes les erreurs et perturbations
S_PREV	Bool	0.3	false	True	True	True	False	False		Afficher l'étape précédente dans le paramètre S_NO
S_NEXT	Bool	0.4	false	True	True	True	False	False		Afficher l'étape suivante dans le paramètre S_NO
SW_AUTO	Bool	0.5	false	True	True	True	False	False		Mode automatique
SW_TAP	Bool	0.6	false	True	True	True	False	False		Mode semi-automatique/Activation de l'étape suivante avec transition
SW_TOP	Bool	0.7	false	True	True	True	False	False		Mode semi-automatique/Ignorer la transition
SW_MAN	Bool	1.0	false	True	True	True	False	False		Mode manuel
S_SEL	Int	2.0	0	True	True	True	False	False		Sélectionner l'étape à afficher dans S_NO
S_ON	Bool	4.0	false	True	True	True	False	False		Activer l'étape figurant dans le paramètre S_NO
S_OFF	Bool	4.1	false	True	True	True	False	False		Désactiver l'étape figurant dans le paramètre S_NO
T_PUSH	Bool	4.2	false	True	True	True	False	False		Validation de commutation pour transition en mode semi-automatique
Output										
S_NO	Int	6.0	0	True	True	True	False	False		Numéro d'étape
S_MORE	Bool	8.0	false	True	True	True	False	False		Il existe d'autres étapes qui peuvent être affichées dans S_NO.
S_ACTIVE	Bool	8.1	false	True	True	True	False	False		L'étape affichée dans le paramètre S_NO est activée
ERR_FLT	Bool	8.2	false	True	True	True	False	False		Erreur groupée Interlock or Supervision
AUTO_ON	Bool	8.3	false	True	True	True	False	False		Mode automatique activé
TAP_ON	Bool	8.4	false	True	True	True	False	False		Mode semi-automatique/Etape avec transition activé
TOP_ON	Bool	8.5	false	True	True	True	False	False		Mode semi-automatique/Ignorer la transition activé
MAN_ON	Bool	8.6	false	True	True	True	False	False		Mode manuel activé
InOut										
Static										
Trans1	GraphTransition	70.0		True	True	True	False	True		
TV	Bool	70.0	false	True	True	True	False	False		Transition is valid
TT	Bool	70.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	70.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	70.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	72.0	13	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	74.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	78.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	82.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans7	GraphTransition	86.0		True	True	True	False	True		
TV	Bool	86.0	false	True	True	True	False	False		Transition is valid
TT	Bool	86.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	86.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	86.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	88.0	18	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	90.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_OLD	DWord	94.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	98.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans13	GraphTransition	102.0		True	True	True	False	True		
TV	Bool	102.0	false	True	True	True	False	False		Transition is valid
TT	Bool	102.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	102.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	102.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	104.0	19	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	106.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	110.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	114.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans26	GraphTransition	118.0		True	True	True	False	True		
TV	Bool	118.0	false	True	True	True	False	False		Transition is valid
TT	Bool	118.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	118.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	118.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	120.0	26	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	122.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	126.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	130.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans27	GraphTransition	134.0		True	True	True	False	True		
TV	Bool	134.0	false	True	True	True	False	False		Transition is valid
TT	Bool	134.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	134.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	134.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	136.0	27	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	138.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	142.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	146.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans28	GraphTransition	150.0		True	True	True	False	True		
TV	Bool	150.0	false	True	True	True	False	False		Transition is valid
TT	Bool	150.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	150.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	150.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	152.0	28	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	154.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	158.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	162.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans29	GraphTransition	166.0		True	True	True	False	True		
TV	Bool	166.0	false	True	True	True	False	False		Transition is valid
TT	Bool	166.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	166.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	166.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	168.0	29	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	170.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	174.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	178.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans30	GraphTransition	182.0		True	True	True	False	True		

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TV	Bool	182.0	false	True	True	True	False	False		Transition is valid
TT	Bool	182.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	182.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	182.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	184.0	30	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	186.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	190.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	194.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans31	GraphTransition	198.0		True	True	True	False	True		
TV	Bool	198.0	false	True	True	True	False	False		Transition is valid
TT	Bool	198.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	198.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	198.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	200.0	31	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	202.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	206.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	210.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans32	GraphTransition	214.0		True	True	True	False	True		
TV	Bool	214.0	false	True	True	True	False	False		Transition is valid
TT	Bool	214.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	214.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	214.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	216.0	32	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	218.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	222.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	226.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans33	GraphTransition	230.0		True	True	True	False	True		
TV	Bool	230.0	false	True	True	True	False	False		Transition is valid
TT	Bool	230.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	230.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	230.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	232.0	33	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	234.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	238.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	242.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans34	GraphTransition	246.0		True	True	True	False	True		
TV	Bool	246.0	false	True	True	True	False	False		Transition is valid
TT	Bool	246.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	246.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	246.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	248.0	34	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	250.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	254.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	258.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans35	GraphTransition	262.0		True	True	True	False	True		
TV	Bool	262.0	false	True	True	True	False	False		Transition is valid
TT	Bool	262.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	262.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	262.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TNO	Int	264.0	35	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	266.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	270.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	274.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans36	GraphTransition	278.0		True	True	True	False	True		
TV	Bool	278.0	false	True	True	True	False	False		Transition is valid
TT	Bool	278.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	278.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	278.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	280.0	36	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	282.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	286.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	290.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans37	GraphTransition	294.0		True	True	True	False	True		
TV	Bool	294.0	false	True	True	True	False	False		Transition is valid
TT	Bool	294.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	294.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	294.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	296.0	37	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	298.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	302.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	306.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans38	GraphTransition	310.0		True	True	True	False	True		
TV	Bool	310.0	false	True	True	True	False	False		Transition is valid
TT	Bool	310.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	310.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	310.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	312.0	38	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	314.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	318.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	322.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans39	GraphTransition	326.0		True	True	True	False	True		
TV	Bool	326.0	false	True	True	True	False	False		Transition is valid
TT	Bool	326.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	326.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	326.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	328.0	39	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	330.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	334.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	338.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans41	GraphTransition	342.0		True	False	False	False	True		Transition structure
TV	Bool	342.0	false	True	False	False	False	False		Transition is valid
TT	Bool	342.1	false	True	False	False	False	False		Transition is satisfied
TS	Bool	342.2	false	True	False	False	False	False		Transition switches
CF_IV	Bool	342.3	True	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	344.0	41	True	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	346.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_OLD	DWord	350.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	354.0	16#0	True	False	False	False	False		Copy of CRIT if an error occurs
Trans42	GraphTransition	358.0		True	False	False	False	True		Transition structure
TV	Bool	358.0	false	True	False	False	False	False		Transition is valid
TT	Bool	358.1	false	True	False	False	False	False		Transition is satisfied
TS	Bool	358.2	false	True	False	False	False	False		Transition switches
CF_IV	Bool	358.3	True	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	360.0	42	True	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	362.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	366.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	370.0	16#0	True	False	False	False	False		Copy of CRIT if an error occurs
Trans43	GraphTransition	374.0		True	False	False	False	True		Transition structure
TV	Bool	374.0	false	True	False	False	False	False		Transition is valid
TT	Bool	374.1	false	True	False	False	False	False		Transition is satisfied
TS	Bool	374.2	false	True	False	False	False	False		Transition switches
CF_IV	Bool	374.3	True	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	376.0	43	True	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	378.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	382.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	386.0	16#0	True	False	False	False	False		Copy of CRIT if an error occurs
Trans44	GraphTransition	390.0		True	False	False	False	True		Transition structure
TV	Bool	390.0	false	True	False	False	False	False		Transition is valid
TT	Bool	390.1	false	True	False	False	False	False		Transition is satisfied
TS	Bool	390.2	false	True	False	False	False	False		Transition switches
CF_IV	Bool	390.3	True	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	392.0	44	True	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	394.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	398.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	402.0	16#0	True	False	False	False	False		Copy of CRIT if an error occurs
Trans45	GraphTransition	406.0		True	False	False	False	True		Transition structure
TV	Bool	406.0	false	True	False	False	False	False		Transition is valid
TT	Bool	406.1	false	True	False	False	False	False		Transition is satisfied
TS	Bool	406.2	false	True	False	False	False	False		Transition switches
CF_IV	Bool	406.3	True	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	408.0	45	True	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	410.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	414.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	418.0	16#0	True	False	False	False	False		Copy of CRIT if an error occurs
Trans46	GraphTransition	422.0		True	False	False	False	True		Transition structure
TV	Bool	422.0	false	True	False	False	False	False		Transition is valid
TT	Bool	422.1	false	True	False	False	False	False		Transition is satisfied
TS	Bool	422.2	false	True	False	False	False	False		Transition switches
CF_IV	Bool	422.3	True	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	424.0	46	True	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	426.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	430.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	434.0	16#0	True	False	False	False	False		Copy of CRIT if an error occurs
Trans47	GraphTransition	438.0		True	False	False	False	True		Transition structure

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TV	Bool	438.0	false	True	False	False	False	False		Transition is valid
TT	Bool	438.1	false	True	False	False	False	False		Transition is satisfied
TS	Bool	438.2	false	True	False	False	False	False		Transition switches
CF_IV	Bool	438.3	True	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	440.0	47	True	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	442.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	446.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	450.0	16#0	True	False	False	False	False		Copy of CRIT if an error occurs
▼ Trans48	GraphTransition	454.0		True	False	False	False	True		Transition structure
TV	Bool	454.0	false	True	False	False	False	False		Transition is valid
TT	Bool	454.1	false	True	False	False	False	False		Transition is satisfied
TS	Bool	454.2	false	True	False	False	False	False		Transition switches
CF_IV	Bool	454.3	True	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	456.0	48	True	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	458.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	462.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	466.0	16#0	True	False	False	False	False		Copy of CRIT if an error occurs
▼ Step1	GraphStep	502.0		True	True	True	False	True		
S1	Bool	502.0	false	True	True	True	False	False		Step is activated
L1	Bool	502.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	502.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	502.3	false	True	True	True	False	False		Reserved
A1	Bool	502.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	502.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	502.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	502.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	503.0	false	True	True	True	False	False		Step is active
LA	Bool	503.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	503.2	false	True	True	True	False	False		Supervision active
RA	Bool	503.3	false	True	True	True	False	False		Reserved
AA	Bool	503.4	false	True	True	True	False	False		Reserved
SS	Bool	503.5	false	True	True	True	False	False		System-internal
LS	Bool	503.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	503.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	504.0	1	True	True	True	False	False		User step number
T	Time	506.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	510.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	514.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	518.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	522.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	526.0	false	True	True	True	False	False		System-internal
LP	Bool	526.1	false	True	True	True	False	False		System-internal
LN	Bool	526.2	false	True	True	True	False	False		System-internal
VP	Bool	526.3	false	True	True	True	False	False		System-internal
VN	Bool	526.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	527.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	528.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	530.0	16#0	True	True	True	False	False		Reserve
▼ Step7	GraphStep	534.0		True	True	True	False	True		
S1	Bool	534.0	false	True	True	True	False	False		Step is activated
L1	Bool	534.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	534.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	534.3	false	True	True	True	False	False		Reserved
A1	Bool	534.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	534.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	534.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	534.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	535.0	false	True	True	True	False	False		Step is active

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
LA	Bool	535.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	535.2	false	True	True	True	False	False		Supervision active
RA	Bool	535.3	false	True	True	True	False	False		Reserved
AA	Bool	535.4	false	True	True	True	False	False		Reserved
SS	Bool	535.5	false	True	True	True	False	False		System-internal
LS	Bool	535.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	535.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	536.0	7	True	True	True	False	False		User step number
T	Time	538.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	542.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	546.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	550.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	554.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	558.0	false	True	True	True	False	False		System-internal
LP	Bool	558.1	false	True	True	True	False	False		System-internal
LN	Bool	558.2	false	True	True	True	False	False		System-internal
VP	Bool	558.3	false	True	True	True	False	False		System-internal
VN	Bool	558.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	559.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	560.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	562.0	16#0	True	True	True	False	False		Reserve
▼ Step10	GraphStep	566.0		True	True	True	False	True		
S1	Bool	566.0	false	True	True	True	False	False		Step is activated
L1	Bool	566.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	566.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	566.3	false	True	True	True	False	False		Reserved
A1	Bool	566.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	566.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	566.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	566.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	567.0	false	True	True	True	False	False		Step is active
LA	Bool	567.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	567.2	false	True	True	True	False	False		Supervision active
RA	Bool	567.3	false	True	True	True	False	False		Reserved
AA	Bool	567.4	false	True	True	True	False	False		Reserved
SS	Bool	567.5	false	True	True	True	False	False		System-internal
LS	Bool	567.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	567.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	568.0	10	True	True	True	False	False		User step number
T	Time	570.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	574.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	578.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	582.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	586.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	590.0	false	True	True	True	False	False		System-internal
LP	Bool	590.1	false	True	True	True	False	False		System-internal
LN	Bool	590.2	false	True	True	True	False	False		System-internal
VP	Bool	590.3	false	True	True	True	False	False		System-internal
VN	Bool	590.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	591.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	592.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	594.0	16#0	True	True	True	False	False		Reserve
▼ Step11	GraphStep	598.0		True	True	True	False	True		
S1	Bool	598.0	false	True	True	True	False	False		Step is activated
L1	Bool	598.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	598.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	598.3	false	True	True	True	False	False		Reserved
A1	Bool	598.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	598.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	598.6	false	True	True	True	False	False		Interlock entering state

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
V0	Bool	598.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	599.0	false	True	True	True	False	False		Step is active
LA	Bool	599.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	599.2	false	True	True	True	False	False		Supervision active
RA	Bool	599.3	false	True	True	True	False	False		Reserved
AA	Bool	599.4	false	True	True	True	False	False		Reserved
SS	Bool	599.5	false	True	True	True	False	False		System-internal
LS	Bool	599.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	599.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	600.0	11	True	True	True	False	False		User step number
T	Time	602.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	606.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	610.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	614.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	618.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	622.0	false	True	True	True	False	False		System-internal
LP	Bool	622.1	false	True	True	True	False	False		System-internal
LN	Bool	622.2	false	True	True	True	False	False		System-internal
VP	Bool	622.3	false	True	True	True	False	False		System-internal
VN	Bool	622.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	623.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	624.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	626.0	16#0	True	True	True	False	False		Reserve
▼ Step12	GraphStep	630.0		True	True	True	False	True		
S1	Bool	630.0	false	True	True	True	False	False		Step is activated
L1	Bool	630.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	630.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	630.3	false	True	True	True	False	False		Reserved
A1	Bool	630.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	630.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	630.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	630.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	631.0	false	True	True	True	False	False		Step is active
LA	Bool	631.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	631.2	false	True	True	True	False	False		Supervision active
RA	Bool	631.3	false	True	True	True	False	False		Reserved
AA	Bool	631.4	false	True	True	True	False	False		Reserved
SS	Bool	631.5	false	True	True	True	False	False		System-internal
LS	Bool	631.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	631.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	632.0	12	True	True	True	False	False		User step number
T	Time	634.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	638.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	642.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	646.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	650.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	654.0	false	True	True	True	False	False		System-internal
LP	Bool	654.1	false	True	True	True	False	False		System-internal
LN	Bool	654.2	false	True	True	True	False	False		System-internal
VP	Bool	654.3	false	True	True	True	False	False		System-internal
VN	Bool	654.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	655.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	656.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	658.0	16#0	True	True	True	False	False		Reserve
▼ Step13	GraphStep	662.0		True	True	True	False	True		
S1	Bool	662.0	false	True	True	True	False	False		Step is activated
L1	Bool	662.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	662.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	662.3	false	True	True	True	False	False		Reserved
A1	Bool	662.4	false	True	True	True	False	False		Error is acknowledged

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
S0	Bool	662.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	662.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	662.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	663.0	false	True	True	True	False	False		Step is active
LA	Bool	663.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	663.2	false	True	True	True	False	False		Supervision active
RA	Bool	663.3	false	True	True	True	False	False		Reserved
AA	Bool	663.4	false	True	True	True	False	False		Reserved
SS	Bool	663.5	false	True	True	True	False	False		System-internal
LS	Bool	663.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	663.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	664.0	13	True	True	True	False	False		User step number
T	Time	666.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	670.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	674.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	678.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	682.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	686.0	false	True	True	True	False	False		System-internal
LP	Bool	686.1	false	True	True	True	False	False		System-internal
LN	Bool	686.2	false	True	True	True	False	False		System-internal
VP	Bool	686.3	false	True	True	True	False	False		System-internal
VN	Bool	686.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	687.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	688.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	690.0	16#0	True	True	True	False	False		Reserve
▼ Step14	GraphStep	694.0		True	True	True	False	True		
S1	Bool	694.0	false	True	True	True	False	False		Step is activated
L1	Bool	694.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	694.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	694.3	false	True	True	True	False	False		Reserved
A1	Bool	694.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	694.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	694.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	694.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	695.0	false	True	True	True	False	False		Step is active
LA	Bool	695.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	695.2	false	True	True	True	False	False		Supervision active
RA	Bool	695.3	false	True	True	True	False	False		Reserved
AA	Bool	695.4	false	True	True	True	False	False		Reserved
SS	Bool	695.5	false	True	True	True	False	False		System-internal
LS	Bool	695.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	695.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	696.0	14	True	True	True	False	False		User step number
T	Time	698.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	702.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	706.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	710.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	714.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	718.0	false	True	True	True	False	False		System-internal
LP	Bool	718.1	false	True	True	True	False	False		System-internal
LN	Bool	718.2	false	True	True	True	False	False		System-internal
VP	Bool	718.3	false	True	True	True	False	False		System-internal
VN	Bool	718.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	719.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	720.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	722.0	16#0	True	True	True	False	False		Reserve
▼ Step15	GraphStep	726.0		True	True	True	False	True		
S1	Bool	726.0	false	True	True	True	False	False		Step is activated
L1	Bool	726.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	726.2	false	True	True	True	False	False		Supervision entering state

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
R1	Bool	726.3	false	True	True	True	False	False		Reserved
A1	Bool	726.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	726.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	726.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	726.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	727.0	false	True	True	True	False	False		Step is active
LA	Bool	727.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	727.2	false	True	True	True	False	False		Supervision active
RA	Bool	727.3	false	True	True	True	False	False		Reserved
AA	Bool	727.4	false	True	True	True	False	False		Reserved
SS	Bool	727.5	false	True	True	True	False	False		System-internal
LS	Bool	727.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	727.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	728.0	15	True	True	True	False	False		User step number
T	Time	730.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	734.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	738.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	742.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	746.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	750.0	false	True	True	True	False	False		System-internal
LP	Bool	750.1	false	True	True	True	False	False		System-internal
LN	Bool	750.2	false	True	True	True	False	False		System-internal
VP	Bool	750.3	false	True	True	True	False	False		System-internal
VN	Bool	750.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	751.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	752.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	754.0	16#0	True	True	True	False	False		Reserve
▼ Step16	GraphStep	758.0		True	True	True	False	True		
S1	Bool	758.0	false	True	True	True	False	False		Step is activated
L1	Bool	758.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	758.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	758.3	false	True	True	True	False	False		Reserved
A1	Bool	758.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	758.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	758.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	758.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	759.0	false	True	True	True	False	False		Step is active
LA	Bool	759.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	759.2	false	True	True	True	False	False		Supervision active
RA	Bool	759.3	false	True	True	True	False	False		Reserved
AA	Bool	759.4	false	True	True	True	False	False		Reserved
SS	Bool	759.5	false	True	True	True	False	False		System-internal
LS	Bool	759.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	759.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	760.0	16	True	True	True	False	False		User step number
T	Time	762.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	766.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	770.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	774.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	778.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	782.0	false	True	True	True	False	False		System-internal
LP	Bool	782.1	false	True	True	True	False	False		System-internal
LN	Bool	782.2	false	True	True	True	False	False		System-internal
VP	Bool	782.3	false	True	True	True	False	False		System-internal
VN	Bool	782.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	783.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	784.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	786.0	16#0	True	True	True	False	False		Reserve
▼ Step17	GraphStep	790.0		True	True	True	False	True		
S1	Bool	790.0	false	True	True	True	False	False		Step is activated

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
L1	Bool	790.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	790.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	790.3	false	True	True	True	False	False		Reserved
A1	Bool	790.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	790.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	790.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	790.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	791.0	false	True	True	True	False	False		Step is active
LA	Bool	791.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	791.2	false	True	True	True	False	False		Supervision active
RA	Bool	791.3	false	True	True	True	False	False		Reserved
AA	Bool	791.4	false	True	True	True	False	False		Reserved
SS	Bool	791.5	false	True	True	True	False	False		System-internal
LS	Bool	791.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	791.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	792.0	17	True	True	True	False	False		User step number
T	Time	794.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	798.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	802.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	806.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	810.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	814.0	false	True	True	True	False	False		System-internal
LP	Bool	814.1	false	True	True	True	False	False		System-internal
LN	Bool	814.2	false	True	True	True	False	False		System-internal
VP	Bool	814.3	false	True	True	True	False	False		System-internal
VN	Bool	814.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	815.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	816.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	818.0	16#0	True	True	True	False	False		Reserve
▼ Step18	GraphStep	822.0		True	True	True	False	True		
S1	Bool	822.0	false	True	True	True	False	False		Step is activated
L1	Bool	822.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	822.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	822.3	false	True	True	True	False	False		Reserved
A1	Bool	822.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	822.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	822.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	822.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	823.0	false	True	True	True	False	False		Step is active
LA	Bool	823.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	823.2	false	True	True	True	False	False		Supervision active
RA	Bool	823.3	false	True	True	True	False	False		Reserved
AA	Bool	823.4	false	True	True	True	False	False		Reserved
SS	Bool	823.5	false	True	True	True	False	False		System-internal
LS	Bool	823.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	823.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	824.0	18	True	True	True	False	False		User step number
T	Time	826.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	830.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	834.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	838.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	842.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	846.0	false	True	True	True	False	False		System-internal
LP	Bool	846.1	false	True	True	True	False	False		System-internal
LN	Bool	846.2	false	True	True	True	False	False		System-internal
VP	Bool	846.3	false	True	True	True	False	False		System-internal
VN	Bool	846.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	847.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	848.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	850.0	16#0	True	True	True	False	False		Reserve

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
Step19	GraphStep	854.0		True	True	True	False	True		
S1	Bool	854.0	false	True	True	True	False	False		Step is activated
L1	Bool	854.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	854.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	854.3	false	True	True	True	False	False		Reserved
A1	Bool	854.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	854.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	854.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	854.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	855.0	false	True	True	True	False	False		Step is active
LA	Bool	855.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	855.2	false	True	True	True	False	False		Supervision active
RA	Bool	855.3	false	True	True	True	False	False		Reserved
AA	Bool	855.4	false	True	True	True	False	False		Reserved
SS	Bool	855.5	false	True	True	True	False	False		System-internal
LS	Bool	855.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	855.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	856.0	19	True	True	True	False	False		User step number
T	Time	858.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	862.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	866.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	870.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	874.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	878.0	false	True	True	True	False	False		System-internal
LP	Bool	878.1	false	True	True	True	False	False		System-internal
LN	Bool	878.2	false	True	True	True	False	False		System-internal
VP	Bool	878.3	false	True	True	True	False	False		System-internal
VN	Bool	878.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	879.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	880.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	882.0	16#0	True	True	True	False	False		Reserve
Step20	GraphStep	886.0		True	True	True	False	True		
S1	Bool	886.0	false	True	True	True	False	False		Step is activated
L1	Bool	886.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	886.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	886.3	false	True	True	True	False	False		Reserved
A1	Bool	886.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	886.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	886.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	886.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	887.0	false	True	True	True	False	False		Step is active
LA	Bool	887.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	887.2	false	True	True	True	False	False		Supervision active
RA	Bool	887.3	false	True	True	True	False	False		Reserved
AA	Bool	887.4	false	True	True	True	False	False		Reserved
SS	Bool	887.5	false	True	True	True	False	False		System-internal
LS	Bool	887.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	887.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	888.0	20	True	True	True	False	False		User step number
T	Time	890.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	894.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	898.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	902.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	906.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	910.0	false	True	True	True	False	False		System-internal
LP	Bool	910.1	false	True	True	True	False	False		System-internal
LN	Bool	910.2	false	True	True	True	False	False		System-internal
VP	Bool	910.3	false	True	True	True	False	False		System-internal
VN	Bool	910.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	911.0	16#0	True	True	True	False	False		System-internal

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
H_SV_FLT	Byte	912.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	914.0	16#0	True	True	True	False	False		Reserve
▼ Step21	GraphStep	918.0		True	True	True	False	True		
S1	Bool	918.0	false	True	True	True	False	False		Step is activated
L1	Bool	918.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	918.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	918.3	false	True	True	True	False	False		Reserved
A1	Bool	918.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	918.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	918.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	918.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	919.0	false	True	True	True	False	False		Step is active
LA	Bool	919.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	919.2	false	True	True	True	False	False		Supervision active
RA	Bool	919.3	false	True	True	True	False	False		Reserved
AA	Bool	919.4	false	True	True	True	False	False		Reserved
SS	Bool	919.5	false	True	True	True	False	False		System-internal
LS	Bool	919.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	919.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	920.0	21	True	True	True	False	False		User step number
T	Time	922.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	926.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	930.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	934.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	938.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	942.0	false	True	True	True	False	False		System-internal
LP	Bool	942.1	false	True	True	True	False	False		System-internal
LN	Bool	942.2	false	True	True	True	False	False		System-internal
VP	Bool	942.3	false	True	True	True	False	False		System-internal
VN	Bool	942.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	943.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	944.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	946.0	16#0	True	True	True	False	False		Reserve
▼ Step22	GraphStep	950.0		True	True	True	False	True		
S1	Bool	950.0	false	True	True	True	False	False		Step is activated
L1	Bool	950.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	950.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	950.3	false	True	True	True	False	False		Reserved
A1	Bool	950.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	950.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	950.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	950.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	951.0	false	True	True	True	False	False		Step is active
LA	Bool	951.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	951.2	false	True	True	True	False	False		Supervision active
RA	Bool	951.3	false	True	True	True	False	False		Reserved
AA	Bool	951.4	false	True	True	True	False	False		Reserved
SS	Bool	951.5	false	True	True	True	False	False		System-internal
LS	Bool	951.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	951.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	952.0	22	True	True	True	False	False		User step number
T	Time	954.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	958.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	962.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	966.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	970.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	974.0	false	True	True	True	False	False		System-internal
LP	Bool	974.1	false	True	True	True	False	False		System-internal
LN	Bool	974.2	false	True	True	True	False	False		System-internal
VP	Bool	974.3	false	True	True	True	False	False		System-internal

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
VN	Bool	974.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	975.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	976.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	978.0	16#0	True	True	True	False	False		Reserve
Step23	GraphStep	982.0		True	False	False	False	True		Step structure
S1	Bool	982.0	false	True	False	False	False	False		Step is activated
L1	Bool	982.1	false	True	False	False	False	False		interlock leaving state
V1	Bool	982.2	false	True	False	False	False	False		Supervision entering state
R1	Bool	982.3	false	True	False	False	False	False		Reserved
A1	Bool	982.4	false	True	False	False	False	False		Error is acknowledged
S0	Bool	982.5	false	True	False	False	False	False		Step is deactivated
L0	Bool	982.6	false	True	False	False	False	False		Interlock entering state
V0	Bool	982.7	false	True	False	False	False	False		Supervision leaving state
X	Bool	983.0	false	True	False	False	False	False		Step is active
LA	Bool	983.1	false	True	False	False	False	False		Interlock is not satisfied
VA	Bool	983.2	false	True	False	False	False	False		Supervision active
RA	Bool	983.3	false	True	False	False	False	False		Reserved
AA	Bool	983.4	false	True	False	False	False	False		Reserved
SS	Bool	983.5	false	True	False	False	False	False		System-internal
LS	Bool	983.6	True	True	False	False	False	False		Direct result of the programmed interlock
VS	Bool	983.7	false	True	False	False	False	False		Direct result of the programmed supervision
SNO	Int	984.0	23	True	False	False	False	False		User step number
T	Time	986.0	T#0ms	True	False	False	False	False		Total step activation time
U	Time	990.0	T#0ms	True	False	False	False	False		Step activation time without disturbance
CRIT_LOC	DWord	994.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	998.0	16#0	True	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1002.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1006.0	false	True	False	False	False	False		System-internal
LP	Bool	1006.1	false	True	False	False	False	False		System-internal
LN	Bool	1006.2	false	True	False	False	False	False		System-internal
VP	Bool	1006.3	false	True	False	False	False	False		System-internal
VN	Bool	1006.4	false	True	False	False	False	False		System-internal
H_IL_ERR	Byte	1007.0	16#0	True	False	False	False	False		System-internal
H_SV_FLT	Byte	1008.0	16#0	True	False	False	False	False		System-internal
RESERVED	DWord	1010.0	16#0	True	False	False	False	False		Reserve
Step24	GraphStep	1014.0		True	False	False	False	True		Step structure
S1	Bool	1014.0	false	True	False	False	False	False		Step is activated
L1	Bool	1014.1	false	True	False	False	False	False		interlock leaving state
V1	Bool	1014.2	false	True	False	False	False	False		Supervision entering state
R1	Bool	1014.3	false	True	False	False	False	False		Reserved
A1	Bool	1014.4	false	True	False	False	False	False		Error is acknowledged
S0	Bool	1014.5	false	True	False	False	False	False		Step is deactivated
L0	Bool	1014.6	false	True	False	False	False	False		Interlock entering state
V0	Bool	1014.7	false	True	False	False	False	False		Supervision leaving state
X	Bool	1015.0	false	True	False	False	False	False		Step is active
LA	Bool	1015.1	false	True	False	False	False	False		Interlock is not satisfied
VA	Bool	1015.2	false	True	False	False	False	False		Supervision active
RA	Bool	1015.3	false	True	False	False	False	False		Reserved
AA	Bool	1015.4	false	True	False	False	False	False		Reserved
SS	Bool	1015.5	false	True	False	False	False	False		System-internal
LS	Bool	1015.6	True	True	False	False	False	False		Direct result of the programmed interlock
VS	Bool	1015.7	false	True	False	False	False	False		Direct result of the programmed supervision
SNO	Int	1016.0	24	True	False	False	False	False		User step number
T	Time	1018.0	T#0ms	True	False	False	False	False		Total step activation time
U	Time	1022.0	T#0ms	True	False	False	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1026.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1030.0	16#0	True	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1034.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1038.0	false	True	False	False	False	False		System-internal
LP	Bool	1038.1	false	True	False	False	False	False		System-internal

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
LN	Bool	1038.2	false	True	False	False	False	False		System-internal
VP	Bool	1038.3	false	True	False	False	False	False		System-internal
VN	Bool	1038.4	false	True	False	False	False	False		System-internal
H_IL_ERR	Byte	1039.0	16#0	True	False	False	False	False		System-internal
H_SV_FLT	Byte	1040.0	16#0	True	False	False	False	False		System-internal
RESERVED	DWord	1042.0	16#0	True	False	False	False	False		Reserve
Step25	GraphStep	1046.0		True	False	False	False	True		Step structure
S1	Bool	1046.0	false	True	False	False	False	False		Step is activated
L1	Bool	1046.1	false	True	False	False	False	False		interlock leaving state
V1	Bool	1046.2	false	True	False	False	False	False		Supervision entering state
R1	Bool	1046.3	false	True	False	False	False	False		Reserved
A1	Bool	1046.4	false	True	False	False	False	False		Error is acknowledged
S0	Bool	1046.5	false	True	False	False	False	False		Step is deactivated
L0	Bool	1046.6	false	True	False	False	False	False		Interlock entering state
V0	Bool	1046.7	false	True	False	False	False	False		Supervision leaving state
X	Bool	1047.0	false	True	False	False	False	False		Step is active
LA	Bool	1047.1	false	True	False	False	False	False		Interlock is not satisfied
VA	Bool	1047.2	false	True	False	False	False	False		Supervision active
RA	Bool	1047.3	false	True	False	False	False	False		Reserved
AA	Bool	1047.4	false	True	False	False	False	False		Reserved
SS	Bool	1047.5	false	True	False	False	False	False		System-internal
LS	Bool	1047.6	True	True	False	False	False	False		Direct result of the programmed interlock
VS	Bool	1047.7	false	True	False	False	False	False		Direct result of the programmed supervision
SNO	Int	1048.0	25	True	False	False	False	False		User step number
T	Time	1050.0	T#0ms	True	False	False	False	False		Total step activation time
U	Time	1054.0	T#0ms	True	False	False	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1058.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1062.0	16#0	True	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1066.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1070.0	false	True	False	False	False	False		System-internal
LP	Bool	1070.1	false	True	False	False	False	False		System-internal
LN	Bool	1070.2	false	True	False	False	False	False		System-internal
VP	Bool	1070.3	false	True	False	False	False	False		System-internal
VN	Bool	1070.4	false	True	False	False	False	False		System-internal
H_IL_ERR	Byte	1071.0	16#0	True	False	False	False	False		System-internal
H_SV_FLT	Byte	1072.0	16#0	True	False	False	False	False		System-internal
RESERVED	DWord	1074.0	16#0	True	False	False	False	False		Reserve
Step26	GraphStep	1078.0		True	False	False	False	True		Step structure
S1	Bool	1078.0	false	True	False	False	False	False		Step is activated
L1	Bool	1078.1	false	True	False	False	False	False		interlock leaving state
V1	Bool	1078.2	false	True	False	False	False	False		Supervision entering state
R1	Bool	1078.3	false	True	False	False	False	False		Reserved
A1	Bool	1078.4	false	True	False	False	False	False		Error is acknowledged
S0	Bool	1078.5	false	True	False	False	False	False		Step is deactivated
L0	Bool	1078.6	false	True	False	False	False	False		Interlock entering state
V0	Bool	1078.7	false	True	False	False	False	False		Supervision leaving state
X	Bool	1079.0	false	True	False	False	False	False		Step is active
LA	Bool	1079.1	false	True	False	False	False	False		Interlock is not satisfied
VA	Bool	1079.2	false	True	False	False	False	False		Supervision active
RA	Bool	1079.3	false	True	False	False	False	False		Reserved
AA	Bool	1079.4	false	True	False	False	False	False		Reserved
SS	Bool	1079.5	false	True	False	False	False	False		System-internal
LS	Bool	1079.6	True	True	False	False	False	False		Direct result of the programmed interlock
VS	Bool	1079.7	false	True	False	False	False	False		Direct result of the programmed supervision
SNO	Int	1080.0	26	True	False	False	False	False		User step number
T	Time	1082.0	T#0ms	True	False	False	False	False		Total step activation time
U	Time	1086.0	T#0ms	True	False	False	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1090.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1094.0	16#0	True	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1098.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
SM	Bool	1102.0	false	True	False	False	False	False		System-internal
LP	Bool	1102.1	false	True	False	False	False	False		System-internal
LN	Bool	1102.2	false	True	False	False	False	False		System-internal
VP	Bool	1102.3	false	True	False	False	False	False		System-internal
VN	Bool	1102.4	false	True	False	False	False	False		System-internal
H_IL_ERR	Byte	1103.0	16#0	True	False	False	False	False		System-internal
H_SV_FLT	Byte	1104.0	16#0	True	False	False	False	False		System-internal
RESERVED	DWord	1106.0	16#0	True	False	False	False	False		Reserve
Step27	GraphStep	1110.0		True	False	False	False	True		Step structure
S1	Bool	1110.0	false	True	False	False	False	False		Step is activated
L1	Bool	1110.1	false	True	False	False	False	False		interlock leaving state
V1	Bool	1110.2	false	True	False	False	False	False		Supervision entering state
R1	Bool	1110.3	false	True	False	False	False	False		Reserved
A1	Bool	1110.4	false	True	False	False	False	False		Error is acknowledged
S0	Bool	1110.5	false	True	False	False	False	False		Step is deactivated
L0	Bool	1110.6	false	True	False	False	False	False		Interlock entering state
V0	Bool	1110.7	false	True	False	False	False	False		Supervision leaving state
X	Bool	1111.0	false	True	False	False	False	False		Step is active
LA	Bool	1111.1	false	True	False	False	False	False		Interlock is not satisfied
VA	Bool	1111.2	false	True	False	False	False	False		Supervision active
RA	Bool	1111.3	false	True	False	False	False	False		Reserved
AA	Bool	1111.4	false	True	False	False	False	False		Reserved
SS	Bool	1111.5	false	True	False	False	False	False		System-internal
LS	Bool	1111.6	True	True	False	False	False	False		Direct result of the programmed interlock
VS	Bool	1111.7	false	True	False	False	False	False		Direct result of the programmed supervision
SNO	Int	1112.0	27	True	False	False	False	False		User step number
T	Time	1114.0	T#0ms	True	False	False	False	False		Total step activation time
U	Time	1118.0	T#0ms	True	False	False	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1122.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1126.0	16#0	True	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1130.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1134.0	false	True	False	False	False	False		System-internal
LP	Bool	1134.1	false	True	False	False	False	False		System-internal
LN	Bool	1134.2	false	True	False	False	False	False		System-internal
VP	Bool	1134.3	false	True	False	False	False	False		System-internal
VN	Bool	1134.4	false	True	False	False	False	False		System-internal
H_IL_ERR	Byte	1135.0	16#0	True	False	False	False	False		System-internal
H_SV_FLT	Byte	1136.0	16#0	True	False	False	False	False		System-internal
RESERVED	DWord	1138.0	16#0	True	False	False	False	False		Reserve
Step28	GraphStep	1142.0		True	False	False	False	True		Step structure
S1	Bool	1142.0	false	True	False	False	False	False		Step is activated
L1	Bool	1142.1	false	True	False	False	False	False		interlock leaving state
V1	Bool	1142.2	false	True	False	False	False	False		Supervision entering state
R1	Bool	1142.3	false	True	False	False	False	False		Reserved
A1	Bool	1142.4	false	True	False	False	False	False		Error is acknowledged
S0	Bool	1142.5	false	True	False	False	False	False		Step is deactivated
L0	Bool	1142.6	false	True	False	False	False	False		Interlock entering state
V0	Bool	1142.7	false	True	False	False	False	False		Supervision leaving state
X	Bool	1143.0	false	True	False	False	False	False		Step is active
LA	Bool	1143.1	false	True	False	False	False	False		Interlock is not satisfied
VA	Bool	1143.2	false	True	False	False	False	False		Supervision active
RA	Bool	1143.3	false	True	False	False	False	False		Reserved
AA	Bool	1143.4	false	True	False	False	False	False		Reserved
SS	Bool	1143.5	false	True	False	False	False	False		System-internal
LS	Bool	1143.6	True	True	False	False	False	False		Direct result of the programmed interlock
VS	Bool	1143.7	false	True	False	False	False	False		Direct result of the programmed supervision
SNO	Int	1144.0	28	True	False	False	False	False		User step number
T	Time	1146.0	T#0ms	True	False	False	False	False		Total step activation time
U	Time	1150.0	T#0ms	True	False	False	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1154.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1158.0	16#0	True	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_SUP	DWord	1162.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1166.0	false	True	False	False	False	False		System-internal
LP	Bool	1166.1	false	True	False	False	False	False		System-internal
LN	Bool	1166.2	false	True	False	False	False	False		System-internal
VP	Bool	1166.3	false	True	False	False	False	False		System-internal
VN	Bool	1166.4	false	True	False	False	False	False		System-internal
H_IL_ERR	Byte	1167.0	16#0	True	False	False	False	False		System-internal
H_SV_FLT	Byte	1168.0	16#0	True	False	False	False	False		System-internal
RESERVED	DWord	1170.0	16#0	True	False	False	False	False		Reserve
S_DISPLAY	Int	1350.0	0	True	True	True	False	False		Affichage interne du paramètre de sortie S_NO
S_SEL_OLD	Int	1352.0	0	True	True	True	False	False		Valeur précédente dans S_SEL
S_DISPIDX	Byte	1354.0	16#0	True	True	True	False	False		Indice de l'étape affichée dans S_NO
T_DISPIDX	Byte	1355.0	16#0	True	True	True	False	False		Indice de la transition affichée dans T_NO
MOP	Struct	1360.0		True	True	True	False	False		Mode de fonctionnement
AUTO	Bool	1360.0	true	True	True	True	False	False		Etat : Mode automatique
MAN	Bool	1360.1	false	True	True	True	False	False		Etat : Mode manuel
TAP	Bool	1360.2	false	True	True	True	False	False		Etat : Mode semi-automatique/Activation de l'étape suivante avec transition
TOP	Bool	1360.3	false	True	True	True	False	False		Etat : Mode semi-automatique/Ignorer la transition
ACK_S	Bool	1360.4	false	True	True	True	False	False		Requête : Acquitter l'étape figurant dans le paramètre S_NO
REG_S	Bool	1360.5	false	True	True	True	False	False		Requête : Enregistrer l'étape figurant dans le paramètre S_NO
T_PREV	Bool	1360.6	false	True	True	True	False	False		Requête : Afficher la transition valide précédente dans le paramètre T_NO
T_NEXT	Bool	1360.7	false	True	True	True	False	False		Requête : Afficher la transition valide suivante dans le paramètre T_NO
LOCK	Bool	1361.0	true	True	True	True	False	False		Etat : Traitement des Interlocks activé
SUP	Bool	1361.1	true	True	True	True	False	False		Etat : Traitement de Supervision activé
ACKREQ	Bool	1361.2	true	True	True	True	False	False		Etat : Acquittement requis
SSKIP	Bool	1361.3	false	True	True	True	False	False		Etat : "Ignorer les étapes" activé
OFF	Bool	1361.4	false	True	True	True	False	False		Requête : Désactiver toutes les étapes
INIT	Bool	1361.5	true	True	True	True	False	False		Requête : Mettre le graphe à l'état initial
HALT	Bool	1361.6	false	True	True	True	False	False		Etat : Le graphe est arrêté
TMS_HALT	Bool	1361.7	false	True	True	True	False	False		Etat : Toutes les temporisations internes sont arrêtées
OPS_ZERO	Bool	1362.0	false	True	True	True	False	False		Etat : Tous les opérandes traités avec N, L, D mis à 0
SACT_DISP	Bool	1362.1	true	True	True	True	False	False		Etat : N'afficher que les étapes actives
SEF_DISP	Bool	1362.2	false	True	True	True	False	False		Etat : Afficher uniquement les étapes avec erreurs et perturbations
SALL_DISP	Bool	1362.3	false	True	True	True	False	False		Etat : Afficher toutes les étapes
S_PREV	Bool	1362.4	false	True	True	True	False	False		Requête : Afficher l'étape précédente dans le paramètre S_NO
S_NEXT	Bool	1362.5	false	True	True	True	False	False		Requête : Afficher l'étape suivante dans le paramètre S_NO
S_SELOK	Bool	1362.6	false	True	True	True	False	False		Requête : Afficher dans le paramètre S_NO le numéro d'étape figurant dans S_SEL
S_ON	Bool	1362.7	false	True	True	True	False	False		Requête : Activer l'étape figurant dans le paramètre S_NO
S_OFF	Bool	1363.0	false	True	True	True	False	False		Requête : Désactiver l'étape figurant dans le paramètre S_NO
T_PUSH	Bool	1363.1	false	True	True	True	False	False		Requête : Validation de commutation pour transition
REG	Bool	1363.2	false	True	True	True	False	False		Requête : Enregistrer toutes les erreurs Interlock et Supervision
ACK	Bool	1363.3	false	True	True	True	False	False		Requête : Acquitter toutes les erreurs Interlock et Supervision
IL_PERM	Bool	1363.4	false	True	True	True	False	False		Etat : Traitement permanent de tous les Interlocks
T_PERM	Bool	1363.5	false	True	True	True	False	False		Etat : Traitement permanent de toutes les transitions
ILP_MAN	Bool	1363.6	false	True	True	True	False	False		Etat : Traitement permanent de tous les Interlocks en mode manuel
TICKS	Struct	1364.0		True	True	True	False	False		Périodes d'horloge

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
DELTA	Time	1364.0	T#0ms	True	True	True	False	False		Différence de temps entre deux cycles
OLD	Time	1368.0	T#0ms	True	True	True	False	False		Valeur du compteur de périodes 10 ms au dernier cycle
NEW	Time	1372.0	T#0ms	True	True	True	False	False		Valeur du compteur de périodes 10 ms dans ce cycle
▼ SQ_FLAGS	Struct	1376.0		True	True	True	False	False		Mémentos de graphe
ERR_FLT	Bool	1376.0	false	True	True	True	False	False		Erreur groupée Interlock et Supervision
ERROR	Bool	1376.1	false	True	True	True	False	False		Erreur groupée Interlock
FAULT	Bool	1376.2	false	True	True	True	False	False		Erreur groupée Supervision
RT_FAIL	Bool	1376.3	false	True	True	True	False	False		Erreur à l'exécution
NO_SNO	Bool	1376.4	false	True	True	True	False	False		Numéro d'étape requis introuvable
NF_OFL	Bool	1376.5	false	True	True	True	False	False		Débordement : Trop de requêtes ON ou OFF
SA_OFL	Bool	1376.6	false	True	True	True	False	False		Débordement : Trop d'étapes activées
TV_OFL	Bool	1376.7	false	True	True	True	False	False		Débordement : Trop de transitions valides
MSG_OFL	Bool	1377.0	false	True	True	True	False	False		Débordement : Pas assez de ressources système pour ALARM_S
NO_SWI	Bool	1377.1	false	True	True	True	False	False		Pas de commutation dans ce cycle
CYC_OP	Bool	1377.2	false	True	True	True	False	False		Exécution cyclique du graphe après l'initialisation
AS_MSG	Bool	1377.3	true	True	True	True	False	False		Messages à l'exécution activés ou désactivés par instruction
AS_SEND	Bool	1377.4	false	True	True	True	False	False		Envoyer les messages de WR_USMSG ou les entrer uniquement dans le tampon de diagnostic
SQ_BUSY	Bool	1377.5	false	True	True	True	False	False		Mémento de front interne pour le traitement séquentiel
SA_BUSY	Bool	1377.6	false	True	True	True	False	False		Mémento de front interne pour le traitement séquentiel
AS_SIG	Bool	1377.7	false	True	True	True	False	False		Mémentos de front pour messages de Alarm_S et Alarm_SQ

electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / Program blocks

ELECTRO CHLOR STATION_DB [DB2]

ELECTRO CHLOR STATION_DB Properties							
General							
Name	ELECTRO CHLOR STATION_DB	**Number**	2	**Type**	DB	**Language**	DB
Numbering	Automatic						
Information							
Title		**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision				Comment
Input													
OFF_SQ	Bool	0.0	false	True	True	True	False	False					Désactiver le graphe
INIT_SQ	Bool	0.1	false	True	True	True	False	False					Mettre le graphe à l'état initial
ACK_EF	Bool	0.2	false	True	True	True	False	False					Acquitter toutes les erreurs et perturbations
S_PREV	Bool	0.3	false	True	True	True	False	False					Afficher l'étape précédente dans le paramètre S_NO
S_NEXT	Bool	0.4	false	True	True	True	False	False					Afficher l'étape suivante dans le paramètre S_NO
SW_AUTO	Bool	0.5	false	True	True	True	False	False					Mode automatique
SW_TAP	Bool	0.6	false	True	True	True	False	False					Mode semi-automatique/Activation de l'étape suivante avec transition
SW_TOP	Bool	0.7	false	True	True	True	False	False					Mode semi-automatique/Ignorer la transition
SW_MAN	Bool	1.0	false	True	True	True	False	False					Mode manuel
S_SEL	Int	2.0	0	True	True	True	False	False					Sélectionner l'étape à afficher dans S_NO
S_ON	Bool	4.0	false	True	True	True	False	False					Activer l'étape figurant dans le paramètre S_NO
S_OFF	Bool	4.1	false	True	True	True	False	False					Désactiver l'étape figurant dans le paramètre S_NO
T_PUSH	Bool	4.2	false	True	True	True	False	False					Validation de commutation pour transition en mode semi-automatique
Output													
S_NO	Int	6.0	0	True	True	True	False	False					Numéro d'étape
S_MORE	Bool	8.0	false	True	True	True	False	False					Il existe d'autres étapes qui peuvent être affichées dans S_NO.
S_ACTIVE	Bool	8.1	false	True	True	True	False	False					L'étape affichée dans le paramètre S_NO est activée
ERR_FLT	Bool	8.2	false	True	True	True	False	False					Erreur groupée Interlock or Supervision
AUTO_ON	Bool	8.3	false	True	True	True	False	False					Mode automatique activé
TAP_ON	Bool	8.4	false	True	True	True	False	False					Mode semi-automatique/Etape avec transition activé
TOP_ON	Bool	8.5	false	True	True	True	False	False					Mode semi-automatique/Ignorer la transition activé
MAN_ON	Bool	8.6	false	True	True	True	False	False					Mode manuel activé
InOut													
Static													
Trans1	GraphTransition	234.0		True	True	True	False	True					
TV	Bool	234.0	false	True	True	True	False	False					Transition is valid
TT	Bool	234.1	false	True	True	True	False	False					Transition is satisfied
TS	Bool	234.2	false	True	True	True	False	False					Transition switches
CF_IV	Bool	234.3	true	True	True	True	False	False					The CRIT_FLT entry is invalid
TNO	Int	236.0	1	True	True	True	False	False					Indicates the user-defined transition number
CRIT	DWord	238.0	16#0	True	True	True	False	False					Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	242.0	16#0	True	True	True	False	False					Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	246.0	16#0	True	True	True	False	False					Copy of CRIT if an error occurs
Trans2	GraphTransition	250.0		True	True	True	False	True					
TV	Bool	250.0	false	True	True	True	False	False					Transition is valid
TT	Bool	250.1	false	True	True	True	False	False					Transition is satisfied
TS	Bool	250.2	false	True	True	True	False	False					Transition switches
CF_IV	Bool	250.3	true	True	True	True	False	False					The CRIT_FLT entry is invalid
TNO	Int	252.0	2	True	True	True	False	False					Indicates the user-defined transition number
CRIT	DWord	254.0	16#0	True	True	True	False	False					Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_OLD	DWord	258.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	262.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans3	GraphTransition	266.0		True	True	True	False	True		
TV	Bool	266.0	false	True	True	True	False	False		Transition is valid
TT	Bool	266.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	266.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	266.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	268.0	3	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	270.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	274.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	278.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans4	GraphTransition	282.0		True	True	True	False	True		
TV	Bool	282.0	false	True	True	True	False	False		Transition is valid
TT	Bool	282.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	282.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	282.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	284.0	4	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	286.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	290.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	294.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans5	GraphTransition	298.0		True	True	True	False	True		
TV	Bool	298.0	false	True	True	True	False	False		Transition is valid
TT	Bool	298.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	298.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	298.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	300.0	5	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	302.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	306.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	310.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans6	GraphTransition	314.0		True	True	True	False	True		
TV	Bool	314.0	false	True	True	True	False	False		Transition is valid
TT	Bool	314.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	314.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	314.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	316.0	6	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	318.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	322.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	326.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans7	GraphTransition	330.0		True	True	True	False	True		
TV	Bool	330.0	false	True	True	True	False	False		Transition is valid
TT	Bool	330.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	330.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	330.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	332.0	7	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	334.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	338.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	342.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans8	GraphTransition	346.0		True	True	True	False	True		

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TV	Bool	346.0	false	True	True	True	False	False		Transition is valid
TT	Bool	346.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	346.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	346.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	348.0	8	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	350.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	354.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	358.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans9	GraphTransition	362.0		True	True	True	False	True		
TV	Bool	362.0	false	True	True	True	False	False		Transition is valid
TT	Bool	362.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	362.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	362.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	364.0	9	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	366.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	370.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	374.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans10	GraphTransition	378.0		True	True	True	False	True		
TV	Bool	378.0	false	True	True	True	False	False		Transition is valid
TT	Bool	378.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	378.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	378.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	380.0	10	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	382.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	386.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	390.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans13	GraphTransition	394.0		True	True	True	False	True		
TV	Bool	394.0	false	True	True	True	False	False		Transition is valid
TT	Bool	394.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	394.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	394.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	396.0	13	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	398.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	402.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	406.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans14	GraphTransition	410.0		True	True	True	False	True		
TV	Bool	410.0	false	True	True	True	False	False		Transition is valid
TT	Bool	410.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	410.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	410.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	412.0	14	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	414.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	418.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	422.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans15	GraphTransition	426.0		True	True	True	False	True		
TV	Bool	426.0	false	True	True	True	False	False		Transition is valid
TT	Bool	426.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	426.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	426.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TNO	Int	428.0	15	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	430.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	434.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	438.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans16	GraphTransition	442.0		True	True	True	False	True		
TV	Bool	442.0	false	True	True	True	False	False		Transition is valid
TT	Bool	442.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	442.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	442.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	444.0	16	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	446.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	450.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	454.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans21	GraphTransition	458.0		True	True	True	False	True		
TV	Bool	458.0	false	True	True	True	False	False		Transition is valid
TT	Bool	458.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	458.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	458.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	460.0	21	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	462.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	466.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	470.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans22	GraphTransition	474.0		True	True	True	False	True		
TV	Bool	474.0	false	True	True	True	False	False		Transition is valid
TT	Bool	474.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	474.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	474.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	476.0	22	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	478.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	482.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	486.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans23	GraphTransition	490.0		True	True	True	False	True		
TV	Bool	490.0	false	True	True	True	False	False		Transition is valid
TT	Bool	490.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	490.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	490.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	492.0	23	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	494.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	498.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	502.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans24	GraphTransition	506.0		True	True	True	False	True		
TV	Bool	506.0	false	True	True	True	False	False		Transition is valid
TT	Bool	506.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	506.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	506.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	508.0	24	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	510.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_OLD	DWord	514.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	518.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans25	GraphTransition	522.0		True	True	True	False	True		
TV	Bool	522.0	false	True	True	True	False	False		Transition is valid
TT	Bool	522.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	522.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	522.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	524.0	25	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	526.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	530.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	534.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans28	GraphTransition	538.0		True	True	True	False	True		
TV	Bool	538.0	false	True	True	True	False	False		Transition is valid
TT	Bool	538.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	538.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	538.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	540.0	28	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	542.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	546.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	550.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans29	GraphTransition	554.0		True	True	True	False	True		
TV	Bool	554.0	false	True	True	True	False	False		Transition is valid
TT	Bool	554.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	554.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	554.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	556.0	29	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	558.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	562.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	566.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans30	GraphTransition	570.0		True	True	True	False	True		
TV	Bool	570.0	false	True	True	True	False	False		Transition is valid
TT	Bool	570.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	570.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	570.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	572.0	30	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	574.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	578.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	582.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans31	GraphTransition	586.0		True	True	True	False	True		
TV	Bool	586.0	false	True	True	True	False	False		Transition is valid
TT	Bool	586.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	586.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	586.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	588.0	31	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	590.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	594.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	598.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans32	GraphTransition	602.0		True	True	True	False	True		

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TV	Bool	602.0	false	True	True	True	False	False		Transition is valid
TT	Bool	602.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	602.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	602.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	604.0	32	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	606.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	610.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	614.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans33	GraphTransition	618.0		True	True	True	False	True		
TV	Bool	618.0	false	True	True	True	False	False		Transition is valid
TT	Bool	618.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	618.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	618.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	620.0	33	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	622.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	626.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	630.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans34	GraphTransition	634.0		True	True	True	False	True		
TV	Bool	634.0	false	True	True	True	False	False		Transition is valid
TT	Bool	634.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	634.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	634.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	636.0	34	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	638.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	642.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	646.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans35	GraphTransition	650.0		True	True	True	False	True		
TV	Bool	650.0	false	True	True	True	False	False		Transition is valid
TT	Bool	650.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	650.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	650.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	652.0	35	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	654.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	658.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	662.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans37	GraphTransition	666.0		True	True	True	False	True		
TV	Bool	666.0	false	True	True	True	False	False		Transition is valid
TT	Bool	666.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	666.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	666.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	668.0	37	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	670.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	674.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	678.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans38	GraphTransition	682.0		True	True	True	False	True		
TV	Bool	682.0	false	True	True	True	False	False		Transition is valid
TT	Bool	682.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	682.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	682.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TNO	Int	684.0	38	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	686.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	690.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	694.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans40	GraphTransition	698.0		True	True	True	False	True		
TV	Bool	698.0	false	True	True	True	False	False		Transition is valid
TT	Bool	698.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	698.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	698.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	700.0	40	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	702.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	706.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	710.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans41	GraphTransition	714.0		True	True	True	False	True		
TV	Bool	714.0	false	True	True	True	False	False		Transition is valid
TT	Bool	714.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	714.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	714.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	716.0	41	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	718.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	722.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	726.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans42	GraphTransition	730.0		True	True	True	False	True		
TV	Bool	730.0	false	True	True	True	False	False		Transition is valid
TT	Bool	730.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	730.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	730.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	732.0	42	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	734.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	738.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	742.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans43	GraphTransition	746.0		True	True	True	False	True		
TV	Bool	746.0	false	True	True	True	False	False		Transition is valid
TT	Bool	746.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	746.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	746.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	748.0	43	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	750.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	754.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	758.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans44	GraphTransition	762.0		True	True	True	False	True		
TV	Bool	762.0	false	True	True	True	False	False		Transition is valid
TT	Bool	762.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	762.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	762.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	764.0	44	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	766.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_OLD	DWord	770.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	774.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans45	GraphTransition	778.0		True	True	True	False	True		
TV	Bool	778.0	false	True	True	True	False	False		Transition is valid
TT	Bool	778.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	778.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	778.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	780.0	45	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	782.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	786.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	790.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans46	GraphTransition	794.0		True	True	True	False	True		
TV	Bool	794.0	false	True	True	True	False	False		Transition is valid
TT	Bool	794.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	794.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	794.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	796.0	46	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	798.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	802.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	806.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans47	GraphTransition	810.0		True	True	True	False	True		
TV	Bool	810.0	false	True	True	True	False	False		Transition is valid
TT	Bool	810.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	810.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	810.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	812.0	47	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	814.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	818.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	822.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans48	GraphTransition	826.0		True	True	True	False	True		
TV	Bool	826.0	false	True	True	True	False	False		Transition is valid
TT	Bool	826.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	826.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	826.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	828.0	48	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	830.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	834.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	838.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans49	GraphTransition	842.0		True	True	True	False	True		
TV	Bool	842.0	false	True	True	True	False	False		Transition is valid
TT	Bool	842.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	842.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	842.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	844.0	49	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	846.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	850.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	854.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans50	GraphTransition	858.0		True	True	True	False	True		

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TV	Bool	858.0	false	True	True	True	False	False		Transition is valid
TT	Bool	858.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	858.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	858.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	860.0	50	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	862.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	866.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	870.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans51	GraphTransition	874.0		True	True	True	False	True		
TV	Bool	874.0	false	True	True	True	False	False		Transition is valid
TT	Bool	874.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	874.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	874.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	876.0	51	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	878.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	882.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	886.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans52	GraphTransition	890.0		True	True	True	False	True		
TV	Bool	890.0	false	True	True	True	False	False		Transition is valid
TT	Bool	890.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	890.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	890.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	892.0	52	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	894.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	898.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	902.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans53	GraphTransition	906.0		True	True	True	False	True		
TV	Bool	906.0	false	True	True	True	False	False		Transition is valid
TT	Bool	906.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	906.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	906.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	908.0	53	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	910.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	914.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	918.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans54	GraphTransition	922.0		True	True	True	False	True		
TV	Bool	922.0	false	True	True	True	False	False		Transition is valid
TT	Bool	922.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	922.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	922.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	924.0	54	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	926.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	930.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	934.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans55	GraphTransition	938.0		True	True	True	False	True		
TV	Bool	938.0	false	True	True	True	False	False		Transition is valid
TT	Bool	938.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	938.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	938.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TNO	Int	940.0	55	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	942.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	946.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	950.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans56	GraphTransition	954.0		True	True	True	False	True		
TV	Bool	954.0	false	True	True	True	False	False		Transition is valid
TT	Bool	954.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	954.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	954.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	956.0	56	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	958.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	962.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	966.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans57	GraphTransition	970.0		True	True	True	False	True		
TV	Bool	970.0	false	True	True	True	False	False		Transition is valid
TT	Bool	970.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	970.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	970.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	972.0	57	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	974.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	978.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	982.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans58	GraphTransition	986.0		True	True	True	False	True		
TV	Bool	986.0	false	True	True	True	False	False		Transition is valid
TT	Bool	986.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	986.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	986.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	988.0	58	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	990.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	994.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	998.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans59	GraphTransition	1002.0		True	True	True	False	True		
TV	Bool	1002.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1002.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1002.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1002.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1004.0	59	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1006.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1010.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1014.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans60	GraphTransition	1018.0		True	True	True	False	True		
TV	Bool	1018.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1018.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1018.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1018.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1020.0	60	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1022.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_OLD	DWord	1026.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1030.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans61	GraphTransition	1034.0		True	True	True	False	True		
TV	Bool	1034.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1034.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1034.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1034.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1036.0	61	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1038.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1042.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1046.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans62	GraphTransition	1050.0		True	True	True	False	True		
TV	Bool	1050.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1050.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1050.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1050.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1052.0	62	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1054.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1058.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1062.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans63	GraphTransition	1066.0		True	True	True	False	True		
TV	Bool	1066.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1066.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1066.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1066.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1068.0	63	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1070.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1074.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1078.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans64	GraphTransition	1082.0		True	True	True	False	True		
TV	Bool	1082.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1082.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1082.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1082.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1084.0	64	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1086.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1090.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1094.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans65	GraphTransition	1098.0		True	True	True	False	True		
TV	Bool	1098.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1098.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1098.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1098.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1100.0	65	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1102.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1106.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1110.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans66	GraphTransition	1114.0		True	True	True	False	True		

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TV	Bool	1114.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1114.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1114.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1114.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1116.0	66	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1118.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1122.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1126.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans67	GraphTransition	1130.0		True	True	True	False	True		
TV	Bool	1130.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1130.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1130.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1130.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1132.0	67	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1134.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1138.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1142.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans68	GraphTransition	1146.0		True	False	False	False	True		Transition structure
TV	Bool	1146.0	false	True	False	False	False	False		Transition is valid
TT	Bool	1146.1	false	True	False	False	False	False		Transition is satisfied
TS	Bool	1146.2	false	True	False	False	False	False		Transition switches
CF_IV	Bool	1146.3	True	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	1148.0	68	True	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	1150.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1154.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1158.0	16#0	True	False	False	False	False		Copy of CRIT if an error occurs
▼ Trans69	GraphTransition	1162.0		True	True	True	False	True		
TV	Bool	1162.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1162.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1162.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1162.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1164.0	69	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1166.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1170.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1174.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans70	GraphTransition	1178.0		True	True	True	False	True		
TV	Bool	1178.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1178.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1178.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1178.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1180.0	70	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1182.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1186.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1190.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans71	GraphTransition	1194.0		True	False	False	False	True		Transition structure
TV	Bool	1194.0	false	True	False	False	False	False		Transition is valid
TT	Bool	1194.1	false	True	False	False	False	False		Transition is satisfied
TS	Bool	1194.2	false	True	False	False	False	False		Transition switches
CF_IV	Bool	1194.3	True	True	False	False	False	False		The CRIT_FLT entry is invalid

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TNO	Int	1196.0	71	True	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	1198.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1202.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1206.0	16#0	True	False	False	False	False		Copy of CRIT if an error occurs
Trans72	GraphTransition	1210.0		True	True	True	False	True		
TV	Bool	1210.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1210.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1210.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1210.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1212.0	72	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1214.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1218.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1222.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans73	GraphTransition	1226.0		True	True	True	False	True		
TV	Bool	1226.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1226.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1226.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1226.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1228.0	73	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1230.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1234.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1238.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans74	GraphTransition	1242.0		True	True	True	False	True		
TV	Bool	1242.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1242.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1242.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1242.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1244.0	74	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1246.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1250.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1254.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans75	GraphTransition	1258.0		True	True	True	False	True		
TV	Bool	1258.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1258.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1258.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1258.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1260.0	75	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1262.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1266.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1270.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans76	GraphTransition	1274.0		True	True	True	False	True		
TV	Bool	1274.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1274.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1274.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1274.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1276.0	76	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1278.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_OLD	DWord	1282.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1286.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans77	GraphTransition	1290.0		True	False	False	False	True		Transition structure
TV	Bool	1290.0	false	True	False	False	False	False		Transition is valid
TT	Bool	1290.1	false	True	False	False	False	False		Transition is satisfied
TS	Bool	1290.2	false	True	False	False	False	False		Transition switches
CF_IV	Bool	1290.3	True	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	1292.0	77	True	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	1294.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1298.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1302.0	16#0	True	False	False	False	False		Copy of CRIT if an error occurs
▼ Trans78	GraphTransition	1306.0		True	False	False	False	True		Transition structure
TV	Bool	1306.0	false	True	False	False	False	False		Transition is valid
TT	Bool	1306.1	false	True	False	False	False	False		Transition is satisfied
TS	Bool	1306.2	false	True	False	False	False	False		Transition switches
CF_IV	Bool	1306.3	True	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	1308.0	78	True	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	1310.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1314.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1318.0	16#0	True	False	False	False	False		Copy of CRIT if an error occurs
▼ Trans79	GraphTransition	1322.0		True	False	False	False	True		Transition structure
TV	Bool	1322.0	false	True	False	False	False	False		Transition is valid
TT	Bool	1322.1	false	True	False	False	False	False		Transition is satisfied
TS	Bool	1322.2	false	True	False	False	False	False		Transition switches
CF_IV	Bool	1322.3	True	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	1324.0	79	True	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	1326.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1330.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1334.0	16#0	True	False	False	False	False		Copy of CRIT if an error occurs
▼ Trans80	GraphTransition	1338.0		True	True	True	False	True		
TV	Bool	1338.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1338.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1338.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1338.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1340.0	80	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1342.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1346.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1350.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans81	GraphTransition	1354.0		True	False	False	False	True		Transition structure
TV	Bool	1354.0	false	True	False	False	False	False		Transition is valid
TT	Bool	1354.1	false	True	False	False	False	False		Transition is satisfied
TS	Bool	1354.2	false	True	False	False	False	False		Transition switches
CF_IV	Bool	1354.3	True	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	1356.0	81	True	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	1358.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1362.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1366.0	16#0	True	False	False	False	False		Copy of CRIT if an error occurs
▼ Step1	GraphStep	1402.0		True	True	True	False	True		

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
S1	Bool	1402.0	false	True	True	True	False	False		Step is activated
L1	Bool	1402.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1402.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1402.3	false	True	True	True	False	False		Reserved
A1	Bool	1402.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1402.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1402.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1402.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1403.0	false	True	True	True	False	False		Step is active
LA	Bool	1403.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1403.2	false	True	True	True	False	False		Supervision active
RA	Bool	1403.3	false	True	True	True	False	False		Reserved
AA	Bool	1403.4	false	True	True	True	False	False		Reserved
SS	Bool	1403.5	false	True	True	True	False	False		System-internal
LS	Bool	1403.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1403.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1404.0	1	True	True	True	False	False		User step number
T	Time	1406.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1410.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1414.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1418.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1422.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1426.0	false	True	True	True	False	False		System-internal
LP	Bool	1426.1	false	True	True	True	False	False		System-internal
LN	Bool	1426.2	false	True	True	True	False	False		System-internal
VP	Bool	1426.3	false	True	True	True	False	False		System-internal
VN	Bool	1426.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1427.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1428.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1430.0	16#0	True	True	True	False	False		Reserve
▼ Step2	GraphStep	1434.0		True	True	True	False	True		
S1	Bool	1434.0	false	True	True	True	False	False		Step is activated
L1	Bool	1434.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1434.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1434.3	false	True	True	True	False	False		Reserved
A1	Bool	1434.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1434.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1434.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1434.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1435.0	false	True	True	True	False	False		Step is active
LA	Bool	1435.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1435.2	false	True	True	True	False	False		Supervision active
RA	Bool	1435.3	false	True	True	True	False	False		Reserved
AA	Bool	1435.4	false	True	True	True	False	False		Reserved
SS	Bool	1435.5	false	True	True	True	False	False		System-internal
LS	Bool	1435.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1435.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1436.0	2	True	True	True	False	False		User step number
T	Time	1438.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1442.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1446.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1450.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1454.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1458.0	false	True	True	True	False	False		System-internal
LP	Bool	1458.1	false	True	True	True	False	False		System-internal
LN	Bool	1458.2	false	True	True	True	False	False		System-internal
VP	Bool	1458.3	false	True	True	True	False	False		System-internal
VN	Bool	1458.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1459.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1460.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1462.0	16#0	True	True	True	False	False		Reserve

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
▼ Step3	GraphStep	1466.0		True	True	True	False	True		
S1	Bool	1466.0	false	True	True	True	False	False		Step is activated
L1	Bool	1466.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1466.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1466.3	false	True	True	True	False	False		Reserved
A1	Bool	1466.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1466.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1466.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1466.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1467.0	false	True	True	True	False	False		Step is active
LA	Bool	1467.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1467.2	false	True	True	True	False	False		Supervision active
RA	Bool	1467.3	false	True	True	True	False	False		Reserved
AA	Bool	1467.4	false	True	True	True	False	False		Reserved
SS	Bool	1467.5	false	True	True	True	False	False		System-internal
LS	Bool	1467.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1467.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1468.0	3	True	True	True	False	False		User step number
T	Time	1470.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1474.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1478.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1482.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1486.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1490.0	false	True	True	True	False	False		System-internal
LP	Bool	1490.1	false	True	True	True	False	False		System-internal
LN	Bool	1490.2	false	True	True	True	False	False		System-internal
VP	Bool	1490.3	false	True	True	True	False	False		System-internal
VN	Bool	1490.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1491.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1492.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1494.0	16#0	True	True	True	False	False		Reserve
▼ Step4	GraphStep	1498.0		True	True	True	False	True		
S1	Bool	1498.0	false	True	True	True	False	False		Step is activated
L1	Bool	1498.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1498.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1498.3	false	True	True	True	False	False		Reserved
A1	Bool	1498.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1498.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1498.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1498.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1499.0	false	True	True	True	False	False		Step is active
LA	Bool	1499.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1499.2	false	True	True	True	False	False		Supervision active
RA	Bool	1499.3	false	True	True	True	False	False		Reserved
AA	Bool	1499.4	false	True	True	True	False	False		Reserved
SS	Bool	1499.5	false	True	True	True	False	False		System-internal
LS	Bool	1499.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1499.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1500.0	4	True	True	True	False	False		User step number
T	Time	1502.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1506.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1510.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1514.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1518.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1522.0	false	True	True	True	False	False		System-internal
LP	Bool	1522.1	false	True	True	True	False	False		System-internal
LN	Bool	1522.2	false	True	True	True	False	False		System-internal
VP	Bool	1522.3	false	True	True	True	False	False		System-internal
VN	Bool	1522.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1523.0	16#0	True	True	True	False	False		System-internal

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
H_SV_FLT	Byte	1524.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1526.0	16#0	True	True	True	False	False		Reserve
Step5	GraphStep	1530.0		True	True	True	False	True		
S1	Bool	1530.0	false	True	True	True	False	False		Step is activated
L1	Bool	1530.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1530.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1530.3	false	True	True	True	False	False		Reserved
A1	Bool	1530.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1530.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1530.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1530.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1531.0	false	True	True	True	False	False		Step is active
LA	Bool	1531.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1531.2	false	True	True	True	False	False		Supervision active
RA	Bool	1531.3	false	True	True	True	False	False		Reserved
AA	Bool	1531.4	false	True	True	True	False	False		Reserved
SS	Bool	1531.5	false	True	True	True	False	False		System-internal
LS	Bool	1531.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1531.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1532.0	5	True	True	True	False	False		User step number
T	Time	1534.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1538.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1542.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1546.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1550.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1554.0	false	True	True	True	False	False		System-internal
LP	Bool	1554.1	false	True	True	True	False	False		System-internal
LN	Bool	1554.2	false	True	True	True	False	False		System-internal
VP	Bool	1554.3	false	True	True	True	False	False		System-internal
VN	Bool	1554.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1555.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1556.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1558.0	16#0	True	True	True	False	False		Reserve
Step6	GraphStep	1562.0		True	True	True	False	True		
S1	Bool	1562.0	false	True	True	True	False	False		Step is activated
L1	Bool	1562.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1562.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1562.3	false	True	True	True	False	False		Reserved
A1	Bool	1562.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1562.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1562.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1562.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1563.0	false	True	True	True	False	False		Step is active
LA	Bool	1563.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1563.2	false	True	True	True	False	False		Supervision active
RA	Bool	1563.3	false	True	True	True	False	False		Reserved
AA	Bool	1563.4	false	True	True	True	False	False		Reserved
SS	Bool	1563.5	false	True	True	True	False	False		System-internal
LS	Bool	1563.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1563.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1564.0	6	True	True	True	False	False		User step number
T	Time	1566.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1570.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1574.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1578.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1582.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1586.0	false	True	True	True	False	False		System-internal
LP	Bool	1586.1	false	True	True	True	False	False		System-internal
LN	Bool	1586.2	false	True	True	True	False	False		System-internal
VP	Bool	1586.3	false	True	True	True	False	False		System-internal

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
VN	Bool	1586.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1587.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1588.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1590.0	16#0	True	True	True	False	False		Reserve
Step7	GraphStep	1594.0		True	True	True	False	True		
S1	Bool	1594.0	false	True	True	True	False	False		Step is activated
L1	Bool	1594.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1594.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1594.3	false	True	True	True	False	False		Reserved
A1	Bool	1594.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1594.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1594.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1594.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1595.0	false	True	True	True	False	False		Step is active
LA	Bool	1595.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1595.2	false	True	True	True	False	False		Supervision active
RA	Bool	1595.3	false	True	True	True	False	False		Reserved
AA	Bool	1595.4	false	True	True	True	False	False		Reserved
SS	Bool	1595.5	false	True	True	True	False	False		System-internal
LS	Bool	1595.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1595.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1596.0	7	True	True	True	False	False		User step number
T	Time	1598.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1602.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1606.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1610.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1614.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1618.0	false	True	True	True	False	False		System-internal
LP	Bool	1618.1	false	True	True	True	False	False		System-internal
LN	Bool	1618.2	false	True	True	True	False	False		System-internal
VP	Bool	1618.3	false	True	True	True	False	False		System-internal
VN	Bool	1618.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1619.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1620.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1622.0	16#0	True	True	True	False	False		Reserve
Step8	GraphStep	1626.0		True	True	True	False	True		
S1	Bool	1626.0	false	True	True	True	False	False		Step is activated
L1	Bool	1626.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1626.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1626.3	false	True	True	True	False	False		Reserved
A1	Bool	1626.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1626.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1626.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1626.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1627.0	false	True	True	True	False	False		Step is active
LA	Bool	1627.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1627.2	false	True	True	True	False	False		Supervision active
RA	Bool	1627.3	false	True	True	True	False	False		Reserved
AA	Bool	1627.4	false	True	True	True	False	False		Reserved
SS	Bool	1627.5	false	True	True	True	False	False		System-internal
LS	Bool	1627.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1627.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1628.0	8	True	True	True	False	False		User step number
T	Time	1630.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1634.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1638.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1642.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1646.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1650.0	false	True	True	True	False	False		System-internal
LP	Bool	1650.1	false	True	True	True	False	False		System-internal

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
LN	Bool	1650.2	false	True	True	True	False	False		System-internal
VP	Bool	1650.3	false	True	True	True	False	False		System-internal
VN	Bool	1650.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1651.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1652.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1654.0	16#0	True	True	True	False	False		Reserve
Step9	GraphStep	1658.0		True	True	True	False	True		
S1	Bool	1658.0	false	True	True	True	False	False		Step is activated
L1	Bool	1658.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1658.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1658.3	false	True	True	True	False	False		Reserved
A1	Bool	1658.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1658.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1658.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1658.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1659.0	false	True	True	True	False	False		Step is active
LA	Bool	1659.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1659.2	false	True	True	True	False	False		Supervision active
RA	Bool	1659.3	false	True	True	True	False	False		Reserved
AA	Bool	1659.4	false	True	True	True	False	False		Reserved
SS	Bool	1659.5	false	True	True	True	False	False		System-internal
LS	Bool	1659.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1659.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1660.0	9	True	True	True	False	False		User step number
T	Time	1662.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1666.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1670.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1674.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1678.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1682.0	false	True	True	True	False	False		System-internal
LP	Bool	1682.1	false	True	True	True	False	False		System-internal
LN	Bool	1682.2	false	True	True	True	False	False		System-internal
VP	Bool	1682.3	false	True	True	True	False	False		System-internal
VN	Bool	1682.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1683.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1684.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1686.0	16#0	True	True	True	False	False		Reserve
Step10	GraphStep	1690.0		True	True	True	False	True		
S1	Bool	1690.0	false	True	True	True	False	False		Step is activated
L1	Bool	1690.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1690.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1690.3	false	True	True	True	False	False		Reserved
A1	Bool	1690.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1690.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1690.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1690.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1691.0	false	True	True	True	False	False		Step is active
LA	Bool	1691.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1691.2	false	True	True	True	False	False		Supervision active
RA	Bool	1691.3	false	True	True	True	False	False		Reserved
AA	Bool	1691.4	false	True	True	True	False	False		Reserved
SS	Bool	1691.5	false	True	True	True	False	False		System-internal
LS	Bool	1691.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1691.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1692.0	10	True	True	True	False	False		User step number
T	Time	1694.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1698.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1702.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1706.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1710.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
SM	Bool	1714.0	false	True	True	True	False	False		System-internal
LP	Bool	1714.1	false	True	True	True	False	False		System-internal
LN	Bool	1714.2	false	True	True	True	False	False		System-internal
VP	Bool	1714.3	false	True	True	True	False	False		System-internal
VN	Bool	1714.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1715.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1716.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1718.0	16#0	True	True	True	False	False		Reserve
Step11	GraphStep	1722.0		True	True	True	False	True		
S1	Bool	1722.0	false	True	True	True	False	False		Step is activated
L1	Bool	1722.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1722.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1722.3	false	True	True	True	False	False		Reserved
A1	Bool	1722.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1722.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1722.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1722.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1723.0	false	True	True	True	False	False		Step is active
LA	Bool	1723.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1723.2	false	True	True	True	False	False		Supervision active
RA	Bool	1723.3	false	True	True	True	False	False		Reserved
AA	Bool	1723.4	false	True	True	True	False	False		Reserved
SS	Bool	1723.5	false	True	True	True	False	False		System-internal
LS	Bool	1723.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1723.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1724.0	11	True	True	True	False	False		User step number
T	Time	1726.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1730.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1734.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1738.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1742.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1746.0	false	True	True	True	False	False		System-internal
LP	Bool	1746.1	false	True	True	True	False	False		System-internal
LN	Bool	1746.2	false	True	True	True	False	False		System-internal
VP	Bool	1746.3	false	True	True	True	False	False		System-internal
VN	Bool	1746.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1747.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1748.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1750.0	16#0	True	True	True	False	False		Reserve
Step12	GraphStep	1754.0		True	True	True	False	True		
S1	Bool	1754.0	false	True	True	True	False	False		Step is activated
L1	Bool	1754.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1754.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1754.3	false	True	True	True	False	False		Reserved
A1	Bool	1754.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1754.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1754.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1754.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1755.0	false	True	True	True	False	False		Step is active
LA	Bool	1755.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1755.2	false	True	True	True	False	False		Supervision active
RA	Bool	1755.3	false	True	True	True	False	False		Reserved
AA	Bool	1755.4	false	True	True	True	False	False		Reserved
SS	Bool	1755.5	false	True	True	True	False	False		System-internal
LS	Bool	1755.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1755.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1756.0	12	True	True	True	False	False		User step number
T	Time	1758.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1762.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1766.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1770.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_SUP	DWord	1774.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1778.0	false	True	True	True	False	False		System-internal
LP	Bool	1778.1	false	True	True	True	False	False		System-internal
LN	Bool	1778.2	false	True	True	True	False	False		System-internal
VP	Bool	1778.3	false	True	True	True	False	False		System-internal
VN	Bool	1778.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1779.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1780.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1782.0	16#0	True	True	True	False	False		Reserve
▼ Step13	GraphStep	1786.0		True	True	True	False	True		
S1	Bool	1786.0	false	True	True	True	False	False		Step is activated
L1	Bool	1786.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1786.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1786.3	false	True	True	True	False	False		Reserved
A1	Bool	1786.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1786.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1786.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1786.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1787.0	false	True	True	True	False	False		Step is active
LA	Bool	1787.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1787.2	false	True	True	True	False	False		Supervision active
RA	Bool	1787.3	false	True	True	True	False	False		Reserved
AA	Bool	1787.4	false	True	True	True	False	False		Reserved
SS	Bool	1787.5	false	True	True	True	False	False		System-internal
LS	Bool	1787.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1787.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1788.0	13	True	True	True	False	False		User step number
T	Time	1790.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1794.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1798.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1802.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1806.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1810.0	false	True	True	True	False	False		System-internal
LP	Bool	1810.1	false	True	True	True	False	False		System-internal
LN	Bool	1810.2	false	True	True	True	False	False		System-internal
VP	Bool	1810.3	false	True	True	True	False	False		System-internal
VN	Bool	1810.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1811.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1812.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1814.0	16#0	True	True	True	False	False		Reserve
▼ Step15	GraphStep	1818.0		True	True	True	False	True		
S1	Bool	1818.0	false	True	True	True	False	False		Step is activated
L1	Bool	1818.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1818.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1818.3	false	True	True	True	False	False		Reserved
A1	Bool	1818.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1818.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1818.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1818.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1819.0	false	True	True	True	False	False		Step is active
LA	Bool	1819.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1819.2	false	True	True	True	False	False		Supervision active
RA	Bool	1819.3	false	True	True	True	False	False		Reserved
AA	Bool	1819.4	false	True	True	True	False	False		Reserved
SS	Bool	1819.5	false	True	True	True	False	False		System-internal
LS	Bool	1819.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1819.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1820.0	15	True	True	True	False	False		User step number
T	Time	1822.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1826.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1830.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_LOC_ERR	DWord	1834.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1838.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1842.0	false	True	True	True	False	False		System-internal
LP	Bool	1842.1	false	True	True	True	False	False		System-internal
LN	Bool	1842.2	false	True	True	True	False	False		System-internal
VP	Bool	1842.3	false	True	True	True	False	False		System-internal
VN	Bool	1842.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1843.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1844.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1846.0	16#0	True	True	True	False	False		Reserve
Step16	GraphStep	1850.0		True	True	True	False	True		
S1	Bool	1850.0	false	True	True	True	False	False		Step is activated
L1	Bool	1850.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1850.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1850.3	false	True	True	True	False	False		Reserved
A1	Bool	1850.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1850.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1850.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1850.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1851.0	false	True	True	True	False	False		Step is active
LA	Bool	1851.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1851.2	false	True	True	True	False	False		Supervision active
RA	Bool	1851.3	false	True	True	True	False	False		Reserved
AA	Bool	1851.4	false	True	True	True	False	False		Reserved
SS	Bool	1851.5	false	True	True	True	False	False		System-internal
LS	Bool	1851.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1851.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1852.0	16	True	True	True	False	False		User step number
T	Time	1854.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1858.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1862.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1866.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1870.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1874.0	false	True	True	True	False	False		System-internal
LP	Bool	1874.1	false	True	True	True	False	False		System-internal
LN	Bool	1874.2	false	True	True	True	False	False		System-internal
VP	Bool	1874.3	false	True	True	True	False	False		System-internal
VN	Bool	1874.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1875.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1876.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1878.0	16#0	True	True	True	False	False		Reserve
Step17	GraphStep	1882.0		True	True	True	False	True		
S1	Bool	1882.0	false	True	True	True	False	False		Step is activated
L1	Bool	1882.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1882.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1882.3	false	True	True	True	False	False		Reserved
A1	Bool	1882.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1882.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1882.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1882.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1883.0	false	True	True	True	False	False		Step is active
LA	Bool	1883.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1883.2	false	True	True	True	False	False		Supervision active
RA	Bool	1883.3	false	True	True	True	False	False		Reserved
AA	Bool	1883.4	false	True	True	True	False	False		Reserved
SS	Bool	1883.5	false	True	True	True	False	False		System-internal
LS	Bool	1883.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1883.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1884.0	17	True	True	True	False	False		User step number
T	Time	1886.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1890.0	T#0ms	True	True	True	False	False		Step activation time without disturbance

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_LOC	DWord	1894.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1898.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1902.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1906.0	false	True	True	True	False	False		System-internal
LP	Bool	1906.1	false	True	True	True	False	False		System-internal
LN	Bool	1906.2	false	True	True	True	False	False		System-internal
VP	Bool	1906.3	false	True	True	True	False	False		System-internal
VN	Bool	1906.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1907.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1908.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1910.0	16#0	True	True	True	False	False		Reserve
Step18	GraphStep	1914.0		True	True	True	False	True		
S1	Bool	1914.0	false	True	True	True	False	False		Step is activated
L1	Bool	1914.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1914.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1914.3	false	True	True	True	False	False		Reserved
A1	Bool	1914.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1914.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1914.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1914.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1915.0	false	True	True	True	False	False		Step is active
LA	Bool	1915.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1915.2	false	True	True	True	False	False		Supervision active
RA	Bool	1915.3	false	True	True	True	False	False		Reserved
AA	Bool	1915.4	false	True	True	True	False	False		Reserved
SS	Bool	1915.5	false	True	True	True	False	False		System-internal
LS	Bool	1915.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1915.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1916.0	18	True	True	True	False	False		User step number
T	Time	1918.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1922.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1926.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1930.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1934.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1938.0	false	True	True	True	False	False		System-internal
LP	Bool	1938.1	false	True	True	True	False	False		System-internal
LN	Bool	1938.2	false	True	True	True	False	False		System-internal
VP	Bool	1938.3	false	True	True	True	False	False		System-internal
VN	Bool	1938.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1939.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1940.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1942.0	16#0	True	True	True	False	False		Reserve
Step19	GraphStep	1946.0		True	True	True	False	True		
S1	Bool	1946.0	false	True	True	True	False	False		Step is activated
L1	Bool	1946.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1946.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1946.3	false	True	True	True	False	False		Reserved
A1	Bool	1946.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1946.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1946.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1946.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1947.0	false	True	True	True	False	False		Step is active
LA	Bool	1947.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1947.2	false	True	True	True	False	False		Supervision active
RA	Bool	1947.3	false	True	True	True	False	False		Reserved
AA	Bool	1947.4	false	True	True	True	False	False		Reserved
SS	Bool	1947.5	false	True	True	True	False	False		System-internal
LS	Bool	1947.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1947.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1948.0	19	True	True	True	False	False		User step number
T	Time	1950.0	T#0ms	True	True	True	False	False		Total step activation time

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
U	Time	1954.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1958.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1962.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1966.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1970.0	false	True	True	True	False	False		System-internal
LP	Bool	1970.1	false	True	True	True	False	False		System-internal
LN	Bool	1970.2	false	True	True	True	False	False		System-internal
VP	Bool	1970.3	false	True	True	True	False	False		System-internal
VN	Bool	1970.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1971.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1972.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1974.0	16#0	True	True	True	False	False		Reserve
Step20	GraphStep	1978.0		True	True	True	False	True		
S1	Bool	1978.0	false	True	True	True	False	False		Step is activated
L1	Bool	1978.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1978.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1978.3	false	True	True	True	False	False		Reserved
A1	Bool	1978.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1978.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1978.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1978.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1979.0	false	True	True	True	False	False		Step is active
LA	Bool	1979.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1979.2	false	True	True	True	False	False		Supervision active
RA	Bool	1979.3	false	True	True	True	False	False		Reserved
AA	Bool	1979.4	false	True	True	True	False	False		Reserved
SS	Bool	1979.5	false	True	True	True	False	False		System-internal
LS	Bool	1979.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1979.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1980.0	20	True	True	True	False	False		User step number
T	Time	1982.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1986.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1990.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1994.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1998.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2002.0	false	True	True	True	False	False		System-internal
LP	Bool	2002.1	false	True	True	True	False	False		System-internal
LN	Bool	2002.2	false	True	True	True	False	False		System-internal
VP	Bool	2002.3	false	True	True	True	False	False		System-internal
VN	Bool	2002.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2003.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2004.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2006.0	16#0	True	True	True	False	False		Reserve
Step21	GraphStep	2010.0		True	True	True	False	True		
S1	Bool	2010.0	false	True	True	True	False	False		Step is activated
L1	Bool	2010.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2010.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2010.3	false	True	True	True	False	False		Reserved
A1	Bool	2010.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2010.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2010.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2010.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2011.0	false	True	True	True	False	False		Step is active
LA	Bool	2011.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2011.2	false	True	True	True	False	False		Supervision active
RA	Bool	2011.3	false	True	True	True	False	False		Reserved
AA	Bool	2011.4	false	True	True	True	False	False		Reserved
SS	Bool	2011.5	false	True	True	True	False	False		System-internal
LS	Bool	2011.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2011.7	false	True	True	True	False	False		Direct result of the programmed supervision

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
SNO	Int	2012.0	21	True	True	True	False	False		User step number
T	Time	2014.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2018.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2022.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2026.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2030.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2034.0	false	True	True	True	False	False		System-internal
LP	Bool	2034.1	false	True	True	True	False	False		System-internal
LN	Bool	2034.2	false	True	True	True	False	False		System-internal
VP	Bool	2034.3	false	True	True	True	False	False		System-internal
VN	Bool	2034.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2035.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2036.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2038.0	16#0	True	True	True	False	False		Reserve
Step22	GraphStep	2042.0		True	True	True	False	True		
S1	Bool	2042.0	false	True	True	True	False	False		Step is activated
L1	Bool	2042.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2042.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2042.3	false	True	True	True	False	False		Reserved
A1	Bool	2042.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2042.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2042.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2042.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2043.0	false	True	True	True	False	False		Step is active
LA	Bool	2043.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2043.2	false	True	True	True	False	False		Supervision active
RA	Bool	2043.3	false	True	True	True	False	False		Reserved
AA	Bool	2043.4	false	True	True	True	False	False		Reserved
SS	Bool	2043.5	false	True	True	True	False	False		System-internal
LS	Bool	2043.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2043.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2044.0	22	True	True	True	False	False		User step number
T	Time	2046.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2050.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2054.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2058.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2062.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2066.0	false	True	True	True	False	False		System-internal
LP	Bool	2066.1	false	True	True	True	False	False		System-internal
LN	Bool	2066.2	false	True	True	True	False	False		System-internal
VP	Bool	2066.3	false	True	True	True	False	False		System-internal
VN	Bool	2066.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2067.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2068.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2070.0	16#0	True	True	True	False	False		Reserve
Step23	GraphStep	2074.0		True	True	True	False	True		
S1	Bool	2074.0	false	True	True	True	False	False		Step is activated
L1	Bool	2074.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2074.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2074.3	false	True	True	True	False	False		Reserved
A1	Bool	2074.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2074.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2074.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2074.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2075.0	false	True	True	True	False	False		Step is active
LA	Bool	2075.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2075.2	false	True	True	True	False	False		Supervision active
RA	Bool	2075.3	false	True	True	True	False	False		Reserved
AA	Bool	2075.4	false	True	True	True	False	False		Reserved
SS	Bool	2075.5	false	True	True	True	False	False		System-internal
LS	Bool	2075.6	true	True	True	True	False	False		Direct result of the programmed interlock

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
VS	Bool	2075.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2076.0	23	True	True	True	False	False		User step number
T	Time	2078.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2082.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2086.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2090.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2094.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2098.0	false	True	True	True	False	False		System-internal
LP	Bool	2098.1	false	True	True	True	False	False		System-internal
LN	Bool	2098.2	false	True	True	True	False	False		System-internal
VP	Bool	2098.3	false	True	True	True	False	False		System-internal
VN	Bool	2098.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2099.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2100.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2102.0	16#0	True	True	True	False	False		Reserve
Step24	GraphStep	2106.0		True	True	True	False	True		
S1	Bool	2106.0	false	True	True	True	False	False		Step is activated
L1	Bool	2106.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2106.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2106.3	false	True	True	True	False	False		Reserved
A1	Bool	2106.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2106.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2106.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2106.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2107.0	false	True	True	True	False	False		Step is active
LA	Bool	2107.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2107.2	false	True	True	True	False	False		Supervision active
RA	Bool	2107.3	false	True	True	True	False	False		Reserved
AA	Bool	2107.4	false	True	True	True	False	False		Reserved
SS	Bool	2107.5	false	True	True	True	False	False		System-internal
LS	Bool	2107.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2107.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2108.0	24	True	True	True	False	False		User step number
T	Time	2110.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2114.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2118.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2122.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2126.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2130.0	false	True	True	True	False	False		System-internal
LP	Bool	2130.1	false	True	True	True	False	False		System-internal
LN	Bool	2130.2	false	True	True	True	False	False		System-internal
VP	Bool	2130.3	false	True	True	True	False	False		System-internal
VN	Bool	2130.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2131.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2132.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2134.0	16#0	True	True	True	False	False		Reserve
Step25	GraphStep	2138.0		True	False	False	False	True		Step structure
S1	Bool	2138.0	false	True	False	False	False	False		Step is activated
L1	Bool	2138.1	false	True	False	False	False	False		interlock leaving state
V1	Bool	2138.2	false	True	False	False	False	False		Supervision entering state
R1	Bool	2138.3	false	True	False	False	False	False		Reserved
A1	Bool	2138.4	false	True	False	False	False	False		Error is acknowledged
S0	Bool	2138.5	false	True	False	False	False	False		Step is deactivated
L0	Bool	2138.6	false	True	False	False	False	False		Interlock entering state
V0	Bool	2138.7	false	True	False	False	False	False		Supervision leaving state
X	Bool	2139.0	false	True	False	False	False	False		Step is active
LA	Bool	2139.1	false	True	False	False	False	False		Interlock is not satisfied
VA	Bool	2139.2	false	True	False	False	False	False		Supervision active
RA	Bool	2139.3	false	True	False	False	False	False		Reserved
AA	Bool	2139.4	false	True	False	False	False	False		Reserved
SS	Bool	2139.5	false	True	False	False	False	False		System-internal

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
LS	Bool	2139.6	True	True	False	False	False	False		Direct result of the programmed interlock
VS	Bool	2139.7	false	True	False	False	False	False		Direct result of the programmed supervision
SNO	Int	2140.0	25	True	False	False	False	False		User step number
T	Time	2142.0	T#0ms	True	False	False	False	False		Total step activation time
U	Time	2146.0	T#0ms	True	False	False	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2150.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2154.0	16#0	True	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2158.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2162.0	false	True	False	False	False	False		System-internal
LP	Bool	2162.1	false	True	False	False	False	False		System-internal
LN	Bool	2162.2	false	True	False	False	False	False		System-internal
VP	Bool	2162.3	false	True	False	False	False	False		System-internal
VN	Bool	2162.4	false	True	False	False	False	False		System-internal
H_IL_ERR	Byte	2163.0	16#0	True	False	False	False	False		System-internal
H_SV_FLT	Byte	2164.0	16#0	True	False	False	False	False		System-internal
RESERVED	DWord	2166.0	16#0	True	False	False	False	False		Reserve
Step26	GraphStep	2170.0		True	True	True	False	True		
S1	Bool	2170.0	false	True	True	True	False	False		Step is activated
L1	Bool	2170.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2170.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2170.3	false	True	True	True	False	False		Reserved
A1	Bool	2170.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2170.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2170.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2170.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2171.0	false	True	True	True	False	False		Step is active
LA	Bool	2171.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2171.2	false	True	True	True	False	False		Supervision active
RA	Bool	2171.3	false	True	True	True	False	False		Reserved
AA	Bool	2171.4	false	True	True	True	False	False		Reserved
SS	Bool	2171.5	false	True	True	True	False	False		System-internal
LS	Bool	2171.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2171.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2172.0	26	True	True	True	False	False		User step number
T	Time	2174.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2178.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2182.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2186.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2190.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2194.0	false	True	True	True	False	False		System-internal
LP	Bool	2194.1	false	True	True	True	False	False		System-internal
LN	Bool	2194.2	false	True	True	True	False	False		System-internal
VP	Bool	2194.3	false	True	True	True	False	False		System-internal
VN	Bool	2194.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2195.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2196.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2198.0	16#0	True	True	True	False	False		Reserve
Step27	GraphStep	2202.0		True	True	True	False	True		
S1	Bool	2202.0	false	True	True	True	False	False		Step is activated
L1	Bool	2202.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2202.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2202.3	false	True	True	True	False	False		Reserved
A1	Bool	2202.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2202.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2202.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2202.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2203.0	false	True	True	True	False	False		Step is active
LA	Bool	2203.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2203.2	false	True	True	True	False	False		Supervision active
RA	Bool	2203.3	false	True	True	True	False	False		Reserved

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
AA	Bool	2203.4	false	True	True	True	False	False		Reserved
SS	Bool	2203.5	false	True	True	True	False	False		System-internal
LS	Bool	2203.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2203.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2204.0	27	True	True	True	False	False		User step number
T	Time	2206.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2210.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2214.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2218.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2222.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2226.0	false	True	True	True	False	False		System-internal
LP	Bool	2226.1	false	True	True	True	False	False		System-internal
LN	Bool	2226.2	false	True	True	True	False	False		System-internal
VP	Bool	2226.3	false	True	True	True	False	False		System-internal
VN	Bool	2226.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2227.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2228.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2230.0	16#0	True	True	True	False	False		Reserve
Step28	GraphStep	2234.0		True	True	True	False	True		
S1	Bool	2234.0	false	True	True	True	False	False		Step is activated
L1	Bool	2234.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2234.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2234.3	false	True	True	True	False	False		Reserved
A1	Bool	2234.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2234.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2234.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2234.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2235.0	false	True	True	True	False	False		Step is active
LA	Bool	2235.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2235.2	false	True	True	True	False	False		Supervision active
RA	Bool	2235.3	false	True	True	True	False	False		Reserved
AA	Bool	2235.4	false	True	True	True	False	False		Reserved
SS	Bool	2235.5	false	True	True	True	False	False		System-internal
LS	Bool	2235.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2235.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2236.0	28	True	True	True	False	False		User step number
T	Time	2238.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2242.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2246.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2250.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2254.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2258.0	false	True	True	True	False	False		System-internal
LP	Bool	2258.1	false	True	True	True	False	False		System-internal
LN	Bool	2258.2	false	True	True	True	False	False		System-internal
VP	Bool	2258.3	false	True	True	True	False	False		System-internal
VN	Bool	2258.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2259.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2260.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2262.0	16#0	True	True	True	False	False		Reserve
Step29	GraphStep	2266.0		True	True	True	False	True		
S1	Bool	2266.0	false	True	True	True	False	False		Step is activated
L1	Bool	2266.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2266.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2266.3	false	True	True	True	False	False		Reserved
A1	Bool	2266.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2266.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2266.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2266.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2267.0	false	True	True	True	False	False		Step is active
LA	Bool	2267.1	false	True	True	True	False	False		Interlock is not satisfied

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
VA	Bool	2267.2	false	True	True	True	False	False		Supervision active
RA	Bool	2267.3	false	True	True	True	False	False		Reserved
AA	Bool	2267.4	false	True	True	True	False	False		Reserved
SS	Bool	2267.5	false	True	True	True	False	False		System-internal
LS	Bool	2267.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2267.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2268.0	29	True	True	True	False	False		User step number
T	Time	2270.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2274.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2278.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2282.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2286.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2290.0	false	True	True	True	False	False		System-internal
LP	Bool	2290.1	false	True	True	True	False	False		System-internal
LN	Bool	2290.2	false	True	True	True	False	False		System-internal
VP	Bool	2290.3	false	True	True	True	False	False		System-internal
VN	Bool	2290.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2291.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2292.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2294.0	16#0	True	True	True	False	False		Reserve
▼ Step30	GraphStep	2298.0		True	True	True	False	True		
S1	Bool	2298.0	false	True	True	True	False	False		Step is activated
L1	Bool	2298.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2298.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2298.3	false	True	True	True	False	False		Reserved
A1	Bool	2298.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2298.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2298.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2298.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2299.0	false	True	True	True	False	False		Step is active
LA	Bool	2299.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2299.2	false	True	True	True	False	False		Supervision active
RA	Bool	2299.3	false	True	True	True	False	False		Reserved
AA	Bool	2299.4	false	True	True	True	False	False		Reserved
SS	Bool	2299.5	false	True	True	True	False	False		System-internal
LS	Bool	2299.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2299.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2300.0	30	True	True	True	False	False		User step number
T	Time	2302.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2306.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2310.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2314.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2318.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2322.0	false	True	True	True	False	False		System-internal
LP	Bool	2322.1	false	True	True	True	False	False		System-internal
LN	Bool	2322.2	false	True	True	True	False	False		System-internal
VP	Bool	2322.3	false	True	True	True	False	False		System-internal
VN	Bool	2322.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2323.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2324.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2326.0	16#0	True	True	True	False	False		Reserve
▼ Step31	GraphStep	2330.0		True	True	True	False	True		
S1	Bool	2330.0	false	True	True	True	False	False		Step is activated
L1	Bool	2330.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2330.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2330.3	false	True	True	True	False	False		Reserved
A1	Bool	2330.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2330.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2330.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2330.7	false	True	True	True	False	False		Supervision leaving state

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
X	Bool	2331.0	false	True	True	True	False	False		Step is active
LA	Bool	2331.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2331.2	false	True	True	True	False	False		Supervision active
RA	Bool	2331.3	false	True	True	True	False	False		Reserved
AA	Bool	2331.4	false	True	True	True	False	False		Reserved
SS	Bool	2331.5	false	True	True	True	False	False		System-internal
LS	Bool	2331.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2331.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2332.0	31	True	True	True	False	False		User step number
T	Time	2334.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2338.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2342.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2346.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2350.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2354.0	false	True	True	True	False	False		System-internal
LP	Bool	2354.1	false	True	True	True	False	False		System-internal
LN	Bool	2354.2	false	True	True	True	False	False		System-internal
VP	Bool	2354.3	false	True	True	True	False	False		System-internal
VN	Bool	2354.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2355.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2356.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2358.0	16#0	True	True	True	False	False		Reserve
▼ Step32	GraphStep	2362.0		True	True	True	False	True		
S1	Bool	2362.0	false	True	True	True	False	False		Step is activated
L1	Bool	2362.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2362.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2362.3	false	True	True	True	False	False		Reserved
A1	Bool	2362.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2362.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2362.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2362.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2363.0	false	True	True	True	False	False		Step is active
LA	Bool	2363.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2363.2	false	True	True	True	False	False		Supervision active
RA	Bool	2363.3	false	True	True	True	False	False		Reserved
AA	Bool	2363.4	false	True	True	True	False	False		Reserved
SS	Bool	2363.5	false	True	True	True	False	False		System-internal
LS	Bool	2363.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2363.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2364.0	32	True	True	True	False	False		User step number
T	Time	2366.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2370.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2374.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2378.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2382.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2386.0	false	True	True	True	False	False		System-internal
LP	Bool	2386.1	false	True	True	True	False	False		System-internal
LN	Bool	2386.2	false	True	True	True	False	False		System-internal
VP	Bool	2386.3	false	True	True	True	False	False		System-internal
VN	Bool	2386.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2387.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2388.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2390.0	16#0	True	True	True	False	False		Reserve
▼ Step33	GraphStep	2394.0		True	True	True	False	True		
S1	Bool	2394.0	false	True	True	True	False	False		Step is activated
L1	Bool	2394.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2394.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2394.3	false	True	True	True	False	False		Reserved
A1	Bool	2394.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2394.5	false	True	True	True	False	False		Step is deactivated

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
L0	Bool	2394.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2394.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2395.0	false	True	True	True	False	False		Step is active
LA	Bool	2395.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2395.2	false	True	True	True	False	False		Supervision active
RA	Bool	2395.3	false	True	True	True	False	False		Reserved
AA	Bool	2395.4	false	True	True	True	False	False		Reserved
SS	Bool	2395.5	false	True	True	True	False	False		System-internal
LS	Bool	2395.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2395.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2396.0	33	True	True	True	False	False		User step number
T	Time	2398.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2402.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2406.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2410.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2414.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2418.0	false	True	True	True	False	False		System-internal
LP	Bool	2418.1	false	True	True	True	False	False		System-internal
LN	Bool	2418.2	false	True	True	True	False	False		System-internal
VP	Bool	2418.3	false	True	True	True	False	False		System-internal
VN	Bool	2418.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2419.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2420.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2422.0	16#0	True	True	True	False	False		Reserve
▼ Step34	GraphStep	2426.0		True	True	True	False	True		
S1	Bool	2426.0	false	True	True	True	False	False		Step is activated
L1	Bool	2426.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2426.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2426.3	false	True	True	True	False	False		Reserved
A1	Bool	2426.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2426.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2426.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2426.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2427.0	false	True	True	True	False	False		Step is active
LA	Bool	2427.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2427.2	false	True	True	True	False	False		Supervision active
RA	Bool	2427.3	false	True	True	True	False	False		Reserved
AA	Bool	2427.4	false	True	True	True	False	False		Reserved
SS	Bool	2427.5	false	True	True	True	False	False		System-internal
LS	Bool	2427.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2427.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2428.0	34	True	True	True	False	False		User step number
T	Time	2430.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2434.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2438.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2442.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2446.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2450.0	false	True	True	True	False	False		System-internal
LP	Bool	2450.1	false	True	True	True	False	False		System-internal
LN	Bool	2450.2	false	True	True	True	False	False		System-internal
VP	Bool	2450.3	false	True	True	True	False	False		System-internal
VN	Bool	2450.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2451.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2452.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2454.0	16#0	True	True	True	False	False		Reserve
▼ Step35	GraphStep	2458.0		True	True	True	False	True		
S1	Bool	2458.0	false	True	True	True	False	False		Step is activated
L1	Bool	2458.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2458.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2458.3	false	True	True	True	False	False		Reserved

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
A1	Bool	2458.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2458.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2458.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2458.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2459.0	false	True	True	True	False	False		Step is active
LA	Bool	2459.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2459.2	false	True	True	True	False	False		Supervision active
RA	Bool	2459.3	false	True	True	True	False	False		Reserved
AA	Bool	2459.4	false	True	True	True	False	False		Reserved
SS	Bool	2459.5	false	True	True	True	False	False		System-internal
LS	Bool	2459.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2459.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2460.0	35	True	True	True	False	False		User step number
T	Time	2462.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2466.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2470.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2474.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2478.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2482.0	false	True	True	True	False	False		System-internal
LP	Bool	2482.1	false	True	True	True	False	False		System-internal
LN	Bool	2482.2	false	True	True	True	False	False		System-internal
VP	Bool	2482.3	false	True	True	True	False	False		System-internal
VN	Bool	2482.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2483.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2484.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2486.0	16#0	True	True	True	False	False		Reserve
▼ Step36	GraphStep	2490.0		True	True	True	False	True		
S1	Bool	2490.0	false	True	True	True	False	False		Step is activated
L1	Bool	2490.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2490.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2490.3	false	True	True	True	False	False		Reserved
A1	Bool	2490.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2490.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2490.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2490.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2491.0	false	True	True	True	False	False		Step is active
LA	Bool	2491.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2491.2	false	True	True	True	False	False		Supervision active
RA	Bool	2491.3	false	True	True	True	False	False		Reserved
AA	Bool	2491.4	false	True	True	True	False	False		Reserved
SS	Bool	2491.5	false	True	True	True	False	False		System-internal
LS	Bool	2491.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2491.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2492.0	36	True	True	True	False	False		User step number
T	Time	2494.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2498.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2502.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2506.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2510.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2514.0	false	True	True	True	False	False		System-internal
LP	Bool	2514.1	false	True	True	True	False	False		System-internal
LN	Bool	2514.2	false	True	True	True	False	False		System-internal
VP	Bool	2514.3	false	True	True	True	False	False		System-internal
VN	Bool	2514.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2515.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2516.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2518.0	16#0	True	True	True	False	False		Reserve
▼ Step37	GraphStep	2522.0		True	True	True	False	True		
S1	Bool	2522.0	false	True	True	True	False	False		Step is activated
L1	Bool	2522.1	false	True	True	True	False	False		interlock leaving state

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
V1	Bool	2522.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2522.3	false	True	True	True	False	False		Reserved
A1	Bool	2522.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2522.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2522.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2522.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2523.0	false	True	True	True	False	False		Step is active
LA	Bool	2523.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2523.2	false	True	True	True	False	False		Supervision active
RA	Bool	2523.3	false	True	True	True	False	False		Reserved
AA	Bool	2523.4	false	True	True	True	False	False		Reserved
SS	Bool	2523.5	false	True	True	True	False	False		System-internal
LS	Bool	2523.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2523.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2524.0	37	True	True	True	False	False		User step number
T	Time	2526.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2530.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2534.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2538.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2542.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2546.0	false	True	True	True	False	False		System-internal
LP	Bool	2546.1	false	True	True	True	False	False		System-internal
LN	Bool	2546.2	false	True	True	True	False	False		System-internal
VP	Bool	2546.3	false	True	True	True	False	False		System-internal
VN	Bool	2546.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2547.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2548.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2550.0	16#0	True	True	True	False	False		Reserve
▼ Step38	GraphStep	2554.0		True	True	True	False	True		
S1	Bool	2554.0	false	True	True	True	False	False		Step is activated
L1	Bool	2554.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2554.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2554.3	false	True	True	True	False	False		Reserved
A1	Bool	2554.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2554.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2554.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2554.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2555.0	false	True	True	True	False	False		Step is active
LA	Bool	2555.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2555.2	false	True	True	True	False	False		Supervision active
RA	Bool	2555.3	false	True	True	True	False	False		Reserved
AA	Bool	2555.4	false	True	True	True	False	False		Reserved
SS	Bool	2555.5	false	True	True	True	False	False		System-internal
LS	Bool	2555.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2555.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2556.0	38	True	True	True	False	False		User step number
T	Time	2558.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2562.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2566.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2570.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2574.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2578.0	false	True	True	True	False	False		System-internal
LP	Bool	2578.1	false	True	True	True	False	False		System-internal
LN	Bool	2578.2	false	True	True	True	False	False		System-internal
VP	Bool	2578.3	false	True	True	True	False	False		System-internal
VN	Bool	2578.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2579.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2580.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2582.0	16#0	True	True	True	False	False		Reserve
▼ Step39	GraphStep	2586.0		True	True	True	False	True		

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
S1	Bool	2586.0	false	True	True	True	False	False		Step is activated
L1	Bool	2586.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2586.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2586.3	false	True	True	True	False	False		Reserved
A1	Bool	2586.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2586.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2586.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2586.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2587.0	false	True	True	True	False	False		Step is active
LA	Bool	2587.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2587.2	false	True	True	True	False	False		Supervision active
RA	Bool	2587.3	false	True	True	True	False	False		Reserved
AA	Bool	2587.4	false	True	True	True	False	False		Reserved
SS	Bool	2587.5	false	True	True	True	False	False		System-internal
LS	Bool	2587.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2587.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2588.0	39	True	True	True	False	False		User step number
T	Time	2590.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2594.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2598.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2602.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2606.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2610.0	false	True	True	True	False	False		System-internal
LP	Bool	2610.1	false	True	True	True	False	False		System-internal
LN	Bool	2610.2	false	True	True	True	False	False		System-internal
VP	Bool	2610.3	false	True	True	True	False	False		System-internal
VN	Bool	2610.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2611.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2612.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2614.0	16#0	True	True	True	False	False		Reserve
▼ Step40	GraphStep	2618.0		True	True	True	False	True		
S1	Bool	2618.0	false	True	True	True	False	False		Step is activated
L1	Bool	2618.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2618.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2618.3	false	True	True	True	False	False		Reserved
A1	Bool	2618.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2618.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2618.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2618.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2619.0	false	True	True	True	False	False		Step is active
LA	Bool	2619.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2619.2	false	True	True	True	False	False		Supervision active
RA	Bool	2619.3	false	True	True	True	False	False		Reserved
AA	Bool	2619.4	false	True	True	True	False	False		Reserved
SS	Bool	2619.5	false	True	True	True	False	False		System-internal
LS	Bool	2619.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2619.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2620.0	40	True	True	True	False	False		User step number
T	Time	2622.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2626.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2630.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2634.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2638.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2642.0	false	True	True	True	False	False		System-internal
LP	Bool	2642.1	false	True	True	True	False	False		System-internal
LN	Bool	2642.2	false	True	True	True	False	False		System-internal
VP	Bool	2642.3	false	True	True	True	False	False		System-internal
VN	Bool	2642.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2643.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2644.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2646.0	16#0	True	True	True	False	False		Reserve

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
Step41	GraphStep	2650.0		True	False	False	False	True		Step structure
S1	Bool	2650.0	false	True	False	False	False	False		Step is activated
L1	Bool	2650.1	false	True	False	False	False	False		interlock leaving state
V1	Bool	2650.2	false	True	False	False	False	False		Supervision entering state
R1	Bool	2650.3	false	True	False	False	False	False		Reserved
A1	Bool	2650.4	false	True	False	False	False	False		Error is acknowledged
S0	Bool	2650.5	false	True	False	False	False	False		Step is deactivated
L0	Bool	2650.6	false	True	False	False	False	False		Interlock entering state
V0	Bool	2650.7	false	True	False	False	False	False		Supervision leaving state
X	Bool	2651.0	false	True	False	False	False	False		Step is active
LA	Bool	2651.1	false	True	False	False	False	False		Interlock is not satisfied
VA	Bool	2651.2	false	True	False	False	False	False		Supervision active
RA	Bool	2651.3	false	True	False	False	False	False		Reserved
AA	Bool	2651.4	false	True	False	False	False	False		Reserved
SS	Bool	2651.5	false	True	False	False	False	False		System-internal
LS	Bool	2651.6	True	True	False	False	False	False		Direct result of the programmed interlock
VS	Bool	2651.7	false	True	False	False	False	False		Direct result of the programmed supervision
SNO	Int	2652.0	41	True	False	False	False	False		User step number
T	Time	2654.0	T#0ms	True	False	False	False	False		Total step activation time
U	Time	2658.0	T#0ms	True	False	False	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2662.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2666.0	16#0	True	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2670.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2674.0	false	True	False	False	False	False		System-internal
LP	Bool	2674.1	false	True	False	False	False	False		System-internal
LN	Bool	2674.2	false	True	False	False	False	False		System-internal
VP	Bool	2674.3	false	True	False	False	False	False		System-internal
VN	Bool	2674.4	false	True	False	False	False	False		System-internal
H_IL_ERR	Byte	2675.0	16#0	True	False	False	False	False		System-internal
H_SV_FLT	Byte	2676.0	16#0	True	False	False	False	False		System-internal
RESERVED	DWord	2678.0	16#0	True	False	False	False	False		Reserve
Step42	GraphStep	2682.0		True	True	True	False	True		
S1	Bool	2682.0	false	True	True	True	False	False		Step is activated
L1	Bool	2682.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2682.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2682.3	false	True	True	True	False	False		Reserved
A1	Bool	2682.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2682.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2682.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2682.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2683.0	false	True	True	True	False	False		Step is active
LA	Bool	2683.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2683.2	false	True	True	True	False	False		Supervision active
RA	Bool	2683.3	false	True	True	True	False	False		Reserved
AA	Bool	2683.4	false	True	True	True	False	False		Reserved
SS	Bool	2683.5	false	True	True	True	False	False		System-internal
LS	Bool	2683.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2683.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2684.0	42	True	True	True	False	False		User step number
T	Time	2686.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2690.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2694.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2698.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2702.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2706.0	false	True	True	True	False	False		System-internal
LP	Bool	2706.1	false	True	True	True	False	False		System-internal
LN	Bool	2706.2	false	True	True	True	False	False		System-internal
VP	Bool	2706.3	false	True	True	True	False	False		System-internal
VN	Bool	2706.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2707.0	16#0	True	True	True	False	False		System-internal

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
H_SV_FLT	Byte	2708.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2710.0	16#0	True	True	True	False	False		Reserve
▼ Step43	GraphStep	2714.0		True	True	True	False	True		
S1	Bool	2714.0	false	True	True	True	False	False		Step is activated
L1	Bool	2714.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2714.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2714.3	false	True	True	True	False	False		Reserved
A1	Bool	2714.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2714.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2714.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2714.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2715.0	false	True	True	True	False	False		Step is active
LA	Bool	2715.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2715.2	false	True	True	True	False	False		Supervision active
RA	Bool	2715.3	false	True	True	True	False	False		Reserved
AA	Bool	2715.4	false	True	True	True	False	False		Reserved
SS	Bool	2715.5	false	True	True	True	False	False		System-internal
LS	Bool	2715.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2715.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2716.0	43	True	True	True	False	False		User step number
T	Time	2718.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2722.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2726.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2730.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2734.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2738.0	false	True	True	True	False	False		System-internal
LP	Bool	2738.1	false	True	True	True	False	False		System-internal
LN	Bool	2738.2	false	True	True	True	False	False		System-internal
VP	Bool	2738.3	false	True	True	True	False	False		System-internal
VN	Bool	2738.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2739.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2740.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2742.0	16#0	True	True	True	False	False		Reserve
▼ Step44	GraphStep	2746.0		True	True	True	False	True		
S1	Bool	2746.0	false	True	True	True	False	False		Step is activated
L1	Bool	2746.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2746.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2746.3	false	True	True	True	False	False		Reserved
A1	Bool	2746.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2746.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2746.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2746.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2747.0	false	True	True	True	False	False		Step is active
LA	Bool	2747.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2747.2	false	True	True	True	False	False		Supervision active
RA	Bool	2747.3	false	True	True	True	False	False		Reserved
AA	Bool	2747.4	false	True	True	True	False	False		Reserved
SS	Bool	2747.5	false	True	True	True	False	False		System-internal
LS	Bool	2747.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2747.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2748.0	44	True	True	True	False	False		User step number
T	Time	2750.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2754.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2758.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2762.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2766.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2770.0	false	True	True	True	False	False		System-internal
LP	Bool	2770.1	false	True	True	True	False	False		System-internal
LN	Bool	2770.2	false	True	True	True	False	False		System-internal
VP	Bool	2770.3	false	True	True	True	False	False		System-internal

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
VN	Bool	2770.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2771.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2772.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2774.0	16#0	True	True	True	False	False		Reserve
Step45	GraphStep	2778.0		True	True	True	False	True		
S1	Bool	2778.0	false	True	True	True	False	False		Step is activated
L1	Bool	2778.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2778.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2778.3	false	True	True	True	False	False		Reserved
A1	Bool	2778.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2778.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2778.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2778.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2779.0	false	True	True	True	False	False		Step is active
LA	Bool	2779.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2779.2	false	True	True	True	False	False		Supervision active
RA	Bool	2779.3	false	True	True	True	False	False		Reserved
AA	Bool	2779.4	false	True	True	True	False	False		Reserved
SS	Bool	2779.5	false	True	True	True	False	False		System-internal
LS	Bool	2779.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2779.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2780.0	45	True	True	True	False	False		User step number
T	Time	2782.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2786.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2790.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2794.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2798.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2802.0	false	True	True	True	False	False		System-internal
LP	Bool	2802.1	false	True	True	True	False	False		System-internal
LN	Bool	2802.2	false	True	True	True	False	False		System-internal
VP	Bool	2802.3	false	True	True	True	False	False		System-internal
VN	Bool	2802.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2803.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2804.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2806.0	16#0	True	True	True	False	False		Reserve
Step46	GraphStep	2810.0		True	True	True	False	True		
S1	Bool	2810.0	false	True	True	True	False	False		Step is activated
L1	Bool	2810.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2810.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2810.3	false	True	True	True	False	False		Reserved
A1	Bool	2810.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2810.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2810.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2810.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2811.0	false	True	True	True	False	False		Step is active
LA	Bool	2811.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2811.2	false	True	True	True	False	False		Supervision active
RA	Bool	2811.3	false	True	True	True	False	False		Reserved
AA	Bool	2811.4	false	True	True	True	False	False		Reserved
SS	Bool	2811.5	false	True	True	True	False	False		System-internal
LS	Bool	2811.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2811.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2812.0	46	True	True	True	False	False		User step number
T	Time	2814.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2818.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2822.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2826.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2830.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2834.0	false	True	True	True	False	False		System-internal
LP	Bool	2834.1	false	True	True	True	False	False		System-internal

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
LN	Bool	2834.2	false	True	True	True	False	False		System-internal
VP	Bool	2834.3	false	True	True	True	False	False		System-internal
VN	Bool	2834.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2835.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2836.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2838.0	16#0	True	True	True	False	False		Reserve
▼ Step47	GraphStep	2842.0		True	True	True	False	True		
S1	Bool	2842.0	false	True	True	True	False	False		Step is activated
L1	Bool	2842.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2842.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2842.3	false	True	True	True	False	False		Reserved
A1	Bool	2842.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2842.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2842.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2842.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2843.0	false	True	True	True	False	False		Step is active
LA	Bool	2843.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2843.2	false	True	True	True	False	False		Supervision active
RA	Bool	2843.3	false	True	True	True	False	False		Reserved
AA	Bool	2843.4	false	True	True	True	False	False		Reserved
SS	Bool	2843.5	false	True	True	True	False	False		System-internal
LS	Bool	2843.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2843.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2844.0	47	True	True	True	False	False		User step number
T	Time	2846.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2850.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2854.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2858.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2862.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2866.0	false	True	True	True	False	False		System-internal
LP	Bool	2866.1	false	True	True	True	False	False		System-internal
LN	Bool	2866.2	false	True	True	True	False	False		System-internal
VP	Bool	2866.3	false	True	True	True	False	False		System-internal
VN	Bool	2866.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2867.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2868.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2870.0	16#0	True	True	True	False	False		Reserve
▼ Step48	GraphStep	2874.0		True	True	True	False	True		
S1	Bool	2874.0	false	True	True	True	False	False		Step is activated
L1	Bool	2874.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2874.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2874.3	false	True	True	True	False	False		Reserved
A1	Bool	2874.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2874.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2874.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2874.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2875.0	false	True	True	True	False	False		Step is active
LA	Bool	2875.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2875.2	false	True	True	True	False	False		Supervision active
RA	Bool	2875.3	false	True	True	True	False	False		Reserved
AA	Bool	2875.4	false	True	True	True	False	False		Reserved
SS	Bool	2875.5	false	True	True	True	False	False		System-internal
LS	Bool	2875.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2875.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2876.0	48	True	True	True	False	False		User step number
T	Time	2878.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2882.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2886.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2890.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2894.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
SM	Bool	2898.0	false	True	True	True	False	False		System-internal
LP	Bool	2898.1	false	True	True	True	False	False		System-internal
LN	Bool	2898.2	false	True	True	True	False	False		System-internal
VP	Bool	2898.3	false	True	True	True	False	False		System-internal
VN	Bool	2898.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2899.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2900.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2902.0	16#0	True	True	True	False	False		Reserve
Step49	GraphStep	2906.0		True	False	False	False	True		Step structure
S1	Bool	2906.0	false	True	False	False	False	False		Step is activated
L1	Bool	2906.1	false	True	False	False	False	False		interlock leaving state
V1	Bool	2906.2	false	True	False	False	False	False		Supervision entering state
R1	Bool	2906.3	false	True	False	False	False	False		Reserved
A1	Bool	2906.4	false	True	False	False	False	False		Error is acknowledged
S0	Bool	2906.5	false	True	False	False	False	False		Step is deactivated
L0	Bool	2906.6	false	True	False	False	False	False		Interlock entering state
V0	Bool	2906.7	false	True	False	False	False	False		Supervision leaving state
X	Bool	2907.0	false	True	False	False	False	False		Step is active
LA	Bool	2907.1	false	True	False	False	False	False		Interlock is not satisfied
VA	Bool	2907.2	false	True	False	False	False	False		Supervision active
RA	Bool	2907.3	false	True	False	False	False	False		Reserved
AA	Bool	2907.4	false	True	False	False	False	False		Reserved
SS	Bool	2907.5	false	True	False	False	False	False		System-internal
LS	Bool	2907.6	True	True	False	False	False	False		Direct result of the programmed interlock
VS	Bool	2907.7	false	True	False	False	False	False		Direct result of the programmed supervision
SNO	Int	2908.0	49	True	False	False	False	False		User step number
T	Time	2910.0	T#0ms	True	False	False	False	False		Total step activation time
U	Time	2914.0	T#0ms	True	False	False	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2918.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2922.0	16#0	True	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2926.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2930.0	false	True	False	False	False	False		System-internal
LP	Bool	2930.1	false	True	False	False	False	False		System-internal
LN	Bool	2930.2	false	True	False	False	False	False		System-internal
VP	Bool	2930.3	false	True	False	False	False	False		System-internal
VN	Bool	2930.4	false	True	False	False	False	False		System-internal
H_IL_ERR	Byte	2931.0	16#0	True	False	False	False	False		System-internal
H_SV_FLT	Byte	2932.0	16#0	True	False	False	False	False		System-internal
RESERVED	DWord	2934.0	16#0	True	False	False	False	False		Reserve
Step50	GraphStep	2938.0		True	False	False	False	True		Step structure
S1	Bool	2938.0	false	True	False	False	False	False		Step is activated
L1	Bool	2938.1	false	True	False	False	False	False		interlock leaving state
V1	Bool	2938.2	false	True	False	False	False	False		Supervision entering state
R1	Bool	2938.3	false	True	False	False	False	False		Reserved
A1	Bool	2938.4	false	True	False	False	False	False		Error is acknowledged
S0	Bool	2938.5	false	True	False	False	False	False		Step is deactivated
L0	Bool	2938.6	false	True	False	False	False	False		Interlock entering state
V0	Bool	2938.7	false	True	False	False	False	False		Supervision leaving state
X	Bool	2939.0	false	True	False	False	False	False		Step is active
LA	Bool	2939.1	false	True	False	False	False	False		Interlock is not satisfied
VA	Bool	2939.2	false	True	False	False	False	False		Supervision active
RA	Bool	2939.3	false	True	False	False	False	False		Reserved
AA	Bool	2939.4	false	True	False	False	False	False		Reserved
SS	Bool	2939.5	false	True	False	False	False	False		System-internal
LS	Bool	2939.6	True	True	False	False	False	False		Direct result of the programmed interlock
VS	Bool	2939.7	false	True	False	False	False	False		Direct result of the programmed supervision
SNO	Int	2940.0	50	True	False	False	False	False		User step number
T	Time	2942.0	T#0ms	True	False	False	False	False		Total step activation time
U	Time	2946.0	T#0ms	True	False	False	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2950.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2954.0	16#0	True	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_SUP	DWord	2958.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2962.0	false	True	False	False	False	False		System-internal
LP	Bool	2962.1	false	True	False	False	False	False		System-internal
LN	Bool	2962.2	false	True	False	False	False	False		System-internal
VP	Bool	2962.3	false	True	False	False	False	False		System-internal
VN	Bool	2962.4	false	True	False	False	False	False		System-internal
H_IL_ERR	Byte	2963.0	16#0	True	False	False	False	False		System-internal
H_SV_FLT	Byte	2964.0	16#0	True	False	False	False	False		System-internal
RESERVED	DWord	2966.0	16#0	True	False	False	False	False		Reserve
S_DISPLAY	Int	3314.0	0	True	True	True	False	False		Affichage interne du paramètre de sortie S_NO
S_SEL_OLD	Int	3316.0	0	True	True	True	False	False		Valeur précédente dans S_SEL
S_DISPIDX	Byte	3318.0	16#0	True	True	True	False	False		Indice de l'étape affichée dans S_NO
T_DISPIDX	Byte	3319.0	16#0	True	True	True	False	False		Indice de la transition affichée dans T_NO
MOP	Struct	3324.0		True	True	True	False	False		Mode de fonctionnement
AUTO	Bool	3324.0	true	True	True	True	False	False		Etat : Mode automatique
MAN	Bool	3324.1	false	True	True	True	False	False		Etat : Mode manuel
TAP	Bool	3324.2	false	True	True	True	False	False		Etat : Mode semi-automatique/Activation de l'étape suivante avec transition
TOP	Bool	3324.3	false	True	True	True	False	False		Etat : Mode semi-automatique/Ignorer la transition
ACK_S	Bool	3324.4	false	True	True	True	False	False		Requête : Acquitter l'étape figurant dans le paramètre S_NO
REG_S	Bool	3324.5	false	True	True	True	False	False		Requête : Enregistrer l'étape figurant dans le paramètre S_NO
T_PREV	Bool	3324.6	false	True	True	True	False	False		Requête : Afficher la transition valide précédente dans le paramètre T_NO
T_NEXT	Bool	3324.7	false	True	True	True	False	False		Requête : Afficher la transition valide suivante dans le paramètre T_NO
LOCK	Bool	3325.0	true	True	True	True	False	False		Etat : Traitement des Interlocks activé
SUP	Bool	3325.1	true	True	True	True	False	False		Etat : Traitement de Supervision activé
ACKREQ	Bool	3325.2	true	True	True	True	False	False		Etat : Acquittement requis
SSKIP	Bool	3325.3	false	True	True	True	False	False		Etat : "Ignorer les étapes" activé
OFF	Bool	3325.4	false	True	True	True	False	False		Requête : Désactiver toutes les étapes
INIT	Bool	3325.5	true	True	True	True	False	False		Requête : Mettre le graphe à l'état initial
HALT	Bool	3325.6	false	True	True	True	False	False		Etat : Le graphe est arrêté
TMS_HALT	Bool	3325.7	false	True	True	True	False	False		Etat : Toutes les temporisations internes sont arrêtées
OPS_ZERO	Bool	3326.0	false	True	True	True	False	False		Etat : Tous les opérandes traités avec N, L, D mis à 0
SACT_DISP	Bool	3326.1	true	True	True	True	False	False		Etat : N'afficher que les étapes actives
SEF_DISP	Bool	3326.2	false	True	True	True	False	False		Etat : Afficher uniquement les étapes avec erreurs et perturbations
SALL_DISP	Bool	3326.3	false	True	True	True	False	False		Etat : Afficher toutes les étapes
S_PREV	Bool	3326.4	false	True	True	True	False	False		Requête : Afficher l'étape précédente dans le paramètre S_NO
S_NEXT	Bool	3326.5	false	True	True	True	False	False		Requête : Afficher l'étape suivante dans le paramètre S_NO
S_SELOK	Bool	3326.6	false	True	True	True	False	False		Requête : Afficher dans le paramètre S_NO le numéro d'étape figurant dans S_SEL
S_ON	Bool	3326.7	false	True	True	True	False	False		Requête : Activer l'étape figurant dans le paramètre S_NO
S_OFF	Bool	3327.0	false	True	True	True	False	False		Requête : Désactiver l'étape figurant dans le paramètre S_NO
T_PUSH	Bool	3327.1	false	True	True	True	False	False		Requête : Validation de commutation pour transition
REG	Bool	3327.2	false	True	True	True	False	False		Requête : Enregistrer toutes les erreurs Interlock et Supervision
ACK	Bool	3327.3	false	True	True	True	False	False		Requête : Acquitter toutes les erreurs Interlock et Supervision
IL_PERM	Bool	3327.4	false	True	True	True	False	False		Etat : Traitement permanent de tous les Interlocks
T_PERM	Bool	3327.5	false	True	True	True	False	False		Etat : Traitement permanent de toutes les transitions
ILP_MAN	Bool	3327.6	false	True	True	True	False	False		Etat : Traitement permanent de tous les Interlocks en mode manuel
TICKS	Struct	3328.0		True	True	True	False	False		Périodes d'horloge

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
DELTA	Time	3328.0	T#0ms	True	True	True	False	False		Différence de temps entre deux cycles
OLD	Time	3332.0	T#0ms	True	True	True	False	False		Valeur du compteur de périodes 10 ms au dernier cycle
NEW	Time	3336.0	T#0ms	True	True	True	False	False		Valeur du compteur de périodes 10 ms dans ce cycle
▼ SQ_FLAGS	Struct	3340.0		True	True	True	False	False		Mémentos de graphe
ERR_FLT	Bool	3340.0	false	True	True	True	False	False		Erreur groupée Interlock et Supervision
ERROR	Bool	3340.1	false	True	True	True	False	False		Erreur groupée Interlock
FAULT	Bool	3340.2	false	True	True	True	False	False		Erreur groupée Supervision
RT_FAIL	Bool	3340.3	false	True	True	True	False	False		Erreur à l'exécution
NO_SNO	Bool	3340.4	false	True	True	True	False	False		Numéro d'étape requis introuvable
NF_OFL	Bool	3340.5	false	True	True	True	False	False		Débordement : Trop de requêtes ON ou OFF
SA_OFL	Bool	3340.6	false	True	True	True	False	False		Débordement : Trop d'étapes activées
TV_OFL	Bool	3340.7	false	True	True	True	False	False		Débordement : Trop de transitions valides
MSG_OFL	Bool	3341.0	false	True	True	True	False	False		Débordement : Pas assez de ressources système pour ALARM_S
NO_SWI	Bool	3341.1	false	True	True	True	False	False		Pas de commutation dans ce cycle
CYC_OP	Bool	3341.2	false	True	True	True	False	False		Exécution cyclique du graphe après l'initialisation
AS_MSG	Bool	3341.3	true	True	True	True	False	False		Messages à l'exécution activés ou désactivés par instruction
AS_SEND	Bool	3341.4	false	True	True	True	False	False		Envoyer les messages de WR_USMSG ou les entrer uniquement dans le tampon de diagnostic
SQ_BUSY	Bool	3341.5	false	True	True	True	False	False		Mémento de front interne pour le traitement séquentiel
SA_BUSY	Bool	3341.6	false	True	True	True	False	False		Mémento de front interne pour le traitement séquentiel
AS_SIG	Bool	3341.7	false	True	True	True	False	False		Mémentos de front pour messages de Alarm_S et Alarm_SQ

electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / Program blocks

Acid Cleaning_DB [DB4]

Acid Cleaning_DB Properties							
General							
Name	Acid Cleaning_DB	**Number**	4	**Type**	DB	**Language**	DB
Numbering	Automatic						
Information							
Title		**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
Input										
OFF_SQ	Bool	0.0	false	True	True	True	False	False		Désactiver le graphe
INIT_SQ	Bool	0.1	false	True	True	True	False	False		Mettre le graphe à l'état initial
ACK_EF	Bool	0.2	false	True	True	True	False	False		Acquitter toutes les erreurs et perturbations
S_PREV	Bool	0.3	false	True	True	True	False	False		Afficher l'étape précédente dans le paramètre S_NO
S_NEXT	Bool	0.4	false	True	True	True	False	False		Afficher l'étape suivante dans le paramètre S_NO
SW_AUTO	Bool	0.5	false	True	True	True	False	False		Mode automatique
SW_TAP	Bool	0.6	false	True	True	True	False	False		Mode semi-automatique/Activation de l'étape suivante avec transition
SW_TOP	Bool	0.7	false	True	True	True	False	False		Mode semi-automatique/Ignorer la transition
SW_MAN	Bool	1.0	false	True	True	True	False	False		Mode manuel
S_SEL	Int	2.0	0	True	True	True	False	False		Sélectionner l'étape à afficher dans S_NO
S_ON	Bool	4.0	false	True	True	True	False	False		Activer l'étape figurant dans le paramètre S_NO
S_OFF	Bool	4.1	false	True	True	True	False	False		Désactiver l'étape figurant dans le paramètre S_NO
T_PUSH	Bool	4.2	false	True	True	True	False	False		Validation de commutation pour transition en mode semi-automatique
Output										
S_NO	Int	6.0	0	True	True	True	False	False		Numéro d'étape
S_MORE	Bool	8.0	false	True	True	True	False	False		Il existe d'autres étapes qui peuvent être affichées dans S_NO.
S_ACTIVE	Bool	8.1	false	True	True	True	False	False		L'étape affichée dans le paramètre S_NO est activée
ERR_FLT	Bool	8.2	false	True	True	True	False	False		Erreur groupée Interlock or Supervision
AUTO_ON	Bool	8.3	false	True	True	True	False	False		Mode automatique activé
TAP_ON	Bool	8.4	false	True	True	True	False	False		Mode semi-automatique/Etape avec transition activé
TOP_ON	Bool	8.5	false	True	True	True	False	False		Mode semi-automatique/Ignorer la transition activé
MAN_ON	Bool	8.6	false	True	True	True	False	False		Mode manuel activé
InOut										
Static										
Trans1	GraphTransition	80.0		True	True	True	False	True		
TV	Bool	80.0	false	True	True	True	False	False		Transition is valid
TT	Bool	80.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	80.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	80.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	82.0	1	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	84.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	88.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	92.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans2	GraphTransition	96.0		True	True	True	False	True		
TV	Bool	96.0	false	True	True	True	False	False		Transition is valid
TT	Bool	96.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	96.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	96.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	98.0	2	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	100.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_OLD	DWord	104.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	108.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans3	GraphTransition	112.0		True	True	True	False	True		
TV	Bool	112.0	false	True	True	True	False	False		Transition is valid
TT	Bool	112.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	112.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	112.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	114.0	3	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	116.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	120.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	124.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans4	GraphTransition	128.0		True	True	True	False	True		
TV	Bool	128.0	false	True	True	True	False	False		Transition is valid
TT	Bool	128.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	128.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	128.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	130.0	4	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	132.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	136.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	140.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans5	GraphTransition	144.0		True	True	True	False	True		
TV	Bool	144.0	false	True	True	True	False	False		Transition is valid
TT	Bool	144.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	144.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	144.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	146.0	5	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	148.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	152.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	156.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans6	GraphTransition	160.0		True	True	True	False	True		
TV	Bool	160.0	false	True	True	True	False	False		Transition is valid
TT	Bool	160.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	160.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	160.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	162.0	6	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	164.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	168.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	172.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans7	GraphTransition	176.0		True	True	True	False	True		
TV	Bool	176.0	false	True	True	True	False	False		Transition is valid
TT	Bool	176.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	176.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	176.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	178.0	7	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	180.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	184.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	188.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans8	GraphTransition	192.0		True	True	True	False	True		

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TV	Bool	192.0	false	True	True	True	False	False		Transition is valid
TT	Bool	192.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	192.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	192.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	194.0	8	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	196.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	200.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	204.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans9	GraphTransition	208.0		True	True	True	False	True		
TV	Bool	208.0	false	True	True	True	False	False		Transition is valid
TT	Bool	208.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	208.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	208.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	210.0	9	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	212.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	216.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	220.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans10	GraphTransition	224.0		True	True	True	False	True		
TV	Bool	224.0	false	True	True	True	False	False		Transition is valid
TT	Bool	224.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	224.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	224.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	226.0	10	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	228.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	232.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	236.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans11	GraphTransition	240.0		True	True	True	False	True		
TV	Bool	240.0	false	True	True	True	False	False		Transition is valid
TT	Bool	240.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	240.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	240.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	242.0	11	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	244.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	248.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	252.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans12	GraphTransition	256.0		True	True	True	False	True		
TV	Bool	256.0	false	True	True	True	False	False		Transition is valid
TT	Bool	256.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	256.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	256.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	258.0	12	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	260.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	264.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	268.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans13	GraphTransition	272.0		True	True	True	False	True		
TV	Bool	272.0	false	True	True	True	False	False		Transition is valid
TT	Bool	272.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	272.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	272.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TNO	Int	274.0	13	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	276.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	280.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	284.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans14	GraphTransition	288.0		True	True	True	False	True		
TV	Bool	288.0	false	True	True	True	False	False		Transition is valid
TT	Bool	288.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	288.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	288.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	290.0	14	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	292.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	296.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	300.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans15	GraphTransition	304.0		True	True	True	False	True		
TV	Bool	304.0	false	True	True	True	False	False		Transition is valid
TT	Bool	304.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	304.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	304.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	306.0	15	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	308.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	312.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	316.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans18	GraphTransition	320.0		True	True	True	False	True		
TV	Bool	320.0	false	True	True	True	False	False		Transition is valid
TT	Bool	320.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	320.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	320.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	322.0	18	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	324.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	328.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	332.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans19	GraphTransition	336.0		True	True	True	False	True		
TV	Bool	336.0	false	True	True	True	False	False		Transition is valid
TT	Bool	336.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	336.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	336.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	338.0	19	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	340.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	344.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	348.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans20	GraphTransition	352.0		True	True	True	False	True		
TV	Bool	352.0	false	True	True	True	False	False		Transition is valid
TT	Bool	352.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	352.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	352.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	354.0	20	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	356.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_OLD	DWord	360.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	364.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans21	GraphTransition	368.0		True	True	True	False	True		
TV	Bool	368.0	false	True	True	True	False	False		Transition is valid
TT	Bool	368.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	368.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	368.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	370.0	21	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	372.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	376.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	380.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans22	GraphTransition	384.0		True	True	True	False	True		
TV	Bool	384.0	false	True	True	True	False	False		Transition is valid
TT	Bool	384.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	384.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	384.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	386.0	22	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	388.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	392.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	396.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans23	GraphTransition	400.0		True	True	True	False	True		
TV	Bool	400.0	false	True	True	True	False	False		Transition is valid
TT	Bool	400.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	400.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	400.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	402.0	23	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	404.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	408.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	412.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans24	GraphTransition	416.0		True	True	True	False	True		
TV	Bool	416.0	false	True	True	True	False	False		Transition is valid
TT	Bool	416.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	416.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	416.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	418.0	24	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	420.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	424.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	428.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans25	GraphTransition	432.0		True	True	True	False	True		
TV	Bool	432.0	false	True	True	True	False	False		Transition is valid
TT	Bool	432.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	432.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	432.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	434.0	25	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	436.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	440.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	444.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans26	GraphTransition	448.0		True	True	True	False	True		

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TV	Bool	448.0	false	True	True	True	False	False		Transition is valid
TT	Bool	448.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	448.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	448.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	450.0	26	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	452.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	456.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	460.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans27	GraphTransition	464.0		True	True	True	False	True		
TV	Bool	464.0	false	True	True	True	False	False		Transition is valid
TT	Bool	464.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	464.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	464.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	466.0	27	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	468.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	472.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	476.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans28	GraphTransition	480.0		True	True	True	False	True		
TV	Bool	480.0	false	True	True	True	False	False		Transition is valid
TT	Bool	480.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	480.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	480.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	482.0	28	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	484.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	488.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	492.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans29	GraphTransition	496.0		True	True	True	False	True		
TV	Bool	496.0	false	True	True	True	False	False		Transition is valid
TT	Bool	496.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	496.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	496.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	498.0	29	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	500.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	504.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	508.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans30	GraphTransition	512.0		True	True	True	False	True		
TV	Bool	512.0	false	True	True	True	False	False		Transition is valid
TT	Bool	512.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	512.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	512.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	514.0	30	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	516.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	520.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	524.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans33	GraphTransition	528.0		True	True	True	False	True		
TV	Bool	528.0	false	True	True	True	False	False		Transition is valid
TT	Bool	528.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	528.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	528.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid

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TNO	Int	530.0	33	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	532.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	536.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	540.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans34	GraphTransition	544.0		True	True	True	False	True		
TV	Bool	544.0	false	True	True	True	False	False		Transition is valid
TT	Bool	544.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	544.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	544.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	546.0	34	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	548.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	552.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	556.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans35	GraphTransition	560.0		True	True	True	False	True		
TV	Bool	560.0	false	True	True	True	False	False		Transition is valid
TT	Bool	560.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	560.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	560.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	562.0	35	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	564.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	568.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	572.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans37	GraphTransition	576.0		True	True	True	False	True		
TV	Bool	576.0	false	True	True	True	False	False		Transition is valid
TT	Bool	576.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	576.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	576.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	578.0	37	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	580.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	584.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	588.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Step1	GraphStep	624.0		True	True	True	False	True		
S1	Bool	624.0	false	True	True	True	False	False		Step is activated
L1	Bool	624.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	624.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	624.3	false	True	True	True	False	False		Reserved
A1	Bool	624.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	624.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	624.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	624.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	625.0	false	True	True	True	False	False		Step is active
LA	Bool	625.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	625.2	false	True	True	True	False	False		Supervision active
RA	Bool	625.3	false	True	True	True	False	False		Reserved
AA	Bool	625.4	false	True	True	True	False	False		Reserved
SS	Bool	625.5	false	True	True	True	False	False		System-internal
LS	Bool	625.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	625.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	626.0	1	True	True	True	False	False		User step number
T	Time	628.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	632.0	T#0ms	True	True	True	False	False		Step activation time without disturbance

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_LOC	DWord	636.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	640.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	644.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	648.0	false	True	True	True	False	False		System-internal
LP	Bool	648.1	false	True	True	True	False	False		System-internal
LN	Bool	648.2	false	True	True	True	False	False		System-internal
VP	Bool	648.3	false	True	True	True	False	False		System-internal
VN	Bool	648.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	649.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	650.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	652.0	16#0	True	True	True	False	False		Reserve
Step2	GraphStep	656.0		True	True	True	False	True		
S1	Bool	656.0	false	True	True	True	False	False		Step is activated
L1	Bool	656.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	656.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	656.3	false	True	True	True	False	False		Reserved
A1	Bool	656.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	656.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	656.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	656.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	657.0	false	True	True	True	False	False		Step is active
LA	Bool	657.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	657.2	false	True	True	True	False	False		Supervision active
RA	Bool	657.3	false	True	True	True	False	False		Reserved
AA	Bool	657.4	false	True	True	True	False	False		Reserved
SS	Bool	657.5	false	True	True	True	False	False		System-internal
LS	Bool	657.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	657.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	658.0	2	True	True	True	False	False		User step number
T	Time	660.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	664.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	668.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	672.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	676.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	680.0	false	True	True	True	False	False		System-internal
LP	Bool	680.1	false	True	True	True	False	False		System-internal
LN	Bool	680.2	false	True	True	True	False	False		System-internal
VP	Bool	680.3	false	True	True	True	False	False		System-internal
VN	Bool	680.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	681.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	682.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	684.0	16#0	True	True	True	False	False		Reserve
Step3	GraphStep	688.0		True	True	True	False	True		
S1	Bool	688.0	false	True	True	True	False	False		Step is activated
L1	Bool	688.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	688.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	688.3	false	True	True	True	False	False		Reserved
A1	Bool	688.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	688.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	688.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	688.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	689.0	false	True	True	True	False	False		Step is active
LA	Bool	689.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	689.2	false	True	True	True	False	False		Supervision active
RA	Bool	689.3	false	True	True	True	False	False		Reserved
AA	Bool	689.4	false	True	True	True	False	False		Reserved
SS	Bool	689.5	false	True	True	True	False	False		System-internal
LS	Bool	689.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	689.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	690.0	3	True	True	True	False	False		User step number
T	Time	692.0	T#0ms	True	True	True	False	False		Total step activation time

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
U	Time	696.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	700.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	704.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	708.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	712.0	false	True	True	True	False	False		System-internal
LP	Bool	712.1	false	True	True	True	False	False		System-internal
LN	Bool	712.2	false	True	True	True	False	False		System-internal
VP	Bool	712.3	false	True	True	True	False	False		System-internal
VN	Bool	712.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	713.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	714.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	716.0	16#0	True	True	True	False	False		Reserve
Step4	GraphStep	720.0		True	True	True	False	True		
S1	Bool	720.0	false	True	True	True	False	False		Step is activated
L1	Bool	720.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	720.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	720.3	false	True	True	True	False	False		Reserved
A1	Bool	720.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	720.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	720.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	720.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	721.0	false	True	True	True	False	False		Step is active
LA	Bool	721.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	721.2	false	True	True	True	False	False		Supervision active
RA	Bool	721.3	false	True	True	True	False	False		Reserved
AA	Bool	721.4	false	True	True	True	False	False		Reserved
SS	Bool	721.5	false	True	True	True	False	False		System-internal
LS	Bool	721.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	721.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	722.0	4	True	True	True	False	False		User step number
T	Time	724.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	728.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	732.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	736.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	740.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	744.0	false	True	True	True	False	False		System-internal
LP	Bool	744.1	false	True	True	True	False	False		System-internal
LN	Bool	744.2	false	True	True	True	False	False		System-internal
VP	Bool	744.3	false	True	True	True	False	False		System-internal
VN	Bool	744.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	745.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	746.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	748.0	16#0	True	True	True	False	False		Reserve
Step5	GraphStep	752.0		True	True	True	False	True		
S1	Bool	752.0	false	True	True	True	False	False		Step is activated
L1	Bool	752.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	752.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	752.3	false	True	True	True	False	False		Reserved
A1	Bool	752.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	752.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	752.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	752.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	753.0	false	True	True	True	False	False		Step is active
LA	Bool	753.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	753.2	false	True	True	True	False	False		Supervision active
RA	Bool	753.3	false	True	True	True	False	False		Reserved
AA	Bool	753.4	false	True	True	True	False	False		Reserved
SS	Bool	753.5	false	True	True	True	False	False		System-internal
LS	Bool	753.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	753.7	false	True	True	True	False	False		Direct result of the programmed supervision

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
SNO	Int	754.0	5	True	True	True	False	False		User step number
T	Time	756.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	760.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	764.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	768.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	772.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	776.0	false	True	True	True	False	False		System-internal
LP	Bool	776.1	false	True	True	True	False	False		System-internal
LN	Bool	776.2	false	True	True	True	False	False		System-internal
VP	Bool	776.3	false	True	True	True	False	False		System-internal
VN	Bool	776.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	777.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	778.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	780.0	16#0	True	True	True	False	False		Reserve
Step6	GraphStep	784.0		True	True	True	False	True		
S1	Bool	784.0	false	True	True	True	False	False		Step is activated
L1	Bool	784.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	784.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	784.3	false	True	True	True	False	False		Reserved
A1	Bool	784.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	784.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	784.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	784.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	785.0	false	True	True	True	False	False		Step is active
LA	Bool	785.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	785.2	false	True	True	True	False	False		Supervision active
RA	Bool	785.3	false	True	True	True	False	False		Reserved
AA	Bool	785.4	false	True	True	True	False	False		Reserved
SS	Bool	785.5	false	True	True	True	False	False		System-internal
LS	Bool	785.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	785.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	786.0	6	True	True	True	False	False		User step number
T	Time	788.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	792.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	796.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	800.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	804.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	808.0	false	True	True	True	False	False		System-internal
LP	Bool	808.1	false	True	True	True	False	False		System-internal
LN	Bool	808.2	false	True	True	True	False	False		System-internal
VP	Bool	808.3	false	True	True	True	False	False		System-internal
VN	Bool	808.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	809.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	810.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	812.0	16#0	True	True	True	False	False		Reserve
Step7	GraphStep	816.0		True	True	True	False	True		
S1	Bool	816.0	false	True	True	True	False	False		Step is activated
L1	Bool	816.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	816.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	816.3	false	True	True	True	False	False		Reserved
A1	Bool	816.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	816.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	816.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	816.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	817.0	false	True	True	True	False	False		Step is active
LA	Bool	817.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	817.2	false	True	True	True	False	False		Supervision active
RA	Bool	817.3	false	True	True	True	False	False		Reserved
AA	Bool	817.4	false	True	True	True	False	False		Reserved
SS	Bool	817.5	false	True	True	True	False	False		System-internal
LS	Bool	817.6	true	True	True	True	False	False		Direct result of the programmed interlock

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
VS	Bool	817.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	818.0	7	True	True	True	False	False		User step number
T	Time	820.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	824.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	828.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	832.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	836.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	840.0	false	True	True	True	False	False		System-internal
LP	Bool	840.1	false	True	True	True	False	False		System-internal
LN	Bool	840.2	false	True	True	True	False	False		System-internal
VP	Bool	840.3	false	True	True	True	False	False		System-internal
VN	Bool	840.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	841.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	842.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	844.0	16#0	True	True	True	False	False		Reserve
Step8	GraphStep	848.0		True	True	True	False	True		
S1	Bool	848.0	false	True	True	True	False	False		Step is activated
L1	Bool	848.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	848.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	848.3	false	True	True	True	False	False		Reserved
A1	Bool	848.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	848.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	848.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	848.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	849.0	false	True	True	True	False	False		Step is active
LA	Bool	849.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	849.2	false	True	True	True	False	False		Supervision active
RA	Bool	849.3	false	True	True	True	False	False		Reserved
AA	Bool	849.4	false	True	True	True	False	False		Reserved
SS	Bool	849.5	false	True	True	True	False	False		System-internal
LS	Bool	849.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	849.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	850.0	8	True	True	True	False	False		User step number
T	Time	852.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	856.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	860.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	864.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	868.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	872.0	false	True	True	True	False	False		System-internal
LP	Bool	872.1	false	True	True	True	False	False		System-internal
LN	Bool	872.2	false	True	True	True	False	False		System-internal
VP	Bool	872.3	false	True	True	True	False	False		System-internal
VN	Bool	872.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	873.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	874.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	876.0	16#0	True	True	True	False	False		Reserve
Step9	GraphStep	880.0		True	True	True	False	True		
S1	Bool	880.0	false	True	True	True	False	False		Step is activated
L1	Bool	880.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	880.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	880.3	false	True	True	True	False	False		Reserved
A1	Bool	880.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	880.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	880.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	880.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	881.0	false	True	True	True	False	False		Step is active
LA	Bool	881.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	881.2	false	True	True	True	False	False		Supervision active
RA	Bool	881.3	false	True	True	True	False	False		Reserved
AA	Bool	881.4	false	True	True	True	False	False		Reserved
SS	Bool	881.5	false	True	True	True	False	False		System-internal

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision				Comment
LS	Bool	881.6	true	True	True	True	False	False					Direct result of the programmed interlock
VS	Bool	881.7	false	True	True	True	False	False					Direct result of the programmed supervision
SNO	Int	882.0	9	True	True	True	False	False					User step number
T	Time	884.0	T#0ms	True	True	True	False	False					Total step activation time
U	Time	888.0	T#0ms	True	True	True	False	False					Step activation time without disturbance
CRIT_LOC	DWord	892.0	16#0	True	True	True	False	False					Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	896.0	16#0	True	True	True	False	False					Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	900.0	16#0	True	True	True	False	False					Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	904.0	false	True	True	True	False	False					System-internal
LP	Bool	904.1	false	True	True	True	False	False					System-internal
LN	Bool	904.2	false	True	True	True	False	False					System-internal
VP	Bool	904.3	false	True	True	True	False	False					System-internal
VN	Bool	904.4	false	True	True	True	False	False					System-internal
H_IL_ERR	Byte	905.0	16#0	True	True	True	False	False					System-internal
H_SV_FLT	Byte	906.0	16#0	True	True	True	False	False					System-internal
RESERVED	DWord	908.0	16#0	True	True	True	False	False					Reserve
▼ Step10	GraphStep	912.0		True	True	True	False	True					
S1	Bool	912.0	false	True	True	True	False	False					Step is activated
L1	Bool	912.1	false	True	True	True	False	False					interlock leaving state
V1	Bool	912.2	false	True	True	True	False	False					Supervision entering state
R1	Bool	912.3	false	True	True	True	False	False					Reserved
A1	Bool	912.4	false	True	True	True	False	False					Error is acknowledged
S0	Bool	912.5	false	True	True	True	False	False					Step is deactivated
L0	Bool	912.6	false	True	True	True	False	False					Interlock entering state
V0	Bool	912.7	false	True	True	True	False	False					Supervision leaving state
X	Bool	913.0	false	True	True	True	False	False					Step is active
LA	Bool	913.1	false	True	True	True	False	False					Interlock is not satisfied
VA	Bool	913.2	false	True	True	True	False	False					Supervision active
RA	Bool	913.3	false	True	True	True	False	False					Reserved
AA	Bool	913.4	false	True	True	True	False	False					Reserved
SS	Bool	913.5	false	True	True	True	False	False					System-internal
LS	Bool	913.6	true	True	True	True	False	False					Direct result of the programmed interlock
VS	Bool	913.7	false	True	True	True	False	False					Direct result of the programmed supervision
SNO	Int	914.0	10	True	True	True	False	False					User step number
T	Time	916.0	T#0ms	True	True	True	False	False					Total step activation time
U	Time	920.0	T#0ms	True	True	True	False	False					Step activation time without disturbance
CRIT_LOC	DWord	924.0	16#0	True	True	True	False	False					Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	928.0	16#0	True	True	True	False	False					Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	932.0	16#0	True	True	True	False	False					Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	936.0	false	True	True	True	False	False					System-internal
LP	Bool	936.1	false	True	True	True	False	False					System-internal
LN	Bool	936.2	false	True	True	True	False	False					System-internal
VP	Bool	936.3	false	True	True	True	False	False					System-internal
VN	Bool	936.4	false	True	True	True	False	False					System-internal
H_IL_ERR	Byte	937.0	16#0	True	True	True	False	False					System-internal
H_SV_FLT	Byte	938.0	16#0	True	True	True	False	False					System-internal
RESERVED	DWord	940.0	16#0	True	True	True	False	False					Reserve
▼ Step11	GraphStep	944.0		True	True	True	False	True					
S1	Bool	944.0	false	True	True	True	False	False					Step is activated
L1	Bool	944.1	false	True	True	True	False	False					interlock leaving state
V1	Bool	944.2	false	True	True	True	False	False					Supervision entering state
R1	Bool	944.3	false	True	True	True	False	False					Reserved
A1	Bool	944.4	false	True	True	True	False	False					Error is acknowledged
S0	Bool	944.5	false	True	True	True	False	False					Step is deactivated
L0	Bool	944.6	false	True	True	True	False	False					Interlock entering state
V0	Bool	944.7	false	True	True	True	False	False					Supervision leaving state
X	Bool	945.0	false	True	True	True	False	False					Step is active
LA	Bool	945.1	false	True	True	True	False	False					Interlock is not satisfied
VA	Bool	945.2	false	True	True	True	False	False					Supervision active
RA	Bool	945.3	false	True	True	True	False	False					Reserved

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
AA	Bool	945.4	false	True	True	True	False	False		Reserved
SS	Bool	945.5	false	True	True	True	False	False		System-internal
LS	Bool	945.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	945.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	946.0	11	True	True	True	False	False		User step number
T	Time	948.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	952.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	956.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	960.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	964.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	968.0	false	True	True	True	False	False		System-internal
LP	Bool	968.1	false	True	True	True	False	False		System-internal
LN	Bool	968.2	false	True	True	True	False	False		System-internal
VP	Bool	968.3	false	True	True	True	False	False		System-internal
VN	Bool	968.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	969.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	970.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	972.0	16#0	True	True	True	False	False		Reserve
Step12	GraphStep	976.0		True	True	True	False	True		
S1	Bool	976.0	false	True	True	True	False	False		Step is activated
L1	Bool	976.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	976.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	976.3	false	True	True	True	False	False		Reserved
A1	Bool	976.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	976.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	976.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	976.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	977.0	false	True	True	True	False	False		Step is active
LA	Bool	977.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	977.2	false	True	True	True	False	False		Supervision active
RA	Bool	977.3	false	True	True	True	False	False		Reserved
AA	Bool	977.4	false	True	True	True	False	False		Reserved
SS	Bool	977.5	false	True	True	True	False	False		System-internal
LS	Bool	977.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	977.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	978.0	12	True	True	True	False	False		User step number
T	Time	980.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	984.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	988.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	992.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	996.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1000.0	false	True	True	True	False	False		System-internal
LP	Bool	1000.1	false	True	True	True	False	False		System-internal
LN	Bool	1000.2	false	True	True	True	False	False		System-internal
VP	Bool	1000.3	false	True	True	True	False	False		System-internal
VN	Bool	1000.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1001.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1002.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1004.0	16#0	True	True	True	False	False		Reserve
Step13	GraphStep	1008.0		True	True	True	False	True		
S1	Bool	1008.0	false	True	True	True	False	False		Step is activated
L1	Bool	1008.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1008.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1008.3	false	True	True	True	False	False		Reserved
A1	Bool	1008.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1008.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1008.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1008.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1009.0	false	True	True	True	False	False		Step is active
LA	Bool	1009.1	false	True	True	True	False	False		Interlock is not satisfied

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
VA	Bool	1009.2	false	True	True	True	False	False		Supervision active
RA	Bool	1009.3	false	True	True	True	False	False		Reserved
AA	Bool	1009.4	false	True	True	True	False	False		Reserved
SS	Bool	1009.5	false	True	True	True	False	False		System-internal
LS	Bool	1009.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1009.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1010.0	13	True	True	True	False	False		User step number
T	Time	1012.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1016.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1020.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1024.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1028.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1032.0	false	True	True	True	False	False		System-internal
LP	Bool	1032.1	false	True	True	True	False	False		System-internal
LN	Bool	1032.2	false	True	True	True	False	False		System-internal
VP	Bool	1032.3	false	True	True	True	False	False		System-internal
VN	Bool	1032.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1033.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1034.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1036.0	16#0	True	True	True	False	False		Reserve
▼ Step15	GraphStep	1040.0		True	True	True	False	True		
S1	Bool	1040.0	false	True	True	True	False	False		Step is activated
L1	Bool	1040.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1040.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1040.3	false	True	True	True	False	False		Reserved
A1	Bool	1040.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1040.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1040.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1040.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1041.0	false	True	True	True	False	False		Step is active
LA	Bool	1041.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1041.2	false	True	True	True	False	False		Supervision active
RA	Bool	1041.3	false	True	True	True	False	False		Reserved
AA	Bool	1041.4	false	True	True	True	False	False		Reserved
SS	Bool	1041.5	false	True	True	True	False	False		System-internal
LS	Bool	1041.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1041.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1042.0	15	True	True	True	False	False		User step number
T	Time	1044.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1048.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1052.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1056.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1060.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1064.0	false	True	True	True	False	False		System-internal
LP	Bool	1064.1	false	True	True	True	False	False		System-internal
LN	Bool	1064.2	false	True	True	True	False	False		System-internal
VP	Bool	1064.3	false	True	True	True	False	False		System-internal
VN	Bool	1064.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1065.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1066.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1068.0	16#0	True	True	True	False	False		Reserve
▼ Step16	GraphStep	1072.0		True	True	True	False	True		
S1	Bool	1072.0	false	True	True	True	False	False		Step is activated
L1	Bool	1072.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1072.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1072.3	false	True	True	True	False	False		Reserved
A1	Bool	1072.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1072.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1072.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1072.7	false	True	True	True	False	False		Supervision leaving state

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
X	Bool	1073.0	false	True	True	True	False	False		Step is active
LA	Bool	1073.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1073.2	false	True	True	True	False	False		Supervision active
RA	Bool	1073.3	false	True	True	True	False	False		Reserved
AA	Bool	1073.4	false	True	True	True	False	False		Reserved
SS	Bool	1073.5	false	True	True	True	False	False		System-internal
LS	Bool	1073.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1073.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1074.0	16	True	True	True	False	False		User step number
T	Time	1076.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1080.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1084.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1088.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1092.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1096.0	false	True	True	True	False	False		System-internal
LP	Bool	1096.1	false	True	True	True	False	False		System-internal
LN	Bool	1096.2	false	True	True	True	False	False		System-internal
VP	Bool	1096.3	false	True	True	True	False	False		System-internal
VN	Bool	1096.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1097.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1098.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1100.0	16#0	True	True	True	False	False		Reserve
▼ Step17	GraphStep	1104.0		True	True	True	False	True		
S1	Bool	1104.0	false	True	True	True	False	False		Step is activated
L1	Bool	1104.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1104.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1104.3	false	True	True	True	False	False		Reserved
A1	Bool	1104.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1104.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1104.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1104.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1105.0	false	True	True	True	False	False		Step is active
LA	Bool	1105.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1105.2	false	True	True	True	False	False		Supervision active
RA	Bool	1105.3	false	True	True	True	False	False		Reserved
AA	Bool	1105.4	false	True	True	True	False	False		Reserved
SS	Bool	1105.5	false	True	True	True	False	False		System-internal
LS	Bool	1105.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1105.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1106.0	17	True	True	True	False	False		User step number
T	Time	1108.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1112.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1116.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1120.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1124.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1128.0	false	True	True	True	False	False		System-internal
LP	Bool	1128.1	false	True	True	True	False	False		System-internal
LN	Bool	1128.2	false	True	True	True	False	False		System-internal
VP	Bool	1128.3	false	True	True	True	False	False		System-internal
VN	Bool	1128.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1129.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1130.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1132.0	16#0	True	True	True	False	False		Reserve
▼ Step18	GraphStep	1136.0		True	True	True	False	True		
S1	Bool	1136.0	false	True	True	True	False	False		Step is activated
L1	Bool	1136.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1136.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1136.3	false	True	True	True	False	False		Reserved
A1	Bool	1136.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1136.5	false	True	True	True	False	False		Step is deactivated

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
L0	Bool	1136.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1136.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1137.0	false	True	True	True	False	False		Step is active
LA	Bool	1137.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1137.2	false	True	True	True	False	False		Supervision active
RA	Bool	1137.3	false	True	True	True	False	False		Reserved
AA	Bool	1137.4	false	True	True	True	False	False		Reserved
SS	Bool	1137.5	false	True	True	True	False	False		System-internal
LS	Bool	1137.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1137.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1138.0	18	True	True	True	False	False		User step number
T	Time	1140.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1144.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1148.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1152.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1156.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1160.0	false	True	True	True	False	False		System-internal
LP	Bool	1160.1	false	True	True	True	False	False		System-internal
LN	Bool	1160.2	false	True	True	True	False	False		System-internal
VP	Bool	1160.3	false	True	True	True	False	False		System-internal
VN	Bool	1160.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1161.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1162.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1164.0	16#0	True	True	True	False	False		Reserve
▼ Step19	GraphStep	1168.0		True	True	True	False	True		
S1	Bool	1168.0	false	True	True	True	False	False		Step is activated
L1	Bool	1168.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1168.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1168.3	false	True	True	True	False	False		Reserved
A1	Bool	1168.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1168.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1168.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1168.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1169.0	false	True	True	True	False	False		Step is active
LA	Bool	1169.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1169.2	false	True	True	True	False	False		Supervision active
RA	Bool	1169.3	false	True	True	True	False	False		Reserved
AA	Bool	1169.4	false	True	True	True	False	False		Reserved
SS	Bool	1169.5	false	True	True	True	False	False		System-internal
LS	Bool	1169.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1169.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1170.0	19	True	True	True	False	False		User step number
T	Time	1172.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1176.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1180.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1184.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1188.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1192.0	false	True	True	True	False	False		System-internal
LP	Bool	1192.1	false	True	True	True	False	False		System-internal
LN	Bool	1192.2	false	True	True	True	False	False		System-internal
VP	Bool	1192.3	false	True	True	True	False	False		System-internal
VN	Bool	1192.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1193.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1194.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1196.0	16#0	True	True	True	False	False		Reserve
▼ Step20	GraphStep	1200.0		True	True	True	False	True		
S1	Bool	1200.0	false	True	True	True	False	False		Step is activated
L1	Bool	1200.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1200.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1200.3	false	True	True	True	False	False		Reserved

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
A1	Bool	1200.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1200.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1200.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1200.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1201.0	false	True	True	True	False	False		Step is active
LA	Bool	1201.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1201.2	false	True	True	True	False	False		Supervision active
RA	Bool	1201.3	false	True	True	True	False	False		Reserved
AA	Bool	1201.4	false	True	True	True	False	False		Reserved
SS	Bool	1201.5	false	True	True	True	False	False		System-internal
LS	Bool	1201.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1201.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1202.0	20	True	True	True	False	False		User step number
T	Time	1204.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1208.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1212.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1216.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1220.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1224.0	false	True	True	True	False	False		System-internal
LP	Bool	1224.1	false	True	True	True	False	False		System-internal
LN	Bool	1224.2	false	True	True	True	False	False		System-internal
VP	Bool	1224.3	false	True	True	True	False	False		System-internal
VN	Bool	1224.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1225.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1226.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1228.0	16#0	True	True	True	False	False		Reserve
▼ Step21	GraphStep	1232.0		True	True	True	False	True		
S1	Bool	1232.0	false	True	True	True	False	False		Step is activated
L1	Bool	1232.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1232.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1232.3	false	True	True	True	False	False		Reserved
A1	Bool	1232.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1232.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1232.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1232.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1233.0	false	True	True	True	False	False		Step is active
LA	Bool	1233.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1233.2	false	True	True	True	False	False		Supervision active
RA	Bool	1233.3	false	True	True	True	False	False		Reserved
AA	Bool	1233.4	false	True	True	True	False	False		Reserved
SS	Bool	1233.5	false	True	True	True	False	False		System-internal
LS	Bool	1233.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1233.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1234.0	21	True	True	True	False	False		User step number
T	Time	1236.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1240.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1244.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1248.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1252.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1256.0	false	True	True	True	False	False		System-internal
LP	Bool	1256.1	false	True	True	True	False	False		System-internal
LN	Bool	1256.2	false	True	True	True	False	False		System-internal
VP	Bool	1256.3	false	True	True	True	False	False		System-internal
VN	Bool	1256.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1257.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1258.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1260.0	16#0	True	True	True	False	False		Reserve
▼ Step22	GraphStep	1264.0		True	True	True	False	True		
S1	Bool	1264.0	false	True	True	True	False	False		Step is activated
L1	Bool	1264.1	false	True	True	True	False	False		interlock leaving state

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
V1	Bool	1264.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1264.3	false	True	True	True	False	False		Reserved
A1	Bool	1264.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1264.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1264.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1264.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1265.0	false	True	True	True	False	False		Step is active
LA	Bool	1265.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1265.2	false	True	True	True	False	False		Supervision active
RA	Bool	1265.3	false	True	True	True	False	False		Reserved
AA	Bool	1265.4	false	True	True	True	False	False		Reserved
SS	Bool	1265.5	false	True	True	True	False	False		System-internal
LS	Bool	1265.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1265.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1266.0	22	True	True	True	False	False		User step number
T	Time	1268.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1272.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1276.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1280.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1284.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1288.0	false	True	True	True	False	False		System-internal
LP	Bool	1288.1	false	True	True	True	False	False		System-internal
LN	Bool	1288.2	false	True	True	True	False	False		System-internal
VP	Bool	1288.3	false	True	True	True	False	False		System-internal
VN	Bool	1288.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1289.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1290.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1292.0	16#0	True	True	True	False	False		Reserve
▼ Step23	GraphStep	1296.0		True	True	True	False	True		
S1	Bool	1296.0	false	True	True	True	False	False		Step is activated
L1	Bool	1296.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1296.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1296.3	false	True	True	True	False	False		Reserved
A1	Bool	1296.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1296.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1296.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1296.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1297.0	false	True	True	True	False	False		Step is active
LA	Bool	1297.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1297.2	false	True	True	True	False	False		Supervision active
RA	Bool	1297.3	false	True	True	True	False	False		Reserved
AA	Bool	1297.4	false	True	True	True	False	False		Reserved
SS	Bool	1297.5	false	True	True	True	False	False		System-internal
LS	Bool	1297.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1297.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1298.0	23	True	True	True	False	False		User step number
T	Time	1300.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1304.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1308.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1312.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1316.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1320.0	false	True	True	True	False	False		System-internal
LP	Bool	1320.1	false	True	True	True	False	False		System-internal
LN	Bool	1320.2	false	True	True	True	False	False		System-internal
VP	Bool	1320.3	false	True	True	True	False	False		System-internal
VN	Bool	1320.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1321.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1322.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1324.0	16#0	True	True	True	False	False		Reserve
▼ Step24	GraphStep	1328.0		True	True	True	False	True		

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
S1	Bool	1328.0	false	True	True	True	False	False		Step is activated
L1	Bool	1328.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1328.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1328.3	false	True	True	True	False	False		Reserved
A1	Bool	1328.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1328.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1328.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1328.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1329.0	false	True	True	True	False	False		Step is active
LA	Bool	1329.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1329.2	false	True	True	True	False	False		Supervision active
RA	Bool	1329.3	false	True	True	True	False	False		Reserved
AA	Bool	1329.4	false	True	True	True	False	False		Reserved
SS	Bool	1329.5	false	True	True	True	False	False		System-internal
LS	Bool	1329.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1329.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1330.0	24	True	True	True	False	False		User step number
T	Time	1332.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1336.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1340.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1344.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1348.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1352.0	false	True	True	True	False	False		System-internal
LP	Bool	1352.1	false	True	True	True	False	False		System-internal
LN	Bool	1352.2	false	True	True	True	False	False		System-internal
VP	Bool	1352.3	false	True	True	True	False	False		System-internal
VN	Bool	1352.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1353.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1354.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1356.0	16#0	True	True	True	False	False		Reserve
▼ Step26	GraphStep	1360.0		True	True	True	False	True		
S1	Bool	1360.0	false	True	True	True	False	False		Step is activated
L1	Bool	1360.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1360.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1360.3	false	True	True	True	False	False		Reserved
A1	Bool	1360.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1360.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1360.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1360.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1361.0	false	True	True	True	False	False		Step is active
LA	Bool	1361.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1361.2	false	True	True	True	False	False		Supervision active
RA	Bool	1361.3	false	True	True	True	False	False		Reserved
AA	Bool	1361.4	false	True	True	True	False	False		Reserved
SS	Bool	1361.5	false	True	True	True	False	False		System-internal
LS	Bool	1361.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1361.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1362.0	26	True	True	True	False	False		User step number
T	Time	1364.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1368.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1372.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1376.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1380.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1384.0	false	True	True	True	False	False		System-internal
LP	Bool	1384.1	false	True	True	True	False	False		System-internal
LN	Bool	1384.2	false	True	True	True	False	False		System-internal
VP	Bool	1384.3	false	True	True	True	False	False		System-internal
VN	Bool	1384.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1385.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1386.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1388.0	16#0	True	True	True	False	False		Reserve

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
Step27	GraphStep	1392.0		True	True	True	False	True		
S1	Bool	1392.0	false	True	True	True	False	False		Step is activated
L1	Bool	1392.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1392.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1392.3	false	True	True	True	False	False		Reserved
A1	Bool	1392.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1392.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1392.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1392.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1393.0	false	True	True	True	False	False		Step is active
LA	Bool	1393.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1393.2	false	True	True	True	False	False		Supervision active
RA	Bool	1393.3	false	True	True	True	False	False		Reserved
AA	Bool	1393.4	false	True	True	True	False	False		Reserved
SS	Bool	1393.5	false	True	True	True	False	False		System-internal
LS	Bool	1393.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1393.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1394.0	27	True	True	True	False	False		User step number
T	Time	1396.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1400.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1404.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1408.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1412.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1416.0	false	True	True	True	False	False		System-internal
LP	Bool	1416.1	false	True	True	True	False	False		System-internal
LN	Bool	1416.2	false	True	True	True	False	False		System-internal
VP	Bool	1416.3	false	True	True	True	False	False		System-internal
VN	Bool	1416.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1417.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1418.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1420.0	16#0	True	True	True	False	False		Reserve
Step28	GraphStep	1424.0		True	True	True	False	True		
S1	Bool	1424.0	false	True	True	True	False	False		Step is activated
L1	Bool	1424.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1424.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1424.3	false	True	True	True	False	False		Reserved
A1	Bool	1424.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1424.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1424.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1424.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1425.0	false	True	True	True	False	False		Step is active
LA	Bool	1425.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1425.2	false	True	True	True	False	False		Supervision active
RA	Bool	1425.3	false	True	True	True	False	False		Reserved
AA	Bool	1425.4	false	True	True	True	False	False		Reserved
SS	Bool	1425.5	false	True	True	True	False	False		System-internal
LS	Bool	1425.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1425.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1426.0	28	True	True	True	False	False		User step number
T	Time	1428.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1432.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1436.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1440.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1444.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1448.0	false	True	True	True	False	False		System-internal
LP	Bool	1448.1	false	True	True	True	False	False		System-internal
LN	Bool	1448.2	false	True	True	True	False	False		System-internal
VP	Bool	1448.3	false	True	True	True	False	False		System-internal
VN	Bool	1448.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1449.0	16#0	True	True	True	False	False		System-internal

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
H_SV_FLT	Byte	1450.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1452.0	16#0	True	True	True	False	False		Reserve
S_DISPLAY	Int	1642.0	0	True	True	True	False	False		Affichage interne du paramètre de sortie S_NO
S_SEL_OLD	Int	1644.0	0	True	True	True	False	False		Valeur précédente dans S_SEL
S_DISPIDX	Byte	1646.0	16#0	True	True	True	False	False		Indice de l'étape affichée dans S_NO
T_DISPIDX	Byte	1647.0	16#0	True	True	True	False	False		Indice de la transition affichée dans T_NO
MOP	Struct	1652.0		True	True	True	False	False		Mode de fonctionnement
AUTO	Bool	1652.0	true	True	True	True	False	False		Etat : Mode automatique
MAN	Bool	1652.1	false	True	True	True	False	False		Etat : Mode manuel
TAP	Bool	1652.2	false	True	True	True	False	False		Etat : Mode semi-automatique/Activation de l'étape suivante avec transition
TOP	Bool	1652.3	false	True	True	True	False	False		Etat : Mode semi-automatique/Ignorer la transition
ACK_S	Bool	1652.4	false	True	True	True	False	False		Requête : Acquitter l'étape figurant dans le paramètre S_NO
REG_S	Bool	1652.5	false	True	True	True	False	False		Requête : Enregistrer l'étape figurant dans le paramètre S_NO
T_PREV	Bool	1652.6	false	True	True	True	False	False		Requête : Afficher la transition valide précédente dans le paramètre T_NO
T_NEXT	Bool	1652.7	false	True	True	True	False	False		Requête : Afficher la transition valide suivante dans le paramètre T_NO
LOCK	Bool	1653.0	true	True	True	True	False	False		Etat : Traitement des Interlocks activé
SUP	Bool	1653.1	true	True	True	True	False	False		Etat : Traitement de Supervision activé
ACKREQ	Bool	1653.2	true	True	True	True	False	False		Etat : Acquittement requis
SSKIP	Bool	1653.3	false	True	True	True	False	False		Etat : "Ignorer les étapes" activé
OFF	Bool	1653.4	false	True	True	True	False	False		Requête : Désactiver toutes les étapes
INIT	Bool	1653.5	true	True	True	True	False	False		Requête : Mettre le graphe à l'état initial
HALT	Bool	1653.6	false	True	True	True	False	False		Etat : Le graphe est arrêté
TMS_HALT	Bool	1653.7	false	True	True	True	False	False		Etat : Toutes les temporisations internes sont arrêtées
OPS_ZERO	Bool	1654.0	false	True	True	True	False	False		Etat : Tous les opérandes traités avec N, L, D mis à 0
SACT_DISP	Bool	1654.1	true	True	True	True	False	False		Etat : N'afficher que les étapes actives
SEF_DISP	Bool	1654.2	false	True	True	True	False	False		Etat : Afficher uniquement les étapes avec erreurs et perturbations
SALL_DISP	Bool	1654.3	false	True	True	True	False	False		Etat : Afficher toutes les étapes
S_PREV	Bool	1654.4	false	True	True	True	False	False		Requête : Afficher l'étape précédente dans le paramètre S_NO
S_NEXT	Bool	1654.5	false	True	True	True	False	False		Requête : Afficher l'étape suivante dans le paramètre S_NO
S_SELOK	Bool	1654.6	false	True	True	True	False	False		Requête : Afficher dans le paramètre S_NO le numéro d'étape figurant dans S_SEL
S_ON	Bool	1654.7	false	True	True	True	False	False		Requête : Activer l'étape figurant dans le paramètre S_NO
S_OFF	Bool	1655.0	false	True	True	True	False	False		Requête : Désactiver l'étape figurant dans le paramètre S_NO
T_PUSH	Bool	1655.1	false	True	True	True	False	False		Requête : Validation de commutation pour transition
REG	Bool	1655.2	false	True	True	True	False	False		Requête : Enregistrer toutes les erreurs Interlock et Supervision
ACK	Bool	1655.3	false	True	True	True	False	False		Requête : Acquitter toutes les erreurs Interlock et Supervision
IL_PERM	Bool	1655.4	false	True	True	True	False	False		Etat : Traitement permanent de tous les Interlocks
T_PERM	Bool	1655.5	false	True	True	True	False	False		Etat : Traitement permanent de toutes les transitions
ILP_MAN	Bool	1655.6	false	True	True	True	False	False		Etat : Traitement permanent de tous les Interlocks en mode manuel
TICKS	Struct	1656.0		True	True	True	False	False		Périodes d'horloge
DELTA	Time	1656.0	T#0ms	True	True	True	False	False		Différence de temps entre deux cycles
OLD	Time	1660.0	T#0ms	True	True	True	False	False		Valeur du compteur de périodes 10 ms au dernier cycle
NEW	Time	1664.0	T#0ms	True	True	True	False	False		Valeur du compteur de périodes 10 ms dans ce cycle
SQ_FLAGS	Struct	1668.0		True	True	True	False	False		Mémentos de graphe
ERR_FLT	Bool	1668.0	false	True	True	True	False	False		Erreur groupée Interlock et Supervision
ERROR	Bool	1668.1	false	True	True	True	False	False		Erreur groupée Interlock

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
FAULT	Bool	1668.2	false	True	True	True	False	False		Erreur groupée Supervision
RT_FAIL	Bool	1668.3	false	True	True	True	False	False		Erreur à l'exécution
NO_SNO	Bool	1668.4	false	True	True	True	False	False		Numéro d'étape requis introuvable
NF_OFL	Bool	1668.5	false	True	True	True	False	False		Débordement : Trop de requêtes ON ou OFF
SA_OFL	Bool	1668.6	false	True	True	True	False	False		Débordement : Trop d'étapes activées
TV_OFL	Bool	1668.7	false	True	True	True	False	False		Débordement : Trop de transitions valides
MSG_OFL	Bool	1669.0	false	True	True	True	False	False		Débordement : Pas assez de ressources système pour ALARM_S
NO_SWI	Bool	1669.1	false	True	True	True	False	False		Pas de commutation dans ce cycle
CYC_OP	Bool	1669.2	false	True	True	True	False	False		Exécution cyclique du graphe après l'initialisation
AS_MSG	Bool	1669.3	true	True	True	True	False	False		Messages à l'exécution activés ou désactivés par instruction
AS_SEND	Bool	1669.4	false	True	True	True	False	False		Envoyer les messages de WR_USMSG ou les entrer uniquement dans le tampon de diagnostic
SQ_BUSY	Bool	1669.5	false	True	True	True	False	False		Mémento de front interne pour le traitement séquentiel
SA_BUSY	Bool	1669.6	false	True	True	True	False	False		Mémento de front interne pour le traitement séquentiel
AS_SIG	Bool	1669.7	false	True	True	True	False	False		Mémentos de front pour messages de Alarm_S et Alarm_SQ

electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / Program blocks

HMI [DB5]

HMI Properties							
General							
Name	HMI	**Number**	5	**Type**	DB	**Language**	DB
Numbering	Automatic						
Information							
Title		**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
Static										
START	Bool	0.0	false	True	True	True	True	False		
HYPOCHLORITE SYSTEM START	Bool	0.1	false	True	True	True	True	False		
SELECT COUNTINOUS PUMP 1	Bool	0.2	false	True	True	True	True	False		
SELECT COUNTINOUS PUMP2	Bool	0.3	false	True	True	True	True	False		
SELECT SHOCK PUMP 1	Bool	0.4	false	True	True	True	True	False		
SELECT SHOCK PUMP 2	Bool	0.5	false	True	True	True	True	False		
STOP	Bool	0.6	false	True	True	True	True	False		
AUTO	Bool	0.7	false	True	True	True	True	False		
seawater pump 1 to elech-station started	Bool	1.0	false	True	True	True	True	False		
seawater pump 2 to elech-station started	Bool	1.1	false	True	True	True	True	False		
select acid pump 1	Bool	1.2	false	True	True	True	True	False		
seawater pump intake 1 started	Bool	1.3	false	True	True	True	True	False		
seawater pump intake 2 started	Bool	1.4	false	True	True	True	True	False		
select acid pump 2	Bool	1.5	false	True	True	True	True	False		
valve to tank opened	Bool	1.6	false	True	True	True	True	False		
valve to tank opened manually	Bool	1.7	false	True	True	True	True	False		
Acid Cleaning pump 1 started	Bool	2.0	false	True	True	True	True	False		
Acid Cleaning pump 2 started	Bool	2.1	false	True	True	True	True	False		
countinous injection pump 1 started	Bool	2.2	false	True	True	True	True	False		
countinous injection pump 2 started	Bool	2.3	false	True	True	True	True	False		
select booster pump 2	Bool	2.4	false	True	True	True	True	False		
select booster pump 1	Bool	2.5	false	True	True	True	True	False		
shocked injection pump 1 started	Bool	2.6	false	True	True	True	True	False		
shocked injection pump 2 started	Bool	2.7	false	True	True	True	True	False		
shocked injection pumps repair	Bool	3.0	false	True	True	True	True	False		
valve before filter 1 opened	Bool	3.1	false	True	True	True	True	False		
valve before filter 2 opened	Bool	3.2	false	True	True	True	True	False		
ELECROLYZER 1 STARTED	Bool	3.3	false	True	True	True	True	False		
ELECROLYZER 2 STARTED	Bool	3.4	false	True	True	True	True	False		
Emergency Stop	Bool	3.5	false	True	True	True	True	False		
acid cleaning start	Bool	3.6	false	True	True	True	True	False		
SELECT SEAWATER INTAKE PUMP 1	Bool	3.7	false	True	True	True	True	False		
SELECT SEAWATER INTAKE PUMP 2	Bool	4.0	false	True	True	True	True	False		

electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / Program blocks

hypoclorite system_DB [DB6]

hypoclorite system_DB Properties							
General							
Name	hypoclorite system_DB	**Number**	6	**Type**	DB	**Language**	DB
Numbering	Automatic						
Information							
Title		**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
Input										
OFF_SQ	Bool	0.0	false	True	True	True	False	False		Désactiver le graphe
INIT_SQ	Bool	0.1	false	True	True	True	False	False		Mettre le graphe à l'état initial
ACK_EF	Bool	0.2	false	True	True	True	False	False		Acquitter toutes les erreurs et perturbations
S_PREV	Bool	0.3	false	True	True	True	False	False		Afficher l'étape précédente dans le paramètre S_NO
S_NEXT	Bool	0.4	false	True	True	True	False	False		Afficher l'étape suivante dans le paramètre S_NO
SW_AUTO	Bool	0.5	false	True	True	True	False	False		Mode automatique
SW_TAP	Bool	0.6	false	True	True	True	False	False		Mode semi-automatique/Activation de l'étape suivante avec transition
SW_TOP	Bool	0.7	false	True	True	True	False	False		Mode semi-automatique/Ignorer la transition
SW_MAN	Bool	1.0	false	True	True	True	False	False		Mode manuel
S_SEL	Int	2.0	0	True	True	True	False	False		Sélectionner l'étape à afficher dans S_NO
S_ON	Bool	4.0	false	True	True	True	False	False		Activer l'étape figurant dans le paramètre S_NO
S_OFF	Bool	4.1	false	True	True	True	False	False		Désactiver l'étape figurant dans le paramètre S_NO
T_PUSH	Bool	4.2	false	True	True	True	False	False		Validation de commutation pour transition en mode semi-automatique
Output										
S_NO	Int	6.0	0	True	True	True	False	False		Numéro d'étape
S_MORE	Bool	8.0	false	True	True	True	False	False		Il existe d'autres étapes qui peuvent être affichées dans S_NO.
S_ACTIVE	Bool	8.1	false	True	True	True	False	False		L'étape affichée dans le paramètre S_NO est activée
ERR_FLT	Bool	8.2	false	True	True	True	False	False		Erreur groupée Interlock or Supervision
AUTO_ON	Bool	8.3	false	True	True	True	False	False		Mode automatique activé
TAP_ON	Bool	8.4	false	True	True	True	False	False		Mode semi-automatique/Etape avec transition activé
TOP_ON	Bool	8.5	false	True	True	True	False	False		Mode semi-automatique/Ignorer la transition activé
MAN_ON	Bool	8.6	false	True	True	True	False	False		Mode manuel activé
InOut										
Static										
Trans1	GraphTransition	234.0		True	True	True	False	True		
TV	Bool	234.0	false	True	True	True	False	False		Transition is valid
TT	Bool	234.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	234.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	234.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	236.0	1	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	238.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	242.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	246.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans2	GraphTransition	250.0		True	True	True	False	True		
TV	Bool	250.0	false	True	True	True	False	False		Transition is valid
TT	Bool	250.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	250.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	250.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	252.0	2	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	254.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_OLD	DWord	258.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	262.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans3	GraphTransition	266.0		True	True	True	False	True		
TV	Bool	266.0	false	True	True	True	False	False		Transition is valid
TT	Bool	266.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	266.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	266.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	268.0	3	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	270.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	274.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	278.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans4	GraphTransition	282.0		True	True	True	False	True		
TV	Bool	282.0	false	True	True	True	False	False		Transition is valid
TT	Bool	282.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	282.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	282.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	284.0	4	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	286.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	290.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	294.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans5	GraphTransition	298.0		True	True	True	False	True		
TV	Bool	298.0	false	True	True	True	False	False		Transition is valid
TT	Bool	298.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	298.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	298.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	300.0	5	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	302.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	306.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	310.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans6	GraphTransition	314.0		True	True	True	False	True		
TV	Bool	314.0	false	True	True	True	False	False		Transition is valid
TT	Bool	314.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	314.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	314.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	316.0	6	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	318.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	322.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	326.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans7	GraphTransition	330.0		True	True	True	False	True		
TV	Bool	330.0	false	True	True	True	False	False		Transition is valid
TT	Bool	330.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	330.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	330.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	332.0	7	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	334.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	338.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	342.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans8	GraphTransition	346.0		True	True	True	False	True		

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TV	Bool	346.0	false	True	True	True	False	False		Transition is valid
TT	Bool	346.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	346.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	346.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	348.0	8	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	350.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	354.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	358.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans9	GraphTransition	362.0		True	True	True	False	True		
TV	Bool	362.0	false	True	True	True	False	False		Transition is valid
TT	Bool	362.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	362.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	362.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	364.0	9	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	366.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	370.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	374.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans10	GraphTransition	378.0		True	True	True	False	True		
TV	Bool	378.0	false	True	True	True	False	False		Transition is valid
TT	Bool	378.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	378.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	378.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	380.0	10	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	382.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	386.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	390.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans13	GraphTransition	394.0		True	True	True	False	True		
TV	Bool	394.0	false	True	True	True	False	False		Transition is valid
TT	Bool	394.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	394.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	394.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	396.0	13	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	398.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	402.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	406.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans14	GraphTransition	410.0		True	True	True	False	True		
TV	Bool	410.0	false	True	True	True	False	False		Transition is valid
TT	Bool	410.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	410.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	410.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	412.0	14	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	414.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	418.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	422.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans15	GraphTransition	426.0		True	True	True	False	True		
TV	Bool	426.0	false	True	True	True	False	False		Transition is valid
TT	Bool	426.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	426.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	426.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TNO	Int	428.0	15	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	430.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	434.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	438.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans16	GraphTransition	442.0		True	True	True	False	True		
TV	Bool	442.0	false	True	True	True	False	False		Transition is valid
TT	Bool	442.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	442.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	442.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	444.0	16	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	446.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	450.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	454.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans21	GraphTransition	458.0		True	True	True	False	True		
TV	Bool	458.0	false	True	True	True	False	False		Transition is valid
TT	Bool	458.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	458.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	458.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	460.0	21	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	462.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	466.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	470.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans22	GraphTransition	474.0		True	True	True	False	True		
TV	Bool	474.0	false	True	True	True	False	False		Transition is valid
TT	Bool	474.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	474.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	474.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	476.0	22	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	478.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	482.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	486.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans23	GraphTransition	490.0		True	True	True	False	True		
TV	Bool	490.0	false	True	True	True	False	False		Transition is valid
TT	Bool	490.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	490.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	490.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	492.0	23	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	494.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	498.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	502.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans24	GraphTransition	506.0		True	True	True	False	True		
TV	Bool	506.0	false	True	True	True	False	False		Transition is valid
TT	Bool	506.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	506.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	506.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	508.0	24	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	510.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_OLD	DWord	514.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	518.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans25	GraphTransition	522.0		True	True	True	False	True		
TV	Bool	522.0	false	True	True	True	False	False		Transition is valid
TT	Bool	522.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	522.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	522.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	524.0	25	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	526.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	530.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	534.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans28	GraphTransition	538.0		True	True	True	False	True		
TV	Bool	538.0	false	True	True	True	False	False		Transition is valid
TT	Bool	538.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	538.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	538.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	540.0	28	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	542.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	546.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	550.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans29	GraphTransition	554.0		True	True	True	False	True		
TV	Bool	554.0	false	True	True	True	False	False		Transition is valid
TT	Bool	554.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	554.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	554.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	556.0	29	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	558.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	562.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	566.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans30	GraphTransition	570.0		True	True	True	False	True		
TV	Bool	570.0	false	True	True	True	False	False		Transition is valid
TT	Bool	570.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	570.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	570.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	572.0	30	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	574.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	578.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	582.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans31	GraphTransition	586.0		True	True	True	False	True		
TV	Bool	586.0	false	True	True	True	False	False		Transition is valid
TT	Bool	586.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	586.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	586.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	588.0	31	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	590.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	594.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	598.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans32	GraphTransition	602.0		True	True	True	False	True		

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TV	Bool	602.0	false	True	True	True	False	False		Transition is valid
TT	Bool	602.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	602.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	602.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	604.0	32	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	606.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	610.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	614.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans33	GraphTransition	618.0		True	True	True	False	True		
TV	Bool	618.0	false	True	True	True	False	False		Transition is valid
TT	Bool	618.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	618.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	618.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	620.0	33	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	622.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	626.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	630.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans34	GraphTransition	634.0		True	True	True	False	True		
TV	Bool	634.0	false	True	True	True	False	False		Transition is valid
TT	Bool	634.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	634.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	634.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	636.0	34	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	638.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	642.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	646.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans35	GraphTransition	650.0		True	True	True	False	True		
TV	Bool	650.0	false	True	True	True	False	False		Transition is valid
TT	Bool	650.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	650.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	650.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	652.0	35	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	654.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	658.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	662.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans37	GraphTransition	666.0		True	True	True	False	True		
TV	Bool	666.0	false	True	True	True	False	False		Transition is valid
TT	Bool	666.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	666.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	666.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	668.0	37	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	670.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	674.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	678.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans38	GraphTransition	682.0		True	True	True	False	True		
TV	Bool	682.0	false	True	True	True	False	False		Transition is valid
TT	Bool	682.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	682.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	682.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TNO	Int	684.0	38	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	686.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	690.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	694.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans40	GraphTransition	698.0		True	True	True	False	True		
TV	Bool	698.0	false	True	True	True	False	False		Transition is valid
TT	Bool	698.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	698.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	698.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	700.0	40	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	702.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	706.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	710.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans41	GraphTransition	714.0		True	True	True	False	True		
TV	Bool	714.0	false	True	True	True	False	False		Transition is valid
TT	Bool	714.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	714.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	714.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	716.0	41	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	718.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	722.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	726.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans42	GraphTransition	730.0		True	True	True	False	True		
TV	Bool	730.0	false	True	True	True	False	False		Transition is valid
TT	Bool	730.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	730.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	730.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	732.0	42	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	734.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	738.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	742.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans43	GraphTransition	746.0		True	True	True	False	True		
TV	Bool	746.0	false	True	True	True	False	False		Transition is valid
TT	Bool	746.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	746.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	746.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	748.0	43	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	750.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	754.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	758.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans44	GraphTransition	762.0		True	True	True	False	True		
TV	Bool	762.0	false	True	True	True	False	False		Transition is valid
TT	Bool	762.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	762.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	762.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	764.0	44	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	766.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_OLD	DWord	770.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	774.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans45	GraphTransition	778.0		True	True	True	False	True		
TV	Bool	778.0	false	True	True	True	False	False		Transition is valid
TT	Bool	778.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	778.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	778.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	780.0	45	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	782.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	786.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	790.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans46	GraphTransition	794.0		True	True	True	False	True		
TV	Bool	794.0	false	True	True	True	False	False		Transition is valid
TT	Bool	794.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	794.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	794.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	796.0	46	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	798.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	802.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	806.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans47	GraphTransition	810.0		True	True	True	False	True		
TV	Bool	810.0	false	True	True	True	False	False		Transition is valid
TT	Bool	810.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	810.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	810.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	812.0	47	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	814.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	818.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	822.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans48	GraphTransition	826.0		True	True	True	False	True		
TV	Bool	826.0	false	True	True	True	False	False		Transition is valid
TT	Bool	826.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	826.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	826.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	828.0	48	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	830.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	834.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	838.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans49	GraphTransition	842.0		True	True	True	False	True		
TV	Bool	842.0	false	True	True	True	False	False		Transition is valid
TT	Bool	842.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	842.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	842.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	844.0	49	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	846.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	850.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	854.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans50	GraphTransition	858.0		True	True	True	False	True		

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TV	Bool	858.0	false	True	True	True	False	False		Transition is valid
TT	Bool	858.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	858.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	858.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	860.0	50	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	862.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	866.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	870.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans51	GraphTransition	874.0		True	True	True	False	True		
TV	Bool	874.0	false	True	True	True	False	False		Transition is valid
TT	Bool	874.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	874.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	874.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	876.0	51	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	878.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	882.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	886.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans52	GraphTransition	890.0		True	True	True	False	True		
TV	Bool	890.0	false	True	True	True	False	False		Transition is valid
TT	Bool	890.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	890.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	890.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	892.0	52	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	894.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	898.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	902.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans53	GraphTransition	906.0		True	True	True	False	True		
TV	Bool	906.0	false	True	True	True	False	False		Transition is valid
TT	Bool	906.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	906.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	906.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	908.0	53	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	910.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	914.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	918.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans54	GraphTransition	922.0		True	True	True	False	True		
TV	Bool	922.0	false	True	True	True	False	False		Transition is valid
TT	Bool	922.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	922.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	922.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	924.0	54	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	926.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	930.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	934.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans55	GraphTransition	938.0		True	True	True	False	True		
TV	Bool	938.0	false	True	True	True	False	False		Transition is valid
TT	Bool	938.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	938.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	938.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TNO	Int	940.0	55	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	942.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	946.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	950.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans56	GraphTransition	954.0		True	True	True	False	True		
TV	Bool	954.0	false	True	True	True	False	False		Transition is valid
TT	Bool	954.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	954.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	954.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	956.0	56	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	958.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	962.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	966.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans57	GraphTransition	970.0		True	True	True	False	True		
TV	Bool	970.0	false	True	True	True	False	False		Transition is valid
TT	Bool	970.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	970.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	970.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	972.0	57	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	974.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	978.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	982.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans58	GraphTransition	986.0		True	True	True	False	True		
TV	Bool	986.0	false	True	True	True	False	False		Transition is valid
TT	Bool	986.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	986.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	986.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	988.0	58	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	990.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	994.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	998.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans59	GraphTransition	1002.0		True	True	True	False	True		
TV	Bool	1002.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1002.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1002.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1002.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1004.0	59	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1006.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1010.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1014.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans60	GraphTransition	1018.0		True	True	True	False	True		
TV	Bool	1018.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1018.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1018.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1018.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1020.0	60	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1022.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_OLD	DWord	1026.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1030.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans61	GraphTransition	1034.0		True	True	True	False	True		
TV	Bool	1034.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1034.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1034.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1034.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1036.0	61	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1038.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1042.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1046.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans62	GraphTransition	1050.0		True	True	True	False	True		
TV	Bool	1050.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1050.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1050.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1050.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1052.0	62	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1054.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1058.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1062.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans63	GraphTransition	1066.0		True	True	True	False	True		
TV	Bool	1066.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1066.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1066.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1066.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1068.0	63	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1070.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1074.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1078.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans64	GraphTransition	1082.0		True	True	True	False	True		
TV	Bool	1082.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1082.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1082.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1082.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1084.0	64	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1086.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1090.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1094.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans65	GraphTransition	1098.0		True	True	True	False	True		
TV	Bool	1098.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1098.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1098.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1098.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1100.0	65	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1102.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1106.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1110.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans66	GraphTransition	1114.0		True	True	True	False	True		

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TV	Bool	1114.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1114.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1114.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1114.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1116.0	66	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1118.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1122.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1126.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans67	GraphTransition	1130.0		True	True	True	False	True		
TV	Bool	1130.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1130.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1130.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1130.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1132.0	67	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1134.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1138.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1142.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans68	GraphTransition	1146.0		True	False	False	False	True		Transition structure
TV	Bool	1146.0	false	True	False	False	False	False		Transition is valid
TT	Bool	1146.1	false	True	False	False	False	False		Transition is satisfied
TS	Bool	1146.2	false	True	False	False	False	False		Transition switches
CF_IV	Bool	1146.3	True	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	1148.0	68	True	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	1150.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1154.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1158.0	16#0	True	False	False	False	False		Copy of CRIT if an error occurs
▼ Trans69	GraphTransition	1162.0		True	True	True	False	True		
TV	Bool	1162.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1162.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1162.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1162.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1164.0	69	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1166.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1170.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1174.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans70	GraphTransition	1178.0		True	True	True	False	True		
TV	Bool	1178.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1178.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1178.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1178.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1180.0	70	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1182.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1186.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1190.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
▼ Trans71	GraphTransition	1194.0		True	False	False	False	True		Transition structure
TV	Bool	1194.0	false	True	False	False	False	False		Transition is valid
TT	Bool	1194.1	false	True	False	False	False	False		Transition is satisfied
TS	Bool	1194.2	false	True	False	False	False	False		Transition switches
CF_IV	Bool	1194.3	True	True	False	False	False	False		The CRIT_FLT entry is invalid

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TNO	Int	1196.0	71	True	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	1198.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1202.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1206.0	16#0	True	False	False	False	False		Copy of CRIT if an error occurs
Trans72	GraphTransition	1210.0		True	True	True	False	True		
TV	Bool	1210.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1210.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1210.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1210.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1212.0	72	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1214.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1218.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1222.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans73	GraphTransition	1226.0		True	True	True	False	True		
TV	Bool	1226.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1226.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1226.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1226.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1228.0	73	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1230.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1234.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1238.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans74	GraphTransition	1242.0		True	True	True	False	True		
TV	Bool	1242.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1242.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1242.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1242.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1244.0	74	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1246.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1250.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1254.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans75	GraphTransition	1258.0		True	True	True	False	True		
TV	Bool	1258.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1258.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1258.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1258.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1260.0	75	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1262.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1266.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1270.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans76	GraphTransition	1274.0		True	True	True	False	True		
TV	Bool	1274.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1274.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1274.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1274.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1276.0	76	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1278.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_OLD	DWord	1282.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1286.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans77	GraphTransition	1290.0		True	False	False	False	True		Transition structure
TV	Bool	1290.0	false	True	False	False	False	False		Transition is valid
TT	Bool	1290.1	false	True	False	False	False	False		Transition is satisfied
TS	Bool	1290.2	false	True	False	False	False	False		Transition switches
CF_IV	Bool	1290.3	True	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	1292.0	77	True	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	1294.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1298.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1302.0	16#0	True	False	False	False	False		Copy of CRIT if an error occurs
Trans78	GraphTransition	1306.0		True	False	False	False	True		Transition structure
TV	Bool	1306.0	false	True	False	False	False	False		Transition is valid
TT	Bool	1306.1	false	True	False	False	False	False		Transition is satisfied
TS	Bool	1306.2	false	True	False	False	False	False		Transition switches
CF_IV	Bool	1306.3	True	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	1308.0	78	True	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	1310.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1314.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1318.0	16#0	True	False	False	False	False		Copy of CRIT if an error occurs
Trans79	GraphTransition	1322.0		True	False	False	False	True		Transition structure
TV	Bool	1322.0	false	True	False	False	False	False		Transition is valid
TT	Bool	1322.1	false	True	False	False	False	False		Transition is satisfied
TS	Bool	1322.2	false	True	False	False	False	False		Transition switches
CF_IV	Bool	1322.3	True	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	1324.0	79	True	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	1326.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1330.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1334.0	16#0	True	False	False	False	False		Copy of CRIT if an error occurs
Trans80	GraphTransition	1338.0		True	True	True	False	True		
TV	Bool	1338.0	false	True	True	True	False	False		Transition is valid
TT	Bool	1338.1	false	True	True	True	False	False		Transition is satisfied
TS	Bool	1338.2	false	True	True	True	False	False		Transition switches
CF_IV	Bool	1338.3	true	True	True	True	False	False		The CRIT_FLT entry is invalid
TNO	Int	1340.0	80	True	True	True	False	False		Indicates the user-defined transition number
CRIT	DWord	1342.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1346.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1350.0	16#0	True	True	True	False	False		Copy of CRIT if an error occurs
Trans81	GraphTransition	1354.0		True	False	False	False	True		Transition structure
TV	Bool	1354.0	false	True	False	False	False	False		Transition is valid
TT	Bool	1354.1	false	True	False	False	False	False		Transition is satisfied
TS	Bool	1354.2	false	True	False	False	False	False		Transition switches
CF_IV	Bool	1354.3	True	True	False	False	False	False		The CRIT_FLT entry is invalid
TNO	Int	1356.0	81	True	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	1358.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	1362.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	1366.0	16#0	True	False	False	False	False		Copy of CRIT if an error occurs
Step1	GraphStep	1402.0		True	True	True	False	True		

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
S1	Bool	1402.0	false	True	True	True	False	False		Step is activated
L1	Bool	1402.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1402.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1402.3	false	True	True	True	False	False		Reserved
A1	Bool	1402.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1402.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1402.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1402.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1403.0	false	True	True	True	False	False		Step is active
LA	Bool	1403.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1403.2	false	True	True	True	False	False		Supervision active
RA	Bool	1403.3	false	True	True	True	False	False		Reserved
AA	Bool	1403.4	false	True	True	True	False	False		Reserved
SS	Bool	1403.5	false	True	True	True	False	False		System-internal
LS	Bool	1403.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1403.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1404.0	1	True	True	True	False	False		User step number
T	Time	1406.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1410.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1414.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1418.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1422.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1426.0	false	True	True	True	False	False		System-internal
LP	Bool	1426.1	false	True	True	True	False	False		System-internal
LN	Bool	1426.2	false	True	True	True	False	False		System-internal
VP	Bool	1426.3	false	True	True	True	False	False		System-internal
VN	Bool	1426.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1427.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1428.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1430.0	16#0	True	True	True	False	False		Reserve
▼ Step2	GraphStep	1434.0		True	True	True	False	True		
S1	Bool	1434.0	false	True	True	True	False	False		Step is activated
L1	Bool	1434.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1434.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1434.3	false	True	True	True	False	False		Reserved
A1	Bool	1434.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1434.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1434.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1434.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1435.0	false	True	True	True	False	False		Step is active
LA	Bool	1435.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1435.2	false	True	True	True	False	False		Supervision active
RA	Bool	1435.3	false	True	True	True	False	False		Reserved
AA	Bool	1435.4	false	True	True	True	False	False		Reserved
SS	Bool	1435.5	false	True	True	True	False	False		System-internal
LS	Bool	1435.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1435.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1436.0	2	True	True	True	False	False		User step number
T	Time	1438.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1442.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1446.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1450.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1454.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1458.0	false	True	True	True	False	False		System-internal
LP	Bool	1458.1	false	True	True	True	False	False		System-internal
LN	Bool	1458.2	false	True	True	True	False	False		System-internal
VP	Bool	1458.3	false	True	True	True	False	False		System-internal
VN	Bool	1458.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1459.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1460.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1462.0	16#0	True	True	True	False	False		Reserve

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
Step3	GraphStep	1466.0		True	True	True	False	True		
S1	Bool	1466.0	false	True	True	True	False	False		Step is activated
L1	Bool	1466.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1466.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1466.3	false	True	True	True	False	False		Reserved
A1	Bool	1466.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1466.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1466.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1466.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1467.0	false	True	True	True	False	False		Step is active
LA	Bool	1467.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1467.2	false	True	True	True	False	False		Supervision active
RA	Bool	1467.3	false	True	True	True	False	False		Reserved
AA	Bool	1467.4	false	True	True	True	False	False		Reserved
SS	Bool	1467.5	false	True	True	True	False	False		System-internal
LS	Bool	1467.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1467.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1468.0	3	True	True	True	False	False		User step number
T	Time	1470.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1474.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1478.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1482.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1486.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1490.0	false	True	True	True	False	False		System-internal
LP	Bool	1490.1	false	True	True	True	False	False		System-internal
LN	Bool	1490.2	false	True	True	True	False	False		System-internal
VP	Bool	1490.3	false	True	True	True	False	False		System-internal
VN	Bool	1490.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1491.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1492.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1494.0	16#0	True	True	True	False	False		Reserve
Step4	GraphStep	1498.0		True	True	True	False	True		
S1	Bool	1498.0	false	True	True	True	False	False		Step is activated
L1	Bool	1498.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1498.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1498.3	false	True	True	True	False	False		Reserved
A1	Bool	1498.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1498.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1498.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1498.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1499.0	false	True	True	True	False	False		Step is active
LA	Bool	1499.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1499.2	false	True	True	True	False	False		Supervision active
RA	Bool	1499.3	false	True	True	True	False	False		Reserved
AA	Bool	1499.4	false	True	True	True	False	False		Reserved
SS	Bool	1499.5	false	True	True	True	False	False		System-internal
LS	Bool	1499.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1499.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1500.0	4	True	True	True	False	False		User step number
T	Time	1502.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1506.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1510.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1514.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1518.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1522.0	false	True	True	True	False	False		System-internal
LP	Bool	1522.1	false	True	True	True	False	False		System-internal
LN	Bool	1522.2	false	True	True	True	False	False		System-internal
VP	Bool	1522.3	false	True	True	True	False	False		System-internal
VN	Bool	1522.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1523.0	16#0	True	True	True	False	False		System-internal

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
H_SV_FLT	Byte	1524.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1526.0	16#0	True	True	True	False	False		Reserve
Step5	GraphStep	1530.0		True	True	True	False	True		
S1	Bool	1530.0	false	True	True	True	False	False		Step is activated
L1	Bool	1530.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1530.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1530.3	false	True	True	True	False	False		Reserved
A1	Bool	1530.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1530.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1530.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1530.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1531.0	false	True	True	True	False	False		Step is active
LA	Bool	1531.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1531.2	false	True	True	True	False	False		Supervision active
RA	Bool	1531.3	false	True	True	True	False	False		Reserved
AA	Bool	1531.4	false	True	True	True	False	False		Reserved
SS	Bool	1531.5	false	True	True	True	False	False		System-internal
LS	Bool	1531.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1531.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1532.0	5	True	True	True	False	False		User step number
T	Time	1534.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1538.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1542.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1546.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1550.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1554.0	false	True	True	True	False	False		System-internal
LP	Bool	1554.1	false	True	True	True	False	False		System-internal
LN	Bool	1554.2	false	True	True	True	False	False		System-internal
VP	Bool	1554.3	false	True	True	True	False	False		System-internal
VN	Bool	1554.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1555.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1556.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1558.0	16#0	True	True	True	False	False		Reserve
Step6	GraphStep	1562.0		True	True	True	False	True		
S1	Bool	1562.0	false	True	True	True	False	False		Step is activated
L1	Bool	1562.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1562.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1562.3	false	True	True	True	False	False		Reserved
A1	Bool	1562.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1562.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1562.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1562.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1563.0	false	True	True	True	False	False		Step is active
LA	Bool	1563.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1563.2	false	True	True	True	False	False		Supervision active
RA	Bool	1563.3	false	True	True	True	False	False		Reserved
AA	Bool	1563.4	false	True	True	True	False	False		Reserved
SS	Bool	1563.5	false	True	True	True	False	False		System-internal
LS	Bool	1563.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1563.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1564.0	6	True	True	True	False	False		User step number
T	Time	1566.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1570.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1574.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1578.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1582.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1586.0	false	True	True	True	False	False		System-internal
LP	Bool	1586.1	false	True	True	True	False	False		System-internal
LN	Bool	1586.2	false	True	True	True	False	False		System-internal
VP	Bool	1586.3	false	True	True	True	False	False		System-internal

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
VN	Bool	1586.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1587.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1588.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1590.0	16#0	True	True	True	False	False		Reserve
Step7	GraphStep	1594.0		True	True	True	False	True		
S1	Bool	1594.0	false	True	True	True	False	False		Step is activated
L1	Bool	1594.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1594.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1594.3	false	True	True	True	False	False		Reserved
A1	Bool	1594.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1594.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1594.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1594.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1595.0	false	True	True	True	False	False		Step is active
LA	Bool	1595.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1595.2	false	True	True	True	False	False		Supervision active
RA	Bool	1595.3	false	True	True	True	False	False		Reserved
AA	Bool	1595.4	false	True	True	True	False	False		Reserved
SS	Bool	1595.5	false	True	True	True	False	False		System-internal
LS	Bool	1595.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1595.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1596.0	7	True	True	True	False	False		User step number
T	Time	1598.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1602.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1606.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1610.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1614.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1618.0	false	True	True	True	False	False		System-internal
LP	Bool	1618.1	false	True	True	True	False	False		System-internal
LN	Bool	1618.2	false	True	True	True	False	False		System-internal
VP	Bool	1618.3	false	True	True	True	False	False		System-internal
VN	Bool	1618.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1619.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1620.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1622.0	16#0	True	True	True	False	False		Reserve
Step8	GraphStep	1626.0		True	True	True	False	True		
S1	Bool	1626.0	false	True	True	True	False	False		Step is activated
L1	Bool	1626.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1626.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1626.3	false	True	True	True	False	False		Reserved
A1	Bool	1626.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1626.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1626.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1626.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1627.0	false	True	True	True	False	False		Step is active
LA	Bool	1627.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1627.2	false	True	True	True	False	False		Supervision active
RA	Bool	1627.3	false	True	True	True	False	False		Reserved
AA	Bool	1627.4	false	True	True	True	False	False		Reserved
SS	Bool	1627.5	false	True	True	True	False	False		System-internal
LS	Bool	1627.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1627.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1628.0	8	True	True	True	False	False		User step number
T	Time	1630.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1634.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1638.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1642.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1646.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1650.0	false	True	True	True	False	False		System-internal
LP	Bool	1650.1	false	True	True	True	False	False		System-internal

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
LN	Bool	1650.2	false	True	True	True	False	False		System-internal
VP	Bool	1650.3	false	True	True	True	False	False		System-internal
VN	Bool	1650.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1651.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1652.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1654.0	16#0	True	True	True	False	False		Reserve
Step9	GraphStep	1658.0		True	True	True	False	True		
S1	Bool	1658.0	false	True	True	True	False	False		Step is activated
L1	Bool	1658.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1658.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1658.3	false	True	True	True	False	False		Reserved
A1	Bool	1658.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1658.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1658.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1658.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1659.0	false	True	True	True	False	False		Step is active
LA	Bool	1659.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1659.2	false	True	True	True	False	False		Supervision active
RA	Bool	1659.3	false	True	True	True	False	False		Reserved
AA	Bool	1659.4	false	True	True	True	False	False		Reserved
SS	Bool	1659.5	false	True	True	True	False	False		System-internal
LS	Bool	1659.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1659.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1660.0	9	True	True	True	False	False		User step number
T	Time	1662.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1666.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1670.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1674.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1678.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1682.0	false	True	True	True	False	False		System-internal
LP	Bool	1682.1	false	True	True	True	False	False		System-internal
LN	Bool	1682.2	false	True	True	True	False	False		System-internal
VP	Bool	1682.3	false	True	True	True	False	False		System-internal
VN	Bool	1682.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1683.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1684.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1686.0	16#0	True	True	True	False	False		Reserve
Step10	GraphStep	1690.0		True	True	True	False	True		
S1	Bool	1690.0	false	True	True	True	False	False		Step is activated
L1	Bool	1690.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1690.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1690.3	false	True	True	True	False	False		Reserved
A1	Bool	1690.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1690.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1690.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1690.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1691.0	false	True	True	True	False	False		Step is active
LA	Bool	1691.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1691.2	false	True	True	True	False	False		Supervision active
RA	Bool	1691.3	false	True	True	True	False	False		Reserved
AA	Bool	1691.4	false	True	True	True	False	False		Reserved
SS	Bool	1691.5	false	True	True	True	False	False		System-internal
LS	Bool	1691.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1691.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1692.0	10	True	True	True	False	False		User step number
T	Time	1694.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1698.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1702.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1706.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1710.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
SM	Bool	1714.0	false	True	True	True	False	False		System-internal
LP	Bool	1714.1	false	True	True	True	False	False		System-internal
LN	Bool	1714.2	false	True	True	True	False	False		System-internal
VP	Bool	1714.3	false	True	True	True	False	False		System-internal
VN	Bool	1714.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1715.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1716.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1718.0	16#0	True	True	True	False	False		Reserve
Step11	GraphStep	1722.0		True	True	True	False	True		
S1	Bool	1722.0	false	True	True	True	False	False		Step is activated
L1	Bool	1722.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1722.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1722.3	false	True	True	True	False	False		Reserved
A1	Bool	1722.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1722.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1722.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1722.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1723.0	false	True	True	True	False	False		Step is active
LA	Bool	1723.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1723.2	false	True	True	True	False	False		Supervision active
RA	Bool	1723.3	false	True	True	True	False	False		Reserved
AA	Bool	1723.4	false	True	True	True	False	False		Reserved
SS	Bool	1723.5	false	True	True	True	False	False		System-internal
LS	Bool	1723.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1723.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1724.0	11	True	True	True	False	False		User step number
T	Time	1726.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1730.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1734.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1738.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1742.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1746.0	false	True	True	True	False	False		System-internal
LP	Bool	1746.1	false	True	True	True	False	False		System-internal
LN	Bool	1746.2	false	True	True	True	False	False		System-internal
VP	Bool	1746.3	false	True	True	True	False	False		System-internal
VN	Bool	1746.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1747.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1748.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1750.0	16#0	True	True	True	False	False		Reserve
Step12	GraphStep	1754.0		True	True	True	False	True		
S1	Bool	1754.0	false	True	True	True	False	False		Step is activated
L1	Bool	1754.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1754.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1754.3	false	True	True	True	False	False		Reserved
A1	Bool	1754.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1754.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1754.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1754.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1755.0	false	True	True	True	False	False		Step is active
LA	Bool	1755.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1755.2	false	True	True	True	False	False		Supervision active
RA	Bool	1755.3	false	True	True	True	False	False		Reserved
AA	Bool	1755.4	false	True	True	True	False	False		Reserved
SS	Bool	1755.5	false	True	True	True	False	False		System-internal
LS	Bool	1755.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1755.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1756.0	12	True	True	True	False	False		User step number
T	Time	1758.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1762.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1766.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1770.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_SUP	DWord	1774.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1778.0	false	True	True	True	False	False		System-internal
LP	Bool	1778.1	false	True	True	True	False	False		System-internal
LN	Bool	1778.2	false	True	True	True	False	False		System-internal
VP	Bool	1778.3	false	True	True	True	False	False		System-internal
VN	Bool	1778.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1779.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1780.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1782.0	16#0	True	True	True	False	False		Reserve
▼ Step13	GraphStep	1786.0		True	True	True	False	True		
S1	Bool	1786.0	false	True	True	True	False	False		Step is activated
L1	Bool	1786.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1786.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1786.3	false	True	True	True	False	False		Reserved
A1	Bool	1786.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1786.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1786.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1786.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1787.0	false	True	True	True	False	False		Step is active
LA	Bool	1787.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1787.2	false	True	True	True	False	False		Supervision active
RA	Bool	1787.3	false	True	True	True	False	False		Reserved
AA	Bool	1787.4	false	True	True	True	False	False		Reserved
SS	Bool	1787.5	false	True	True	True	False	False		System-internal
LS	Bool	1787.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1787.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1788.0	13	True	True	True	False	False		User step number
T	Time	1790.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1794.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1798.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1802.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1806.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1810.0	false	True	True	True	False	False		System-internal
LP	Bool	1810.1	false	True	True	True	False	False		System-internal
LN	Bool	1810.2	false	True	True	True	False	False		System-internal
VP	Bool	1810.3	false	True	True	True	False	False		System-internal
VN	Bool	1810.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1811.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1812.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1814.0	16#0	True	True	True	False	False		Reserve
▼ Step15	GraphStep	1818.0		True	True	True	False	True		
S1	Bool	1818.0	false	True	True	True	False	False		Step is activated
L1	Bool	1818.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1818.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1818.3	false	True	True	True	False	False		Reserved
A1	Bool	1818.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1818.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1818.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1818.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1819.0	false	True	True	True	False	False		Step is active
LA	Bool	1819.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1819.2	false	True	True	True	False	False		Supervision active
RA	Bool	1819.3	false	True	True	True	False	False		Reserved
AA	Bool	1819.4	false	True	True	True	False	False		Reserved
SS	Bool	1819.5	false	True	True	True	False	False		System-internal
LS	Bool	1819.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1819.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1820.0	15	True	True	True	False	False		User step number
T	Time	1822.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1826.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1830.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_LOC_ERR	DWord	1834.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1838.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1842.0	false	True	True	True	False	False		System-internal
LP	Bool	1842.1	false	True	True	True	False	False		System-internal
LN	Bool	1842.2	false	True	True	True	False	False		System-internal
VP	Bool	1842.3	false	True	True	True	False	False		System-internal
VN	Bool	1842.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1843.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1844.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1846.0	16#0	True	True	True	False	False		Reserve
Step16	GraphStep	1850.0		True	True	True	False	True		
S1	Bool	1850.0	false	True	True	True	False	False		Step is activated
L1	Bool	1850.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1850.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1850.3	false	True	True	True	False	False		Reserved
A1	Bool	1850.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1850.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1850.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1850.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1851.0	false	True	True	True	False	False		Step is active
LA	Bool	1851.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1851.2	false	True	True	True	False	False		Supervision active
RA	Bool	1851.3	false	True	True	True	False	False		Reserved
AA	Bool	1851.4	false	True	True	True	False	False		Reserved
SS	Bool	1851.5	false	True	True	True	False	False		System-internal
LS	Bool	1851.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1851.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1852.0	16	True	True	True	False	False		User step number
T	Time	1854.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1858.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1862.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1866.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1870.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1874.0	false	True	True	True	False	False		System-internal
LP	Bool	1874.1	false	True	True	True	False	False		System-internal
LN	Bool	1874.2	false	True	True	True	False	False		System-internal
VP	Bool	1874.3	false	True	True	True	False	False		System-internal
VN	Bool	1874.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1875.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1876.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1878.0	16#0	True	True	True	False	False		Reserve
Step17	GraphStep	1882.0		True	True	True	False	True		
S1	Bool	1882.0	false	True	True	True	False	False		Step is activated
L1	Bool	1882.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1882.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1882.3	false	True	True	True	False	False		Reserved
A1	Bool	1882.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1882.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1882.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1882.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1883.0	false	True	True	True	False	False		Step is active
LA	Bool	1883.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1883.2	false	True	True	True	False	False		Supervision active
RA	Bool	1883.3	false	True	True	True	False	False		Reserved
AA	Bool	1883.4	false	True	True	True	False	False		Reserved
SS	Bool	1883.5	false	True	True	True	False	False		System-internal
LS	Bool	1883.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1883.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1884.0	17	True	True	True	False	False		User step number
T	Time	1886.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1890.0	T#0ms	True	True	True	False	False		Step activation time without disturbance

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_LOC	DWord	1894.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1898.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1902.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1906.0	false	True	True	True	False	False		System-internal
LP	Bool	1906.1	false	True	True	True	False	False		System-internal
LN	Bool	1906.2	false	True	True	True	False	False		System-internal
VP	Bool	1906.3	false	True	True	True	False	False		System-internal
VN	Bool	1906.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1907.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1908.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1910.0	16#0	True	True	True	False	False		Reserve
Step18	GraphStep	1914.0		True	True	True	False	True		
S1	Bool	1914.0	false	True	True	True	False	False		Step is activated
L1	Bool	1914.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1914.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1914.3	false	True	True	True	False	False		Reserved
A1	Bool	1914.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1914.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1914.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1914.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1915.0	false	True	True	True	False	False		Step is active
LA	Bool	1915.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1915.2	false	True	True	True	False	False		Supervision active
RA	Bool	1915.3	false	True	True	True	False	False		Reserved
AA	Bool	1915.4	false	True	True	True	False	False		Reserved
SS	Bool	1915.5	false	True	True	True	False	False		System-internal
LS	Bool	1915.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1915.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1916.0	18	True	True	True	False	False		User step number
T	Time	1918.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1922.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1926.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1930.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1934.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1938.0	false	True	True	True	False	False		System-internal
LP	Bool	1938.1	false	True	True	True	False	False		System-internal
LN	Bool	1938.2	false	True	True	True	False	False		System-internal
VP	Bool	1938.3	false	True	True	True	False	False		System-internal
VN	Bool	1938.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1939.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1940.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1942.0	16#0	True	True	True	False	False		Reserve
Step19	GraphStep	1946.0		True	True	True	False	True		
S1	Bool	1946.0	false	True	True	True	False	False		Step is activated
L1	Bool	1946.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1946.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1946.3	false	True	True	True	False	False		Reserved
A1	Bool	1946.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1946.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1946.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1946.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1947.0	false	True	True	True	False	False		Step is active
LA	Bool	1947.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1947.2	false	True	True	True	False	False		Supervision active
RA	Bool	1947.3	false	True	True	True	False	False		Reserved
AA	Bool	1947.4	false	True	True	True	False	False		Reserved
SS	Bool	1947.5	false	True	True	True	False	False		System-internal
LS	Bool	1947.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1947.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1948.0	19	True	True	True	False	False		User step number
T	Time	1950.0	T#0ms	True	True	True	False	False		Total step activation time

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
U	Time	1954.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1958.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1962.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1966.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	1970.0	false	True	True	True	False	False		System-internal
LP	Bool	1970.1	false	True	True	True	False	False		System-internal
LN	Bool	1970.2	false	True	True	True	False	False		System-internal
VP	Bool	1970.3	false	True	True	True	False	False		System-internal
VN	Bool	1970.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	1971.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	1972.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	1974.0	16#0	True	True	True	False	False		Reserve
Step20	GraphStep	1978.0		True	True	True	False	True		
S1	Bool	1978.0	false	True	True	True	False	False		Step is activated
L1	Bool	1978.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	1978.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	1978.3	false	True	True	True	False	False		Reserved
A1	Bool	1978.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	1978.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	1978.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	1978.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	1979.0	false	True	True	True	False	False		Step is active
LA	Bool	1979.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	1979.2	false	True	True	True	False	False		Supervision active
RA	Bool	1979.3	false	True	True	True	False	False		Reserved
AA	Bool	1979.4	false	True	True	True	False	False		Reserved
SS	Bool	1979.5	false	True	True	True	False	False		System-internal
LS	Bool	1979.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	1979.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	1980.0	20	True	True	True	False	False		User step number
T	Time	1982.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	1986.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	1990.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	1994.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	1998.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2002.0	false	True	True	True	False	False		System-internal
LP	Bool	2002.1	false	True	True	True	False	False		System-internal
LN	Bool	2002.2	false	True	True	True	False	False		System-internal
VP	Bool	2002.3	false	True	True	True	False	False		System-internal
VN	Bool	2002.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2003.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2004.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2006.0	16#0	True	True	True	False	False		Reserve
Step21	GraphStep	2010.0		True	True	True	False	True		
S1	Bool	2010.0	false	True	True	True	False	False		Step is activated
L1	Bool	2010.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2010.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2010.3	false	True	True	True	False	False		Reserved
A1	Bool	2010.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2010.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2010.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2010.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2011.0	false	True	True	True	False	False		Step is active
LA	Bool	2011.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2011.2	false	True	True	True	False	False		Supervision active
RA	Bool	2011.3	false	True	True	True	False	False		Reserved
AA	Bool	2011.4	false	True	True	True	False	False		Reserved
SS	Bool	2011.5	false	True	True	True	False	False		System-internal
LS	Bool	2011.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2011.7	false	True	True	True	False	False		Direct result of the programmed supervision

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
SNO	Int	2012.0	21	True	True	True	False	False		User step number
T	Time	2014.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2018.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2022.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2026.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2030.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2034.0	false	True	True	True	False	False		System-internal
LP	Bool	2034.1	false	True	True	True	False	False		System-internal
LN	Bool	2034.2	false	True	True	True	False	False		System-internal
VP	Bool	2034.3	false	True	True	True	False	False		System-internal
VN	Bool	2034.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2035.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2036.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2038.0	16#0	True	True	True	False	False		Reserve
Step22	GraphStep	2042.0		True	True	True	False	True		
S1	Bool	2042.0	false	True	True	True	False	False		Step is activated
L1	Bool	2042.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2042.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2042.3	false	True	True	True	False	False		Reserved
A1	Bool	2042.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2042.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2042.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2042.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2043.0	false	True	True	True	False	False		Step is active
LA	Bool	2043.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2043.2	false	True	True	True	False	False		Supervision active
RA	Bool	2043.3	false	True	True	True	False	False		Reserved
AA	Bool	2043.4	false	True	True	True	False	False		Reserved
SS	Bool	2043.5	false	True	True	True	False	False		System-internal
LS	Bool	2043.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2043.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2044.0	22	True	True	True	False	False		User step number
T	Time	2046.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2050.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2054.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2058.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2062.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2066.0	false	True	True	True	False	False		System-internal
LP	Bool	2066.1	false	True	True	True	False	False		System-internal
LN	Bool	2066.2	false	True	True	True	False	False		System-internal
VP	Bool	2066.3	false	True	True	True	False	False		System-internal
VN	Bool	2066.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2067.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2068.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2070.0	16#0	True	True	True	False	False		Reserve
Step23	GraphStep	2074.0		True	True	True	False	True		
S1	Bool	2074.0	false	True	True	True	False	False		Step is activated
L1	Bool	2074.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2074.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2074.3	false	True	True	True	False	False		Reserved
A1	Bool	2074.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2074.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2074.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2074.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2075.0	false	True	True	True	False	False		Step is active
LA	Bool	2075.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2075.2	false	True	True	True	False	False		Supervision active
RA	Bool	2075.3	false	True	True	True	False	False		Reserved
AA	Bool	2075.4	false	True	True	True	False	False		Reserved
SS	Bool	2075.5	false	True	True	True	False	False		System-internal
LS	Bool	2075.6	true	True	True	True	False	False		Direct result of the programmed interlock

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
VS	Bool	2075.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2076.0	23	True	True	True	False	False		User step number
T	Time	2078.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2082.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2086.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2090.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2094.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2098.0	false	True	True	True	False	False		System-internal
LP	Bool	2098.1	false	True	True	True	False	False		System-internal
LN	Bool	2098.2	false	True	True	True	False	False		System-internal
VP	Bool	2098.3	false	True	True	True	False	False		System-internal
VN	Bool	2098.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2099.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2100.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2102.0	16#0	True	True	True	False	False		Reserve
Step24	GraphStep	2106.0		True	True	True	False	True		
S1	Bool	2106.0	false	True	True	True	False	False		Step is activated
L1	Bool	2106.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2106.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2106.3	false	True	True	True	False	False		Reserved
A1	Bool	2106.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2106.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2106.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2106.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2107.0	false	True	True	True	False	False		Step is active
LA	Bool	2107.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2107.2	false	True	True	True	False	False		Supervision active
RA	Bool	2107.3	false	True	True	True	False	False		Reserved
AA	Bool	2107.4	false	True	True	True	False	False		Reserved
SS	Bool	2107.5	false	True	True	True	False	False		System-internal
LS	Bool	2107.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2107.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2108.0	24	True	True	True	False	False		User step number
T	Time	2110.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2114.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2118.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2122.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2126.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2130.0	false	True	True	True	False	False		System-internal
LP	Bool	2130.1	false	True	True	True	False	False		System-internal
LN	Bool	2130.2	false	True	True	True	False	False		System-internal
VP	Bool	2130.3	false	True	True	True	False	False		System-internal
VN	Bool	2130.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2131.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2132.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2134.0	16#0	True	True	True	False	False		Reserve
Step25	GraphStep	2138.0		True	False	False	False	True		Step structure
S1	Bool	2138.0	false	True	False	False	False	False		Step is activated
L1	Bool	2138.1	false	True	False	False	False	False		interlock leaving state
V1	Bool	2138.2	false	True	False	False	False	False		Supervision entering state
R1	Bool	2138.3	false	True	False	False	False	False		Reserved
A1	Bool	2138.4	false	True	False	False	False	False		Error is acknowledged
S0	Bool	2138.5	false	True	False	False	False	False		Step is deactivated
L0	Bool	2138.6	false	True	False	False	False	False		Interlock entering state
V0	Bool	2138.7	false	True	False	False	False	False		Supervision leaving state
X	Bool	2139.0	false	True	False	False	False	False		Step is active
LA	Bool	2139.1	false	True	False	False	False	False		Interlock is not satisfied
VA	Bool	2139.2	false	True	False	False	False	False		Supervision active
RA	Bool	2139.3	false	True	False	False	False	False		Reserved
AA	Bool	2139.4	false	True	False	False	False	False		Reserved
SS	Bool	2139.5	false	True	False	False	False	False		System-internal

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
LS	Bool	2139.6	True	True	False	False	False	False		Direct result of the programmed interlock
VS	Bool	2139.7	false	True	False	False	False	False		Direct result of the programmed supervision
SNO	Int	2140.0	25	True	False	False	False	False		User step number
T	Time	2142.0	T#0ms	True	False	False	False	False		Total step activation time
U	Time	2146.0	T#0ms	True	False	False	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2150.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2154.0	16#0	True	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2158.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2162.0	false	True	False	False	False	False		System-internal
LP	Bool	2162.1	false	True	False	False	False	False		System-internal
LN	Bool	2162.2	false	True	False	False	False	False		System-internal
VP	Bool	2162.3	false	True	False	False	False	False		System-internal
VN	Bool	2162.4	false	True	False	False	False	False		System-internal
H_IL_ERR	Byte	2163.0	16#0	True	False	False	False	False		System-internal
H_SV_FLT	Byte	2164.0	16#0	True	False	False	False	False		System-internal
RESERVED	DWord	2166.0	16#0	True	False	False	False	False		Reserve
Step26	GraphStep	2170.0		True	True	True	False	True		
S1	Bool	2170.0	false	True	True	True	False	False		Step is activated
L1	Bool	2170.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2170.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2170.3	false	True	True	True	False	False		Reserved
A1	Bool	2170.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2170.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2170.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2170.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2171.0	false	True	True	True	False	False		Step is active
LA	Bool	2171.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2171.2	false	True	True	True	False	False		Supervision active
RA	Bool	2171.3	false	True	True	True	False	False		Reserved
AA	Bool	2171.4	false	True	True	True	False	False		Reserved
SS	Bool	2171.5	false	True	True	True	False	False		System-internal
LS	Bool	2171.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2171.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2172.0	26	True	True	True	False	False		User step number
T	Time	2174.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2178.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2182.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2186.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2190.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2194.0	false	True	True	True	False	False		System-internal
LP	Bool	2194.1	false	True	True	True	False	False		System-internal
LN	Bool	2194.2	false	True	True	True	False	False		System-internal
VP	Bool	2194.3	false	True	True	True	False	False		System-internal
VN	Bool	2194.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2195.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2196.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2198.0	16#0	True	True	True	False	False		Reserve
Step27	GraphStep	2202.0		True	True	True	False	True		
S1	Bool	2202.0	false	True	True	True	False	False		Step is activated
L1	Bool	2202.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2202.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2202.3	false	True	True	True	False	False		Reserved
A1	Bool	2202.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2202.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2202.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2202.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2203.0	false	True	True	True	False	False		Step is active
LA	Bool	2203.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2203.2	false	True	True	True	False	False		Supervision active
RA	Bool	2203.3	false	True	True	True	False	False		Reserved

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
AA	Bool	2203.4	false	True	True	True	False	False		Reserved
SS	Bool	2203.5	false	True	True	True	False	False		System-internal
LS	Bool	2203.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2203.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2204.0	27	True	True	True	False	False		User step number
T	Time	2206.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2210.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2214.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2218.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2222.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2226.0	false	True	True	True	False	False		System-internal
LP	Bool	2226.1	false	True	True	True	False	False		System-internal
LN	Bool	2226.2	false	True	True	True	False	False		System-internal
VP	Bool	2226.3	false	True	True	True	False	False		System-internal
VN	Bool	2226.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2227.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2228.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2230.0	16#0	True	True	True	False	False		Reserve
Step28	GraphStep	2234.0		True	True	True	False	True		
S1	Bool	2234.0	false	True	True	True	False	False		Step is activated
L1	Bool	2234.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2234.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2234.3	false	True	True	True	False	False		Reserved
A1	Bool	2234.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2234.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2234.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2234.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2235.0	false	True	True	True	False	False		Step is active
LA	Bool	2235.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2235.2	false	True	True	True	False	False		Supervision active
RA	Bool	2235.3	false	True	True	True	False	False		Reserved
AA	Bool	2235.4	false	True	True	True	False	False		Reserved
SS	Bool	2235.5	false	True	True	True	False	False		System-internal
LS	Bool	2235.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2235.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2236.0	28	True	True	True	False	False		User step number
T	Time	2238.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2242.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2246.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2250.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2254.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2258.0	false	True	True	True	False	False		System-internal
LP	Bool	2258.1	false	True	True	True	False	False		System-internal
LN	Bool	2258.2	false	True	True	True	False	False		System-internal
VP	Bool	2258.3	false	True	True	True	False	False		System-internal
VN	Bool	2258.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2259.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2260.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2262.0	16#0	True	True	True	False	False		Reserve
Step29	GraphStep	2266.0		True	True	True	False	True		
S1	Bool	2266.0	false	True	True	True	False	False		Step is activated
L1	Bool	2266.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2266.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2266.3	false	True	True	True	False	False		Reserved
A1	Bool	2266.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2266.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2266.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2266.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2267.0	false	True	True	True	False	False		Step is active
LA	Bool	2267.1	false	True	True	True	False	False		Interlock is not satisfied

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
VA	Bool	2267.2	false	True	True	True	False	False		Supervision active
RA	Bool	2267.3	false	True	True	True	False	False		Reserved
AA	Bool	2267.4	false	True	True	True	False	False		Reserved
SS	Bool	2267.5	false	True	True	True	False	False		System-internal
LS	Bool	2267.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2267.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2268.0	29	True	True	True	False	False		User step number
T	Time	2270.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2274.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2278.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2282.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2286.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2290.0	false	True	True	True	False	False		System-internal
LP	Bool	2290.1	false	True	True	True	False	False		System-internal
LN	Bool	2290.2	false	True	True	True	False	False		System-internal
VP	Bool	2290.3	false	True	True	True	False	False		System-internal
VN	Bool	2290.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2291.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2292.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2294.0	16#0	True	True	True	False	False		Reserve
▼ Step30	GraphStep	2298.0		True	True	True	False	True		
S1	Bool	2298.0	false	True	True	True	False	False		Step is activated
L1	Bool	2298.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2298.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2298.3	false	True	True	True	False	False		Reserved
A1	Bool	2298.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2298.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2298.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2298.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2299.0	false	True	True	True	False	False		Step is active
LA	Bool	2299.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2299.2	false	True	True	True	False	False		Supervision active
RA	Bool	2299.3	false	True	True	True	False	False		Reserved
AA	Bool	2299.4	false	True	True	True	False	False		Reserved
SS	Bool	2299.5	false	True	True	True	False	False		System-internal
LS	Bool	2299.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2299.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2300.0	30	True	True	True	False	False		User step number
T	Time	2302.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2306.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2310.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2314.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2318.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2322.0	false	True	True	True	False	False		System-internal
LP	Bool	2322.1	false	True	True	True	False	False		System-internal
LN	Bool	2322.2	false	True	True	True	False	False		System-internal
VP	Bool	2322.3	false	True	True	True	False	False		System-internal
VN	Bool	2322.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2323.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2324.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2326.0	16#0	True	True	True	False	False		Reserve
▼ Step31	GraphStep	2330.0		True	True	True	False	True		
S1	Bool	2330.0	false	True	True	True	False	False		Step is activated
L1	Bool	2330.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2330.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2330.3	false	True	True	True	False	False		Reserved
A1	Bool	2330.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2330.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2330.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2330.7	false	True	True	True	False	False		Supervision leaving state

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
X	Bool	2331.0	false	True	True	True	False	False		Step is active
LA	Bool	2331.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2331.2	false	True	True	True	False	False		Supervision active
RA	Bool	2331.3	false	True	True	True	False	False		Reserved
AA	Bool	2331.4	false	True	True	True	False	False		Reserved
SS	Bool	2331.5	false	True	True	True	False	False		System-internal
LS	Bool	2331.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2331.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2332.0	31	True	True	True	False	False		User step number
T	Time	2334.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2338.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2342.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2346.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2350.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2354.0	false	True	True	True	False	False		System-internal
LP	Bool	2354.1	false	True	True	True	False	False		System-internal
LN	Bool	2354.2	false	True	True	True	False	False		System-internal
VP	Bool	2354.3	false	True	True	True	False	False		System-internal
VN	Bool	2354.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2355.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2356.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2358.0	16#0	True	True	True	False	False		Reserve
▼ Step32	GraphStep	2362.0		True	True	True	False	True		
S1	Bool	2362.0	false	True	True	True	False	False		Step is activated
L1	Bool	2362.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2362.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2362.3	false	True	True	True	False	False		Reserved
A1	Bool	2362.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2362.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2362.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2362.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2363.0	false	True	True	True	False	False		Step is active
LA	Bool	2363.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2363.2	false	True	True	True	False	False		Supervision active
RA	Bool	2363.3	false	True	True	True	False	False		Reserved
AA	Bool	2363.4	false	True	True	True	False	False		Reserved
SS	Bool	2363.5	false	True	True	True	False	False		System-internal
LS	Bool	2363.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2363.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2364.0	32	True	True	True	False	False		User step number
T	Time	2366.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2370.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2374.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2378.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2382.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2386.0	false	True	True	True	False	False		System-internal
LP	Bool	2386.1	false	True	True	True	False	False		System-internal
LN	Bool	2386.2	false	True	True	True	False	False		System-internal
VP	Bool	2386.3	false	True	True	True	False	False		System-internal
VN	Bool	2386.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2387.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2388.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2390.0	16#0	True	True	True	False	False		Reserve
▼ Step33	GraphStep	2394.0		True	True	True	False	True		
S1	Bool	2394.0	false	True	True	True	False	False		Step is activated
L1	Bool	2394.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2394.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2394.3	false	True	True	True	False	False		Reserved
A1	Bool	2394.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2394.5	false	True	True	True	False	False		Step is deactivated

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
L0	Bool	2394.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2394.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2395.0	false	True	True	True	False	False		Step is active
LA	Bool	2395.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2395.2	false	True	True	True	False	False		Supervision active
RA	Bool	2395.3	false	True	True	True	False	False		Reserved
AA	Bool	2395.4	false	True	True	True	False	False		Reserved
SS	Bool	2395.5	false	True	True	True	False	False		System-internal
LS	Bool	2395.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2395.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2396.0	33	True	True	True	False	False		User step number
T	Time	2398.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2402.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2406.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2410.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2414.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2418.0	false	True	True	True	False	False		System-internal
LP	Bool	2418.1	false	True	True	True	False	False		System-internal
LN	Bool	2418.2	false	True	True	True	False	False		System-internal
VP	Bool	2418.3	false	True	True	True	False	False		System-internal
VN	Bool	2418.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2419.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2420.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2422.0	16#0	True	True	True	False	False		Reserve
▼ Step34	GraphStep	2426.0		True	True	True	False	True		
S1	Bool	2426.0	false	True	True	True	False	False		Step is activated
L1	Bool	2426.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2426.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2426.3	false	True	True	True	False	False		Reserved
A1	Bool	2426.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2426.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2426.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2426.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2427.0	false	True	True	True	False	False		Step is active
LA	Bool	2427.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2427.2	false	True	True	True	False	False		Supervision active
RA	Bool	2427.3	false	True	True	True	False	False		Reserved
AA	Bool	2427.4	false	True	True	True	False	False		Reserved
SS	Bool	2427.5	false	True	True	True	False	False		System-internal
LS	Bool	2427.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2427.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2428.0	34	True	True	True	False	False		User step number
T	Time	2430.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2434.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2438.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2442.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2446.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2450.0	false	True	True	True	False	False		System-internal
LP	Bool	2450.1	false	True	True	True	False	False		System-internal
LN	Bool	2450.2	false	True	True	True	False	False		System-internal
VP	Bool	2450.3	false	True	True	True	False	False		System-internal
VN	Bool	2450.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2451.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2452.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2454.0	16#0	True	True	True	False	False		Reserve
▼ Step35	GraphStep	2458.0		True	True	True	False	True		
S1	Bool	2458.0	false	True	True	True	False	False		Step is activated
L1	Bool	2458.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2458.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2458.3	false	True	True	True	False	False		Reserved

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
A1	Bool	2458.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2458.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2458.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2458.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2459.0	false	True	True	True	False	False		Step is active
LA	Bool	2459.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2459.2	false	True	True	True	False	False		Supervision active
RA	Bool	2459.3	false	True	True	True	False	False		Reserved
AA	Bool	2459.4	false	True	True	True	False	False		Reserved
SS	Bool	2459.5	false	True	True	True	False	False		System-internal
LS	Bool	2459.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2459.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2460.0	35	True	True	True	False	False		User step number
T	Time	2462.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2466.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2470.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2474.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2478.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2482.0	false	True	True	True	False	False		System-internal
LP	Bool	2482.1	false	True	True	True	False	False		System-internal
LN	Bool	2482.2	false	True	True	True	False	False		System-internal
VP	Bool	2482.3	false	True	True	True	False	False		System-internal
VN	Bool	2482.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2483.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2484.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2486.0	16#0	True	True	True	False	False		Reserve
▼ Step36	GraphStep	2490.0		True	True	True	False	True		
S1	Bool	2490.0	false	True	True	True	False	False		Step is activated
L1	Bool	2490.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2490.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2490.3	false	True	True	True	False	False		Reserved
A1	Bool	2490.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2490.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2490.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2490.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2491.0	false	True	True	True	False	False		Step is active
LA	Bool	2491.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2491.2	false	True	True	True	False	False		Supervision active
RA	Bool	2491.3	false	True	True	True	False	False		Reserved
AA	Bool	2491.4	false	True	True	True	False	False		Reserved
SS	Bool	2491.5	false	True	True	True	False	False		System-internal
LS	Bool	2491.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2491.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2492.0	36	True	True	True	False	False		User step number
T	Time	2494.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2498.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2502.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2506.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2510.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2514.0	false	True	True	True	False	False		System-internal
LP	Bool	2514.1	false	True	True	True	False	False		System-internal
LN	Bool	2514.2	false	True	True	True	False	False		System-internal
VP	Bool	2514.3	false	True	True	True	False	False		System-internal
VN	Bool	2514.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2515.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2516.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2518.0	16#0	True	True	True	False	False		Reserve
▼ Step37	GraphStep	2522.0		True	True	True	False	True		
S1	Bool	2522.0	false	True	True	True	False	False		Step is activated
L1	Bool	2522.1	false	True	True	True	False	False		interlock leaving state

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
V1	Bool	2522.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2522.3	false	True	True	True	False	False		Reserved
A1	Bool	2522.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2522.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2522.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2522.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2523.0	false	True	True	True	False	False		Step is active
LA	Bool	2523.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2523.2	false	True	True	True	False	False		Supervision active
RA	Bool	2523.3	false	True	True	True	False	False		Reserved
AA	Bool	2523.4	false	True	True	True	False	False		Reserved
SS	Bool	2523.5	false	True	True	True	False	False		System-internal
LS	Bool	2523.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2523.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2524.0	37	True	True	True	False	False		User step number
T	Time	2526.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2530.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2534.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2538.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2542.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2546.0	false	True	True	True	False	False		System-internal
LP	Bool	2546.1	false	True	True	True	False	False		System-internal
LN	Bool	2546.2	false	True	True	True	False	False		System-internal
VP	Bool	2546.3	false	True	True	True	False	False		System-internal
VN	Bool	2546.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2547.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2548.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2550.0	16#0	True	True	True	False	False		Reserve
▼ Step38	GraphStep	2554.0		True	True	True	False	True		
S1	Bool	2554.0	false	True	True	True	False	False		Step is activated
L1	Bool	2554.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2554.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2554.3	false	True	True	True	False	False		Reserved
A1	Bool	2554.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2554.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2554.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2554.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2555.0	false	True	True	True	False	False		Step is active
LA	Bool	2555.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2555.2	false	True	True	True	False	False		Supervision active
RA	Bool	2555.3	false	True	True	True	False	False		Reserved
AA	Bool	2555.4	false	True	True	True	False	False		Reserved
SS	Bool	2555.5	false	True	True	True	False	False		System-internal
LS	Bool	2555.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2555.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2556.0	38	True	True	True	False	False		User step number
T	Time	2558.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2562.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2566.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2570.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2574.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2578.0	false	True	True	True	False	False		System-internal
LP	Bool	2578.1	false	True	True	True	False	False		System-internal
LN	Bool	2578.2	false	True	True	True	False	False		System-internal
VP	Bool	2578.3	false	True	True	True	False	False		System-internal
VN	Bool	2578.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2579.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2580.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2582.0	16#0	True	True	True	False	False		Reserve
▼ Step39	GraphStep	2586.0		True	True	True	False	True		

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
S1	Bool	2586.0	false	True	True	True	False	False		Step is activated
L1	Bool	2586.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2586.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2586.3	false	True	True	True	False	False		Reserved
A1	Bool	2586.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2586.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2586.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2586.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2587.0	false	True	True	True	False	False		Step is active
LA	Bool	2587.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2587.2	false	True	True	True	False	False		Supervision active
RA	Bool	2587.3	false	True	True	True	False	False		Reserved
AA	Bool	2587.4	false	True	True	True	False	False		Reserved
SS	Bool	2587.5	false	True	True	True	False	False		System-internal
LS	Bool	2587.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2587.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2588.0	39	True	True	True	False	False		User step number
T	Time	2590.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2594.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2598.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2602.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2606.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2610.0	false	True	True	True	False	False		System-internal
LP	Bool	2610.1	false	True	True	True	False	False		System-internal
LN	Bool	2610.2	false	True	True	True	False	False		System-internal
VP	Bool	2610.3	false	True	True	True	False	False		System-internal
VN	Bool	2610.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2611.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2612.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2614.0	16#0	True	True	True	False	False		Reserve
▼ Step40	GraphStep	2618.0		True	True	True	False	True		
S1	Bool	2618.0	false	True	True	True	False	False		Step is activated
L1	Bool	2618.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2618.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2618.3	false	True	True	True	False	False		Reserved
A1	Bool	2618.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2618.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2618.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2618.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2619.0	false	True	True	True	False	False		Step is active
LA	Bool	2619.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2619.2	false	True	True	True	False	False		Supervision active
RA	Bool	2619.3	false	True	True	True	False	False		Reserved
AA	Bool	2619.4	false	True	True	True	False	False		Reserved
SS	Bool	2619.5	false	True	True	True	False	False		System-internal
LS	Bool	2619.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2619.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2620.0	40	True	True	True	False	False		User step number
T	Time	2622.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2626.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2630.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2634.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2638.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2642.0	false	True	True	True	False	False		System-internal
LP	Bool	2642.1	false	True	True	True	False	False		System-internal
LN	Bool	2642.2	false	True	True	True	False	False		System-internal
VP	Bool	2642.3	false	True	True	True	False	False		System-internal
VN	Bool	2642.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2643.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2644.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2646.0	16#0	True	True	True	False	False		Reserve

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
Step41	GraphStep	2650.0		True	False	False	False	True		Step structure
S1	Bool	2650.0	false	True	False	False	False	False		Step is activated
L1	Bool	2650.1	false	True	False	False	False	False		interlock leaving state
V1	Bool	2650.2	false	True	False	False	False	False		Supervision entering state
R1	Bool	2650.3	false	True	False	False	False	False		Reserved
A1	Bool	2650.4	false	True	False	False	False	False		Error is acknowledged
S0	Bool	2650.5	false	True	False	False	False	False		Step is deactivated
L0	Bool	2650.6	false	True	False	False	False	False		Interlock entering state
V0	Bool	2650.7	false	True	False	False	False	False		Supervision leaving state
X	Bool	2651.0	false	True	False	False	False	False		Step is active
LA	Bool	2651.1	false	True	False	False	False	False		Interlock is not satisfied
VA	Bool	2651.2	false	True	False	False	False	False		Supervision active
RA	Bool	2651.3	false	True	False	False	False	False		Reserved
AA	Bool	2651.4	false	True	False	False	False	False		Reserved
SS	Bool	2651.5	false	True	False	False	False	False		System-internal
LS	Bool	2651.6	True	True	False	False	False	False		Direct result of the programmed interlock
VS	Bool	2651.7	false	True	False	False	False	False		Direct result of the programmed supervision
SNO	Int	2652.0	41	True	False	False	False	False		User step number
T	Time	2654.0	T#0ms	True	False	False	False	False		Total step activation time
U	Time	2658.0	T#0ms	True	False	False	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2662.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2666.0	16#0	True	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2670.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2674.0	false	True	False	False	False	False		System-internal
LP	Bool	2674.1	false	True	False	False	False	False		System-internal
LN	Bool	2674.2	false	True	False	False	False	False		System-internal
VP	Bool	2674.3	false	True	False	False	False	False		System-internal
VN	Bool	2674.4	false	True	False	False	False	False		System-internal
H_IL_ERR	Byte	2675.0	16#0	True	False	False	False	False		System-internal
H_SV_FLT	Byte	2676.0	16#0	True	False	False	False	False		System-internal
RESERVED	DWord	2678.0	16#0	True	False	False	False	False		Reserve
Step42	GraphStep	2682.0		True	True	True	False	True		
S1	Bool	2682.0	false	True	True	True	False	False		Step is activated
L1	Bool	2682.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2682.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2682.3	false	True	True	True	False	False		Reserved
A1	Bool	2682.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2682.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2682.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2682.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2683.0	false	True	True	True	False	False		Step is active
LA	Bool	2683.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2683.2	false	True	True	True	False	False		Supervision active
RA	Bool	2683.3	false	True	True	True	False	False		Reserved
AA	Bool	2683.4	false	True	True	True	False	False		Reserved
SS	Bool	2683.5	false	True	True	True	False	False		System-internal
LS	Bool	2683.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2683.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2684.0	42	True	True	True	False	False		User step number
T	Time	2686.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2690.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2694.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2698.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2702.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2706.0	false	True	True	True	False	False		System-internal
LP	Bool	2706.1	false	True	True	True	False	False		System-internal
LN	Bool	2706.2	false	True	True	True	False	False		System-internal
VP	Bool	2706.3	false	True	True	True	False	False		System-internal
VN	Bool	2706.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2707.0	16#0	True	True	True	False	False		System-internal

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
H_SV_FLT	Byte	2708.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2710.0	16#0	True	True	True	False	False		Reserve
▼ Step43	GraphStep	2714.0		True	True	True	False	True		
S1	Bool	2714.0	false	True	True	True	False	False		Step is activated
L1	Bool	2714.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2714.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2714.3	false	True	True	True	False	False		Reserved
A1	Bool	2714.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2714.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2714.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2714.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2715.0	false	True	True	True	False	False		Step is active
LA	Bool	2715.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2715.2	false	True	True	True	False	False		Supervision active
RA	Bool	2715.3	false	True	True	True	False	False		Reserved
AA	Bool	2715.4	false	True	True	True	False	False		Reserved
SS	Bool	2715.5	false	True	True	True	False	False		System-internal
LS	Bool	2715.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2715.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2716.0	43	True	True	True	False	False		User step number
T	Time	2718.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2722.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2726.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2730.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2734.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2738.0	false	True	True	True	False	False		System-internal
LP	Bool	2738.1	false	True	True	True	False	False		System-internal
LN	Bool	2738.2	false	True	True	True	False	False		System-internal
VP	Bool	2738.3	false	True	True	True	False	False		System-internal
VN	Bool	2738.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2739.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2740.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2742.0	16#0	True	True	True	False	False		Reserve
▼ Step44	GraphStep	2746.0		True	True	True	False	True		
S1	Bool	2746.0	false	True	True	True	False	False		Step is activated
L1	Bool	2746.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2746.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2746.3	false	True	True	True	False	False		Reserved
A1	Bool	2746.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2746.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2746.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2746.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2747.0	false	True	True	True	False	False		Step is active
LA	Bool	2747.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2747.2	false	True	True	True	False	False		Supervision active
RA	Bool	2747.3	false	True	True	True	False	False		Reserved
AA	Bool	2747.4	false	True	True	True	False	False		Reserved
SS	Bool	2747.5	false	True	True	True	False	False		System-internal
LS	Bool	2747.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2747.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2748.0	44	True	True	True	False	False		User step number
T	Time	2750.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2754.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2758.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2762.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2766.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2770.0	false	True	True	True	False	False		System-internal
LP	Bool	2770.1	false	True	True	True	False	False		System-internal
LN	Bool	2770.2	false	True	True	True	False	False		System-internal
VP	Bool	2770.3	false	True	True	True	False	False		System-internal

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
VN	Bool	2770.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2771.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2772.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2774.0	16#0	True	True	True	False	False		Reserve
Step45	GraphStep	2778.0		True	True	True	False	True		
S1	Bool	2778.0	false	True	True	True	False	False		Step is activated
L1	Bool	2778.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2778.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2778.3	false	True	True	True	False	False		Reserved
A1	Bool	2778.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2778.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2778.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2778.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2779.0	false	True	True	True	False	False		Step is active
LA	Bool	2779.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2779.2	false	True	True	True	False	False		Supervision active
RA	Bool	2779.3	false	True	True	True	False	False		Reserved
AA	Bool	2779.4	false	True	True	True	False	False		Reserved
SS	Bool	2779.5	false	True	True	True	False	False		System-internal
LS	Bool	2779.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2779.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2780.0	45	True	True	True	False	False		User step number
T	Time	2782.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2786.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2790.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2794.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2798.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2802.0	false	True	True	True	False	False		System-internal
LP	Bool	2802.1	false	True	True	True	False	False		System-internal
LN	Bool	2802.2	false	True	True	True	False	False		System-internal
VP	Bool	2802.3	false	True	True	True	False	False		System-internal
VN	Bool	2802.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2803.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2804.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2806.0	16#0	True	True	True	False	False		Reserve
Step46	GraphStep	2810.0		True	True	True	False	True		
S1	Bool	2810.0	false	True	True	True	False	False		Step is activated
L1	Bool	2810.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2810.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2810.3	false	True	True	True	False	False		Reserved
A1	Bool	2810.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2810.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2810.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2810.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2811.0	false	True	True	True	False	False		Step is active
LA	Bool	2811.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2811.2	false	True	True	True	False	False		Supervision active
RA	Bool	2811.3	false	True	True	True	False	False		Reserved
AA	Bool	2811.4	false	True	True	True	False	False		Reserved
SS	Bool	2811.5	false	True	True	True	False	False		System-internal
LS	Bool	2811.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2811.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2812.0	46	True	True	True	False	False		User step number
T	Time	2814.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2818.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2822.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2826.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2830.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2834.0	false	True	True	True	False	False		System-internal
LP	Bool	2834.1	false	True	True	True	False	False		System-internal

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
LN	Bool	2834.2	false	True	True	True	False	False		System-internal
VP	Bool	2834.3	false	True	True	True	False	False		System-internal
VN	Bool	2834.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2835.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2836.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2838.0	16#0	True	True	True	False	False		Reserve
Step47	GraphStep	2842.0		True	True	True	False	True		
S1	Bool	2842.0	false	True	True	True	False	False		Step is activated
L1	Bool	2842.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2842.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2842.3	false	True	True	True	False	False		Reserved
A1	Bool	2842.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2842.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2842.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2842.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2843.0	false	True	True	True	False	False		Step is active
LA	Bool	2843.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2843.2	false	True	True	True	False	False		Supervision active
RA	Bool	2843.3	false	True	True	True	False	False		Reserved
AA	Bool	2843.4	false	True	True	True	False	False		Reserved
SS	Bool	2843.5	false	True	True	True	False	False		System-internal
LS	Bool	2843.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2843.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2844.0	47	True	True	True	False	False		User step number
T	Time	2846.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2850.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2854.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2858.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2862.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2866.0	false	True	True	True	False	False		System-internal
LP	Bool	2866.1	false	True	True	True	False	False		System-internal
LN	Bool	2866.2	false	True	True	True	False	False		System-internal
VP	Bool	2866.3	false	True	True	True	False	False		System-internal
VN	Bool	2866.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2867.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2868.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2870.0	16#0	True	True	True	False	False		Reserve
Step48	GraphStep	2874.0		True	True	True	False	True		
S1	Bool	2874.0	false	True	True	True	False	False		Step is activated
L1	Bool	2874.1	false	True	True	True	False	False		interlock leaving state
V1	Bool	2874.2	false	True	True	True	False	False		Supervision entering state
R1	Bool	2874.3	false	True	True	True	False	False		Reserved
A1	Bool	2874.4	false	True	True	True	False	False		Error is acknowledged
S0	Bool	2874.5	false	True	True	True	False	False		Step is deactivated
L0	Bool	2874.6	false	True	True	True	False	False		Interlock entering state
V0	Bool	2874.7	false	True	True	True	False	False		Supervision leaving state
X	Bool	2875.0	false	True	True	True	False	False		Step is active
LA	Bool	2875.1	false	True	True	True	False	False		Interlock is not satisfied
VA	Bool	2875.2	false	True	True	True	False	False		Supervision active
RA	Bool	2875.3	false	True	True	True	False	False		Reserved
AA	Bool	2875.4	false	True	True	True	False	False		Reserved
SS	Bool	2875.5	false	True	True	True	False	False		System-internal
LS	Bool	2875.6	true	True	True	True	False	False		Direct result of the programmed interlock
VS	Bool	2875.7	false	True	True	True	False	False		Direct result of the programmed supervision
SNO	Int	2876.0	48	True	True	True	False	False		User step number
T	Time	2878.0	T#0ms	True	True	True	False	False		Total step activation time
U	Time	2882.0	T#0ms	True	True	True	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2886.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2890.0	16#0	True	True	True	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2894.0	16#0	True	True	True	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
SM	Bool	2898.0	false	True	True	True	False	False		System-internal
LP	Bool	2898.1	false	True	True	True	False	False		System-internal
LN	Bool	2898.2	false	True	True	True	False	False		System-internal
VP	Bool	2898.3	false	True	True	True	False	False		System-internal
VN	Bool	2898.4	false	True	True	True	False	False		System-internal
H_IL_ERR	Byte	2899.0	16#0	True	True	True	False	False		System-internal
H_SV_FLT	Byte	2900.0	16#0	True	True	True	False	False		System-internal
RESERVED	DWord	2902.0	16#0	True	True	True	False	False		Reserve
Step49	GraphStep	2906.0		True	False	False	False	True		Step structure
S1	Bool	2906.0	false	True	False	False	False	False		Step is activated
L1	Bool	2906.1	false	True	False	False	False	False		interlock leaving state
V1	Bool	2906.2	false	True	False	False	False	False		Supervision entering state
R1	Bool	2906.3	false	True	False	False	False	False		Reserved
A1	Bool	2906.4	false	True	False	False	False	False		Error is acknowledged
S0	Bool	2906.5	false	True	False	False	False	False		Step is deactivated
L0	Bool	2906.6	false	True	False	False	False	False		Interlock entering state
V0	Bool	2906.7	false	True	False	False	False	False		Supervision leaving state
X	Bool	2907.0	false	True	False	False	False	False		Step is active
LA	Bool	2907.1	false	True	False	False	False	False		Interlock is not satisfied
VA	Bool	2907.2	false	True	False	False	False	False		Supervision active
RA	Bool	2907.3	false	True	False	False	False	False		Reserved
AA	Bool	2907.4	false	True	False	False	False	False		Reserved
SS	Bool	2907.5	false	True	False	False	False	False		System-internal
LS	Bool	2907.6	True	True	False	False	False	False		Direct result of the programmed interlock
VS	Bool	2907.7	false	True	False	False	False	False		Direct result of the programmed supervision
SNO	Int	2908.0	49	True	False	False	False	False		User step number
T	Time	2910.0	T#0ms	True	False	False	False	False		Total step activation time
U	Time	2914.0	T#0ms	True	False	False	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2918.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2922.0	16#0	True	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	2926.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2930.0	false	True	False	False	False	False		System-internal
LP	Bool	2930.1	false	True	False	False	False	False		System-internal
LN	Bool	2930.2	false	True	False	False	False	False		System-internal
VP	Bool	2930.3	false	True	False	False	False	False		System-internal
VN	Bool	2930.4	false	True	False	False	False	False		System-internal
H_IL_ERR	Byte	2931.0	16#0	True	False	False	False	False		System-internal
H_SV_FLT	Byte	2932.0	16#0	True	False	False	False	False		System-internal
RESERVED	DWord	2934.0	16#0	True	False	False	False	False		Reserve
Step50	GraphStep	2938.0		True	False	False	False	True		Step structure
S1	Bool	2938.0	false	True	False	False	False	False		Step is activated
L1	Bool	2938.1	false	True	False	False	False	False		interlock leaving state
V1	Bool	2938.2	false	True	False	False	False	False		Supervision entering state
R1	Bool	2938.3	false	True	False	False	False	False		Reserved
A1	Bool	2938.4	false	True	False	False	False	False		Error is acknowledged
S0	Bool	2938.5	false	True	False	False	False	False		Step is deactivated
L0	Bool	2938.6	false	True	False	False	False	False		Interlock entering state
V0	Bool	2938.7	false	True	False	False	False	False		Supervision leaving state
X	Bool	2939.0	false	True	False	False	False	False		Step is active
LA	Bool	2939.1	false	True	False	False	False	False		Interlock is not satisfied
VA	Bool	2939.2	false	True	False	False	False	False		Supervision active
RA	Bool	2939.3	false	True	False	False	False	False		Reserved
AA	Bool	2939.4	false	True	False	False	False	False		Reserved
SS	Bool	2939.5	false	True	False	False	False	False		System-internal
LS	Bool	2939.6	True	True	False	False	False	False		Direct result of the programmed interlock
VS	Bool	2939.7	false	True	False	False	False	False		Direct result of the programmed supervision
SNO	Int	2940.0	50	True	False	False	False	False		User step number
T	Time	2942.0	T#0ms	True	False	False	False	False		Total step activation time
U	Time	2946.0	T#0ms	True	False	False	False	False		Step activation time without disturbance
CRIT_LOC	DWord	2950.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	2954.0	16#0	True	False	False	False	False		Copy of CRIT_LOC when the interlock leaves the state

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
CRIT_SUP	DWord	2958.0	16#0	True	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	2962.0	false	True	False	False	False	False		System-internal
LP	Bool	2962.1	false	True	False	False	False	False		System-internal
LN	Bool	2962.2	false	True	False	False	False	False		System-internal
VP	Bool	2962.3	false	True	False	False	False	False		System-internal
VN	Bool	2962.4	false	True	False	False	False	False		System-internal
H_IL_ERR	Byte	2963.0	16#0	True	False	False	False	False		System-internal
H_SV_FLT	Byte	2964.0	16#0	True	False	False	False	False		System-internal
RESERVED	DWord	2966.0	16#0	True	False	False	False	False		Reserve
S_DISPLAY	Int	3314.0	0	True	True	True	False	False		Affichage interne du paramètre de sortie S_NO
S_SEL_OLD	Int	3316.0	0	True	True	True	False	False		Valeur précédente dans S_SEL
S_DISPIDX	Byte	3318.0	16#0	True	True	True	False	False		Indice de l'étape affichée dans S_NO
T_DISPIDX	Byte	3319.0	16#0	True	True	True	False	False		Indice de la transition affichée dans T_NO
MOP	Struct	3324.0		True	True	True	False	False		Mode de fonctionnement
AUTO	Bool	3324.0	true	True	True	True	False	False		Etat : Mode automatique
MAN	Bool	3324.1	false	True	True	True	False	False		Etat : Mode manuel
TAP	Bool	3324.2	false	True	True	True	False	False		Etat : Mode semi-automatique/Activation de l'étape suivante avec transition
TOP	Bool	3324.3	false	True	True	True	False	False		Etat : Mode semi-automatique/Ignorer la transition
ACK_S	Bool	3324.4	false	True	True	True	False	False		Requête : Acquitter l'étape figurant dans le paramètre S_NO
REG_S	Bool	3324.5	false	True	True	True	False	False		Requête : Enregistrer l'étape figurant dans le paramètre S_NO
T_PREV	Bool	3324.6	false	True	True	True	False	False		Requête : Afficher la transition valide précédente dans le paramètre T_NO
T_NEXT	Bool	3324.7	false	True	True	True	False	False		Requête : Afficher la transition valide suivante dans le paramètre T_NO
LOCK	Bool	3325.0	true	True	True	True	False	False		Etat : Traitement des Interlocks activé
SUP	Bool	3325.1	true	True	True	True	False	False		Etat : Traitement de Supervision activé
ACKREQ	Bool	3325.2	true	True	True	True	False	False		Etat : Acquittement requis
SSKIP	Bool	3325.3	false	True	True	True	False	False		Etat : "Ignorer les étapes" activé
OFF	Bool	3325.4	false	True	True	True	False	False		Requête : Désactiver toutes les étapes
INIT	Bool	3325.5	true	True	True	True	False	False		Requête : Mettre le graphe à l'état initial
HALT	Bool	3325.6	false	True	True	True	False	False		Etat : Le graphe est arrêté
TMS_HALT	Bool	3325.7	false	True	True	True	False	False		Etat : Toutes les temporisations internes sont arrêtées
OPS_ZERO	Bool	3326.0	false	True	True	True	False	False		Etat : Tous les opérandes traités avec N, L, D mis à 0
SACT_DISP	Bool	3326.1	true	True	True	True	False	False		Etat : N'afficher que les étapes actives
SEF_DISP	Bool	3326.2	false	True	True	True	False	False		Etat : Afficher uniquement les étapes avec erreurs et perturbations
SALL_DISP	Bool	3326.3	false	True	True	True	False	False		Etat : Afficher toutes les étapes
S_PREV	Bool	3326.4	false	True	True	True	False	False		Requête : Afficher l'étape précédente dans le paramètre S_NO
S_NEXT	Bool	3326.5	false	True	True	True	False	False		Requête : Afficher l'étape suivante dans le paramètre S_NO
S_SELOK	Bool	3326.6	false	True	True	True	False	False		Requête : Afficher dans le paramètre S_NO le numéro d'étape figurant dans S_SEL
S_ON	Bool	3326.7	false	True	True	True	False	False		Requête : Activer l'étape figurant dans le paramètre S_NO
S_OFF	Bool	3327.0	false	True	True	True	False	False		Requête : Désactiver l'étape figurant dans le paramètre S_NO
T_PUSH	Bool	3327.1	false	True	True	True	False	False		Requête : Validation de commutation pour transition
REG	Bool	3327.2	false	True	True	True	False	False		Requête : Enregistrer toutes les erreurs Interlock et Supervision
ACK	Bool	3327.3	false	True	True	True	False	False		Requête : Acquitter toutes les erreurs Interlock et Supervision
IL_PERM	Bool	3327.4	false	True	True	True	False	False		Etat : Traitement permanent de tous les Interlocks
T_PERM	Bool	3327.5	false	True	True	True	False	False		Etat : Traitement permanent de toutes les transitions
ILP_MAN	Bool	3327.6	false	True	True	True	False	False		Etat : Traitement permanent de tous les Interlocks en mode manuel
TICKS	Struct	3328.0		True	True	True	False	False		Périodes d'horloge

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
DELTA	Time	3328.0	T#0ms	True	True	True	False	False		Différence de temps entre deux cycles
OLD	Time	3332.0	T#0ms	True	True	True	False	False		Valeur du compteur de périodes 10 ms au dernier cycle
NEW	Time	3336.0	T#0ms	True	True	True	False	False		Valeur du compteur de périodes 10 ms dans ce cycle
▼ SQ_FLAGS	Struct	3340.0		True	True	True	False	False		Mémentos de graphe
ERR_FLT	Bool	3340.0	false	True	True	True	False	False		Erreur groupée Interlock et Supervision
ERROR	Bool	3340.1	false	True	True	True	False	False		Erreur groupée Interlock
FAULT	Bool	3340.2	false	True	True	True	False	False		Erreur groupée Supervision
RT_FAIL	Bool	3340.3	false	True	True	True	False	False		Erreur à l'exécution
NO_SNO	Bool	3340.4	false	True	True	True	False	False		Numéro d'étape requis introuvable
NF_OFL	Bool	3340.5	false	True	True	True	False	False		Débordement : Trop de requêtes ON ou OFF
SA_OFL	Bool	3340.6	false	True	True	True	False	False		Débordement : Trop d'étapes activées
TV_OFL	Bool	3340.7	false	True	True	True	False	False		Débordement : Trop de transitions valides
MSG_OFL	Bool	3341.0	false	True	True	True	False	False		Débordement : Pas assez de ressources système pour ALARM_S
NO_SWI	Bool	3341.1	false	True	True	True	False	False		Pas de commutation dans ce cycle
CYC_OP	Bool	3341.2	false	True	True	True	False	False		Exécution cyclique du graphe après l'initialisation
AS_MSG	Bool	3341.3	true	True	True	True	False	False		Messages à l'exécution activés ou désactivés par instruction
AS_SEND	Bool	3341.4	false	True	True	True	False	False		Envoyer les messages de WR_USMSG ou les entrer uniquement dans le tampon de diagnostic
SQ_BUSY	Bool	3341.5	false	True	True	True	False	False		Mémento de front interne pour le traitement séquentiel
SA_BUSY	Bool	3341.6	false	True	True	True	False	False		Mémento de front interne pour le traitement séquentiel
AS_SIG	Bool	3341.7	false	True	True	True	False	False		Mémentos de front pour messages de Alarm_S et Alarm_SQ

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IEC_Counter_0_DB [DB3]

IEC_Counter_0_DB Properties							
General							
Name	IEC_Counter_0_DB	**Number**	3	**Type**	DB	**Language**	DB
Numbering	Automatic						
Information							
Title		**Author**	SIMATIC	**Comment**		**Family**	IEC_TC
Version	1.0	**User-defined ID**	CTU				

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
Input										
CU	Bool	0.0	false	True	True	True	True	False		
R	Bool	0.1	false	True	True	True	True	False		
PV	Int	2.0	0	True	True	True	True	False		
Output										
Q	Bool	4.0	false	True	True	True	True	False		
CV	Int	6.0	0	True	True	True	True	False		
InOut										
Static										
CUO	Bool	8.0	false	True	True	True	True	False		

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SCALE [FC105]

SCALE Properties							
General							
Name	SCALE	**Number**	105	**Type**	FC	**Language**	STL
Numbering	Automatic						
Information							
Title	SCALING VALUES	**Author**	SEA	**Comment**		**Family**	CONVERT
Version	2.1	**User-defined ID**	SCALE				

Name	Data type	Offset	Default value	Comment
Input				
IN	Int			input value to be scaled
HI_LIM	Real			upper limit in engineering units
LO_LIM	Real			lower limit in engineering units
BIPOLAR	Bool			1=bipolar; 0=unipolar
Output				
OUT	Real			result of the scale conversion
InOut				
Return				
Ret_Val	Word			

electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / Program blocks / System blocks / Program resources

G7_STD_3 [FC72]

G7_STD_3 Properties							
General							
Name	G7_STD_3	**Number**	72	**Type**	FC	**Language**	STL
Numbering	Automatic						
Information							
Title		**Author**	SIMATIC	**Comment**		**Family**	S7GRAPH
Version	2.2	**User-defined ID**	SFMULTI				

Name	Data type	Offset	Default value	Comment
Input				
Output				
InOut				
▼ Return				
Ret_Val	Void			

electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP]

Technology objects

This folder is empty.

electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / PLC tags / Table de variables standard [186]

PLC tags

PLC tags

	Name	Data type	Address	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Supervision				Comment
	start PB	Bool	%I0.0		True	True	True					
	stop PB	Bool	%I0.1		True	True	True					
	SYSTEM STARTED	Bool	%M0.0		True	True	True					
	BAR SCREEN Filter	Bool	%M0.1		True	True	True					
	Rotating Drum Screaan Filter	Bool	%M0.2		True	True	True					
	Seawater Pump 1 To Elech-Station	Bool	%Q0.0		True	True	True					
	Seawater supply valve 1	Bool	%Q0.2		True	True	True					
	Seawater Pump 2 To Elech-Station	Bool	%Q0.1		True	True	True					
	AUTO	Bool	%M0.3		True	True	True					
	Diff Level Of Bar Screen	Int	%IW288		True	True	True					
	Tag_1	Bool	%M10.0		True	True	True					
	Tag_2	Word	%MW100		True	True	True					
	DL of BAR SCREEN (mm)	Real	%MD102		True	True	True					
	Diff Level of Rotating Drum Screen	Int	%IW290		True	True	True					
	Tag_3	Bool	%M10.1		True	True	True					
	Tag_4	Word	%MW106		True	True	True					
	DL of Rotating DRum Screen (mm)	Real	%MD108		True	True	True					
	seawater pump 1 to elech-station started	Bool	%I0.2		True	True	True					
	seawater pump 2 to elech-station started	Bool	%I0.3		True	True	True					
	seawater pump 1 FAULT	Bool	%M20.0		True	True	True					
	seawater pump 2 FAULT	Bool	%M20.1		True	True	True					
	seawater supply is OK	Bool	%M0.5		True	True	True					
	seawater pump intake 1	Bool	%Q0.3		True	True	True					
	seawater pump intake 2	Bool	%Q0.4		True	True	True					
	valve of seawater pump intake 1	Bool	%Q0.5		True	True	True					
	valve of seawater pump intake 2	Bool	%Q0.6		True	True	True					
	filter 1	Bool	%M0.6		True	True	True					
	electrolizer 1	Bool	%M0.7		True	True	True					
	valve to Tank	Bool	%Q0.7		True	True	True					
	countinous injection pump 1	Bool	%Q1.0		True	True	True					
	countinous injection pump 2	Bool	%Q1.1		True	True	True					
	shocked injection pump 1	Bool	%Q1.2		True	True	True					
	shocked injection pump 2	Bool	%Q1.3		True	True	True					
	H2 blower	Bool	%Q1.4		True	True	True					
	Seawater Flow	Int	%IW292		True	True	True					
	Tag_5	Bool	%M10.2		True	True	True					
	Tag_6	Word	%MW112		True	True	True					
	Seawater Flow (m3/h)	Real	%MD114		True	True	True					
	DP filter	Int	%IW294		True	True	True					
	Tag_8	Bool	%M10.3		True	True	True					
	Tag_7	Word	%MW118		True	True	True					
	DP Filter(mbar)	Real	%MD116		True	True	True					
	CURRENT	Int	%MW52		True	True	True					
	Tag_10	Bool	%M10.4		True	True	True					
	Tag_9	Word	%MW120		True	True	True					
	Current(A)	Real	%MD122		True	True	True					
	Tank level	Int	%MW58		True	True	True					
	Tag_11	Bool	%M10.5		True	True	True					
	Tag_12	Word	%MW126		True	True	True					
	Tank level (mm)	Real	%MD128		True	True	True					
	H2 concentration	Int	%MW62		True	True	True					
	Tag_13	Bool	%M10.6		True	True	True					
	Tag_14	Word	%MW132		True	True	True					

	Name	Data type	Address	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Supervision				Comment
	H2 concentration percent (%)	Real	%MD134		True	True	True					
	Air valve	Bool	%Q1.5		True	True	True					
	Air Flow	Int	%MW60		True	True	True					
	Tag_15	Bool	%M10.7		True	True	True					
	Tag_16	Word	%MW138		True	True	True					
	Air Flow (m3/h)	Real	%MD140		True	True	True					
	seawater pump intake 1 started	Bool	%I0.4		True	True	True					
	seawater pump intake 2 started	Bool	%I0.5		True	True	True					
	seawater pump intake 1 Fault	Bool	%M20.2		True	True	True					
	seawater pump intake 2 Fault	Bool	%M20.3		True	True	True					
	valve to tank opened	Bool	%I0.6		True	True	True					
	valve to tank Fault	Bool	%M20.4		True	True	True					
	valve to tank manually	Bool	%M1.1		True	True	True					
	hypochlorit production is ok	Bool	%M1.2		True	True	True					
	countinous injection pump 1 started	Bool	%I0.7		True	True	True					
	countinous injection pump 2 started	Bool	%I1.0		True	True	True					
	countinous injection pump 1 Fault	Bool	%M20.5		True	True	True					
	countinous injection pump 2 Fault	Bool	%M20.6		True	True	True					
	shocked injection pump 1 Fault	Bool	%M20.7		True	True	True					
	shocked injection pump 2 Fault	Bool	%M21.0		True	True	True					
	shocked injection pump 1 started	Bool	%I1.1		True	True	True					
	shocked injection pump 2 started	Bool	%I1.2		True	True	True					
	countinous injection pumps repair	Bool	%M1.3		True	True	True					
	shocked injection pumps repair	Bool	%M1.4		True	True	True					
	Tank low level	Bool	%M1.5		True	True	True					
	Tank high level	Bool	%M1.6		True	True	True					
	Tank level LL	Bool	%M21.1		True	True	True					
	Tank level HH	Bool	%M21.2		True	True	True					
	H2 concentration H	Bool	%M21.3		True	True	True					
	H2 concentration HH	Bool	%M21.4		True	True	True					
	seawater level	Int	%MW50		True	True	True					
	Tag_17	Bool	%M11.0		True	True	True					
	Tag_18	Word	%MW150		True	True	True					
	Seawater Level (mm)	Real	%MD146		True	True	True					
	seawater level LL	Bool	%M21.5		True	True	True					
	Acid transfer Pump	Bool	%Q1.6		True	True	True					
	Acid cleaning pump 1	Bool	%Q1.7		True	True	True					
	Acid cleaning pump 2	Bool	%Q2.0		True	True	True					
	Acid supply valve	Bool	%Q2.1		True	True	True					
	Acid return Valve	Bool	%Q2.2		True	True	True					
	Electrolizer 1 Drain valve	Bool	%Q2.3		True	True	True					
	Acid Tank Drain valve	Bool	%Q2.4		True	True	True					
	Service water valve	Bool	%Q2.5		True	True	True					
	BULK Tank Level	Int	%IW304		True	True	True					
	Tag_19	Bool	%M11.1		True	True	True					
	Bulk Tank Level (mm)	Real	%MD152		True	True	True					
	Acid Cleaning Tank	Int	%IW306		True	True	True					
	Tag_21	Bool	%M11.2		True	True	True					
	Tag_22	Word	%MW156		True	True	True					
	Acid cleaning Tank(mm)	Real	%MD158		True	True	True					
	Ph analyser	Int	%IW308		True	True	True					
	Tag_20	Bool	%M11.3		True	True	True					
	Tag_23	Word	%MW162		True	True	True					
	PH analyser (ph)	Real	%MD164		True	True	True					
	Acid Concentration	Int	%IW310		True	True	True					
	Tag_24	Bool	%M11.4		True	True	True					
	Tag_25	Word	%MW168		True	True	True					
	Acid Concentration (%)	Real	%MD170		True	True	True					
	Acid tank level HH	Bool	%M21.6		True	True	True					

	Name	Data type	Address	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Supervision				Comment
	Bulk Acid tank level HH	Bool	%M21.7		True	True	True					
	Bulk Tank Level LL	Bool	%M22.0		True	True	True					
	Acid Cleaning pump 1 started	Bool	%I1.3		True	True	True					
	Acid Cleaning pump 2 started	Bool	%I1.4		True	True	True					
	Acid Cleaning pump 1 Fault	Bool	%M22.1		True	True	True					
	Acid Cleaning pump 2 Fault	Bool	%M22.2		True	True	True					
	Acid Cleaning Pumps repair	Bool	%M1.7		True	True	True					
	system Hypochlorite is clean	Bool	%M2.0		True	True	True					
	counter is ON	Int	%MW174		True	True	True					
	Rectifier	Bool	%M2.1		True	True	True					
	desired current	Bool	%M2.2		True	True	True					
	Hypoclorite temp	Int	%MW54		True	True	True					
	Tag_26	Bool	%M11.5		True	True	True					
	Tag_27	Word	%MW176		True	True	True					
	Hypoclorite temperature (C°)	Real	%MD178		True	True	True					
	Hypoclorite temperature HH	Bool	%M22.3		True	True	True					
	washing pomps started	Bool	%M2.3		True	True	True					
	washing supply valve	Bool	%M2.4		True	True	True					
	washing filters valve	Bool	%M2.5		True	True	True					
	hypochlorite Flow	Int	%MW56		True	True	True					
	Tag_28	Bool	%M11.6		True	True	True					
	Tag_29	Word	%MW182		True	True	True					
	Hypochlorite Flow (m3/h)	Real	%MD184		True	True	True					
	Desired hypochlorit flow	Bool	%M2.6		True	True	True					
	Hypochlorite Flow is not ok	Bool	%M22.4		True	True	True					
	air flow is not ok	Bool	%M22.5		True	True	True					
	Seawater supply valve 2	Bool	%Q2.6		True	True	True					
	filter 2	Bool	%M2.7		True	True	True					
	electrolizer 2	Bool	%M3.0		True	True	True					
	Electrolizer 2 Drain valve	Bool	%Q2.7		True	True	True					
	Bulk Tank Level H	Bool	%M22.6		True	True	True					
	FILTER 1 VALVE	Bool	%Q3.0		True	True	True					
	FILTER 2 VALVE	Bool	%Q3.1		True	True	True					
	Acid tank level LL	Bool	%M22.7		True	True	True					
	seawater flow is not ok	Bool	%M23.0		True	True	True					
	Before FILTER 1 VALVE	Bool	%Q3.2		True	True	True					
	Before FILTER 2 VALVE	Bool	%Q3.3		True	True	True					
	Emergency stop	Bool	%M3.1		True	True	True					
	acid cleaning system started	Bool	%M3.2		True	True	True					
	Hypochloration system START	Bool	%M3.3		True	True	True					
	Tag_30	Bool	%M30.0		True	True	True					
	Tag_31	Bool	%M30.1		True	True	True					
	pump selected	Bool	%M3.4		True	True	True					
	SELECT POMPE 1	Bool	%M3.5		True	True	True					
	SELECT PUMP 2	Bool	%M3.6		True	True	True					
	Tag_32	Bool	%M30.2		True	True	True					
	ALARM OF SYSTEM 1	Int	%MW20		True	True	True					
	ALARM OF SYSTEM 2	Int	%MW22		True	True	True					
	Tag_33	Bool	%M30.3		True	True	True					
	SELECT SHOCKED PUMP 1	Bool	%M3.7		True	True	True					
	SELECT SHOCKED PUMP 2	Bool	%M4.0		True	True	True					
	pumps are not ok	Bool	%M4.1		True	True	True					
	water cleaning is done	Bool	%M4.2		True	True	True					
	water cleaning is started	Bool	%M4.3		True	True	True					
	select booster pump 1	Bool	%M4.4		True	True	True					
	Tag_34	Bool	%M30.4		True	True	True					
	Tag_35	Bool	%M30.5		True	True	True					
	select booster pump 2	Bool	%M4.5		True	True	True					
	Tag_36	Bool	%M30.6		True	True	True					
	select acid pump 1	Bool	%M4.6		True	True	True					

	Name	Data type	Address	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Supervision				Comment
	Tag_37	Bool	%M30.7		True	True	True					
	select acid pump 2	Bool	%M4.7		True	True	True					
	Tag_38	Bool	%M31.0		True	True	True					
	SELECT ELECT 1	Bool	%M5.0		True	True	True					
	SELECT ELECT 2	Bool	%M5.1		True	True	True					
	Tag_39	Bool	%M31.1		True	True	True					
	Tag_40	Bool	%M31.2		True	True	True					
	OPEN VALVE 1 FILTER	Bool	%M5.2		True	True	True					
	Tag_41	Bool	%M31.3		True	True	True					
	OPEN VALVE 2 FILTER	Bool	%M5.3		True	True	True					
	Tag_42	Bool	%M31.4		True	True	True					
	Tag_43	Bool	%M31.5		True	True	True					
	SELECT INTAKE PUMP 1	Bool	%M5.4		True	True	True					
	SELECT INTAKE PUMP 2	Bool	%M5.5		True	True	True					

electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / PLC tags / Table de variables standard [186]

User constants

User constants				
	Name	Data type	Value	Comment

electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / PLC data types / System data types

GraphTransition

GraphTransition Properties							
General							
Name	GraphTransition	**Number**	3	**Type**	UDT	**Language**	
Numbering							
Information							
Title		**Author**		**Comment**		**Family**	
Version		**User-defined ID**					

Name	Data type	Offset	Default value	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
TV	Bool	0.0	false	True	True	False		Transition is valid
TT	Bool	0.1	false	True	True	False		Transition is satisfied
TS	Bool	0.2	false	True	True	False		Transition switches
CF_IV	Bool	0.3	True	True	True	False		The CRIT_FLT entry is invalid
TNO	Int	2.0	0	True	True	False		Indicates the user-defined transition number
CRIT	DWord	4.0	16#0	True	True	False		Status of the maximum 32 LAD/FBD elements of the transition in the current processing cycle
CRIT_OLD	DWord	8.0	16#0	True	True	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	12.0	16#0	True	True	False		Copy of CRIT if an error occurs

electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / PLC data types / System data types

GraphStep

GraphStep Properties							
General							
Name	GraphStep	**Number**	1	**Type**	UDT	**Language**	
Numbering							
Information							
Title		**Author**		**Comment**		**Family**	
Version		**User-defined ID**					

Name	Data type	Offset	Default value	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
S1	Bool	0.0	false	True	True	False		Step is activated
L1	Bool	0.1	false	True	True	False		interlock leaving state
V1	Bool	0.2	false	True	True	False		Supervision entering state
R1	Bool	0.3	false	True	True	False		Reserved
A1	Bool	0.4	false	True	True	False		Error is acknowledged
S0	Bool	0.5	false	True	True	False		Step is deactivated
L0	Bool	0.6	false	True	True	False		Interlock entering state
V0	Bool	0.7	false	True	True	False		Supervision leaving state
X	Bool	1.0	false	True	True	False		Step is active
LA	Bool	1.1	false	True	True	False		Interlock is not satisfied
VA	Bool	1.2	false	True	True	False		Supervision active
RA	Bool	1.3	false	True	True	False		Reserved
AA	Bool	1.4	false	True	True	False		Reserved
SS	Bool	1.5	false	True	True	False		System-internal
LS	Bool	1.6	True	True	True	False		Direct result of the programmed interlock
VS	Bool	1.7	false	True	True	False		Direct result of the programmed supervision
SNO	Int	2.0	0	True	True	False		User step number
T	Time	4.0	T#0ms	True	True	False		Total step activation time
U	Time	8.0	T#0ms	True	True	False		Step activation time without disturbance
CRIT_LOC	DWord	12.0	16#0	True	True	False		Status of the maximum 32 LAD/FBD elements in the interlock in the current processing cycle
CRIT_LOC_ERR	DWord	16.0	16#0	True	True	False		Copy of CRIT_LOC when the interlock leaves the state
CRIT_SUP	DWord	20.0	16#0	True	True	False		Status of the maximum 32 LAD/FBD elements in the supervision in the current processing cycle
SM	Bool	24.0	false	True	True	False		System-internal
LP	Bool	24.1	false	True	True	False		System-internal
LN	Bool	24.2	false	True	True	False		System-internal
VP	Bool	24.3	false	True	True	False		System-internal
VN	Bool	24.4	false	True	True	False		System-internal
H_IL_ERR	Byte	25.0	16#0	True	True	False		System-internal
H_SV_FLT	Byte	26.0	16#0	True	True	False		System-internal
RESERVED	DWord	28.0	16#0	True	True	False		Reserve

electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / Watch and force tables

Table de forçage permanent

Name	Address	Display format	Force value	Comment

electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / PLC supervisions & alarms

PLC alarms

PLC alarms					
Name	M_EVIL	**Type**	Alarm_s	**ID**	
Range	4	**Location**	Acid Cleaning	**Alarm text**	Category : Supervision type : PLC name : FB name : Step name : Step number : Step specific field
Info text		**Alarm class**	No Acknowledgement	**Acknowledgment**	False
Priority	0*	**Display class**	0	**Report**	False
Created by	GRAPH	**Date created**	5/18/2025 11:28 AM	**Last change**	6/5/2025 3:06 PM
Group ID	0	**Additional text 1**		**Additional text 2**	
Additional text 3		**Additional text 4**		**Additional text 5**	
Additional text 6		**Additional text 7**		**Additional text 8**	
Additional text 9					
Name	M_EVIL	**Type**	Alarm_s	**ID**	1610612741
Range	4	**Location**	Acid Cleaning_DB	**Alarm text**	Error : GRAPH-Interlock : $$Cpu-Name$$: Acid Cleaning : @1W%t#SYSTEM_StepNames_3@ : S@1W%03u@ : @1W%t#SYSTEM_In-terlockAlarmTexts_3@
Info text		**Alarm class**	No Acknowledgement	**Acknowledgment**	False
Priority	0	**Display class**	0	**Report**	False
Created by	GRAPH	**Date created**	5/18/2025 11:28 AM	**Last change**	6/5/2025 3:06 PM
Group ID	0	**Additional text 1**		**Additional text 2**	
Additional text 3		**Additional text 4**		**Additional text 5**	
Additional text 6		**Additional text 7**		**Additional text 8**	
Additional text 9					
Name	M_EVSV	**Type**	Alarm_s	**ID**	
Range	4	**Location**	Acid Cleaning	**Alarm text**	Category : Supervision type : PLC name : FB name : Step name : Step number : Step specific field
Info text		**Alarm class**	No Acknowledgement	**Acknowledgment**	False
Priority	0*	**Display class**	0	**Report**	False
Created by	GRAPH	**Date created**	5/18/2025 11:28 AM	**Last change**	6/5/2025 3:06 PM
Group ID	0	**Additional text 1**		**Additional text 2**	
Additional text 3		**Additional text 4**		**Additional text 5**	
Additional text 6		**Additional text 7**		**Additional text 8**	
Additional text 9					
Name	M_EVSV	**Type**	Alarm_s	**ID**	1610612742
Range	4	**Location**	Acid Cleaning_DB	**Alarm text**	Error : GRAPH-Supervision : $$Cpu-Name$$: Acid Cleaning : @1W%t#SYSTEM_StepNames_3@ : S@1W%03u@ : @1W%t#SYSTEM_Su-pervisionAlarmTexts_3@
Info text		**Alarm class**	No Acknowledgement	**Acknowledgment**	False
Priority	0	**Display class**	0	**Report**	False
Created by	GRAPH	**Date created**	5/18/2025 11:28 AM	**Last change**	6/5/2025 3:06 PM
Group ID	0	**Additional text 1**		**Additional text 2**	
Additional text 3		**Additional text 4**		**Additional text 5**	
Additional text 6		**Additional text 7**		**Additional text 8**	
Additional text 9					
Name	M_EVIL	**Type**	Alarm_s	**ID**	
Range	5	**Location**	hypoclorite system	**Alarm text**	Category : Supervision type : PLC name : FB name : Step name : Step number : Step specific field
Info text		**Alarm class**	No Acknowledgement	**Acknowledgment**	False
Priority	0*	**Display class**	0	**Report**	False
Created by	GRAPH	**Date created**	5/6/2025 11:00 AM	**Last change**	6/5/2025 3:06 PM
Group ID	0	**Additional text 1**		**Additional text 2**	
Additional text 3		**Additional text 4**		**Additional text 5**	
Additional text 6		**Additional text 7**		**Additional text 8**	
Additional text 9					
Name	M_EVIL	**Type**	Alarm_s	**ID**	1610612739
Range	5	**Location**	ELECTRO CHLOR STATION_DB	**Alarm text**	Error : GRAPH-Interlock : $$Cpu-Name$$: hypoclorite system : @1W%t#SYSTEM_StepNames_1@ : S@1W%03u@ : @1W%t#SYSTEM_In-terlockAlarmTexts_1@
Info text		**Alarm class**	No Acknowledgement	**Acknowledgment**	False
Priority	0	**Display class**	0	**Report**	False
Created by	GRAPH	**Date created**	5/6/2025 11:01 AM	**Last change**	6/5/2025 3:06 PM
Group ID	0	**Additional text 1**		**Additional text 2**	
Additional text 3		**Additional text 4**		**Additional text 5**	
Additional text 6		**Additional text 7**		**Additional text 8**	
Additional text 9					
Name	M_EVIL	**Type**	Alarm_s	**ID**	1610612743
Range	5	**Location**	hypoclorite system_DB	**Alarm text**	Error : GRAPH-Interlock : $$Cpu-Name$$: hypoclorite system : @1W%t#SYSTEM_StepNames_1@ : S@1W%03u@ : @1W%t#SYSTEM_In-terlockAlarmTexts_1@
Info text		**Alarm class**	No Acknowledgement	**Acknowledgment**	False
Priority	0	**Display class**	0	**Report**	False

Created by	GRAPH	**Date created**	6/4/2025 12:40 PM	**Last change**	6/5/2025 3:06 PM
Group ID	0	**Additional text 1**		**Additional text 2**	
Additional text 3		**Additional text 4**		**Additional text 5**	
Additional text 6		**Additional text 7**		**Additional text 8**	
Additional text 9					
Name	M_EVSV	**Type**	Alarm_s	**ID**	
Range	5	**Location**	hypoclorite system	**Alarm text**	Category : Supervision type : PLC name : FB name : Step name : Step number : Step specific field
Info text		**Alarm class**	No Acknowledgement	**Acknowledgment**	False
Priority	0*	**Display class**	0	**Report**	False
Created by	GRAPH	**Date created**	5/6/2025 11:00 AM	**Last change**	6/5/2025 3:06 PM
Group ID	0	**Additional text 1**		**Additional text 2**	
Additional text 3		**Additional text 4**		**Additional text 5**	
Additional text 6		**Additional text 7**		**Additional text 8**	
Additional text 9					
Name	M_EVSV	**Type**	Alarm_s	**ID**	1610612740
Range	5	**Location**	ELECTRO CHLOR STATION_DB	**Alarm text**	Error : GRAPH-Supervision : $$Cpu-Name$$: hypoclorite system : @1W%t#SYSTEM_StepNames_1@ : S@1W%03u@ : @1W%t#SYSTEM_Su-pervisionAlarmTexts_1@
Info text		**Alarm class**	No Acknowledgement	**Acknowledgment**	False
Priority	0	**Display class**	0	**Report**	False
Created by	GRAPH	**Date created**	5/6/2025 11:01 AM	**Last change**	6/5/2025 3:06 PM
Group ID	0	**Additional text 1**		**Additional text 2**	
Additional text 3		**Additional text 4**		**Additional text 5**	
Additional text 6		**Additional text 7**		**Additional text 8**	
Additional text 9					
Name	M_EVSV	**Type**	Alarm_s	**ID**	1610612744
Range	5	**Location**	hypoclorite system_DB	**Alarm text**	Error : GRAPH-Supervision : $$Cpu-Name$$: hypoclorite system : @1W%t#SYSTEM_StepNames_1@ : S@1W%03u@ : @1W%t#SYSTEM_Su-pervisionAlarmTexts_1@
Info text		**Alarm class**	No Acknowledgement	**Acknowledgment**	False
Priority	0	**Display class**	0	**Report**	False
Created by	GRAPH	**Date created**	6/4/2025 12:40 PM	**Last change**	6/5/2025 3:06 PM
Group ID	0	**Additional text 1**		**Additional text 2**	
Additional text 3		**Additional text 4**		**Additional text 5**	
Additional text 6		**Additional text 7**		**Additional text 8**	
Additional text 9					
Name	M_EVIL	**Type**	Alarm_s	**ID**	
Range	5	**Location**	SEAWATER Intake	**Alarm text**	Category : Supervision type : PLC name : FB name : Step name : Step number : Step specific field
Info text		**Alarm class**	No Acknowledgement	**Acknowledgment**	False
Priority	0*	**Display class**	0	**Report**	False
Created by	GRAPH	**Date created**	5/6/2025 10:59 AM	**Last change**	6/5/2025 3:06 PM
Group ID	0	**Additional text 1**		**Additional text 2**	
Additional text 3		**Additional text 4**		**Additional text 5**	
Additional text 6		**Additional text 7**		**Additional text 8**	
Additional text 9					
Name	M_EVIL	**Type**	Alarm_s	**ID**	1610612737
Range	5	**Location**	SEAWATER Intake_DB	**Alarm text**	Error : GRAPH-Interlock : $$Cpu-Name$$: SEAWATER Intake : @1W%t#SYSTEM_StepNames_2@ : S@1W%03u@ : @1W%t#SYSTEM_In-terlockAlarmTexts_2@
Info text		**Alarm class**	No Acknowledgement	**Acknowledgment**	False
Priority	0	**Display class**	0	**Report**	False
Created by	GRAPH	**Date created**	5/6/2025 11:01 AM	**Last change**	6/5/2025 3:06 PM
Group ID	0	**Additional text 1**		**Additional text 2**	
Additional text 3		**Additional text 4**		**Additional text 5**	
Additional text 6		**Additional text 7**		**Additional text 8**	
Additional text 9					
Name	M_EVSV	**Type**	Alarm_s	**ID**	
Range	5	**Location**	SEAWATER Intake	**Alarm text**	Category : Supervision type : PLC name : FB name : Step name : Step number : Step specific field
Info text		**Alarm class**	No Acknowledgement	**Acknowledgment**	False
Priority	0*	**Display class**	0	**Report**	False
Created by	GRAPH	**Date created**	5/6/2025 10:59 AM	**Last change**	6/5/2025 3:06 PM
Group ID	0	**Additional text 1**		**Additional text 2**	
Additional text 3		**Additional text 4**		**Additional text 5**	
Additional text 6		**Additional text 7**		**Additional text 8**	
Additional text 9					
Name	M_EVSV	**Type**	Alarm_s	**ID**	1610612738
Range	5	**Location**	SEAWATER Intake_DB	**Alarm text**	Error : GRAPH-Supervision : $$Cpu-Name$$: SEAWATER Intake : @1W%t#SYSTEM_StepNames_2@ : S@1W%03u@ : @1W%t#SYSTEM_Su-pervisionAlarmTexts_2@
Info text		**Alarm class**	No Acknowledgement	**Acknowledgment**	False
Priority	0	**Display class**	0	**Report**	False
Created by	GRAPH	**Date created**	5/6/2025 11:01 AM	**Last change**	6/5/2025 3:06 PM

Group ID	0	**Additional text 1**		**Additional text 2**	
Additional text 3		**Additional text 4**		**Additional text 5**	
Additional text 6		**Additional text 7**		**Additional text 8**	
Additional text 9					

electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / PLC supervisions & alarms

User diagnostics alarms

User diagnostics alarms
No entries

electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / PLC supervisions & alarms

System alarms

This folder is empty.

electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP]

PLC alarm text lists

SYSTEM_StepNames_1			
Selection	Decimal	**ID**	4095
Comment			

Text list entries of SYSTEM_StepNames_1		
Range from	**Range to**	**Entry**
1	1	Step1
2	2	Step2
3	3	Step3
4	4	Step4
5	5	Step5
6	6	Step6
7	7	Step7
8	8	Step8
10	10	Step10
11	11	Step11
12	12	Step12
13	13	Step13
15	15	Step15
16	16	Step16
17	17	Step17
18	18	Step18
20	20	Step20
21	21	Step21
22	22	Step22
23	23	Step23
24	24	Step24
26	26	Step26
27	27	Step27
29	29	Step29
30	30	Step30
9	9	Step9
33	33	Step33
28	28	Step28
34	34	Step34
35	35	Step35
36	36	Step36
37	37	Step37
38	38	Step38
39	39	Step39
42	42	Step42
45	45	Step45
31	31	Step31
32	32	Step32
47	47	Step47
40	40	Step40
43	43	Step43
44	44	Step44
46	46	Step46
48	48	Step48
19	19	Step19
25	25	Step25
41	41	Step41
49	49	Step49
50	50	Step50

SYSTEM_SupervisionAlarmTexts_1			
Selection	Decimal	**ID**	4094
Comment			

Text list entries of SYSTEM_SupervisionAlarmTexts_1		
Range from	**Range to**	**Entry**
1	1	
2	2	
3	3	
4	4	
5	5	
6	6	
7	7	
8	8	
10	10	
11	11	
12	12	
13	13	
15	15	
16	16	
17	17	
18	18	
20	20	
21	21	
22	22	
23	23	

Range from	Range to	Entry
24	24	
26	26	
27	27	
29	29	
30	30	
9	9	
33	33	
28	28	
34	34	
35	35	
36	36	
37	37	
38	38	
39	39	
42	42	
45	45	
31	31	
32	32	
47	47	
40	40	
43	43	
44	44	
46	46	
48	48	
19	19	
25	25	
41	41	
49	49	
50	50	

SYSTEM_InterlockAlarmTexts_1			
Selection	Decimal	**ID**	4093
Comment			

Text list entries of SYSTEM_InterlockAlarmTexts_1		
Range from	**Range to**	**Entry**
1	1	
2	2	
3	3	
4	4	
5	5	
6	6	
7	7	
8	8	
10	10	
11	11	
12	12	
13	13	
15	15	
16	16	
17	17	
18	18	
20	20	
21	21	
22	22	
23	23	
24	24	
26	26	
27	27	
29	29	
30	30	
9	9	
33	33	
28	28	
34	34	
35	35	
36	36	
37	37	
38	38	
39	39	
42	42	
45	45	
31	31	
32	32	
47	47	
40	40	
43	43	
44	44	
46	46	
48	48	
19	19	
25	25	
41	41	
49	49	
50	50	

SYSTEM_StepNames_2			
Selection	Decimal	**ID**	4092
Comment			

Text list entries of SYSTEM_StepNames_2		
Range from	**Range to**	**Entry**
1	1	Step1
7	7	Step7
10	10	Step10
11	11	Step11
12	12	Step12
13	13	Step13
14	14	Step14
15	15	Step15
16	16	Step16
19	19	Step19
20	20	Step20
21	21	Step21
17	17	Step17
18	18	Step18
22	22	Step22
23	23	Step23
24	24	Step24
25	25	Step25
26	26	Step26
27	27	Step27
28	28	Step28

SYSTEM_SupervisionAlarmTexts_2			
Selection	Decimal	**ID**	4091
Comment			

Text list entries of SYSTEM_SupervisionAlarmTexts_2		
Range from	**Range to**	**Entry**
1	1	
7	7	
10	10	
11	11	
12	12	
13	13	
14	14	
15	15	
16	16	
19	19	
20	20	
21	21	
17	17	
18	18	
22	22	
23	23	
24	24	
25	25	
26	26	
27	27	
28	28	

SYSTEM_InterlockAlarmTexts_2			
Selection	Decimal	**ID**	4090
Comment			

Text list entries of SYSTEM_InterlockAlarmTexts_2		
Range from	**Range to**	**Entry**
1	1	
7	7	
10	10	
11	11	
12	12	
13	13	
14	14	
15	15	
16	16	
19	19	
20	20	
21	21	
17	17	
18	18	
22	22	
23	23	
24	24	
25	25	
26	26	
27	27	
28	28	

SYSTEM_StepNames_3			
Selection	Decimal	**ID**	4089
Comment			

Text list entries of SYSTEM_StepNames_3		
Range from	**Range to**	**Entry**
1	1	Step1
2	2	Step2
3	3	Step3
4	4	Step4
5	5	Step5
6	6	Step6
7	7	Step7
8	8	Step8
9	9	Step9
10	10	Step10
11	11	Step11
12	12	Step12
15	15	Step15
16	16	Step16
17	17	Step17
18	18	Step18
19	19	Step19
20	20	Step20
21	21	Step21
22	22	Step22
23	23	Step23
24	24	Step24
26	26	Step26
27	27	Step27
28	28	Step28
13	13	Step13

SYSTEM_SupervisionAlarmTexts_3			
Selection	Decimal	**ID**	4088
Comment			

Text list entries of SYSTEM_SupervisionAlarmTexts_3		
Range from	**Range to**	**Entry**
1	1	
2	2	
3	3	
4	4	
5	5	
6	6	
7	7	
8	8	
9	9	
10	10	
11	11	
12	12	
15	15	
16	16	
17	17	
18	18	
19	19	
20	20	
21	21	
22	22	
23	23	
24	24	
26	26	
27	27	
28	28	
13	13	

SYSTEM_InterlockAlarmTexts_3			
Selection	Decimal	**ID**	4087
Comment			

Text list entries of SYSTEM_InterlockAlarmTexts_3		
Range from	**Range to**	**Entry**
1	1	
2	2	
3	3	
4	4	
5	5	
6	6	
7	7	
8	8	
9	9	
10	10	
11	11	
12	12	
15	15	
16	16	
17	17	
18	18	
19	19	
20	20	
21	21	

Range from	Range to	Entry
22	22	
23	23	
24	24	
26	26	
27	27	
28	28	
13	13	

electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / Local modules

PS 305 2A Outdoor_1

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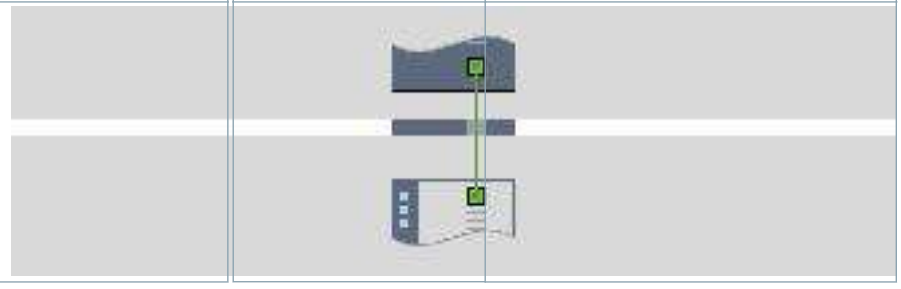
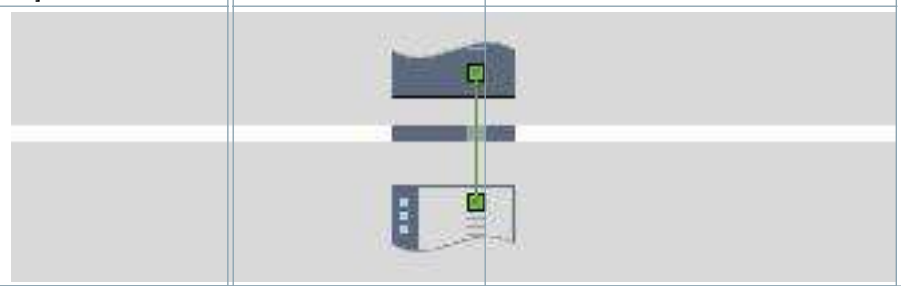
electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / Local modules

PLC_1 [CPU 317-2 PN/DP]

PLC_1					
General					
Name	PLC_1	**Author**	malki.lamine	**Comment**	
Rack	0	**Slot**	2		
General\Catalog information					
Short designation	CPU 317-2 PN/DP	**Description**	Work memory 1024KB; 0.025ms/1000 instructions; PROFINET interface; S7 communication (loadable FBs/FCs); PROFINET IO controller; supports RT/IRT; 2 ports, MRP; PROFINET CBA; PROFINET CBA proxy; transport protocol TCP/IP; combined MPI/DP interface (MPI or DP master or DP slave); multi-tier configuration up to 32 modules; constant bus cycle time; routing; firmware V3.2	**Article number**	6ES7 317-2EK14-0AB0
Firmware version	V3.2		False		
General\Identification & Maintenance					
Plant designation		**Location identifier**			
MPI/DP interface [X1]\General					
Name	Interface MPI/DP_1	**Comment**			
MPI/DP interface [X1]\MPI address\Interface networked with					
Subnet:	Not networked				
MPI/DP interface [X1]\MPI address\Parameters					
Interface type:	Mpi	**Address:**	2	**Highest address:**	
Transmission speed:					
MPI/DP interface [X1]\Time-of-day synchronization\SIMATIC mode					
Type of synchronization	None	**Time interval**	None		
MPI/DP interface [X1]\Diagnostics addresses\Diagnostics addresses					
Start address	8191				
PROFINET interface [X2]\General					
Name	Interface PROFINET_1	**Comment**			
PROFINET interface [X2]\Ethernet addresses\Interface networked with					
Subnet:	PN/IE_1				
PROFINET interface [X2]\Ethernet addresses\Internet protocol version 4 (IPv4)					
IP configuration	Set IP address in the project	**IP address:**	192.168.0.1	**Subnet mask:**	255.255.255.0
Use router	False				
PROFINET interface [X2]\Ethernet addresses\PROFINET					
PROFINET device name is set directly at the device	False	**Generate PROFINET device name automatically**	True	**PROFINET device name:**	plc_1
Converted name:	plcxb1d0ed	**Device number:**	0		
PROFINET interface [X2]\Time-of-day synchronization\NTP mode					
Enable time synchronization via NTP server	False		IP addresses	**NTP Server 1**	0.0.0.0
NTP Server 2	0.0.0.0	**NTP Server 3**	0.0.0.0	**NTP Server 4**	0.0.0.0
Update interval	10s				
PROFINET interface [X2]\Operating mode					
IO controller	True	**IO system**		**Device number**	0
IO device	False				
PROFINET interface [X2]\Advanced options\Interface options					
Call the user program if communication errors occur	False	**Support device replacement without exchangeable medium**	True	**Use IEC V2.2 LLDP mode**	True
Keep-Alive connection monitoring:	30s				
PROFINET interface [X2]\Advanced options\Media redundancy					
MRP domain	mrpdomain-1	**Media redundancy role:**	Not device in the ring		
PROFINET interface [X2]\Advanced options\Real time settings\IO communication					
Send clock:	1.000ms				
PROFINET interface [X2]\Advanced options\Real time settings\Synchronization					
Sync domain:	Sync-Domain_1	**Synchronization role:**	Unsynchronized	**RT class:**	RT,IRT
PROFINET interface [X2]\Advanced options\Real time settings\Real time options					
Calculated bandwidth for cyclic IO data:	0.000ms	**Calculated bandwidth for cyclic IO data:**	0.000%		
PROFINET interface [X2]\Advanced options\Port [X2 P1]\General					
Name	Port_1	**Comment**			
PROFINET interface [X2]\Advanced options\Port [X2 P1]\Port interconnection\Local port:					
Local port:	PLC_1\Interface PROFINET_1 [X2]\Port_1 [X2 P1 R]	**Medium:**	Copper	**Cable name:**	---
					

PROFINET interface [X2]\Advanced options\Port [X2 P1]\Port interconnection\Partner port:					
	Monitoring of partner port is not possible	**Partner port:**	Any partner		
PROFINET interface [X2]\Advanced options\Port [X2 P1]\Port options\Activate					
Activate this port for use	True				
PROFINET interface [X2]\Advanced options\Port [X2 P1]\Port options\Connection					
Transmission rate / duplex:	Automatic	**Monitor**	False	**Enable autonegotiation**	True
PROFINET interface [X2]\Advanced options\Port [X2 P1]\Port options\Boundaries					
End of detection of accessible devices	False	**End of topology discovery**	False	**End of the sync domain**	False
PROFINET interface [X2]\Advanced options\Port [X2 P1]\Diagnostics addresses\Diagnostics addresses					
Start address	8189				
PROFINET interface [X2]\Advanced options\Port [X2 P2]\General					
Name	Port_2	**Comment**			
PROFINET interface [X2]\Advanced options\Port [X2 P2]\Port interconnection\Local port:					
Local port:	PLC_1\Interface PROFINET_1 [X2]\Port_2 [X2 P2 R]	**Medium:**	Copper	**Cable name:**	---
					
PROFINET interface [X2]\Advanced options\Port [X2 P2]\Port interconnection\Partner port:					
	Monitoring of partner port is not possible	**Partner port:**	Any partner		
PROFINET interface [X2]\Advanced options\Port [X2 P2]\Port options\Activate					
Activate this port for use	True				
PROFINET interface [X2]\Advanced options\Port [X2 P2]\Port options\Connection					
Transmission rate / duplex:	Automatic	**Monitor**	False	**Enable autonegotiation**	True
PROFINET interface [X2]\Advanced options\Port [X2 P2]\Port options\Boundaries					
End of detection of accessible devices	False	**End of topology discovery**	False	**End of the sync domain**	False
PROFINET interface [X2]\Advanced options\Port [X2 P2]\Diagnostics addresses\Diagnostics addresses					
Start address	8188				
PROFINET interface [X2]\Diagnostics addresses\Diagnostics addresses					
Start address	8190				
Startup					
Startup if preset configuration does not match actual configuration	True	**Startup after POWER ON**	Warm restart		
Startup\Monitoring time for					
Finished messages of the modules [100ms]	650x 100 ms	**Transfer of parameters to modules [100ms]**	100x 100 ms		
Cycle					
Cycle monitoring time [ms]	150ms	**Cycle load due to communication [%]**	20%	**Size of the process image input:**	256
Size of the process image output:	256	**OB85 call if I/O access error occurs**	No OB85 call		
Cycle\Prioritized OCM communication					
Prioritized OCM communication	False				
Clock memory					
Clock memory	False	**Memory byte**	0		

Interrupts\Time-of-day interrupts

OB number	Priority	Active	Execution	Start time
OB 10	2	False	None	1994-01-01 00:00:00.000

Interrupts\Time-delay interrupts

OB number	Priority	Process image partition(s)
OB 20	3	None
OB 21	4	None

Interrupts\Cyclic interrupts

OB number	Priority	Execution	Phase offset	Unit
OB 32	9	1000	0	ms
OB 33	10	500	0	ms
OB 34	11	200	0	ms
OB 35	12	100	0	ms

Interrupts\Hardware interrupts

OB number	Priority	Process image partition(s)
OB 40	16	None

Interrupts\Interrupts for DPV1

OB number	Priority
OB 55	2
OB 56	2
OB 57	2

Interrupts\Isochronous mode interrupts

OB number	Priority	Distributed I/O	Process image partition(s)	Delay time (ms)	Automatic setting
OB 61	25	0		0.000	True

Interrupts\Isochronous mode interrupts\OB 61					
Application cycle:	0ms	**Delay time:**	0.000ms	**Automatic setting**	True
Distributed I/O:	0				

Interrupts\Isochronous mode interrupts\OB 61\Process image partition	
PIP:	

Interrupts\Asynchronous error interrupts

OB number	Priority
OB 82	26
OB 83	26
OB 85	26
OB 86	26
OB 87	26

Diagnostics system					
Report cause of STOP	True	**Number of alarms in the diagnostics buffer**	10		

System diagnostics\General	
Activate system diagnostics for this device	False

Time of day	
Correction factor	0ms

Time of day\Synchronization on PLC					
Type of synchronization	None	**Time interval**	None		

Time of day\Synchronization on MPI					
Type of synchronization	None	**Time interval**	None		

Web server\General					
Activate web server on this module	False	**Permit access only with HTTPS**	False		

Web server\Automatic update					
Enable automatic update	False	**Update interval**	0s		

Web server\Languages

Active	Web server language	Assign project language
False	German	None
False	English	None
False	French	None
False	Spanish	None
False	Italian	None
False	Japanese	None
False	Chinese (simplified)	None

Web server\User management

User name	User rights
Everybody	

Web server\User-defined web pages

Application name	HTML source path	Default HTML page	Files with dynamic content	Web DB number	Fragment DB number
		index.htm	.htm;.html	333	334

Web server\Display class of alarm

Display class	Active
0	True
1	True
2	True
3	True
4	True
5	True
6	True
7	True
8	True
9	True
10	True
11	True
12	True
13	True
14	True
15	True
16	True

Retentive memory					
Number of memory bytes starting at MB 0	16	**Number of S7 timers starting at T 0**	0	**Number of S7 counters starting at C 0**	8

Protection			
Password		**Confirm password**	

Protection\	
Level of protection	No protection

Protection\ \Can be canceled with password	
Can be canceled with password	False

Connection resources					
PG communication:	1	**OP communication:**	1	**S7 basic communication:**	0
S7 communication:	0	**Maximum number of S7 connection resources:**	32		

Overview of addresses\Overview of addresses\Overview of addresses					
Inputs	True	**Outputs**	True	**Address gaps**	False
Slot	True				

Type	Addr. from	Addr. to	Module	PIP	Device name	Device number	Size	Master / IO system	Rack	Slot
I*	8191	8191	Interface MPI/DP_1	---	PLC_1 [CPU 317-2 PN/DP]	-	0 Bits	-	0	2 X1
I*	8189	8189	Port_1	---	PLC_1 [CPU 317-2 PN/DP]	-	0 Bits	-	0	2 X2 P1 R
I*	8188	8188	Port_2	---	PLC_1 [CPU 317-2 PN/DP]	-	0 Bits	-	0	2 X2 P2 R
I*	8190	8190	Interface PROFINET_1	---	PLC_1 [CPU 317-2 PN/DP]	-	0 Bits	-	0	2 X2
I	0	1	DI 16x24VDC_1	OB1-PI	PLC_1 [CPU 317-2 PN/DP]	-	2 Bytes	-	0	4
O	2	3	DO 16x24VDC/0.5A_2	OB1-PI	PLC_1 [CPU 317-2 PN/DP]	-	2 Bytes	-	0	8
O	0	1	DO 16x24VDC/0.5A_1	OB1-PI	PLC_1 [CPU 317-2 PN/DP]	-	2 Bytes	-	0	5
O	288	295	AO 4x16BIT_1	OB1-PI	PLC_1 [CPU 317-2 PN/DP]	-	8 Bytes	-	0	7
I	288	303	AI 8x16BIT_1	OB1-PI	PLC_1 [CPU 317-2 PN/DP]	-	16 Bytes	-	0	6
I	304	319	AI 8x16BIT_2	OB1-PI	PLC_1 [CPU 317-2 PN/DP]	-	16 Bytes	-	0	9
I	352	367	AI 8x16BIT_3	OB1-PI	PLC_1 [CPU 317-2 PN/DP]	-	16 Bytes	-	0	10
O	28	29	DO 16x24/48VUC_1	OB1-PI	PLC_1 [CPU 317-2 PN/DP]	-	2 Bytes	-	0	11

electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / Local modules

DI 16x24VDC_1

DI 16x24VDC_1					
General					
Name	DI 16x24VDC_1	**Author**	malki.lamine	**Comment**	
Rack	0	**Slot**	4		
General\Catalog information					
Short designation	DI 16x24VDC	**Description**	Digital input module DI16 x 24VDC, with hardware and diagnostics alarm, grouping 16, supports isochronous mode; single channel diagnostics possible; also available as SIPLUS module with article number 6AG1 321-7BH01-2AB0.	**Article number**	6ES7 321-7BH01-0AB0 HF
Firmware version			False		
Inputs\General\Diagnostics					
Enable diagnostics interrupt	Deactivated				
Inputs\General\Hardware interrupts					
Enable hardware interrupt	Deactivated	**RidPrefixHwInterrupt**	49152	**Event name:**	0
Hardware interrupt:	0	**Hardware interrupt**	Hardware interrupt	**HardwareInterruptChannelForModule**	32768
HardwareInterruptEventIdNull	0				
Inputs\General\Input parameters					
Input delay / voltage type	3 (DC)ms				
Inputs\Channel 0 - 7					
No sensor supply	Deactivated				
Inputs\Channel 0 - 7\Channel 0\Diagnostics					
Wire break	Deactivated				
Inputs\Channel 0 - 7\Channel 0\Hardware interrupts					
Enable rising edge detection	Deactivated	**Enable falling edge detection**	Deactivated		
Inputs\Channel 0 - 7\Channel 1\Diagnostics					
Wire break	Deactivated				
Inputs\Channel 0 - 7\Channel 1\Hardware interrupts					
Enable rising edge detection	Deactivated	**Enable falling edge detection**	Deactivated		
Inputs\Channel 0 - 7\Channel 2\Diagnostics					
Wire break	Deactivated				
Inputs\Channel 0 - 7\Channel 2\Hardware interrupts					
Enable rising edge detection	Deactivated	**Enable falling edge detection**	Deactivated		
Inputs\Channel 0 - 7\Channel 3\Diagnostics					
Wire break	Deactivated				
Inputs\Channel 0 - 7\Channel 3\Hardware interrupts					
Enable rising edge detection	Deactivated	**Enable falling edge detection**	Deactivated		
Inputs\Channel 0 - 7\Channel 4\Diagnostics					
Wire break	Deactivated				
Inputs\Channel 0 - 7\Channel 4\Hardware interrupts					
Enable rising edge detection	Deactivated	**Enable falling edge detection**	Deactivated		
Inputs\Channel 0 - 7\Channel 5\Diagnostics					
Wire break	Deactivated				
Inputs\Channel 0 - 7\Channel 5\Hardware interrupts					
Enable rising edge detection	Deactivated	**Enable falling edge detection**	Deactivated		
Inputs\Channel 0 - 7\Channel 6\Diagnostics					
Wire break	Deactivated				
Inputs\Channel 0 - 7\Channel 6\Hardware interrupts					
Enable rising edge detection	Deactivated	**Enable falling edge detection**	Deactivated		
Inputs\Channel 0 - 7\Channel 7\Diagnostics					
Wire break	Deactivated				
Inputs\Channel 0 - 7\Channel 7\Hardware interrupts					
Enable rising edge detection	Deactivated	**Enable falling edge detection**	Deactivated		
Inputs\Channel 8 - 15					
No sensor supply	Deactivated				
Inputs\Channel 8 - 15\Channel 8\Diagnostics					
Wire break	Deactivated				
Inputs\Channel 8 - 15\Channel 8\Hardware interrupts					
Enable rising edge detection	Deactivated	**Enable falling edge detection**	Deactivated		
Inputs\Channel 8 - 15\Channel 9\Diagnostics					
Wire break	Deactivated				
Inputs\Channel 8 - 15\Channel 9\Hardware interrupts					
Enable rising edge detection	Deactivated	**Enable falling edge detection**	Deactivated		
Inputs\Channel 8 - 15\Channel 10\Diagnostics					
Wire break	Deactivated				

Inputs\Channel 8 - 15\Channel 10\Hardware interrupts					
Enable rising edge detection	Deactivated	**Enable falling edge detection**	Deactivated		
Inputs\Channel 8 - 15\Channel 11\Diagnostics					
Wire break	Deactivated				
Inputs\Channel 8 - 15\Channel 11\Hardware interrupts					
Enable rising edge detection	Deactivated	**Enable falling edge detection**	Deactivated		
Inputs\Channel 8 - 15\Channel 12\Diagnostics					
Wire break	Deactivated				
Inputs\Channel 8 - 15\Channel 12\Hardware interrupts					
Enable rising edge detection	Deactivated	**Enable falling edge detection**	Deactivated		
Inputs\Channel 8 - 15\Channel 13\Diagnostics					
Wire break	Deactivated				
Inputs\Channel 8 - 15\Channel 13\Hardware interrupts					
Enable rising edge detection	Deactivated	**Enable falling edge detection**	Deactivated		
Inputs\Channel 8 - 15\Channel 14\Diagnostics					
Wire break	Deactivated				
Inputs\Channel 8 - 15\Channel 14\Hardware interrupts					
Enable rising edge detection	Deactivated	**Enable falling edge detection**	Deactivated		
Inputs\Channel 8 - 15\Channel 15\Diagnostics					
Wire break	Deactivated				
Inputs\Channel 8 - 15\Channel 15\Hardware interrupts					
Enable rising edge detection	Deactivated	**Enable falling edge detection**	Deactivated		
I/O addresses\Input addresses					
Start address	0.0	**End address**	1.7	**Process image**	OB1-PI
Interrupt OB number	40				
Anchor (RedundantIOPartnerMenu)					
The TreeNode RedundantIOPartnerMenu was not filled by some ACF					

electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / Local modules

DO 16x24VDC/0.5A_1

DO 16x24VDC/0.5A_1					
General					
Name	DO 16x24VDC/0.5A_1	**Author**	malki.lamine	**Comment**	
Rack	0	**Slot**	5		
General\Catalog information					
Short designation	DO 16x24VDC/0.5A	**Description**	Digital output module, DO16 x 24VDC / 0.5A, with diagnostics, parameters can be reassigned online, redundancy	**Article number**	6ES7 322-8BH10-0AB0
Firmware version			False		
General\Identification & Maintenance					
Plant designation		**Location identifier**		**Installation date**	2025-04-30 15:02:06.203
Additional information					
Outputs					
Enable diagnostics interrupt	Deactivated	**Reaction to CPU STOP**	Substitute a value		
Outputs\Output 0 - 3					
No load voltage L+	Deactivated	**Diagnostics: Discrepancy error**	Deactivated		
Outputs\Output 0 - 3\Channel 0					
Apply substitute value "1"	Deactivated	**Group diagnostics**	Active		
Outputs\Output 0 - 3\Channel 1					
Apply substitute value "1"	Deactivated	**Group diagnostics**	Active		
Outputs\Output 0 - 3\Channel 2					
Apply substitute value "1"	Deactivated	**Group diagnostics**	Active		
Outputs\Output 0 - 3\Channel 3					
Apply substitute value "1"	Deactivated	**Group diagnostics**	Active		
Outputs\Output 4 - 7					
No load voltage L+	Deactivated	**Diagnostics: Discrepancy error**	Deactivated		
Outputs\Output 4 - 7\Channel 4					
Apply substitute value "1"	Deactivated	**Group diagnostics**	Active		
Outputs\Output 4 - 7\Channel 5					
Apply substitute value "1"	Deactivated	**Group diagnostics**	Active		
Outputs\Output 4 - 7\Channel 6					
Apply substitute value "1"	Deactivated	**Group diagnostics**	Active		
Outputs\Output 4 - 7\Channel 7					
Apply substitute value "1"	Deactivated	**Group diagnostics**	Active		
Outputs\Output 8 - 11					
No load voltage L+	Deactivated	**Diagnostics: Discrepancy error**	Deactivated		
Outputs\Output 8 - 11\Channel 8					
Apply substitute value "1"	Deactivated	**Group diagnostics**	Active		
Outputs\Output 8 - 11\Channel 9					
Apply substitute value "1"	Deactivated	**Group diagnostics**	Active		
Outputs\Output 8 - 11\Channel 10					
Apply substitute value "1"	Deactivated	**Group diagnostics**	Active		
Outputs\Output 8 - 11\Channel 11					
Apply substitute value "1"	Deactivated	**Group diagnostics**	Active		
Outputs\Output 12 - 15					
No load voltage L+	Deactivated	**Diagnostics: Discrepancy error**	Deactivated		
Outputs\Output 12 - 15\Channel 12					
Apply substitute value "1"	Deactivated	**Group diagnostics**	Active		
Outputs\Output 12 - 15\Channel 13					
Apply substitute value "1"	Deactivated	**Group diagnostics**	Active		
Outputs\Output 12 - 15\Channel 14					
Apply substitute value "1"	Deactivated	**Group diagnostics**	Active		
Outputs\Output 12 - 15\Channel 15					
Apply substitute value "1"	Deactivated	**Group diagnostics**	Active		
I/O addresses\Output addresses					
Start address	0.0	**End address**	1.7	**Process image**	OB1-PI

electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / Local modules

AI 8x16BIT_1

AI 8x16BIT_1					
General					
Name	AI 8x16BIT_1	**Author**	malki.lamine	**Comment**	
Rack	0	**Slot**	6		
General\Catalog information					
Short designation	AI 8x16BIT	**Description**	Analog input module AI8 x U/I; 16 bits of resolution; accuracy appr. 0.1%; grouping 2; common mode voltage appr. 75VDC; configurable diagnostics; hardware interrupts; 40-pin front connector	**Article number**	6ES7 331-7NF10-0AB0
Firmware version			False		
Inputs\General\Diagnostics					
Diagnostics interrupt	Deactivated				
Inputs\General\Hardware interrupt					
Hardware interrupt at end of scan cycle	Deactivated				
Inputs\General\Hardware interrupt					
Hardware interrupt when limit violated	Deactivated	**RidPrefixHwInterrupt**	49152	**Event name:**	0
Hardware interrupt:	0	**Hardware interrupt**	Hardware interrupt	**HardwareInterruptChannelForModule**	32768
HardwareInterruptEventIdNull	0				
Inputs\Measuring					
Operating mode	8 channels	**Update time**	190ms		
Inputs\Channel group 0 - 1\Diagnostics					
Group diagnostics	Deactivated	**Check for wire break**	Deactivated		
Inputs\Channel group 0 - 1\Measuring					
Measurement type	Voltage	**Measuring range**	+/- 10 V	**Interference frequency suppression**	50/60/400Hz
Integration time	2/16/20ms	**Smoothing**	None		
Inputs\Channel group 0 - 1\Hardware interrupt\Channel 0					
High limit		**Low limit**			
Inputs\Channel group 0 - 1\Hardware interrupt\Channel 1					
High limit		**Low limit**			
Inputs\Channel group 2 - 3\Diagnostics					
Group diagnostics	Deactivated	**Check for wire break**	Deactivated		
Inputs\Channel group 2 - 3\Measuring					
Measurement type	Voltage	**Measuring range**	+/- 10 V	**Interference frequency suppression**	50/60/400Hz
Integration time	2/16/20ms	**Smoothing**	None		
Inputs\Channel group 2 - 3\Hardware interrupt\Channel 2					
High limit		**Low limit**			
Inputs\Channel group 2 - 3\Hardware interrupt\Channel 3					
High limit		**Low limit**			
Inputs\Channel group 4 - 5\Diagnostics					
Group diagnostics	Deactivated	**Check for wire break**	Deactivated		
Inputs\Channel group 4 - 5\Measuring					
Measurement type	Voltage	**Measuring range**	+/- 10 V	**Interference frequency suppression**	50/60/400Hz
Integration time	2/16/20ms	**Smoothing**	None		
Inputs\Channel group 4 - 5\Trigger for hardware interrupt\Channel 4					
High limit		**Low limit**			
Inputs\Channel group 4 - 5\Trigger for hardware interrupt\Channel 5					
High limit		**Low limit**			
Inputs\Channel group 6 - 7\Diagnostics					
Group diagnostics	Deactivated	**Check for wire break**	Deactivated		
Inputs\Channel group 6 - 7\Measuring					
Measurement type	Voltage	**Measuring range**	+/- 10 V	**Interference frequency suppression**	50/60/400Hz
Integration time	2/16/20ms	**Smoothing**	None		
Inputs\Channel group 6 - 7\Trigger for hardware interrupt\Channel 6					
High limit		**Low limit**			
Inputs\Channel group 6 - 7\Trigger for hardware interrupt\Channel 7					
High limit		**Low limit**			
I/O addresses\Input addresses					
Start address	288	**End address**	303	**Process image**	None
Interrupt OB number	40				

electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / Local modules

AO 4x16BIT_1

AO 4x16BIT_1					
General					
Name	AO 4x16BIT_1	**Author**	malki.lamine	**Comment**	
Rack	0	**Slot**	7		
General\Catalog information					
Short designation	AO 4x16BIT	**Description**	Analog output module AO4 x U/I 16bits of resolution; accuracy appr. 0.2%; grouping 1; galvanic isolation 200VDC; configurable diagnostics; isochronous mode; configurable substitute value for output; high speed with 4 channels with up to 0.2ms; 20-pin front connector	**Article number**	6ES7 332-7ND02-0AB0
Firmware version			False		
Outputs\Enable					
Diagnostics interrupt	Deactivated				
Outputs\Channel 0\Diagnostics					
Group diagnostics	Deactivated				
Outputs\Channel 0\Output					
Output type	Voltage	**Output range**	+/- 10 V	**Reaction to CPU STOP**	Output has no current or voltage
Substitute value					
Outputs\Channel 1\Diagnostics					
Group diagnostics	Deactivated				
Outputs\Channel 1\Output					
Output type	Voltage	**Output range**	+/- 10 V	**Reaction to CPU STOP**	Output has no current or voltage
Substitute value					
Outputs\Channel 2\Diagnostics					
Group diagnostics	Deactivated				
Outputs\Channel 2\Output					
Output type	Voltage	**Output range**	+/- 10 V	**Reaction to CPU STOP**	Output has no current or voltage
Substitute value					
Outputs\Channel 3\Diagnostics					
Group diagnostics	Deactivated				
Outputs\Channel 3\Output					
Output type	Voltage	**Output range**	+/- 10 V	**Reaction to CPU STOP**	Output has no current or voltage
Substitute value					
I/O addresses\Output addresses					
Start address	288	**End address**	295	**Process image**	None

electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / Local modules

DO 16x24VDC/0.5A_2

DO 16x24VDC/0.5A_2					
General					
Name	DO 16x24VDC/0.5A_2	**Author**	malki.lamine	**Comment**	
Rack	0	**Slot**	8		
General\Catalog information					
Short designation	DO 16x24VDC/0.5A	**Description**	Digital output module DO16 x 24VDC/ 0.5A; grouping 8, 4A per group; 20-pin front connector	**Article number**	6ES7 322-1BH01-0AA0
Firmware version			False		
I/O addresses\Output addresses					
Start address	2.0	**End address**	3.7	**Process image**	OB1-PI

electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / Local modules

AI 8x16BIT_2

AI 8x16BIT_2					
General					
Name	AI 8x16BIT_2	**Author**	malki.lamine	**Comment**	
Rack	0	**Slot**	9		
General\Catalog information					
Short designation	AI 8x16BIT	**Description**	Analog input module AI8 x U/I; 16 bits of resolution; accuracy appr. 0.1%; grouping 2; common mode voltage appr. 75VDC; configurable diagnostics; hardware interrupts; 40-pin front connector	**Article number**	6ES7 331-7NF10-0AB0
Firmware version			False		
Inputs\General\Diagnostics					
Diagnostics interrupt	Deactivated				
Inputs\General\Hardware interrupt					
Hardware interrupt at end of scan cycle	Deactivated				
Inputs\General\Hardware interrupt					
Hardware interrupt when limit violated	Deactivated	**RidPrefixHwInterrupt**	49152	**Event name:**	0
Hardware interrupt:	0	**Hardware interrupt**	Hardware interrupt	**HardwareInterruptChannelForModule**	32768
HardwareInterruptEventIdNull	0				
Inputs\Measuring					
Operating mode	8 channels	**Update time**	190ms		
Inputs\Channel group 0 - 1\Diagnostics					
Group diagnostics	Deactivated	**Check for wire break**	Deactivated		
Inputs\Channel group 0 - 1\Measuring					
Measurement type	Voltage	**Measuring range**	+/- 10 V	**Interference frequency suppression**	50/60/400Hz
Integration time	2/16/20ms	**Smoothing**	None		
Inputs\Channel group 0 - 1\Hardware interrupt\Channel 0					
High limit		**Low limit**			
Inputs\Channel group 0 - 1\Hardware interrupt\Channel 1					
High limit		**Low limit**			
Inputs\Channel group 2 - 3\Diagnostics					
Group diagnostics	Deactivated	**Check for wire break**	Deactivated		
Inputs\Channel group 2 - 3\Measuring					
Measurement type	Voltage	**Measuring range**	+/- 10 V	**Interference frequency suppression**	50/60/400Hz
Integration time	2/16/20ms	**Smoothing**	None		
Inputs\Channel group 2 - 3\Hardware interrupt\Channel 2					
High limit		**Low limit**			
Inputs\Channel group 2 - 3\Hardware interrupt\Channel 3					
High limit		**Low limit**			
Inputs\Channel group 4 - 5\Diagnostics					
Group diagnostics	Deactivated	**Check for wire break**	Deactivated		
Inputs\Channel group 4 - 5\Measuring					
Measurement type	Voltage	**Measuring range**	+/- 10 V	**Interference frequency suppression**	50/60/400Hz
Integration time	2/16/20ms	**Smoothing**	None		
Inputs\Channel group 4 - 5\Trigger for hardware interrupt\Channel 4					
High limit		**Low limit**			
Inputs\Channel group 4 - 5\Trigger for hardware interrupt\Channel 5					
High limit		**Low limit**			
Inputs\Channel group 6 - 7\Diagnostics					
Group diagnostics	Deactivated	**Check for wire break**	Deactivated		
Inputs\Channel group 6 - 7\Measuring					
Measurement type	Voltage	**Measuring range**	+/- 10 V	**Interference frequency suppression**	50/60/400Hz
Integration time	2/16/20ms	**Smoothing**	None		
Inputs\Channel group 6 - 7\Trigger for hardware interrupt\Channel 6					
High limit		**Low limit**			
Inputs\Channel group 6 - 7\Trigger for hardware interrupt\Channel 7					
High limit		**Low limit**			
I/O addresses\Input addresses					
Start address	304	**End address**	319	**Process image**	None
Interrupt OB number	40				

electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / Local modules

AI 8x16BIT_3

AI 8x16BIT_3					
General					
Name	AI 8x16BIT_3	**Author**	Bsat	**Comment**	
Rack	0	**Slot**	10		
General\Catalog information					
Short designation	AI 8x16BIT	**Description**	Analog input module AI8 x U/I; 16 bits of resolution; accuracy appr. 0.3%; grouping 8; common mode voltage appr. 50VDC; configurable diagnostics; hardware interrupts; 40-pin front connector	**Article number**	6ES7 331-7NF00-0AB0
Firmware version			False		
Inputs\General\Diagnostics					
Diagnostics interrupt	Deactivated				
Inputs\General\Hardware interrupts					
Hardware interrupt when limit violated	Deactivated	**RidPrefixHwInterrupt**	49152	**Event name:**	0
Hardware interrupt:	0	**Hardware interrupt**	Hardware interrupt	**HardwareInterruptChannelForModule**	32768
HardwareInterruptEventIdNull	0				
Inputs\Channel 0 - 1\Diagnostics					
Group diagnostics	Deactivated	**Check for wire break**	Deactivated		
Inputs\Channel 0 - 1\Measuring					
Measurement type	Voltage	**Measuring range**	+/- 10 V	**Interference frequency suppression**	50Hz
Integration time	20ms				
Inputs\Channel 0 - 1\Hardware interrupt\Channel 0					
High limit		**Low limit**			
Inputs\Channel 2 - 3\Diagnostics					
Group diagnostics	Deactivated	**Check for wire break**	Deactivated		
Inputs\Channel 2 - 3\Measuring					
Measurement type	Voltage	**Measuring range**	+/- 10 V	**Interference frequency suppression**	50Hz
Integration time	20ms				
Inputs\Channel 2 - 3\Hardware interrupt\Channel 2					
High limit		**Low limit**			
Inputs\Channel 4 - 5\Diagnostics					
Group diagnostics	Deactivated	**Check for wire break**	Deactivated		
Inputs\Channel 4 - 5\Measuring					
Measurement type	Voltage	**Measuring range**	+/- 10 V	**Interference frequency suppression**	50Hz
Integration time	20ms				
Inputs\Channel 6 - 7\Diagnostics					
Group diagnostics	Deactivated	**Check for wire break**	Deactivated		
Inputs\Channel 6 - 7\Measuring					
Measurement type	Voltage	**Measuring range**	+/- 10 V	**Interference frequency suppression**	50Hz
Integration time	20ms				
I/O addresses\Input addresses					
Start address	352	**End address**	367	**Process image**	None
Interrupt OB number	40				

electro-chlorination station 3_V13_SP1_V18 / PLC_1 [CPU 317-2 PN/DP] / Local modules

DO 16x24/48VUC_1

DO 16x24/48VUC_1					
General					
Name	DO 16x24/48VUC_1	**Author**	Bsat	**Comment**	
Rack	0	**Slot**	11		
General\Catalog information					
Short designation	DO 16x24/48VUC	**Description**	Digital output module DO16 x 24/48VDC/0.5A; grouping 1, 0.5A per group; configurable diagnostics; configurable substitute value for output; 40-pin front connector	**Article number**	6ES7 322-5GH00-0AB0
Firmware version			False		
Outputs\Enable					
Diagnostics interrupt	Deactivated				
Outputs\Reaction to CPU STOP					
Reaction to CPU STOP	Substitute a value				
Outputs\Output 0					
Apply substitute value "1"	Deactivated				
Outputs\Output 1					
Apply substitute value "1"	Deactivated				
Outputs\Output 2					
Apply substitute value "1"	Deactivated				
Outputs\Output 3					
Apply substitute value "1"	Deactivated				
Outputs\Output 4					
Apply substitute value "1"	Deactivated				
Outputs\Output 5					
Apply substitute value "1"	Deactivated				
Outputs\Output 6					
Apply substitute value "1"	Deactivated				
Outputs\Output 7					
Apply substitute value "1"	Deactivated				
Outputs\Output 8					
Apply substitute value "1"	Deactivated				
Outputs\Output 9					
Apply substitute value "1"	Deactivated				
Outputs\Output 10					
Apply substitute value "1"	Deactivated				
Outputs\Output 11					
Apply substitute value "1"	Deactivated				
Outputs\Output 12					
Apply substitute value "1"	Deactivated				
Outputs\Output 13					
Apply substitute value "1"	Deactivated				
Outputs\Output 14					
Apply substitute value "1"	Deactivated				
Outputs\Output 15					
Apply substitute value "1"	Deactivated				
I/O addresses\Output addresses					
Start address	28.0	**End address**	29.7	**Process image**	OB1-PI

electro-chlorination station 3_V13_SP1_V18

HMI_1 [TP1500 Basic color PN]

HMI_1	
General	
Name	HMI_1

electro-chlorination station 3_V13_SP1_V18 / HMI_1 [TP1500 Basic color PN]

Runtime settings

General

Start screen	HOME	**Default template**		**Screen resolution**	1024, 768
Project ID	0				

Screens

Bit selection for text and graphic lists	Off

Keyboard

Use screen keyboard	Enabled	**Release button on exit**	Disabled

Alarms

Controller alarms

Buffer overflow	10 %	**Acknowledgment group text**	QGR	**Use alarm class color**	Disabled
Use help texts for system diagnostics	Enabled	**System event duration**	2 Seconds	**Connection**	HMI_Liaison_1

User administration

Enable limit for logon attempts	Enabled	**Invalid logon attempts**	3	**Logon with password**	Disabled
Group-specific rights	Disabled	**Password aging**	Disabled	**Validity period**	90
Warning period	7	**Password generations**	3	**At least one special character**	Disabled
At least one number	Disabled	**Minimum password length**	3		

Language & font

Preset runtime language	French (France)

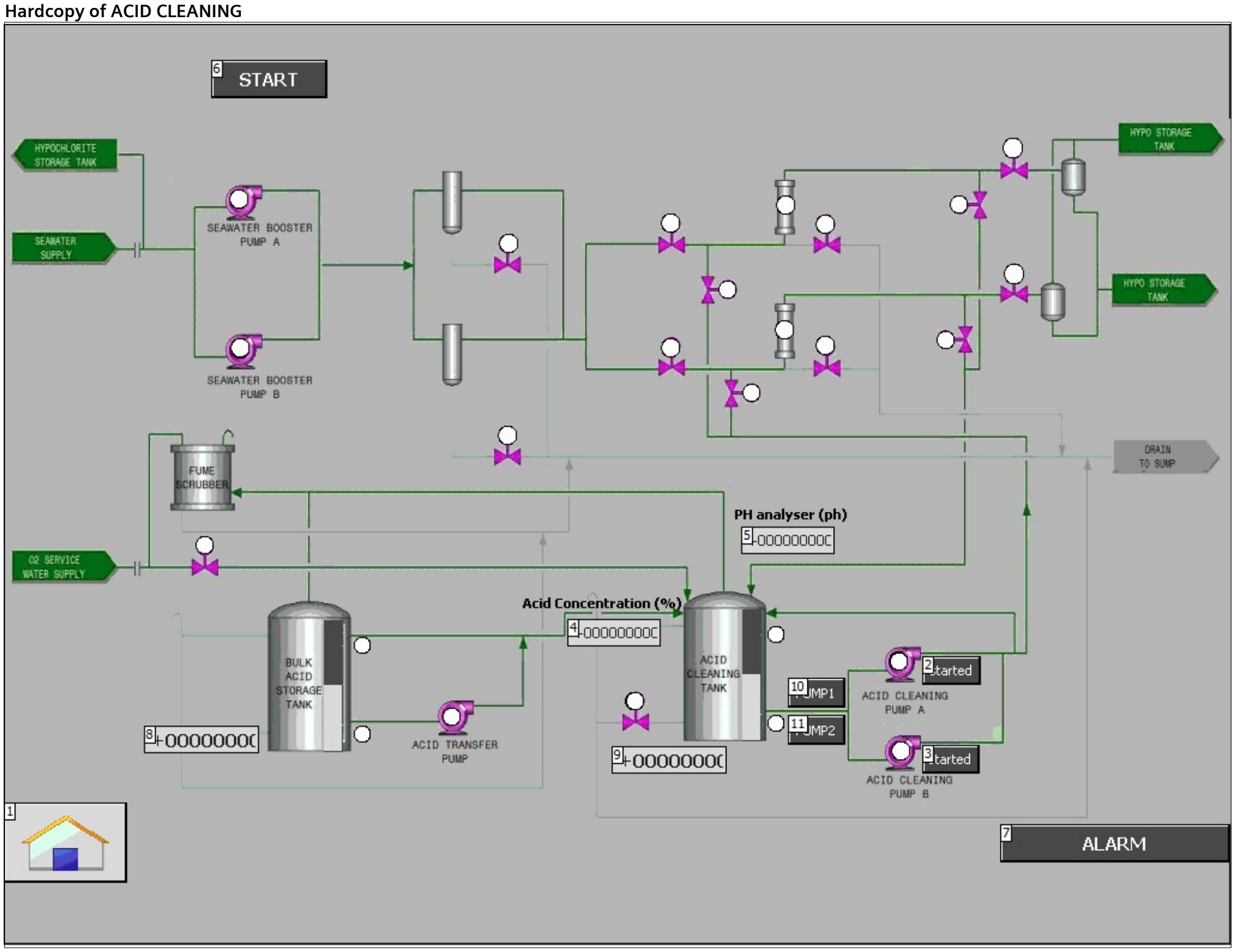
French (France)

Runtime language	Enabled	**Fixed font 1**	Tahoma	**Default font**	Tahoma, 11 Pixel
Configured font 1					

electro-chlorination station 3_V13_SP1_V18 / HMI_1 [TP1500 Basic color PN] / Screens

ACID CLEANING

Hardcopy of ACID CLEANING



General					
Name	ACID CLEANING	**Background color**	109, 109, 109	**Grid color**	182, 182, 182
Number	3	**Template**		**Tooltip**	
Layers					
Active layer	0				

Layer_0	Enabled
Layer_1	Enabled
Layer_2	Enabled
Layer_3	Enabled
Layer_4	Enabled
Layer_5	Enabled
Layer_6	Enabled
Layer_7	Enabled
Layer_8	Enabled
Layer_9	Enabled
Layer_10	Enabled
Layer_11	Enabled
Layer_12	Enabled
Layer_13	Enabled
Layer_14	Enabled
Layer_15	Enabled
Layer_16	Enabled
Layer_17	Enabled
Layer_18	Enabled
Layer_19	Enabled
Layer_20	Enabled
Layer_21	Enabled
Layer_22	Enabled
Layer_23	Enabled
Layer_24	Enabled
Layer_25	Enabled
Layer_26	Enabled

Layer_27	Enabled
Layer_28	Enabled
Layer_29	Enabled
Layer_30	Enabled
Layer_31	Enabled

Rectangle_1

Type	Rectangle				
Appearance					
Background color	182, 182, 182	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	0	**Y position**	0	**Width**	1023
Height	767	**Round corner width**	0	**Round corner height**	0
Miscellaneous					
Name	Rectangle_1	**Layer**	0 - Layer_0		

Vue de graphique_1

Type	Graphic view				
General					
Graphic	ACID CLEAN_2				
Appearance					
Background color	182, 182, 182	**Border width**	0	**Line style**	Solid
Border color	255, 255, 255				
Layout					
X position	1	**Y position**	79	**Width**	1023
Height	588	**Fit embedded graphic object to screen size**	Fit graphic to object size	**Fit object to contents**	Disabled
Miscellaneous					
Name	Vue de graphique_1	**Layer**	0 - Layer_0		

Bouton_1

Type	Button				
General					
Mode	Graphic	**Text OFF**	HOME	**Text ON**	HOME
Text list		**Graphic OFF**	Home	**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	0	**Y position**	649	**Width**	103
Height	67	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_1	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Click

Function list\ActivateScreen

Screen name	HOME	**Object number**	0

Bargraphe_1

Type	Bar				
General					
Maximum value	7000	**Minimum value**	0	**Process value**	0
Appearance					
Foreground color	218, 218, 218	**Background color of bar**	72, 72, 72	**Segment coloring**	Segmented
Background color	72, 72, 72	**Color of scale**	218, 218, 218	**Limit lines (layout)**	Disabled
Limit marking (layout)	Enabled				
Border type					
Bar edge style	None				
Scales					
Show scale	Disabled	**Auto-scale**	Enabled	**Divisions**	5
Large mark labeling	1	**Scale gradation**	0		
Label					
Show scale marks	Enabled	**Show "+" for positive numbers**	Disabled	**Use exponential format**	Disabled
Double-lined scale label	Disabled	**Unit**		**Integer digits**	4
Decimal places	0				
Layout					
X position	267	**Y position**	497	**Width**	16

Height	110	**Scale position**	Left/up	**Bar orientation**	Top
Text format					
Font	Tahoma, 8px				
Limits/Ranges					
Color range high 2	255, 109, 118	**Color range low 2**	255, 218, 44		
Miscellaneous					
Name	Bargraphe_1	**Layer**	0 - Layer_0		
Dynamizations\Tag connection					
Property name	Process value	**Tag**	Bulk Tank Level (mm)		

Bargraphe_2

Type	Bar				
General					
Maximum value	7000	**Minimum value**	0	**Process value**	0
Appearance					
Foreground color	218, 218, 218	**Background color of bar**	72, 72, 72	**Segment coloring**	Segmented
Background color	72, 72, 72	**Color of scale**	218, 218, 218	**Limit lines (layout)**	Disabled
Limit marking (lay-out)	Enabled				
Border type					
Bar edge style	None				
Scales					
Show scale	Disabled	**Auto-scale**	Enabled	**Divisions**	5
Large mark labeling	1	**Scale gradation**	0		
Label					
Show scale marks	Enabled	**Show "+" for positive numbers**	Disabled	**Use exponential for-mat**	Disabled
Double-lined scale la-bel	Disabled	**Unit**		**Integer digits**	4
Decimal places	0				
Layout					
X position	616	**Y position**	488	**Width**	16
Height	110	**Scale position**	Left/up	**Bar orientation**	Top
Text format					
Font	Tahoma, 8px				
Limits/Ranges					
Color range high 2	255, 109, 118	**Color range low 2**	255, 218, 44		
Miscellaneous					
Name	Bargraphe_2	**Layer**	0 - Layer_0		
Dynamizations\Tag connection					
Property name	Process value	**Tag**	Acid cleaning Tank(mm)		

Cercle_1

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pat-tern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	637	**Y position**	576	**Width**	14
Height	14	**Radius**	7		
Miscellaneous					
Name	Cercle_1	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Acid tank level LL -	**Data type**	Range	**Range**	1..1
Foreground color	0, 0, 0	**Background color**	255, 255, 63	**Flashing**	Yes
Dynamizations\Visibility					
Tag - Cycle	Acid tank level LL -	**Data type**	Range	**Start range**	1
End range	1	**Visibility**	Visible		

Cercle_2

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pat-tern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	637	**Y position**	502	**Width**	14
Height	14	**Radius**	7		
Miscellaneous					
Name	Cercle_2	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Acid tank level HH -	**Data type**	Range	**Range**	1..1
Foreground color	0, 0, 0	**Background color**	255, 255, 63	**Flashing**	Yes
Dynamizations\Visibility					
Tag - Cycle	Acid tank level HH -	**Data type**	Range	**Start range**	1
End range	1	**Visibility**	Visible		

Cercle_4

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pat-tern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		

Layout					
X position	738	**Y position**	523	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_4	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Acid Cleaning pump 1 Fault -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				
Dynamizations\Visibility					
Tag - Cycle	Acid Cleaning pump 1 Fault -	**Data type**	Range	**Start range**	1
End range	1	**Visibility**	Visible		

Cercle_17

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	366	**Y position**	570	**Width**	15
Height	15	**Radius**	7		
Miscellaneous					
Name	Cercle_17	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Acid transfer Pump -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_3

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	518	**Y position**	557	**Width**	15
Height	15	**Radius**	7		
Miscellaneous					
Name	Cercle_3	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Acid Tank Drain valve -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_7

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	596	**Y position**	214	**Width**	15
Height	15	**Radius**	7		
Miscellaneous					
Name	Cercle_7	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Acid supply valve -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_8

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	519	**Y position**	557	**Width**	15
Height	15	**Radius**	7		
Miscellaneous					
Name	Cercle_8	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Acid Tank Drain valve -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_9

Type	Circle

Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	616	**Y position**	300	**Width**	15
Height	15	**Radius**	7		
Miscellaneous					
Name	Cercle_9	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Acid supply valve -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_10

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	789	**Y position**	143	**Width**	15
Height	15	**Radius**	7		
Miscellaneous					
Name	Cercle_10	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Acid return Valve -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_11

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	778	**Y position**	256	**Width**	15
Height	15	**Radius**	7		
Miscellaneous					
Name	Cercle_11	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Acid return Valve -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_12

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	739	**Y position**	524	**Width**	15
Height	15	**Radius**	7		
Miscellaneous					
Name	Cercle_12	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Acid cleaning pump 1 -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_13

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	740	**Y position**	598	**Width**	15
Height	15	**Radius**	7		
Miscellaneous					
Name	Cercle_13	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Acid cleaning pump 2 -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_14

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	160	**Y position**	427	**Width**	15
Height	15	**Radius**	7		
Miscellaneous					
Name	Cercle_14	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Service water valve -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_16

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	740	**Y position**	598	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_16	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Acid Cleaning pump 2 Fault -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				
Dynamizations\Visibility					
Tag - Cycle	Acid Cleaning pump 2 Fault -	**Data type**	Range	**Start range**	1
End range	1	**Visibility**	Visible		

Bouton_5

Type	Button				
General					
Mode	Text	**Text OFF**	started	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	766	**Y position**	527	**Width**	49
Height	24	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 11px	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_5	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_Acid Cleaning pump 1 started

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_Acid Cleaning pump 1 started

Dynamizations\Appearance					
Tag - Cycle	HMI_Acid Cleaning pump 1 started -	**Data type**	Range	**Range**	0..0
Foreground color	255, 255, 255	**Background color**	72, 72, 72	**Flashing**	No
Range	1..1	**Foreground color**	255, 255, 255	**Background color**	255, 109, 54
Flashing	No				

Bouton_2

Type	Button				
General					
Mode	Text	**Text OFF**	started	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0

Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	766	**Y position**	601	**Width**	48
Height	24	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 11px	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_2	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_Acid Cleaning pump 2 started

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_Acid Cleaning pump 2 started

Cercle_27

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	188	**Y position**	138	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_27	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	seawater pump intake 1 Fault -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				
Dynamizations\Visibility					
Tag - Cycle	seawater pump intake 1 Fault -	**Data type**	Range	**Start range**	1
End range	1	**Visibility**	Visible		

Cercle_28

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	188	**Y position**	138	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_28	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	seawater pump intake 1 -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_29

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	189	**Y position**	263	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_29	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	seawater pump intake 2 Fault -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				
Dynamizations\Visibility					
Tag - Cycle	seawater pump intake 2 Fault -	**Data type**	Range	**Start range**	1

End range	1	**Visibility**	Visible

Cercle_30

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	189	**Y position**	262	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_30	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	seawater pump intake 2 -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Champ d'E/S_6

Type	I/O field				
General					
Process value		**Mode**	Input/output	**Display format**	Decimal
Shift decimal point	0	**Field length**	13	**Show leading zeros**	Disabled
Format pattern	s999999999.999				
Appearance					
Background color	218, 218, 218	**Background fill pattern**	Solid	**Foreground color**	36, 36, 36
Unit		**Border width**	1	**Line style**	Solid
Border color	0, 0, 0				
Characteristics					
Hidden input	Disabled				
Layout					
X position	470	**Y position**	496	**Width**	78
Height	23	**Left margin**	5	**Top margin**	2
Right margin	2	**Bottom margin**	2	**Fit object to contents**	Disabled
Text format					
Font	Tahoma, 12px	**Horizontal alignment**	Left	**Vertical alignment**	Middle
Line break	Disabled				
Limits					
Color for High limit violated	255, 0, 31	**Color for Low limit violated**	218, 255, 44		
Miscellaneous					
Name	Champ d'E/S_6	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		
Dynamizations\Tag connection					
Property name	Process value	**Tag**	Acid Concentration (%)		

Champ d'E/S_1

Type	I/O field				
General					
Process value		**Mode**	Input/output	**Display format**	Decimal
Shift decimal point	0	**Field length**	13	**Show leading zeros**	Disabled
Format pattern	s999999999.999				
Appearance					
Background color	218, 218, 218	**Background fill pattern**	Solid	**Foreground color**	36, 36, 36
Unit		**Border width**	1	**Line style**	Solid
Border color	0, 0, 0				
Characteristics					
Hidden input	Disabled				
Layout					
X position	615	**Y position**	419	**Width**	78
Height	23	**Left margin**	5	**Top margin**	2
Right margin	2	**Bottom margin**	2	**Fit object to contents**	Disabled
Text format					
Font	Tahoma, 12px	**Horizontal alignment**	Left	**Vertical alignment**	Middle
Line break	Disabled				
Limits					
Color for High limit violated	255, 0, 31	**Color for Low limit violated**	218, 255, 44		
Miscellaneous					
Name	Champ d'E/S_1	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		
Dynamizations\Tag connection					
Property name	Process value	**Tag**	PH analyser (ph)		

Cercle_31

Type	Circle

Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	292	**Y position**	511	**Width**	14
Height	14	**Radius**	7		
Miscellaneous					
Name	Cercle_31	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Bulk Acid tank level HH -	**Data type**	Range	**Range**	1..1
Foreground color	0, 0, 0	**Background color**	255, 255, 63	**Flashing**	Yes
Dynamizations\Visibility					
Tag - Cycle	Bulk Acid tank level HH -	**Data type**	Range	**Start range**	1
End range	1	**Visibility**	Visible		

Cercle_24

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	292	**Y position**	585	**Width**	14
Height	14	**Radius**	7		
Miscellaneous					
Name	Cercle_24	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Bulk Tank Level LL -	**Data type**	Range	**Range**	1..1
Foreground color	0, 0, 0	**Background color**	255, 255, 63	**Flashing**	Yes
Dynamizations\Visibility					
Tag - Cycle	Bulk Tank Level LL -	**Data type**	Range	**Start range**	1
End range	1	**Visibility**	Visible		

Cercle_5

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	548	**Y position**	158	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_5	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	FILTER 1 VALVE -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_6

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	548	**Y position**	262	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_6	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	FILTER 2 VALVE -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_20

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	644	**Y position**	143	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_20	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	electrolizer 1 -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_21

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	643	**Y position**	247	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_21	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	electrolizer 2 -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_25

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	677	**Y position**	159	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_25	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Electrolizer 1 Drain valve -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_26

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	677	**Y position**	260	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_26	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Electrolizer 2 Drain valve -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_43

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	413	**Y position**	175	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_43	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	filter 1 -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	255, 0, 31	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	36, 145, 90
Flashing	Yes				

Cercle_44

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	412	**Y position**	335	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_44	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	filter 2 -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	255, 0, 31	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	36, 145, 90
Flashing	Yes				

Cercle_18

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	833	**Y position**	95	**Width**	17
Height	17	**Radius**	8		
Miscellaneous					
Name	Cercle_18	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	valve to Tank -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	No				

Cercle_19

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	834	**Y position**	200	**Width**	17
Height	17	**Radius**	8		
Miscellaneous					
Name	Cercle_19	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	valve to Tank -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	No				

Bouton_3

Type	Button				
General					
Mode	Text	**Text OFF**	START	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	173	**Y position**	30	**Width**	97
Height	32	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_3	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_acid cleaning start

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_acid cleaning start

Bouton_4

Type	Button				
General					
Mode	Text	**Text OFF**	ALARM	**Text ON**	ALARM
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	831	**Y position**	667	**Width**	192
Height	32	**Fit object to contents**	Disabled		

Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_4	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Click

Function list\ActivateScreen

Screen name	ALARM	**Object number**	0

Champ d'E/S_2

Type	I/O field				
General					
Process value		**Mode**	Input/output	**Display format**	Decimal
Shift decimal point	0	**Field length**	13	**Show leading zeros**	Disabled
Format pattern	s999999999.999				
Appearance					
Background color	218, 218, 218	**Background fill pattern**	Solid	**Foreground color**	36, 36, 36
Unit		**Border width**	1	**Line style**	Solid
Border color	0, 0, 0				
Characteristics					
Hidden input	Disabled				
Layout					
X position	117	**Y position**	585	**Width**	96
Height	23	**Left margin**	5	**Top margin**	2
Right margin	2	**Bottom margin**	2	**Fit object to contents**	Disabled
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment**	Left	**Vertical alignment**	Middle
Line break	Disabled				
Limits					
Color for High limit violated	255, 0, 31	**Color for Low limit violated**	218, 255, 44		
Miscellaneous					
Name	Champ d'E/S_2	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		
Dynamizations\Tag connection					
Property name	Process value	**Tag**	Bulk Tank Level (mm)		

Champ d'E/S_3

Type	I/O field				
General					
Process value		**Mode**	Input/output	**Display format**	Decimal
Shift decimal point	0	**Field length**	13	**Show leading zeros**	Disabled
Format pattern	s999999999.999				
Appearance					
Background color	218, 218, 218	**Background fill pattern**	Solid	**Foreground color**	36, 36, 36
Unit		**Border width**	1	**Line style**	Solid
Border color	0, 0, 0				
Characteristics					
Hidden input	Disabled				
Layout					
X position	507	**Y position**	602	**Width**	96
Height	23	**Left margin**	5	**Top margin**	2
Right margin	2	**Bottom margin**	2	**Fit object to contents**	Disabled
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment**	Left	**Vertical alignment**	Middle
Line break	Disabled				
Limits					
Color for High limit violated	255, 0, 31	**Color for Low limit violated**	218, 255, 44		
Miscellaneous					
Name	Champ d'E/S_3	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		
Dynamizations\Tag connection					
Property name	Process value	**Tag**	Acid cleaning Tank(mm)		

Bouton_20

Type	Button				
General					
Mode	Text	**Text OFF**	PUMP1	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2

Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	654	**Y position**	545	**Width**	48
Height	25	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 11px	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_20	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_SELECT COUNTINOUS PUMP 1

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_SELECT COUNTINOUS PUMP 1

Bouton_22

Type	Button				
General					
Mode	Text	**Text OFF**	PUMP2	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	654	**Y position**	576	**Width**	48
Height	25	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 11px	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_22	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_SELECT SHOCK PUMP 2

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_SELECT SHOCK PUMP 2

Text field_1

Type	Text field				
General					
Text	Acid Concentration (%)				
Appearance					
Background color	145, 145, 145	**Background fill pattern**	Transparent	**Foreground color**	0, 0, 0
Border width	0	**Line style**	Solid	**Border color**	0, 0, 0
Layout					
X position	433	**Y position**	474	**Width**	135
Height	15	**Left margin**	0	**Top margin**	2
Right margin	2	**Bottom margin**	0	**Fit object to contents**	Enabled
Text format					
Font	Tahoma, 11px, style=Bold	**Horizontal alignment**	Left	**Vertical alignment**	Middle
Line break	Disabled				
Flashing					
Flashing	Disabled				
Miscellaneous					
Name	Text field_1	**Layer**	0 - Layer_0		

Text field_2

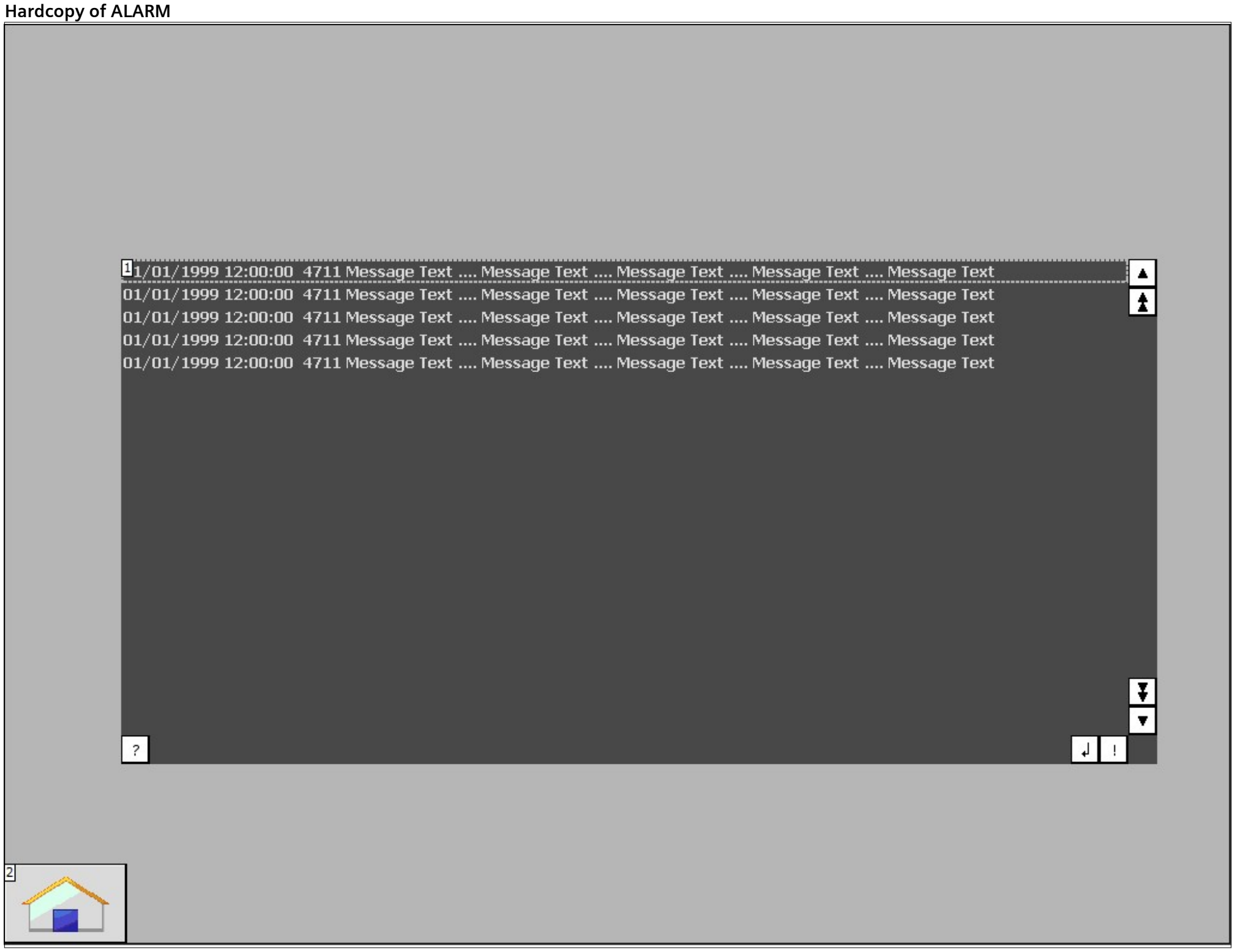
Type	Text field

General					
Text	PH analyser (ph)				
Appearance					
Background color	145, 145, 145	**Background fill pattern**	Transparent	**Foreground color**	0, 0, 0
Border width	0	**Line style**	Solid	**Border color**	0, 0, 0
Layout					
X position	610	**Y position**	400	**Width**	96
Height	15	**Left margin**	0	**Top margin**	2
Right margin	2	**Bottom margin**	0	**Fit object to contents**	Enabled
Text format					
Font	Tahoma, 11px, style=Bold	**Horizontal alignment**	Left	**Vertical alignment**	Middle
Line break	Disabled				
Flashing					
Flashing	Disabled				
Miscellaneous					
Name	Text field_2	**Layer**	0 - Layer_0		

electro-chlorination station 3_V13_SP1_V18 / HMI_1 [TP1500 Basic color PN] / Screens

ALARM

Hardcopy of ALARM



General					
Name	ALARM	**Background color**	109, 109, 109	**Grid color**	182, 182, 182
Number	5	**Template**		**Tooltip**	
Layers					
Active layer	0				

Layer_0	Enabled
Layer_1	Enabled
Layer_2	Enabled
Layer_3	Enabled
Layer_4	Enabled
Layer_5	Enabled
Layer_6	Enabled
Layer_7	Enabled
Layer_8	Enabled
Layer_9	Enabled
Layer_10	Enabled
Layer_11	Enabled
Layer_12	Enabled
Layer_13	Enabled
Layer_14	Enabled
Layer_15	Enabled
Layer_16	Enabled
Layer_17	Enabled
Layer_18	Enabled
Layer_19	Enabled
Layer_20	Enabled
Layer_21	Enabled
Layer_22	Enabled
Layer_23	Enabled
Layer_24	Enabled
Layer_25	Enabled
Layer_26	Enabled

Layer_27	Enabled
Layer_28	Enabled
Layer_29	Enabled
Layer_30	Enabled
Layer_31	Enabled

Rectangle_1

Type	Rectangle				
Appearance					
Background color	182, 182, 182	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	0	**Y position**	0	**Width**	1023
Height	767	**Round corner width**	0	**Round corner height**	0
Miscellaneous					
Name	Rectangle_1	**Layer**	0 - Layer_0		

Vue des alarmes_1

Type	Alarm view				
General					
Alarm classes	Errors, Warnings, System	**Source of alarms**	Alarms	**Pending alarms**	Enabled
Unacknowledged alarms	Enabled				
Appearance					
Background color of table	72, 72, 72	**Foreground color of table**	218, 218, 218		
Layout					
X position	98	**Y position**	196	**Width**	864
Height	421	**Fit object to contents**	Disabled	**Lines per alarms**	1
Visible alarms	5				
Display					
Vertical scroll bar	Enabled	**Vertical scrolling**	Enabled		
Text format					
Table font	Tahoma, 13px, style=Bold				
Toolbar					
"Info text" button	Enabled	**Acknowledge button**	Enabled	**Loop-In-Alarm button**	Enabled
Columns					
Columns	Alarm number, Time, Alarm text, Date, Alarm class	**Time in milliseconds**	Disabled	**Time sorting order**	Descending
Miscellaneous					
Name	Vue des alarmes_1	**Layer**	0 - Layer_0		
Security					
Allow operator control	Enabled				

Bouton_1

Type	Button				
General					
Mode	Graphic	**Text OFF**	HOME	**Text ON**	HOME
Text list		**Graphic OFF**	Home	**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	0	**Y position**	700	**Width**	103
Height	67	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_1	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Click

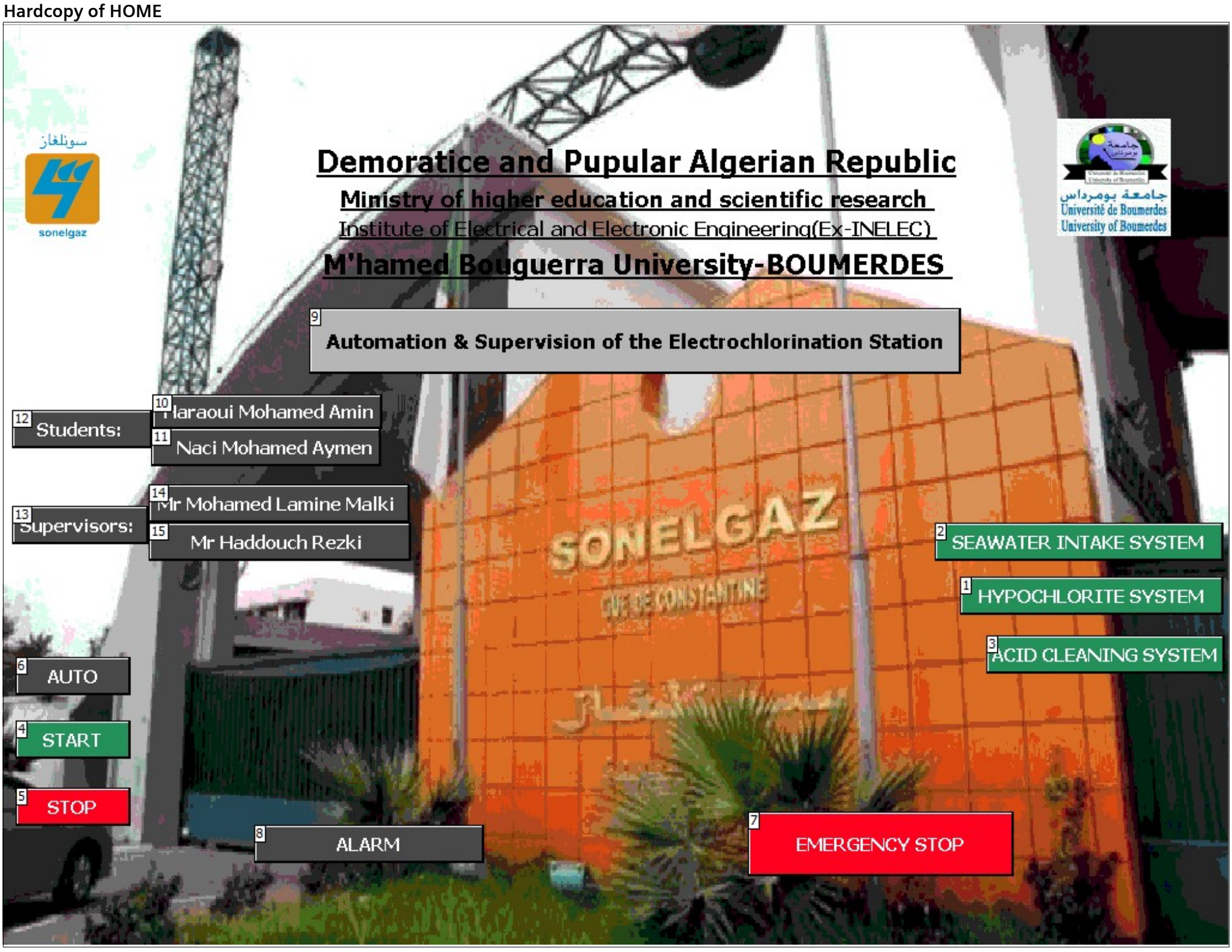
Function list\ActivateScreen

Screen name	HOME	**Object number**	0

electro-chlorination station 3_V13_SP1_V18 / HMI_1 [TP1500 Basic color PN] / Screens

HOME

Hardcopy of HOME



General					
Name	HOME	**Background color**	182, 182, 182	**Grid color**	182, 182, 182
Number	1	**Template**	Modèle_1	**Tooltip**	
Layers					
Active layer	0				

Layer_0	Enabled
Layer_1	Enabled
Layer_2	Enabled
Layer_3	Enabled
Layer_4	Enabled
Layer_5	Enabled
Layer_6	Enabled
Layer_7	Enabled
Layer_8	Enabled
Layer_9	Enabled
Layer_10	Enabled
Layer_11	Enabled
Layer_12	Enabled
Layer_13	Enabled
Layer_14	Enabled
Layer_15	Enabled
Layer_16	Enabled
Layer_17	Enabled
Layer_18	Enabled
Layer_19	Enabled
Layer_20	Enabled
Layer_21	Enabled
Layer_22	Enabled
Layer_23	Enabled
Layer_24	Enabled
Layer_25	Enabled
Layer_26	Enabled

Layer_27	Enabled
Layer_28	Enabled
Layer_29	Enabled
Layer_30	Enabled
Layer_31	Enabled

Vue de graphique_1

Type	Graphic view				
General					
Graphic	Graphique_2				
Appearance					
Background color	0, 0, 0	**Border width**	0	**Line style**	Solid
Border color	255, 255, 255				
Layout					
X position	0	**Y position**	1	**Width**	1023
Height	767	**Fit embedded graphic object to screen size**	Fit graphic to object size	**Fit object to contents**	Disabled
Miscellaneous					
Name	Vue de graphique_1	**Layer**	0 - Layer_0		

Bouton_1

Type	Button				
General					
Mode	Text	**Text OFF**	HYPOCHLORITE SYSTEM	**Text ON**	HYPOCHLORIT SYSTEM
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	36, 145, 90	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	799	**Y position**	461	**Width**	220
Height	32	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_1	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Click

Function list\ActivateScreen

Screen name	HYPOCHLORIT SYSTEM	**Object number**	0

Bouton_2

Type	Button				
General					
Mode	Text	**Text OFF**	SEAWATER INTAKE SYSTEM	**Text ON**	SEAWATER INTAKE
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	36, 145, 90	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	778	**Y position**	416	**Width**	241
Height	32	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_2	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Click

Function list\ActivateScreen

Screen name	SEAWATER INTAKE	**Object number**	0

Bouton_3

Type	Button				
General					
Mode	Text	**Text OFF**	ACID CLEANING SYSTEM	**Text ON**	ACID CLEANING
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0

Appearance					
Background color	36, 145, 90	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	821	**Y position**	510	**Width**	199
Height	32	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_3	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Click

Function list\ActivateScreen

Screen name	ACID CLEANING	**Object number**	0

Vue de graphique_2

Type	Graphic view				
General					
Graphic	Graphique_3				
Appearance					
Background color	182, 182, 182	**Border width**	0	**Line style**	Solid
Border color	255, 255, 255				
Layout					
X position	19	**Y position**	91	**Width**	62
Height	88	**Fit embedded graphic object to screen size**	Fit graphic to object size	**Fit object to contents**	Disabled
Miscellaneous					
Name	Vue de graphique_2	**Layer**	0 - Layer_0		

Vue de graphique_3

Type	Graphic view				
General					
Graphic	Graphique_4				
Appearance					
Background color	182, 182, 182	**Border width**	0	**Line style**	Solid
Border color	255, 255, 255				
Layout					
X position	880	**Y position**	79	**Width**	95
Height	98	**Fit embedded graphic object to screen size**	Fit graphic to object size	**Fit object to contents**	Disabled
Miscellaneous					
Name	Vue de graphique_3	**Layer**	0 - Layer_0		

Bouton_4

Type	Button				
General					
Mode	Text	**Text OFF**	START	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	36, 145, 90	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	11	**Y position**	581	**Width**	96
Height	32	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_4	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_START

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_START

Bouton_5

Type	Button				
General					
Mode	Text	**Text OFF**	STOP	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	255, 0, 31	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	11	**Y position**	637	**Width**	96
Height	32	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_5	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_STOP

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_STOP

Bouton_6

Type	Button				
General					
Mode	Text	**Text OFF**	AUTO	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	11	**Y position**	528	**Width**	96
Height	32	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_6	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_AUTO

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_AUTO

Bouton_7

Type	Button				
General					
Mode	Text	**Text OFF**	EMERGENCY STOP	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	255, 0, 31	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				

Design					
Focus color	0, 182, 219				
Layout					
X position	622	**Y position**	657	**Width**	222
Height	54	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_7	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_Emergency Stop

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_Emergency Stop

Bouton_8

Type	Button				
General					
Mode	Text	**Text OFF**	ALARM	**Text ON**	ALARM
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	210	**Y position**	668	**Width**	192
Height	32	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_8	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Click

Function list\ActivateScreen

Screen name	ALARM	**Object number**	0

Text field_1

Type	Text field				
General					
Text	Demoratice and Pupular Algerian Republic				
Appearance					
Background color	145, 145, 145	**Background fill pattern**	Transparent	**Foreground color**	0, 0, 0
Border width	0	**Line style**	Solid	**Border color**	0, 0, 0
Layout					
X position	262	**Y position**	97	**Width**	536
Height	33	**Left margin**	0	**Top margin**	2
Right margin	2	**Bottom margin**	0	**Fit object to contents**	Enabled
Text format					
Font	Tahoma, 25px, style=Bold, Underline	**Horizontal alignment**	Left	**Vertical alignment**	Middle
Line break	Disabled				
Flashing					
Flashing	Disabled				
Miscellaneous					
Name	Text field_1	**Layer**	0 - Layer_0		

Text field_2

Type	Text field				
General					
Text	Ministry of higher education and scientific research				
Appearance					
Background color	145, 145, 145	**Background fill pattern**	Transparent	**Foreground color**	0, 0, 0
Border width	0	**Line style**	Solid	**Border color**	0, 0, 0

Layout					
X position	282	**Y position**	132	**Width**	498
Height	26	**Left margin**	0	**Top margin**	2
Right margin	2	**Bottom margin**	0	**Fit object to contents**	Enabled
Text format					
Font	Tahoma, 19px, style=Bold, Underline	**Horizontal alignment**	Left	**Vertical alignment**	Middle
Line break	Disabled				
Flashing					
Flashing	Disabled				
Miscellaneous					
Name	Text field_2	**Layer**	0 - Layer_0		

Text field_3

Type	Text field				
General					
Text	Institute of Electrical and Electronic Engineering(Ex-INELEC)				
Appearance					
Background color	145, 145, 145	**Background fill pattern**	Transparent	**Foreground color**	0, 0, 0
Border width	0	**Line style**	Solid	**Border color**	0, 0, 0
Layout					
X position	281	**Y position**	159	**Width**	501
Height	22	**Left margin**	0	**Top margin**	2
Right margin	2	**Bottom margin**	0	**Fit object to contents**	Enabled
Text format					
Font	Tahoma, 16px, style=Bold, Underline	**Horizontal alignment**	Left	**Vertical alignment**	Middle
Line break	Disabled				
Flashing					
Flashing	Disabled				
Miscellaneous					
Name	Text field_3	**Layer**	0 - Layer_0		

Text field_4

Type	Text field				
General					
Text	M'hamed Bouguerra University-BOUMERDES				
Appearance					
Background color	145, 145, 145	**Background fill pattern**	Transparent	**Foreground color**	0, 0, 0
Border width	0	**Line style**	Solid	**Border color**	0, 0, 0
Layout					
X position	267	**Y position**	184	**Width**	528
Height	31	**Left margin**	0	**Top margin**	2
Right margin	2	**Bottom margin**	0	**Fit object to contents**	Enabled
Text format					
Font	Tahoma, 23px, style=Bold, Underline	**Horizontal alignment**	Left	**Vertical alignment**	Middle
Line break	Disabled				
Flashing					
Flashing	Disabled				
Miscellaneous					
Name	Text field_4	**Layer**	0 - Layer_0		

Button_1

Type	Button				
General					
Mode	Text	**Text OFF**	Automation & Supervision of the Electrochlorination Station	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	182, 182, 182	**Foreground color**	0, 0, 0	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	256	**Y position**	237	**Width**	544
Height	55	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 17px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Button_1	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Button_2

Type	Button				
General					
Mode	Text	**Text OFF**	Haraoui Mohamed Amin	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0

Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	125	**Y position**	309	**Width**	192
Height	29	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Button_2	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Button_3

Type	Button				
General					
Mode	Text	**Text OFF**	Naci Mohamed Aymen	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	124	**Y position**	337	**Width**	192
Height	32	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Button_3	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Button_4

Type	Button				
General					
Mode	Text	**Text OFF**	Students:	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	8	**Y position**	322	**Width**	117
Height	32	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Button_4	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Button_5

Type	Button				
General					
Mode	Text	**Text OFF**	Supervisors:	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	8	**Y position**	402	**Width**	114
Height	32	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Button_5	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Button_6

Type	Button				
General					
Mode	Text	**Text OFF**	Mr Mohamed Lamine Malki	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	122	**Y position**	384	**Width**	218
Height	32	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Button_6	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

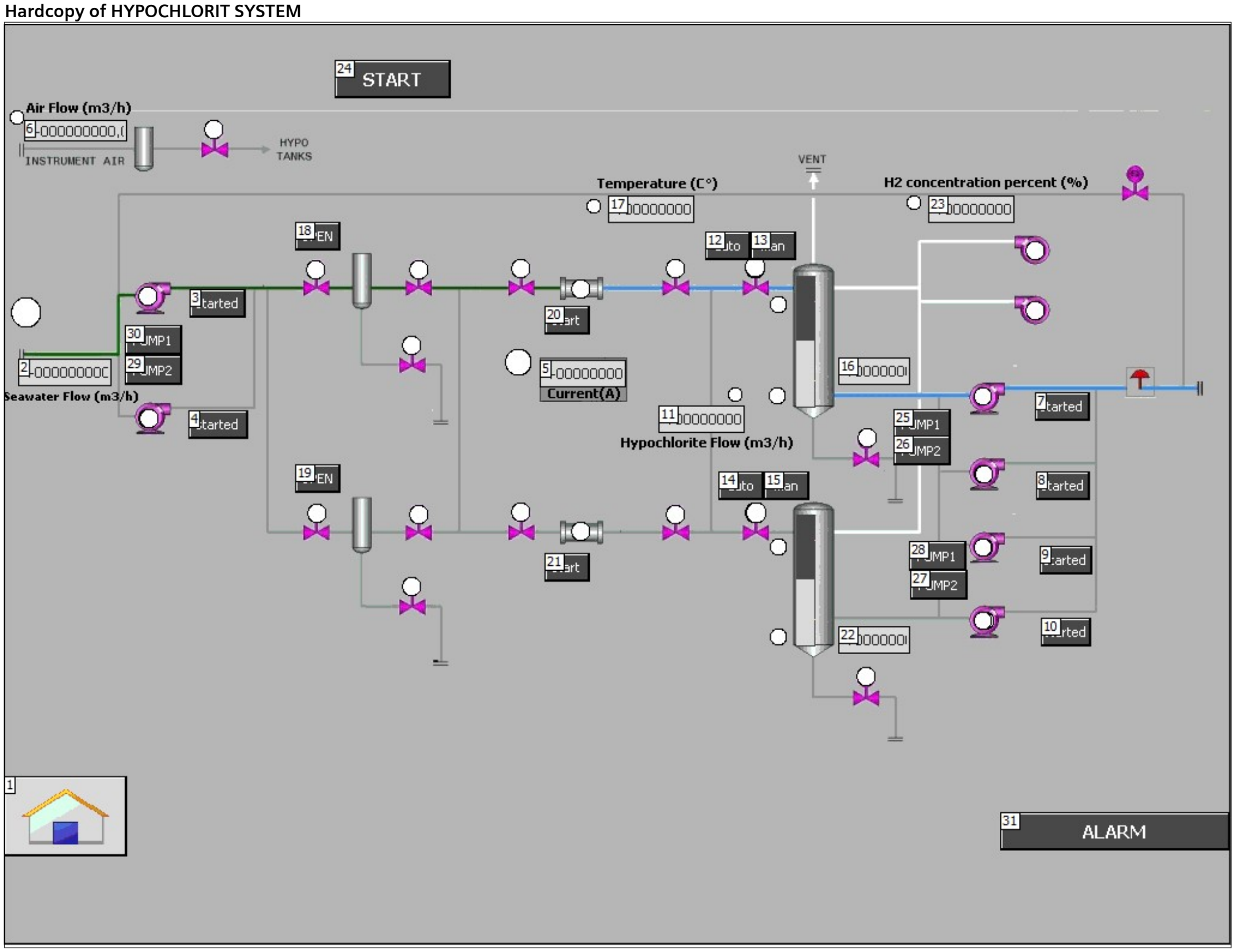
Button_7

Type	Button				
General					
Mode	Text	**Text OFF**	Mr Haddouch Rezki	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	122	**Y position**	416	**Width**	219
Height	32	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Button_7	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

electro-chlorination station 3_V13_SP1_V18 / HMI_1 [TP1500 Basic color PN] / Screens

HYPOCHLORIT SYSTEM

Hardcopy of HYPOCHLORIT SYSTEM



General					
Name	HYPOCHLORIT SYSTEM	**Background color**	109, 109, 109	**Grid color**	182, 182, 182
Number	2	**Template**		**Tooltip**	
Layers					
Active layer	0				

Layer_0	Enabled
Layer_1	Enabled
Layer_2	Enabled
Layer_3	Enabled
Layer_4	Enabled
Layer_5	Enabled
Layer_6	Enabled
Layer_7	Enabled
Layer_8	Enabled
Layer_9	Enabled
Layer_10	Enabled
Layer_11	Enabled
Layer_12	Enabled
Layer_13	Enabled
Layer_14	Enabled
Layer_15	Enabled
Layer_16	Enabled
Layer_17	Enabled
Layer_18	Enabled
Layer_19	Enabled
Layer_20	Enabled
Layer_21	Enabled
Layer_22	Enabled
Layer_23	Enabled
Layer_24	Enabled
Layer_25	Enabled
Layer_26	Enabled

Layer_27	Enabled
Layer_28	Enabled
Layer_29	Enabled
Layer_30	Enabled
Layer_31	Enabled

Rectangle_1

Type	Rectangle				
Appearance					
Background color	182, 182, 182	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	0	**Y position**	0	**Width**	1023
Height	767	**Round corner width**	0	**Round corner height**	0
Miscellaneous					
Name	Rectangle_1	**Layer**	0 - Layer_0		

Vue de graphique_1

Type	Graphic view				
General					
Graphic	HYPOCHLORIT GENERAL_1				
Appearance					
Background color	182, 182, 182	**Border width**	0	**Line style**	Solid
Border color	255, 255, 255				
Layout					
X position	6	**Y position**	72	**Width**	1001
Height	532	**Fit embedded graphic object to screen size**	Fit graphic to object size	**Fit object to contents**	Disabled
Miscellaneous					
Name	Vue de graphique_1	**Layer**	0 - Layer_0		

Bouton_1

Type	Button				
General					
Mode	Graphic	**Text OFF**	HOME	**Text ON**	HOME
Text list		**Graphic OFF**	Home	**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	0	**Y position**	627	**Width**	103
Height	67	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_1	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Click

Function list\ActivateScreen

Screen name	HOME	**Object number**	0

Cercle_1

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	6	**Y position**	228	**Width**	25
Height	25	**Radius**	12		
Miscellaneous					
Name	Cercle_1	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	seawater supply is OK -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	No				

Champ d'E/S_1

Type	I/O field				
General					
Process value		**Mode**	Input/output	**Display format**	Decimal
Shift decimal point	0	**Field length**	13	**Show leading zeros**	Disabled

Format pattern	s999999999.999				
Appearance					
Background color	218, 218, 218	**Background fill pattern**	Solid	**Foreground color**	36, 36, 36
Unit		**Border width**	1	**Line style**	Solid
Border color	0, 0, 0				
Characteristics					
Hidden input	Disabled				
Layout					
X position	12	**Y position**	279	**Width**	78
Height	23	**Left margin**	5	**Top margin**	2
Right margin	2	**Bottom margin**	2	**Fit object to contents**	Disabled
Text format					
Font	Tahoma, 12px	**Horizontal alignment**	Left	**Vertical alignment**	Middle
Line break	Disabled				
Limits					
Color for High limit violated	255, 0, 31	**Color for Low limit violated**	218, 255, 44		
Miscellaneous					
Name	Champ d'E/S_1	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		
Dynamizations\Tag connection					
Property name	Process value	**Tag**	Seawater Flow (m3/h)		

Cercle_2

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	112	**Y position**	219	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_2	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	seawater pump intake 1 -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_3

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	112	**Y position**	321	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_3	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	seawater pump intake 2 -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_4

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	112	**Y position**	219	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_4	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	seawater pump intake 1 Fault -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				
Dynamizations\Visibility					
Tag - Cycle	seawater pump intake 1 Fault -	**Data type**	Range	**Start range**	1
End range	1	**Visibility**	Visible		

Cercle_5

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1

Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	113	**Y position**	321	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_5	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	seawater pump intake 2 Fault -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				
Dynamizations\Visibility					
Tag - Cycle	seawater pump intake 2 Fault -	**Data type**	Range	**Start range**	1
End range	1	**Visibility**	Visible		

Bouton_2

Type	Button				
General					
Mode	Text	**Text OFF**	started	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	155	**Y position**	221	**Width**	47
Height	24	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 11px	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_2	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_seawater pump intake 1 started

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_seawater pump intake 1 started

Bouton_3

Type	Button				
General					
Mode	Text	**Text OFF**	started	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	154	**Y position**	322	**Width**	50
Height	24	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 11px	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_3	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_seawater pump intake 2 started

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_seawater pump intake 2 started

Champ d'E/S_2

Type	I/O field				
General					
Process value		**Mode**	Input/output	**Display format**	Decimal
Shift decimal point	0	**Field length**	13	**Show leading zeros**	Disabled
Format pattern	s999999999.999				
Appearance					
Background color	218, 218, 218	**Background fill pattern**	Solid	**Foreground color**	36, 36, 36
Unit		**Border width**	1	**Line style**	Solid
Border color	0, 0, 0				
Characteristics					
Hidden input	Disabled				
Layout					
X position	447	**Y position**	280	**Width**	72
Height	23	**Left margin**	5	**Top margin**	2
Right margin	2	**Bottom margin**	2	**Fit object to contents**	Disabled
Text format					
Font	Tahoma, 12px	**Horizontal alignment**	Left	**Vertical alignment**	Middle
Line break	Disabled				
Limits					
Color for High limit violated	255, 0, 31	**Color for Low limit violated**	218, 255, 44		
Miscellaneous					
Name	Champ d'E/S_2	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		
Dynamizations\Tag connection					
Property name	Process value	**Tag**	Current(A)		

Cercle_10

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	418	**Y position**	271	**Width**	22
Height	22	**Radius**	11		
Miscellaneous					
Name	Cercle_10	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Rectifier -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_11

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	167	**Y position**	80	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_11	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Air valve -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Champ d'E/S_3

Type	I/O field				
General					
Process value		**Mode**	Input/output	**Display format**	Decimal
Shift decimal point	0	**Field length**	13	**Show leading zeros**	Disabled
Format pattern	s999999999.999				
Appearance					
Background color	218, 218, 218	**Background fill pattern**	Solid	**Foreground color**	36, 36, 36
Unit		**Border width**	1	**Line style**	Solid
Border color	0, 0, 0				
Characteristics					
Hidden input	Disabled				
Layout					
X position	17	**Y position**	80	**Width**	86
Height	18	**Left margin**	5	**Top margin**	2
Right margin	2	**Bottom margin**	2	**Fit object to contents**	Disabled
Text format					
Font	Tahoma, 12px	**Horizontal alignment**	Left	**Vertical alignment**	Middle

Line break	Disabled				
Limits					
Color for High limit violated	255, 0, 31	**Color for Low limit violated**	218, 255, 44		
Miscellaneous					
Name	Champ d'E/S_3	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		
Dynamizations\Tag connection					
Property name	Process value	**Tag**	Air Flow (m3/h)		

Cercle_12

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	852	**Y position**	181	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_12	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	H2 blower -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_13

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	852	**Y position**	231	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_13	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	H2 blower -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_22

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	618	**Y position**	195	**Width**	17
Height	17	**Radius**	8		
Miscellaneous					
Name	Cercle_22	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	valve to Tank -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Bargraphe_1

Type	Bar				
General					
Maximum value	7000	**Minimum value**	0	**Process value**	0
Appearance					
Foreground color	218, 218, 218	**Background color of bar**	72, 72, 72	**Segment coloring**	Segmented
Background color	72, 72, 72	**Color of scale**	218, 218, 218	**Limit lines (layout)**	Disabled
Limit marking (layout)	Enabled				
Border type					
Bar edge style	None				
Scales					
Show scale	Disabled	**Auto-scale**	Enabled	**Divisions**	5
Large mark labeling	1	**Scale gradation**	0		
Label					
Show scale marks	Enabled	**Show "+" for positive numbers**	Disabled	**Use exponential format**	Disabled
Double-lined scale label	Disabled	**Unit**		**Integer digits**	4
Decimal places	0				

Layout					
X position	661	**Y position**	210	**Width**	16
Height	110	**Scale position**	Left/up	**Bar orientation**	Top
Text format					
Font	Tahoma, 8px				
Limits/Ranges					
Color range high 2	255, 109, 118	**Color range low 2**	255, 218, 44		
Miscellaneous					
Name	Bargraphe_1	**Layer**	0 - Layer_0		
Dynamizations\Tag connection					
Property name	Process value	**Tag**	Tank level (mm)		

Bargraphe_2

Type	Bar				
General					
Maximum value	7000	**Minimum value**	0	**Process value**	0
Appearance					
Foreground color	218, 218, 218	**Background color of bar**	72, 72, 72	**Segment coloring**	Segmented
Background color	72, 72, 72	**Color of scale**	218, 218, 218	**Limit lines (layout)**	Disabled
Limit marking (layout)	Enabled				
Border type					
Bar edge style	None				
Scales					
Show scale	Disabled	**Auto-scale**	Enabled	**Divisions**	5
Large mark labeling	1	**Scale gradation**	0		
Label					
Show scale marks	Enabled	**Show "+" for positive numbers**	Disabled	**Use exponential format**	Disabled
Double-lined scale label	Disabled	**Unit**		**Integer digits**	4
Decimal places	0				
Layout					
X position	661	**Y position**	409	**Width**	16
Height	110	**Scale position**	Left/up	**Bar orientation**	Top
Text format					
Font	Tahoma, 8px				
Limits/Ranges					
Color range high 2	255, 109, 118	**Color range low 2**	255, 218, 44		
Miscellaneous					
Name	Bargraphe_2	**Layer**	0 - Layer_0		
Dynamizations\Tag connection					
Property name	Process value	**Tag**	Tank level (mm)		

Cercle_24

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	638	**Y position**	303	**Width**	14
Height	14	**Radius**	7		
Miscellaneous					
Name	Cercle_24	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Tank level LL -	**Data type**	Range	**Range**	1..1
Foreground color	0, 0, 0	**Background color**	255, 255, 63	**Flashing**	Yes
Dynamizations\Visibility					
Tag - Cycle	Tank level LL -	**Data type**	Range	**Start range**	1
End range	1	**Visibility**	Visible		

Cercle_25

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	639	**Y position**	504	**Width**	14
Height	14	**Radius**	7		
Miscellaneous					
Name	Cercle_25	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Tank level LL -	**Data type**	Range	**Range**	1..1
Foreground color	0, 0, 0	**Background color**	255, 255, 63	**Flashing**	Yes
Dynamizations\Visibility					
Tag - Cycle	Tank level LL -	**Data type**	Range	**Start range**	1
End range	1	**Visibility**	Visible		

Cercle_26

Type	Circle

Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	639	**Y position**	227	**Width**	14
Height	14	**Radius**	7		
Miscellaneous					
Name	Cercle_26	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Tank level HH -	**Data type**	Range	**Range**	1..1
Foreground color	0, 0, 0	**Background color**	255, 255, 63	**Flashing**	Yes
Dynamizations\Visibility					
Tag - Cycle	Tank level HH -	**Data type**	Range	**Start range**	1
End range	1	**Visibility**	Visible		

Cercle_27

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	639	**Y position**	429	**Width**	14
Height	14	**Radius**	7		
Miscellaneous					
Name	Cercle_27	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Tank level HH -	**Data type**	Range	**Range**	1..1
Foreground color	0, 0, 0	**Background color**	255, 255, 63	**Flashing**	Yes
Dynamizations\Visibility					
Tag - Cycle	Tank level HH -	**Data type**	Range	**Start range**	1
End range	1	**Visibility**	Visible		

Cercle_28

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	809	**Y position**	303	**Width**	15
Height	15	**Radius**	7		
Miscellaneous					
Name	Cercle_28	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	countinous injection pump 1 -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_29

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	809	**Y position**	368	**Width**	15
Height	15	**Radius**	7		
Miscellaneous					
Name	Cercle_29	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	countinous injection pump 2 -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_30

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	810	**Y position**	429	**Width**	15
Height	15	**Radius**	7		
Miscellaneous					
Name	Cercle_30	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	shocked injection pump 1 -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_31

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	811	**Y position**	490	**Width**	15
Height	15	**Radius**	7		
Miscellaneous					
Name	Cercle_31	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	shocked injection pump 2 -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_32

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	809	**Y position**	303	**Width**	15
Height	15	**Radius**	7		
Miscellaneous					
Name	Cercle_32	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	countinous injection pump 1 Fault -	**Data type**	Range	**Range**	1..1
Foreground color	0, 0, 0	**Background color**	255, 255, 63	**Flashing**	Yes
Dynamizations\Visibility					
Tag - Cycle	countinous injection pump 1 Fault -	**Data type**	Range	**Start range**	1
End range	1	**Visibility**	Visible		

Cercle_33

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	810	**Y position**	368	**Width**	15
Height	15	**Radius**	7		
Miscellaneous					
Name	Cercle_33	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	countinous injection pump 2 Fault -	**Data type**	Range	**Range**	1..1
Foreground color	0, 0, 0	**Background color**	255, 255, 63	**Flashing**	Yes
Dynamizations\Visibility					
Tag - Cycle	countinous injection pump 2 Fault -	**Data type**	Range	**Start range**	1
End range	1	**Visibility**	Visible		

Cercle_34

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	809	**Y position**	429	**Width**	15
Height	15	**Radius**	7		
Miscellaneous					
Name	Cercle_34	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	shocked injection pump 1 Fault -	**Data type**	Range	**Range**	1..1
Foreground color	0, 0, 0	**Background color**	255, 255, 63	**Flashing**	Yes
Dynamizations\Visibility					
Tag - Cycle	shocked injection pump 1 Fault -	**Data type**	Range	**Start range**	1
End range	1	**Visibility**	Visible		

Cercle_35

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	809	**Y position**	490	**Width**	15
Height	15	**Radius**	7		
Miscellaneous					
Name	Cercle_35	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	shocked injection pump 2 Fault -	**Data type**	Range	**Range**	1..1

Foreground color	0, 0, 0	**Background color**	255, 255, 63	**Flashing**	Yes
Dynamizations\Visibility					
Tag - Cycle	shocked injection pump 2 Fault -	**Data type**	Range	**Start range**	1
End range	1	**Visibility**	Visible		

Bouton_5

Type	Button				
General					
Mode	Text	**Text OFF**	started	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	860	**Y position**	307	**Width**	46
Height	24	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 11px	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_5	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_countinous injection pump 1 started

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_countinous injection pump 1 started

Bouton_6

Type	Button				
General					
Mode	Text	**Text OFF**	started	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	861	**Y position**	373	**Width**	45
Height	24	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 11px	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_6	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_countinous injection pump 2 started

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_countinous injection pump 2 started

Bouton_8

Type	Button				
General					
Mode	Text	**Text OFF**	started	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				

Design					
Focus color	0, 182, 219				
Layout					
X position	864	**Y position**	435	**Width**	44
Height	24	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 11px	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_8	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_shocked injection pump 1 started

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_shocked injection pump 1 started

Bouton_9

Type	Button				
General					
Mode	Text	**Text OFF**	started	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	865	**Y position**	495	**Width**	42
Height	24	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 11px	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_9	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_shocked injection pump 2 started

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_shocked injection pump 2 started

Champ d'E/S_4

Type	I/O field				
General					
Process value		**Mode**	Input/output	**Display format**	Decimal
Shift decimal point	0	**Field length**	13	**Show leading zeros**	Disabled
Format pattern	s999999999.999				
Appearance					
Background color	218, 218, 218	**Background fill pattern**	Solid	**Foreground color**	36, 36, 36
Unit		**Border width**	1	**Line style**	Solid
Border color	0, 0, 0				
Characteristics					
Hidden input	Disabled				
Layout					
X position	546	**Y position**	318	**Width**	72
Height	23	**Left margin**	5	**Top margin**	2
Right margin	2	**Bottom margin**	2	**Fit object to contents**	Disabled
Text format					
Font	Tahoma, 12px	**Horizontal alignment**	Left	**Vertical alignment**	Middle
Line break	Disabled				
Limits					
Color for High limit violated	255, 0, 31	**Color for Low limit violated**	218, 255, 44		

Miscellaneous					
Name	Champ d'E/S_4	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		
Dynamizations\Tag connection					
Property name	Process value	**Tag**	Hypochlorite Flow (m3/h)		

Cercle_23

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	618	**Y position**	399	**Width**	17
Height	17	**Radius**	8		
Miscellaneous					
Name	Cercle_23	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	valve to Tank -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_36

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	618	**Y position**	194	**Width**	17
Height	17	**Radius**	8		
Miscellaneous					
Name	Cercle_36	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	valve to tank Fault -	**Data type**	Range	**Range**	1..1
Foreground color	0, 0, 0	**Background color**	255, 255, 63	**Flashing**	No
Dynamizations\Visibility					
Tag - Cycle	valve to tank Fault -	**Data type**	Range	**Start range**	1
End range	1	**Visibility**	Visible		

Cercle_38

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	619	**Y position**	399	**Width**	17
Height	17	**Radius**	8		
Miscellaneous					
Name	Cercle_38	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	valve to tank Fault -	**Data type**	Range	**Range**	1..1
Foreground color	0, 0, 0	**Background color**	255, 255, 63	**Flashing**	No
Dynamizations\Visibility					
Tag - Cycle	valve to tank Fault -	**Data type**	Range	**Start range**	1
End range	1	**Visibility**	Visible		

Bouton_11

Type	Button				
General					
Mode	Text	**Text OFF**	auto	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	585	**Y position**	173	**Width**	38
Height	24	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 11px	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_11	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_valve to tank opened

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_valve to tank opened

Bouton_12

Type	Button				
General					
Mode	Text	Text OFF	man	Text ON	Text
Text list		Graphic OFF		Graphic ON	
Graphic list		Process value		Bit number	0
Appearance					
Background color	72, 72, 72	Foreground color	255, 255, 255	Border width	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	623	Y position	173	Width	38
Height	24	Fit object to contents	Disabled		
Text format					
Font	Tahoma, 11px	Horizontal alignment of the text	Centered	Vertical alignment of the text	Middle
Miscellaneous					
Name	Bouton_12	Layer	0 - Layer_0	Tooltip	
Security					
Authorization		Allow operator control	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_valve to tank opened manually

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_valve to tank opened manually

Bouton_13

Type	Button				
General					
Mode	Text	Text OFF	auto	Text ON	Text
Text list		Graphic OFF		Graphic ON	
Graphic list		Process value		Bit number	0
Appearance					
Background color	72, 72, 72	Foreground color	255, 255, 255	Border width	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	596	Y position	373	Width	38
Height	24	Fit object to contents	Disabled		
Text format					
Font	Tahoma, 11px	Horizontal alignment of the text	Centered	Vertical alignment of the text	Middle
Miscellaneous					
Name	Bouton_13	Layer	0 - Layer_0	Tooltip	
Security					
Authorization		Allow operator control	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_valve to tank opened

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_valve to tank opened

Bouton_14

Type	Button				
General					
Mode	Text	**Text OFF**	man	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	634	**Y position**	373	**Width**	38
Height	24	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 11px	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_14	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_valve to tank opened manually
Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_valve to tank opened manually

Cercle_39

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	5	**Y position**	72	**Width**	12
Height	12	**Radius**	6		
Miscellaneous					
Name	Cercle_39	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	air flow is not ok -	**Data type**	Range	**Range**	1..1
Foreground color	0, 0, 0	**Background color**	255, 255, 63	**Flashing**	Yes
Dynamizations\Visibility					
Tag - Cycle	air flow is not ok -	**Data type**	Range	**Start range**	1
End range	1	**Visibility**	Visible		

Champ d'E/S_6

Type	I/O field				
General					
Process value		**Mode**	Input/output	**Display format**	Decimal
Shift decimal point	0	**Field length**	13	**Show leading zeros**	Disabled
Format pattern	s999999999.999				
Appearance					
Background color	218, 218, 218	**Background fill pattern**	Solid	**Foreground color**	36, 36, 36
Unit		**Border width**	1	**Line style**	Solid
Border color	0, 0, 0				
Characteristics					
Hidden input	Disabled				
Layout					
X position	696	**Y position**	278	**Width**	60
Height	23	**Left margin**	5	**Top margin**	2
Right margin	2	**Bottom margin**	2	**Fit object to contents**	Disabled
Text format					
Font	Tahoma, 12px	**Horizontal alignment**	Left	**Vertical alignment**	Middle
Line break	Disabled				
Limits					
Color for High limit violated	255, 0, 31	**Color for Low limit violated**	218, 255, 44		
Miscellaneous					
Name	Champ d'E/S_6	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		
Dynamizations\Tag connection					
Property name	Process value	**Tag**	Tank level (mm)		

Cercle_41

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	604	**Y position**	303	**Width**	12
Height	12	**Radius**	6		
Miscellaneous					
Name	Cercle_41	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Hypochlorite Flow is not ok -	**Data type**	Range	**Range**	1..1
Foreground color	0, 0, 0	**Background color**	255, 255, 63	**Flashing**	Yes
Dynamizations\Visibility					
Tag - Cycle	Hypochlorite Flow is not ok -	**Data type**	Range	**Start range**	1
End range	1	**Visibility**	Visible		

Champ d'E/S_7

Type	I/O field				
General					
Process value		**Mode**	Input/output	**Display format**	Decimal
Shift decimal point	0	**Field length**	13	**Show leading zeros**	Disabled
Format pattern	s999999999.999				
Appearance					
Background color	218, 218, 218	**Background fill pattern**	Solid	**Foreground color**	36, 36, 36
Unit		**Border width**	1	**Line style**	Solid
Border color	0, 0, 0				
Characteristics					
Hidden input	Disabled				
Layout					
X position	504	**Y position**	143	**Width**	72
Height	23	**Left margin**	5	**Top margin**	2
Right margin	2	**Bottom margin**	2	**Fit object to contents**	Disabled
Text format					
Font	Tahoma, 12px	**Horizontal alignment**	Left	**Vertical alignment**	Middle
Line break	Disabled				
Limits					
Color for High limit violated	255, 0, 31	**Color for Low limit violated**	218, 255, 44		
Miscellaneous					
Name	Champ d'E/S_7	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		
Dynamizations\Tag connection					
Property name	Process value	**Tag**	Hypoclorite temperature (C°)		

Cercle_42

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	486	**Y position**	146	**Width**	12
Height	12	**Radius**	6		
Miscellaneous					
Name	Cercle_42	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Hypoclorite temperature HH -	**Data type**	Range	**Range**	1..1
Foreground color	0, 0, 0	**Background color**	255, 255, 63	**Flashing**	Yes
Dynamizations\Visibility					
Tag - Cycle	Hypoclorite temperature HH -	**Data type**	Range	**Start range**	1
End range	1	**Visibility**	Visible		

Bouton_15

Type	Button				
General					
Mode	Text	**Text OFF**	OPEN	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	243	**Y position**	165	**Width**	38
Height	24	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 11px	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle

Miscellaneous					
Name	Bouton_15	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_valve before filter 1 opened

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_valve before filter 1 opened

Dynamizations\Appearance					
Tag - Cycle	HMI_valve before filter 1 opened -	**Data type**	Range	**Range**	0..0
Foreground color	255, 255, 255	**Background color**	72, 72, 72	**Flashing**	No
Range	1..1	**Foreground color**	255, 255, 255	**Background color**	255, 109, 54
Flashing	No				

Bouton_16

Type	Button				
General					
Mode	Text	**Text OFF**	OPEN	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	243	**Y position**	367	**Width**	38
Height	24	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 11px	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_16	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_valve before filter 2 opened

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_valve before filter 2 opened

Dynamizations\Appearance					
Tag - Cycle	HMI_valve before filter 2 opened -	**Data type**	Range	**Range**	0..0
Foreground color	255, 255, 255	**Background color**	72, 72, 72	**Flashing**	No
Range	1..1	**Foreground color**	255, 255, 255	**Background color**	255, 109, 54
Flashing	No				

Cercle_8

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	253	**Y position**	400	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_8	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	filter 2 -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_18

Type	Circle

Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	252	**Y position**	197	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_18	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	filter 1 -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_7

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	473	**Y position**	213	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_7	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	electrolizer 1 -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_9

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	423	**Y position**	196	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_9	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	electrolizer 1 -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_15

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	473	**Y position**	415	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_15	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	electrolizer 2 -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_16

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	423	**Y position**	399	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_16	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	electrolizer 2 -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_14

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	552	**Y position**	401	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_14	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	electrolizer 2 -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_19

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	552	**Y position**	196	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_19	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	electrolizer 1 -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_20

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	338	**Y position**	401	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_20	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	FILTER 2 VALVE -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_37

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	338	**Y position**	197	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_37	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	FILTER 1 VALVE -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_43

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	332	**Y position**	260	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_43	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	filter 1 -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	255, 0, 31	**Flashing**	Yes
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	36, 145, 90
Flashing	No				

Cercle_44

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	332	**Y position**	461	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_44	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	filter 2 -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	255, 0, 31	**Flashing**	Yes
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	36, 145, 90
Flashing	No				

Bouton_17

Type	Button				
General					
Mode	Text	**Text OFF**	start	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	451	**Y position**	235	**Width**	38
Height	24	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 11px	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_17	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_ELECROLYZER 1 STARTED

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_ELECROLYZER 1 STARTED

Bouton_18

Type	Button				
General					
Mode	Text	**Text OFF**	start	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	451	**Y position**	441	**Width**	38
Height	24	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 11px	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_18	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_ELECROLYZER 2 STARTED

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_ELECROLYZER 2 STARTED

Champ d'E/S_5

Type	I/O field				
General					
Process value		**Mode**	Input/output	**Display format**	Decimal
Shift decimal point	0	**Field length**	13	**Show leading zeros**	Disabled
Format pattern	s999999999.999				
Appearance					
Background color	218, 218, 218	**Background fill pattern**	Solid	**Foreground color**	36, 36, 36
Unit		**Border width**	1	**Line style**	Solid
Border color	0, 0, 0				
Characteristics					
Hidden input	Disabled				
Layout					
X position	696	**Y position**	502	**Width**	60
Height	23	**Left margin**	5	**Top margin**	2
Right margin	2	**Bottom margin**	2	**Fit object to contents**	Disabled
Text format					
Font	Tahoma, 12px	**Horizontal alignment**	Left	**Vertical alignment**	Middle
Line break	Disabled				
Limits					
Color for High limit violated	255, 0, 31	**Color for Low limit violated**	218, 255, 44		
Miscellaneous					
Name	Champ d'E/S_5	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		
Dynamizations\Tag connection					
Property name	Process value	**Tag**	Tank level (mm)		

Champ d'E/S_8

Type	I/O field				
General					
Process value		**Mode**	Input/output	**Display format**	Decimal
Shift decimal point	0	**Field length**	13	**Show leading zeros**	Disabled
Format pattern	s999999999.999				
Appearance					
Background color	218, 218, 218	**Background fill pattern**	Solid	**Foreground color**	36, 36, 36
Unit		**Border width**	1	**Line style**	Solid
Border color	0, 0, 0				
Characteristics					
Hidden input	Disabled				
Layout					
X position	771	**Y position**	143	**Width**	72
Height	23	**Left margin**	5	**Top margin**	2
Right margin	2	**Bottom margin**	2	**Fit object to contents**	Disabled
Text format					
Font	Tahoma, 12px	**Horizontal alignment**	Left	**Vertical alignment**	Middle
Line break	Disabled				
Limits					
Color for High limit violated	255, 0, 31	**Color for Low limit violated**	218, 255, 44		
Miscellaneous					
Name	Champ d'E/S_8	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		
Dynamizations\Tag connection					
Property name	Process value	**Tag**	H2 concentration percent (%)		

Bouton_19

Type	Button				
General					
Mode	Text	**Text OFF**	START	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	276	**Y position**	30	**Width**	97
Height	32	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_19	**Layer**	0 - Layer_0	**Tooltip**	

Security			
Authorization		**Allow operator control**	Enabled

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_HYPOCHLORITE SYSTEM START

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_HYPOCHLORITE SYSTEM START

Bouton_20

Type	Button				
General					
Mode	Text	**Text OFF**	PUMP1	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	742	**Y position**	321	**Width**	48
Height	25	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 11px	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_20	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_SELECT COUNTINOUS PUMP 1

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_SELECT COUNTINOUS PUMP 1

Bouton_21

Type	Button				
General					
Mode	Text	**Text OFF**	PUMP2	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	742	**Y position**	343	**Width**	48
Height	25	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 11px	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_21	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_SELECT COUNTINOUS PUMP2

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_SELECT COUNTINOUS PUMP2

Bouton_22

Type	Button				
General					
Mode	Text	**Text OFF**	PUMP2	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	756	**Y position**	455	**Width**	48
Height	25	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 11px	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_22	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_SELECT SHOCK PUMP 2

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_SELECT SHOCK PUMP 2

Bouton_23

Type	Button				
General					
Mode	Text	**Text OFF**	PUMP1	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	755	**Y position**	431	**Width**	48
Height	25	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 11px	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_23	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_SELECT SHOCK PUMP 1

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_SELECT SHOCK PUMP 1

Cercle_21

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	753	**Y position**	143	**Width**	12
Height	12	**Radius**	6		

Miscellaneous					
Name	Cercle_21	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	H2 concentration H -	**Data type**	Range	**Range**	1..1
Foreground color	0, 0, 0	**Background color**	255, 255, 63	**Flashing**	Yes
Dynamizations\Visibility					
Tag - Cycle	H2 concentration H -	**Data type**	Range	**Start range**	1
End range	1	**Visibility**	Visible		

Bouton_24

Type	Button				
General					
Mode	Text	**Text OFF**	PUMP2	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	101	**Y position**	276	**Width**	48
Height	25	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 11px	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_24	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_select booster pump 2

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_select booster pump 2

Dynamizations\Appearance					
Tag - Cycle	HMI_select booster pump 2 -	**Data type**	Range	**Range**	0..0
Foreground color	255, 255, 255	**Background color**	72, 72, 72	**Flashing**	No
Range	1..1	**Foreground color**	255, 255, 255	**Background color**	255, 109, 54
Flashing	No				

Bouton_4

Type	Button				
General					
Mode	Text	**Text OFF**	PUMP1	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	101	**Y position**	251	**Width**	48
Height	25	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 11px	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_4	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_select booster pump 1

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_select booster pump 1

Dynamizations\Appearance					
Tag - Cycle	HMI_select booster pump 1 -	**Data type**	Range	**Range**	1..1
Foreground color	255, 255, 255	**Background color**	255, 109, 54	**Flashing**	No
Range	0..0	**Foreground color**	255, 255, 255	**Background color**	72, 72, 72
Flashing	No				

Cercle_40

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	712	**Y position**	337	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_40	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	H2 blower -	**Data type**	Range	**Range**	1..1
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	0..0	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_45

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	711	**Y position**	536	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_45	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	H2 blower -	**Data type**	Range	**Range**	1..1
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	0..0	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Bouton_7

Type	Button				
General					
Mode	Text	**Text OFF**	ALARM	**Text ON**	ALARM
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	831	**Y position**	657	**Width**	192
Height	32	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_7	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Click

Function list\ActivateScreen

Screen name	ALARM	**Object number**	0

Text field_1

Type	Text field				
General					
Text	Seawater Flow (m3/h)				
Appearance					
Background color	145, 145, 145	**Background fill pattern**	Transparent	**Foreground color**	0, 0, 0
Border width	0	**Line style**	Solid	**Border color**	0, 0, 0
Layout					
X position	0	**Y position**	303	**Width**	115
Height	13	**Left margin**	0	**Top margin**	2
Right margin	2	**Bottom margin**	0	**Fit object to contents**	Enabled
Text format					
Font	Tahoma, 9px, style=Bold	**Horizontal alignment**	Left	**Vertical alignment**	Middle
Line break	Disabled				

Flashing					
Flashing	Disabled				
Miscellaneous					
Name	Text field_1	**Layer**	0 - Layer_0		

Text field_2

Type	Text field				
General					
Text	Air Flow (m3/h)				
Appearance					
Background color	145, 145, 145	**Background fill pattern**	Transparent	**Foreground color**	0, 0, 0
Border width	0	**Line style**	Solid	**Border color**	0, 0, 0
Layout					
X position	19	**Y position**	61	**Width**	90
Height	15	**Left margin**	0	**Top margin**	2
Right margin	2	**Bottom margin**	0	**Fit object to contents**	Enabled
Text format					
Font	Tahoma, 11px, style=Bold	**Horizontal alignment**	Left	**Vertical alignment**	Middle
Line break	Disabled				
Flashing					
Flashing	Disabled				
Miscellaneous					
Name	Text field_2	**Layer**	0 - Layer_0		

Text field_3

Type	Text field				
General					
Text	Current(A)				
Appearance					
Background color	145, 145, 145	**Background fill pattern**	Transparent	**Foreground color**	0, 0, 0
Border width	0	**Line style**	Solid	**Border color**	0, 0, 0
Layout					
X position	454	**Y position**	299	**Width**	63
Height	15	**Left margin**	0	**Top margin**	2
Right margin	2	**Bottom margin**	0	**Fit object to contents**	Enabled
Text format					
Font	Tahoma, 11px, style=Bold	**Horizontal alignment**	Left	**Vertical alignment**	Middle
Line break	Disabled				
Flashing					
Flashing	Disabled				
Miscellaneous					
Name	Text field_3	**Layer**	0 - Layer_0		

Text field_4

Type	Text field				
General					
Text	Temperature (C°)				
Appearance					
Background color	145, 145, 145	**Background fill pattern**	Transparent	**Foreground color**	0, 0, 0
Border width	0	**Line style**	Solid	**Border color**	0, 0, 0
Layout					
X position	492	**Y position**	124	**Width**	106
Height	15	**Left margin**	0	**Top margin**	2
Right margin	2	**Bottom margin**	0	**Fit object to contents**	Enabled
Text format					
Font	Tahoma, 11px, style=Bold	**Horizontal alignment**	Left	**Vertical alignment**	Middle
Line break	Disabled				
Flashing					
Flashing	Disabled				
Miscellaneous					
Name	Text field_4	**Layer**	0 - Layer_0		

Text field_5

Type	Text field				
General					
Text	Hypochlorite Flow (m3/h)				
Appearance					
Background color	145, 145, 145	**Background fill pattern**	Transparent	**Foreground color**	0, 0, 0
Border width	0	**Line style**	Solid	**Border color**	0, 0, 0
Layout					
X position	515	**Y position**	340	**Width**	146
Height	15	**Left margin**	0	**Top margin**	2
Right margin	2	**Bottom margin**	0	**Fit object to contents**	Enabled
Text format					
Font	Tahoma, 11px, style=Bold	**Horizontal alignment**	Left	**Vertical alignment**	Middle
Line break	Disabled				
Flashing					
Flashing	Disabled				
Miscellaneous					
Name	Text field_5	**Layer**	0 - Layer_0		

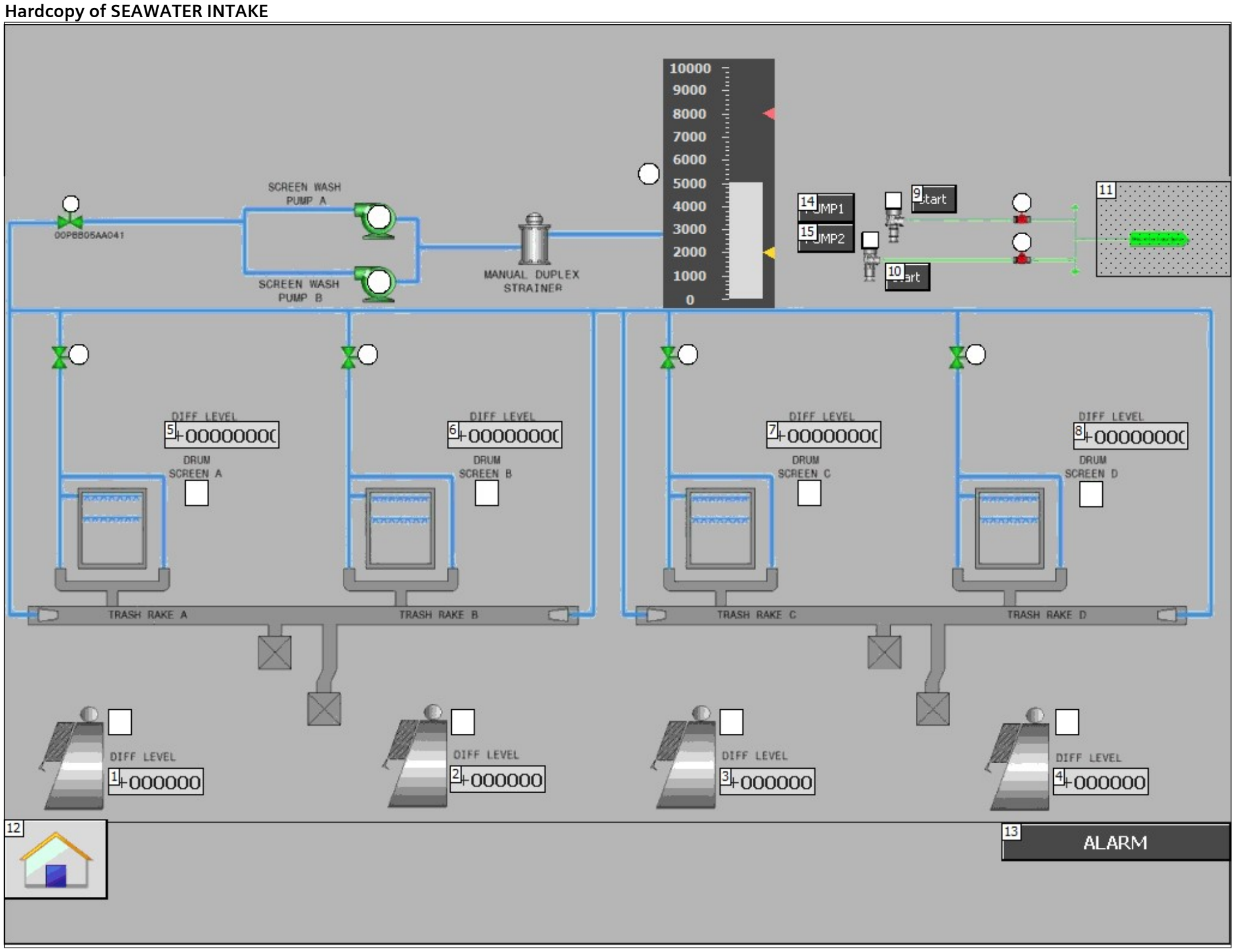
Text field_7

Type	Text field				
General					
Text	H2 concentration percent (%)				
Appearance					
Background color	145, 145, 145	**Background fill pattern**	Transparent	**Foreground color**	0, 0, 0
Border width	0	**Line style**	Solid	**Border color**	0, 0, 0
Layout					
X position	735	**Y position**	123	**Width**	172
Height	15	**Left margin**	0	**Top margin**	2
Right margin	2	**Bottom margin**	0	**Fit object to contents**	Enabled
Text format					
Font	Tahoma, 11px, style=Bold	**Horizontal alignment**	Left	**Vertical alignment**	Middle
Line break	Disabled				
Flashing					
Flashing	Disabled				
Miscellaneous					
Name	Text field_7	**Layer**	0 - Layer_0		

electro-chlorination station 3_V13_SP1_V18 / HMI_1 [TP1500 Basic color PN] / Screens

SEAWATER INTAKE

Hardcopy of SEAWATER INTAKE



General					
Name	SEAWATER INTAKE	**Background color**	109, 109, 109	**Grid color**	182, 182, 182
Number	4	**Template**		**Tooltip**	
Layers					
Active layer	0				

Layer_0	Enabled
Layer_1	Enabled
Layer_2	Enabled
Layer_3	Enabled
Layer_4	Enabled
Layer_5	Enabled
Layer_6	Enabled
Layer_7	Enabled
Layer_8	Enabled
Layer_9	Enabled
Layer_10	Enabled
Layer_11	Enabled
Layer_12	Enabled
Layer_13	Enabled
Layer_14	Enabled
Layer_15	Enabled
Layer_16	Enabled
Layer_17	Enabled
Layer_18	Enabled
Layer_19	Enabled
Layer_20	Enabled
Layer_21	Enabled
Layer_22	Enabled
Layer_23	Enabled
Layer_24	Enabled
Layer_25	Enabled
Layer_26	Enabled

Layer_27	Enabled
Layer_28	Enabled
Layer_29	Enabled
Layer_30	Enabled
Layer_31	Enabled

Rectangle_1

Type	Rectangle				
Appearance					
Background color	182, 182, 182	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	0	**Y position**	0	**Width**	1023
Height	767	**Round corner width**	0	**Round corner height**	0
Miscellaneous					
Name	Rectangle_1	**Layer**	0 - Layer_0		

Vue de graphique_1

Type	Graphic view				
General					
Graphic	seawater intake_1				
Appearance					
Background color	182, 182, 182	**Border width**	0	**Line style**	Solid
Border color	255, 255, 255				
Layout					
X position	0	**Y position**	127	**Width**	1024
Height	539	**Fit embedded graphic object to screen size**	Fit graphic to object size	**Fit object to contents**	Disabled
Miscellaneous					
Name	Vue de graphique_1	**Layer**	0 - Layer_0		

Rectangle_2

Type	Rectangle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	87	**Y position**	571	**Width**	20
Height	22	**Round corner width**	0	**Round corner height**	0
Miscellaneous					
Name	Rectangle_2	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	BAR SCREEN Filter -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Champ d'E/S_2

Type	I/O field				
General					
Process value		**Mode**	Input/output	**Display format**	Decimal
Shift decimal point	0	**Field length**	13	**Show leading zeros**	Disabled
Format pattern	s999999999.999				
Appearance					
Background color	218, 218, 218	**Background fill pattern**	Solid	**Foreground color**	36, 36, 36
Unit		**Border width**	1	**Line style**	Solid
Border color	0, 0, 0				
Characteristics					
Hidden input	Disabled				
Layout					
X position	87	**Y position**	620	**Width**	80
Height	23	**Left margin**	5	**Top margin**	2
Right margin	2	**Bottom margin**	2	**Fit object to contents**	Disabled
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment**	Left	**Vertical alignment**	Middle
Line break	Disabled				
Limits					
Color for High limit violated	255, 0, 31	**Color for Low limit violated**	218, 255, 44		
Miscellaneous					
Name	Champ d'E/S_2	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		
Dynamizations\Tag connection					
Property name	Process value	**Tag**	DL of BAR SCREEN (mm)		

Champ d'E/S_3

Type	I/O field				
General					
Process value		**Mode**	Input/output	**Display format**	Decimal

Shift decimal point	0	**Field length**	13	**Show leading zeros**	Disabled
Format pattern	s999999999.999				
Appearance					
Background color	218, 218, 218	**Background fill pattern**	Solid	**Foreground color**	36, 36, 36
Unit		**Border width**	1	**Line style**	Solid
Border color	0, 0, 0				
Characteristics					
Hidden input	Disabled				
Layout					
X position	372	**Y position**	618	**Width**	80
Height	23	**Left margin**	5	**Top margin**	2
Right margin	2	**Bottom margin**	2	**Fit object to contents**	Disabled
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment**	Left	**Vertical alignment**	Middle
Line break	Disabled				
Limits					
Color for High limit violated	255, 0, 31	**Color for Low limit violated**	218, 255, 44		
Miscellaneous					
Name	Champ d'E/S_3	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		
Dynamizations\Tag connection					
Property name	Process value	**Tag**	DL of BAR SCREEN (mm)		

Champ d'E/S_4

Type	I/O field				
General					
Process value		**Mode**	Input/output	**Display format**	Decimal
Shift decimal point	0	**Field length**	13	**Show leading zeros**	Disabled
Format pattern	s999999999.999				
Appearance					
Background color	218, 218, 218	**Background fill pattern**	Solid	**Foreground color**	36, 36, 36
Unit		**Border width**	1	**Line style**	Solid
Border color	0, 0, 0				
Characteristics					
Hidden input	Disabled				
Layout					
X position	597	**Y position**	620	**Width**	80
Height	23	**Left margin**	5	**Top margin**	2
Right margin	2	**Bottom margin**	2	**Fit object to contents**	Disabled
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment**	Left	**Vertical alignment**	Middle
Line break	Disabled				
Limits					
Color for High limit violated	255, 0, 31	**Color for Low limit violated**	218, 255, 44		
Miscellaneous					
Name	Champ d'E/S_4	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		
Dynamizations\Tag connection					
Property name	Process value	**Tag**	DL of BAR SCREEN (mm)		

Champ d'E/S_5

Type	I/O field				
General					
Process value		**Mode**	Input/output	**Display format**	Decimal
Shift decimal point	0	**Field length**	13	**Show leading zeros**	Disabled
Format pattern	s999999999.999				
Appearance					
Background color	218, 218, 218	**Background fill pattern**	Solid	**Foreground color**	36, 36, 36
Unit		**Border width**	1	**Line style**	Solid
Border color	0, 0, 0				
Characteristics					
Hidden input	Disabled				
Layout					
X position	875	**Y position**	620	**Width**	80
Height	23	**Left margin**	5	**Top margin**	2
Right margin	2	**Bottom margin**	2	**Fit object to contents**	Disabled
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment**	Left	**Vertical alignment**	Middle
Line break	Disabled				
Limits					
Color for High limit violated	255, 0, 31	**Color for Low limit violated**	218, 255, 44		
Miscellaneous					
Name	Champ d'E/S_5	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Tag connection			
Property name	Process value	**Tag**	DL of BAR SCREEN (mm)

Rectangle_9

Type	Rectangle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	151	**Y position**	380	**Width**	20
Height	22	**Round corner width**	0	**Round corner height**	0
Miscellaneous					
Name	Rectangle_9	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Rotating Drum Screaan Filter -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Champ d'E/S_6

Type	I/O field				
General					
Process value		**Mode**	Input/output	**Display format**	Decimal
Shift decimal point	0	**Field length**	13	**Show leading zeros**	Disabled
Format pattern	s999999999.999				
Appearance					
Background color	218, 218, 218	**Background fill pattern**	Solid	**Foreground color**	36, 36, 36
Unit		**Border width**	1	**Line style**	Solid
Border color	0, 0, 0				
Characteristics					
Hidden input	Disabled				
Layout					
X position	134	**Y position**	331	**Width**	96
Height	23	**Left margin**	5	**Top margin**	2
Right margin	2	**Bottom margin**	2	**Fit object to contents**	Disabled
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment**	Left	**Vertical alignment**	Middle
Line break	Disabled				
Limits					
Color for High limit violated	255, 0, 31	**Color for Low limit violated**	218, 255, 44		
Miscellaneous					
Name	Champ d'E/S_6	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		
Dynamizations\Tag connection					
Property name	Process value	**Tag**	DL of Rotating DRum Screen (mm)		

Rectangle_6

Type	Rectangle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	393	**Y position**	380	**Width**	20
Height	22	**Round corner width**	0	**Round corner height**	0
Miscellaneous					
Name	Rectangle_6	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Rotating Drum Screaan Filter -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Rectangle_7

Type	Rectangle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	662	**Y position**	380	**Width**	20
Height	22	**Round corner width**	0	**Round corner height**	0
Miscellaneous					
Name	Rectangle_7	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Rotating Drum Screaan Filter -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Rectangle_8

Type	Rectangle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	897	**Y position**	381	**Width**	20
Height	22	**Round corner width**	0	**Round corner height**	0
Miscellaneous					
Name	Rectangle_8	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Rotating Drum Screaan Filter -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Champ d'E/S_7

Type	I/O field				
General					
Process value		**Mode**	Input/output	**Display format**	Decimal
Shift decimal point	0	**Field length**	13	**Show leading zeros**	Disabled
Format pattern	s999999999.999				
Appearance					
Background color	218, 218, 218	**Background fill pattern**	Solid	**Foreground color**	36, 36, 36
Unit		**Border width**	1	**Line style**	Solid
Border color	0, 0, 0				
Characteristics					
Hidden input	Disabled				
Layout					
X position	370	**Y position**	331	**Width**	96
Height	23	**Left margin**	5	**Top margin**	2
Right margin	2	**Bottom margin**	2	**Fit object to contents**	Disabled
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment**	Left	**Vertical alignment**	Middle
Line break	Disabled				
Limits					
Color for High limit violated	255, 0, 31	**Color for Low limit violated**	218, 255, 44		
Miscellaneous					
Name	Champ d'E/S_7	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		
Dynamizations\Tag connection					
Property name	Process value	**Tag**	DL of Rotating DRum Screen (mm)		

Champ d'E/S_8

Type	I/O field				
General					
Process value		**Mode**	Input/output	**Display format**	Decimal
Shift decimal point	0	**Field length**	13	**Show leading zeros**	Disabled
Format pattern	s999999999.999				
Appearance					
Background color	218, 218, 218	**Background fill pattern**	Solid	**Foreground color**	36, 36, 36
Unit		**Border width**	1	**Line style**	Solid
Border color	0, 0, 0				
Characteristics					
Hidden input	Disabled				
Layout					
X position	636	**Y position**	331	**Width**	96
Height	23	**Left margin**	5	**Top margin**	2
Right margin	2	**Bottom margin**	2	**Fit object to contents**	Disabled
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment**	Left	**Vertical alignment**	Middle
Line break	Disabled				
Limits					
Color for High limit violated	255, 0, 31	**Color for Low limit violated**	218, 255, 44		
Miscellaneous					
Name	Champ d'E/S_8	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		
Dynamizations\Tag connection					
Property name	Process value	**Tag**	DL of Rotating DRum Screen (mm)		

Champ d'E/S_9

Type	I/O field				
General					
Process value		**Mode**	Input/output	**Display format**	Decimal
Shift decimal point	0	**Field length**	13	**Show leading zeros**	Disabled

Format pattern	s999999999.999				
Appearance					
Background color	218, 218, 218	**Background fill pattern**	Solid	**Foreground color**	36, 36, 36
Unit		**Border width**	1	**Line style**	Solid
Border color	0, 0, 0				
Characteristics					
Hidden input	Disabled				
Layout					
X position	892	**Y position**	332	**Width**	96
Height	23	**Left margin**	5	**Top margin**	2
Right margin	2	**Bottom margin**	2	**Fit object to contents**	Disabled
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment**	Left	**Vertical alignment**	Middle
Line break	Disabled				
Limits					
Color for High limit violated	255, 0, 31	**Color for Low limit violated**	218, 255, 44		
Miscellaneous					
Name	Champ d'E/S_9	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		
Dynamizations\Tag connection					
Property name	Process value	**Tag**	DL of Rotating DRum Screen (mm)		

Cercle_1

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	303	**Y position**	151	**Width**	20
Height	20	**Radius**	10		
Miscellaneous					
Name	Cercle_1	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	washing pomps started -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_3

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	49	**Y position**	143	**Width**	14
Height	14	**Radius**	7		
Miscellaneous					
Name	Cercle_3	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	washing supply valve -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_4

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	54	**Y position**	267	**Width**	17
Height	17	**Radius**	8		
Miscellaneous					
Name	Cercle_4	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	washing filters valve -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_5

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	295	**Y position**	267	**Width**	17

Height	17	**Radius**	8		
Miscellaneous					
Name	Cercle_5	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	washing filters valve -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_6

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	562	**Y position**	267	**Width**	17
Height	17	**Radius**	8		
Miscellaneous					
Name	Cercle_6	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	washing filters valve -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_7

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	802	**Y position**	267	**Width**	17
Height	17	**Radius**	8		
Miscellaneous					
Name	Cercle_7	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	washing filters valve -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_2

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	303	**Y position**	205	**Width**	20
Height	20	**Radius**	10		
Miscellaneous					
Name	Cercle_2	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	washing pomps started -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Cercle_8

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	841	**Y position**	141	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_8	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Seawater supply valve 1 -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	No				

Bargraphe_1

Type	Bar				
General					
Maximum value	10000	**Minimum value**	0	**Process value**	0
Appearance					
Foreground color	218, 218, 218	**Background color of bar**	72, 72, 72	**Segment coloring**	Segmented

Background color	72, 72, 72	**Color of scale**	218, 218, 218	**Limit lines (layout)**	Disabled
Limit marking (layout)	Enabled				
Border type					
Bar edge style	None				
Scales					
Show scale	Enabled	**Auto-scale**	Enabled	**Divisions**	5
Large mark labeling	1	**Scale gradation**	0		
Label					
Show scale marks	Enabled	**Show "+" for positive numbers**	Disabled	**Use exponential format**	Disabled
Double-lined scale label	Disabled	**Unit**		**Integer digits**	5
Decimal places	0				
Layout					
X position	550	**Y position**	29	**Width**	93
Height	208	**Scale position**	Left/up	**Bar orientation**	Top
Text format					
Font	Tahoma, 11px, style=Bold				
Limits/Ranges					
Color range high 2	255, 109, 118	**Color range low 2**	255, 218, 44		
Miscellaneous					
Name	Bargraphe_1	**Layer**	0 - Layer_0		
Dynamizations\Tag connection					
Property name	Process value	**Tag**	Seawater Level (mm)		

Bouton_2

Type	Button				
General					
Mode	Text	**Text OFF**	start	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	757	**Y position**	134	**Width**	38
Height	24	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 11px	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_2	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_seawater pump 1 to elech-station started

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_seawater pump 1 to elech-station started

Bouton_3

Type	Button				
General					
Mode	Text	**Text OFF**	start	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	735	**Y position**	199	**Width**	38
Height	24	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 11px	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_3	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_seawater pump 2 to elech-station started
Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_seawater pump 2 to elech-station started

Cercle_9

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	841	**Y position**	174	**Width**	16
Height	16	**Radius**	8		
Miscellaneous					
Name	Cercle_9	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Seawater supply valve 2 -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Bouton_5

Type	Button				
General					
Mode	Invisible	**Text OFF**	HYPOCHLORIT SYSTEM	**Text ON**	HYPOCHLORIT SYSTEM
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	911	**Y position**	131	**Width**	113
Height	80	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_5	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Click

Function list\ActivateScreen

Screen name	HYPOCHLORIT SYSTEM	**Object number**	0

Rectangle_10

Type	Rectangle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	735	**Y position**	141	**Width**	14
Height	14	**Round corner width**	0	**Round corner height**	0
Miscellaneous					
Name	Rectangle_10	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Seawater Pump 1 To Elech-Station -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Bouton_1

Type	Button				
General					
Mode	Graphic	**Text OFF**	HOME	**Text ON**	HOME
Text list		**Graphic OFF**	Home	**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				

Layout					
X position	0	**Y position**	663	**Width**	87
Height	67	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_1	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Click

Function list\ActivateScreen

Screen name	HOME	**Object number**	0

Cercle_10

Type	Circle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	529	**Y position**	116	**Width**	18
Height	18	**Radius**	9		
Miscellaneous					
Name	Cercle_10	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	seawater level LL -	**Data type**	Range	**Range**	1..1
Foreground color	0, 0, 0	**Background color**	255, 255, 63	**Flashing**	Yes
Dynamizations\Visibility					
Tag - Cycle	seawater level LL -	**Data type**	Range	**Start range**	1
End range	1	**Visibility**	Visible		

Rectangle_3

Type	Rectangle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	373	**Y position**	571	**Width**	20
Height	22	**Round corner width**	0	**Round corner height**	0
Miscellaneous					
Name	Rectangle_3	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	BAR SCREEN Filter -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Rectangle_4

Type	Rectangle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	597	**Y position**	571	**Width**	20
Height	22	**Round corner width**	0	**Round corner height**	0
Miscellaneous					
Name	Rectangle_4	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	BAR SCREEN Filter -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Rectangle_5

Type	Rectangle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	877	**Y position**	571	**Width**	20
Height	22	**Round corner width**	0	**Round corner height**	0
Miscellaneous					
Name	Rectangle_5	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	BAR SCREEN Filter -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No

Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Bouton_6

Type	Button				
General					
Mode	Text	**Text OFF**	ALARM	**Text ON**	ALARM
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	832	**Y position**	666	**Width**	192
Height	32	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_6	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Click

Function list\ActivateScreen

Screen name	ALARM	**Object number**	0

Bouton_4

Type	Button				
General					
Mode	Text	**Text OFF**	PUMP1	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	662	**Y position**	141	**Width**	48
Height	25	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 11px	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Bouton_4	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_SELECT SEAWATER INTAKE PUMP 1

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_SELECT SEAWATER INTAKE PUMP 1

Bouton_24

Type	Button				
General					
Mode	Text	**Text OFF**	PUMP2	**Text ON**	Text
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	662	**Y position**	166	**Width**	48
Height	25	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 11px	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle

Miscellaneous					
Name	Bouton_24	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Press

Function list\SetBit

Tag	HMI_SELECT SEAWATER INTAKE PUMP 2

Dynamizations\Event	
Event name	Release

Function list\ResetBit

Tag	HMI_SELECT SEAWATER INTAKE PUMP 2

Rectangle_12

Type	Rectangle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	735	**Y position**	140	**Width**	14
Height	14	**Round corner width**	0	**Round corner height**	0
Miscellaneous					
Name	Rectangle_12	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	seawater pump 1 FAULT -	**Data type**	Range	**Range**	1..1
Foreground color	0, 0, 0	**Background color**	255, 255, 63	**Flashing**	Yes
Dynamizations\Visibility					
Tag - Cycle	seawater pump 1 FAULT -	**Data type**	Range	**Start range**	1
End range	1	**Visibility**	Visible		

Rectangle_11

Type	Rectangle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	715	**Y position**	174	**Width**	14
Height	14	**Round corner width**	0	**Round corner height**	0
Miscellaneous					
Name	Rectangle_11	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	Seawater Pump 2 To Elech-Station -	**Data type**	Range	**Range**	0..0
Foreground color	0, 0, 0	**Background color**	36, 145, 90	**Flashing**	No
Range	1..1	**Foreground color**	0, 0, 0	**Background color**	255, 0, 31
Flashing	Yes				

Rectangle_13

Type	Rectangle				
Appearance					
Background color	255, 255, 255	**Background fill pattern**	Solid	**Border width**	1
Line style	Solid	**Border color**	0, 0, 0		
Layout					
X position	716	**Y position**	173	**Width**	14
Height	14	**Round corner width**	0	**Round corner height**	0
Miscellaneous					
Name	Rectangle_13	**Layer**	0 - Layer_0		
Dynamizations\Appearance					
Tag - Cycle	seawater pump 2 FAULT -	**Data type**	Range	**Range**	1..1
Foreground color	0, 0, 0	**Background color**	255, 255, 63	**Flashing**	Yes
Dynamizations\Visibility					
Tag - Cycle	seawater pump 2 FAULT -	**Data type**	Range	**Start range**	1
End range	1	**Visibility**	Visible		

electro-chlorination station 3_V13_SP1_V18 / HMI_1 [TP1500 Basic color PN] / Screen management / Templates

Modèle_1

Hardcopy of Modèle_1

General					
Name	Modèle_1	**Background color**	109, 109, 109	**Grid color**	182, 182, 182
Tab sequence in foreground	Enabled				
Layers					
Active layer	0				

Layer_0	Enabled
Layer_1	Enabled
Layer_2	Enabled
Layer_3	Enabled
Layer_4	Enabled
Layer_5	Enabled
Layer_6	Enabled
Layer_7	Enabled
Layer_8	Enabled
Layer_9	Enabled
Layer_10	Enabled
Layer_11	Enabled
Layer_12	Enabled
Layer_13	Enabled
Layer_14	Enabled
Layer_15	Enabled
Layer_16	Enabled
Layer_17	Enabled
Layer_18	Enabled
Layer_19	Enabled
Layer_20	Enabled
Layer_21	Enabled
Layer_22	Enabled
Layer_23	Enabled
Layer_24	Enabled

Layer_25	Enabled
Layer_26	Enabled
Layer_27	Enabled
Layer_28	Enabled
Layer_29	Enabled
Layer_30	Enabled
Layer_31	Enabled

Template_Button

Type	Button				
General					
Mode	Graphic	**Text OFF**	ExitRuntime	**Text ON**	ExitRuntime
Text list		**Graphic OFF**	ExitRuntime_TP1500_Basic_color_PN	**Graphic ON**	ExitRuntime_TP1500_Basic_color_PN
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	943	**Y position**	707	**Width**	79
Height	59	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Template_Button	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Release

Function list\StopRuntime

Mode	Runtime

Template_Button_1

Type	Button				
General					
Mode	Text	**Text OFF**		**Text ON**	
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	857	**Y position**	707	**Width**	79
Height	59	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Template_Button_1	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Template_Button_2

Type	Button				
General					
Mode	Text	**Text OFF**		**Text ON**	
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	772	**Y position**	707	**Width**	79
Height	59	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Template_Button_2	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Template_Button_3

Type	Button				
General					
Mode	Text	**Text OFF**		**Text ON**	
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	686	**Y position**	707	**Width**	79
Height	59	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Template_Button_3	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Template_Button_4

Type	Button				
General					
Mode	Text	**Text OFF**		**Text ON**	
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	600	**Y position**	707	**Width**	79
Height	59	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Template_Button_4	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Template_Button_5

Type	Button				
General					
Mode	Text	**Text OFF**		**Text ON**	
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	515	**Y position**	707	**Width**	79
Height	59	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Template_Button_5	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Template_Button_6

Type	Button				
General					
Mode	Text	**Text OFF**		**Text ON**	
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	429	**Y position**	707	**Width**	79
Height	59	**Fit object to contents**	Disabled		

Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Template_Button_6	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Template_Button_7

Type	Button				
General					
Mode	Text	**Text OFF**		**Text ON**	
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	344	**Y position**	707	**Width**	79
Height	59	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Template_Button_7	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Template_Button_8

Type	Button				
General					
Mode	Text	**Text OFF**		**Text ON**	
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	258	**Y position**	707	**Width**	79
Height	59	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Template_Button_8	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Template_Button_9

Type	Button				
General					
Mode	Text	**Text OFF**		**Text ON**	
Text list		**Graphic OFF**		**Graphic ON**	
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	172	**Y position**	707	**Width**	79
Height	59	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Template_Button_9	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Template_Button_10

Type	Button				
General					
Mode	Graphic	**Text OFF**	AlarmDisplay	**Text ON**	AlarmDisplay
Text list		**Graphic OFF**	AlarmDisplay_TP1500_Basic_color_PN	**Graphic ON**	AlarmDisplay_TP1500_Basic_color_PN
Graphic list		**Process value**		**Bit number**	0

Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	87	**Y position**	707	**Width**	79
Height	59	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Template_Button_10	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Release

Function list\ShowAlarmWindow

Object name	Fenêtre des alarmes_non acquittée	**Display mode**	Toggle

Template_Button_11

Type	Button				
General					
Mode	Graphic	**Text OFF**	NavigateHome	**Text ON**	NavigateHome
Text list		**Graphic OFF**	NavigateHome_TP1500_Basic_color_PN	**Graphic ON**	NavigateHome_TP1500_Basic_color_PN
Graphic list		**Process value**		**Bit number**	0
Appearance					
Background color	72, 72, 72	**Foreground color**	255, 255, 255	**Border width**	2
Line style	3D style				
Design					
Focus color	0, 182, 219				
Layout					
X position	1	**Y position**	707	**Width**	79
Height	59	**Fit object to contents**	Disabled		
Text format					
Font	Tahoma, 15px, style=Bold	**Horizontal alignment of the text**	Centered	**Vertical alignment of the text**	Middle
Miscellaneous					
Name	Template_Button_11	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

Dynamizations\Event	
Event name	Release

Function list\ActivateScreen

Screen name	HOME	**Object number**	0

HmiScreenItemData

Type	Date/time field				
General					
Display system time	Enabled	**Process value**		**Show date**	Disabled
Show time	Enabled	**Mode**	Output		
Appearance					
Foreground color	0, 0, 0	**Background color**	218, 218, 218	**Background fill pattern**	Transparent
Border width	0	**Line style**	Solid	**Border color**	0, 0, 0
Layout					
X position	919	**Y position**	30	**Width**	105
Height	30	**Left margin**	5	**Top margin**	2
Right margin	2	**Bottom margin**	2	**Fit object to contents**	Disabled
Text format					
Font	Tahoma, 16px	**Horizontal alignment**	Right	**Vertical alignment**	Middle
Miscellaneous					
Name	HmiScreenItemData	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

HmiScreenItemData_1

Type	Date/time field				
General					
Display system time	Enabled	**Process value**		**Show date**	Enabled
Show time	Disabled	**Mode**	Output		
Appearance					
Foreground color	0, 0, 0	**Background color**	218, 218, 218	**Background fill pattern**	Transparent
Border width	0	**Line style**	Solid	**Border color**	0, 0, 0
Layout					
X position	919	**Y position**	0	**Width**	105

Height	30	**Left margin**	5	**Top margin**	2
Right margin	2	**Bottom margin**	2	**Fit object to contents**	Disabled
Text format					
Font	Tahoma, 16px	**Horizontal alignment**	Right	**Vertical alignment**	Middle
Miscellaneous					
Name	HmiScreenItemData_1	**Layer**	0 - Layer_0	**Tooltip**	
Security					
Authorization		**Allow operator control**	Enabled		

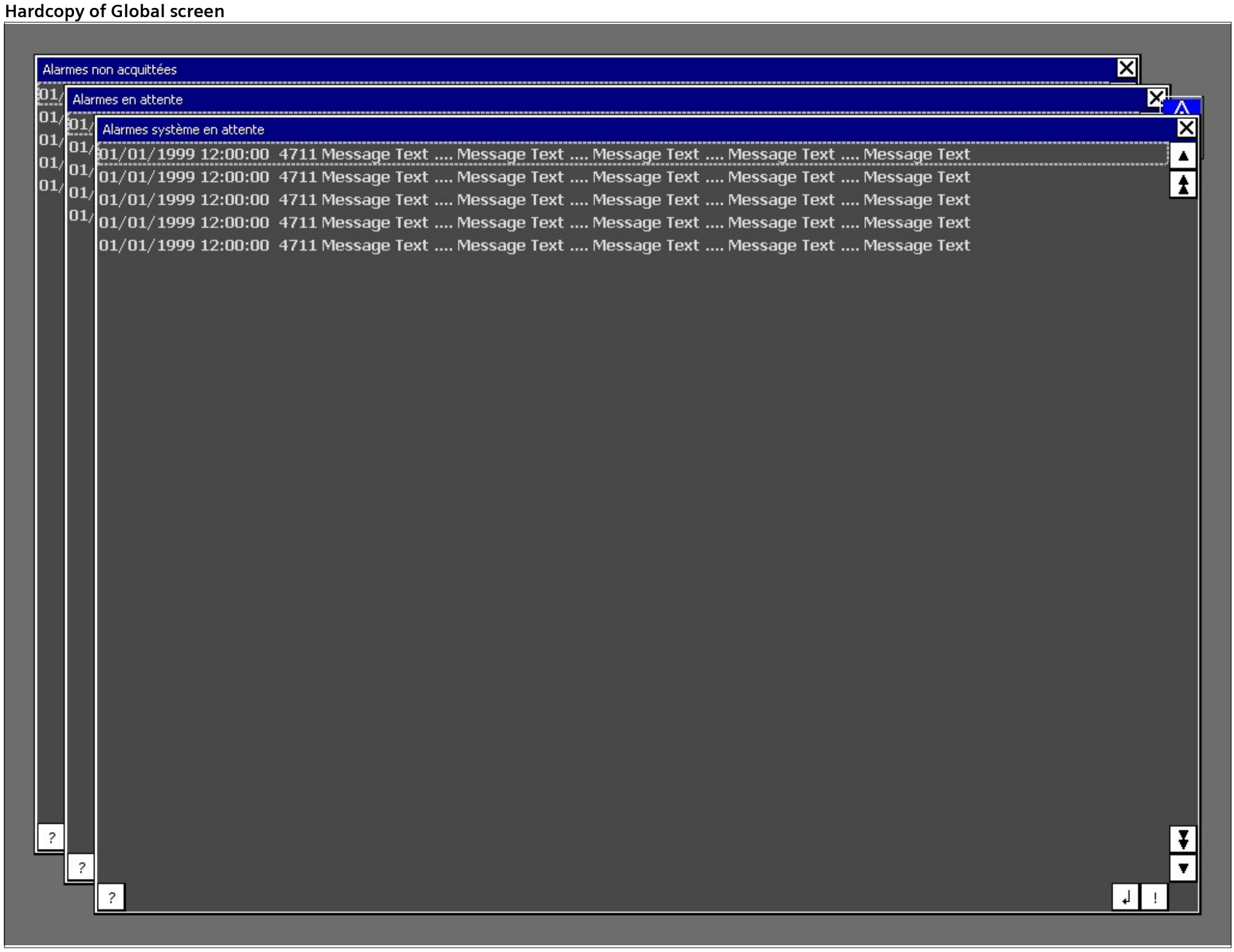
Logo

Type	Graphic view				
General					
Graphic	Logo de HMI_1				
Appearance					
Background color	182, 182, 182	**Border width**	0	**Line style**	Solid
Border color	255, 255, 255				
Layout					
X position	0	**Y position**	0	**Width**	107
Height	60	**Fit embedded graphic object to screen size**	Fit graphic to object size	**Fit object to contents**	Disabled
Miscellaneous					
Name	Logo	**Layer**	0 - Layer_0		

electro-chlorination station 3_V13_SP1_V18 / HMI_1 [TP1500 Basic color PN] / Screen management

Global screen

Hardcopy of Global screen



General					
Name	Global screen	**Background color**	109, 109, 109	**Grid color**	182, 182, 182

Fenêtre des alarmes_non acquittée

Type	Alarm window				
General					
Alarm classes	Errors	**Source of alarms**	Alarms	**Pending alarms**	Disabled
Unacknowledged alarms	Enabled				
Appearance					
Background color of table	72, 72, 72	**Foreground color of table**	218, 218, 218		
Layout					
X position	25	**Y position**	25	**Width**	924
Height	668	**Fit object to contents**	Disabled	**Lines per alarms**	1
Visible alarms	5				
Window					
Display automatically	Enabled	**Modal dialog**	Enabled	**Enabled**	Enabled
Title	Alarmes non acquittées	**Close button**	Enabled		
Display					
Vertical scroll bar	Enabled	**Vertical scrolling**	Enabled		
Text format					
Table font	Tahoma, 13px, style=Bold				
Toolbar					
"Info text" button	Enabled	**Acknowledge button**	Enabled	**Loop-In-Alarm button**	Enabled
Columns					
Columns	Alarm number, Time, Alarm text, Date, Alarm class	**Time in milliseconds**	Disabled	**Time sorting order**	Descending
Miscellaneous					
Name	Fenêtre des alarmes_non acquittée				
Security					
Allow operator control	Enabled				

Fenêtre des alarmes_en attente

Type	Alarm window				
General					
Alarm classes	Errors	**Source of alarms**	Alarms	**Pending alarms**	Enabled
Unacknowledged alarms	Disabled				
Appearance					
Background color of table	72, 72, 72	**Foreground color of table**	218, 218, 218		
Layout					
X position	50	**Y position**	50	**Width**	924
Height	668	**Fit object to contents**	Disabled	**Lines per alarms**	1
Visible alarms	5				
Window					
Display automatically	Enabled	**Modal dialog**	Enabled	**Enabled**	Enabled
Title	Alarmes en attente	**Close button**	Enabled		
Display					
Vertical scroll bar	Enabled	**Vertical scrolling**	Enabled		
Text format					
Table font	Tahoma, 13px, style=Bold				
Toolbar					
"Info text" button	Enabled	**Acknowledge button**	Enabled	**Loop-In-Alarm button**	Enabled
Columns					
Columns	Alarm number, Time, Alarm text, Date, Alarm class	**Time in milliseconds**	Disabled	**Time sorting order**	Descending
Miscellaneous					
Name	Fenêtre des alarmes_en attente				
Security					
Allow operator control	Enabled				

Indicateur d'alarme

Type	Alarm indicator		
Layout			
X position	964	**Y position**	60
General			
Display alarm classes	Acknowledged : Errors Pending : Errors		

Dynamizations\Event	
Event name	Click

Function list\ShowAlarmWindow

Object name	Fenêtre des alarmes_en attente	**Display mode**	Toggle

Dynamizations\Event	
Event name	Click when flashing

Function list\ShowAlarmWindow

Object name	Fenêtre des alarmes_en attente	**Display mode**	Toggle

Alarmes système

Type	Alarm window				
General					
Alarm classes	System	**Source of alarms**	Alarms	**Pending alarms**	Enabled
Unacknowledged alarms	Disabled				
Appearance					
Background color of table	72, 72, 72	**Foreground color of table**	218, 218, 218		
Layout					
X position	75	**Y position**	75	**Width**	924
Height	668	**Fit object to contents**	Disabled	**Lines per alarms**	1
Visible alarms	5				
Window					
Display automatically	Enabled	**Modal dialog**	Enabled	**Enabled**	Enabled
Title	Alarmes système en attente	**Close button**	Enabled		
Display					
Vertical scroll bar	Enabled	**Vertical scrolling**	Enabled		
Text format					
Table font	Tahoma, 13px, style=Bold				
Toolbar					
"Info text" button	Enabled	**Acknowledge button**	Enabled	**Loop-In-Alarm button**	Enabled
Columns					
Columns	Alarm number, Time, Alarm text, Date, Alarm class	**Time in milliseconds**	Disabled	**Time sorting order**	Descending
Miscellaneous					
Name	Alarmes système				
Security					
Allow operator control	Enabled				

electro-chlorination station 3_V13_SP1_V18 / HMI_1 [TP1500 Basic color PN] / HMI tags

Table de variables standard [104]

Seawater Level (mm)

General					
Name	Seawater Level (mm)	**Connection**	HMI_Liaison_1	**Data type**	Real
Array elements	0	**Length**	4	**Address**	%MD146
Access mode	<absolute access>	**Coding**	IEEE754	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Seawater Level (mm)

seawater supply is OK

General					
Name	seawater supply is OK	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M0.5
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

seawater supply is OK

BAR SCREEN Filter

General					
Name	BAR SCREEN Filter	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M0.1
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

BAR SCREEN Filter

DL of BAR SCREEN (mm)

General					
Name	DL of BAR SCREEN (mm)	**Connection**	HMI_Liaison_1	**Data type**	Real
Array elements	0	**Length**	4	**Address**	%MD102
Access mode	<absolute access>	**Coding**	IEEE754	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			

Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

DL of BAR SCREEN (mm)

DL of Rotating DRum Screen (mm)

General					
Name	DL of Rotating DRum Screen (mm)	**Connection**	HMI_Liaison_1	**Data type**	Real
Array elements	0	**Length**	4	**Address**	%MD108
Access mode	<absolute access>	**Coding**	IEEE754	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

DL of Rotating DRum Screen (mm)

Rotating Drum Screaan Filter

General					
Name	Rotating Drum Screaan Filter	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M0.2
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Rotating Drum Screaan Filter

washing filters valve

General					
Name	washing filters valve	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M2.5
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

washing filters valve

washing supply valve

General					
Name	washing supply valve	**Connection**	HMI_Liaison_1	**Data type**	Bool

Array elements	0	**Length**	1	**Address**	%M2.4
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

washing supply valve

Seawater Pump 1 To Elech-Station

General					
Name	Seawater Pump 1 To Elech-Station	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%Q0.0
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Seawater Pump 1 To Elech-Station

Seawater Pump 2 To Elech-Station

General					
Name	Seawater Pump 2 To Elech-Station	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%Q0.1
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Seawater Pump 2 To Elech-Station

seawater pump 1 FAULT

General					
Name	seawater pump 1 FAULT	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M20.0
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

seawater pump 1 FAULT

seawater pump 2 FAULT

General					
Name	seawater pump 2 FAULT	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M20.1
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

seawater pump 2 FAULT

HMI_seawater pump 1 to elech-station started

General					
Name	HMI_seawater pump 1 to elech-station started	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%DB5.DBX1.0
Access mode	<absolute access>	**PLC tag**	HMI."seawater pump 1 to elech-station started"	**Coding**	Binary
PLC name	PLC_1				
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

HMI_seawater pump 2 to elech-station started

General					
Name	HMI_seawater pump 2 to elech-station started	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%DB5.DBX1.1
Access mode	<absolute access>	**PLC tag**	HMI."seawater pump 2 to elech-station started"	**Coding**	Binary
PLC name	PLC_1				
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Seawater Flow (m3/h)

General					
Name	Seawater Flow (m3/h)	**Connection**	HMI_Liaison_1	**Data type**	Real
Array elements	0	**Length**	4	**Address**	%MD114
Access mode	<absolute access>	**Coding**	IEEE754	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0

HMI device value range end value	100	**HMI device value range start value**	0
Values			
ID tag		**Start value**	
Comment			
Comment			
Multiplexing			
Multiplexing	Disabled	**Index tag**	

Seawater Flow (m3/h)

seawater pump intake 1

General					
Name	seawater pump intake 1	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%Q0.3
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

seawater pump intake 1

seawater pump intake 2

General					
Name	seawater pump intake 2	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%Q0.4
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

seawater pump intake 2

seawater pump intake 1 Fault

General					
Name	seawater pump intake 1 Fault	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M20.2
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

seawater pump intake 1 Fault

seawater pump intake 2 Fault

General					
Name	seawater pump intake 2 Fault	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M20.3
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1

Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

seawater pump intake 2 Fault

HMI_seawater pump intake 1 started

General					
Name	HMI_seawater pump intake 1 started	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%DB5.DBX1.3
Access mode	<absolute access>	**PLC tag**	HMI."seawater pump intake 1 started"	**Coding**	Binary
PLC name	PLC_1				
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

HMI_seawater pump intake 2 started

General					
Name	HMI_seawater pump intake 2 started	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%DB5.DBX1.4
Access mode	<absolute access>	**PLC tag**	HMI."seawater pump intake 2 started"	**Coding**	Binary
PLC name	PLC_1				
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Current(A)

General					
Name	Current(A)	**Connection**	HMI_Liaison_1	**Data type**	Real
Array elements	0	**Length**	4	**Address**	%MD122
Access mode	<absolute access>	**Coding**	IEEE754	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Current(A)

Rectifier

General					
Name	Rectifier	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M2.1
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Rectifier

Air valve

General					
Name	Air valve	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%Q1.5
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Air valve

Air Flow (m3/h)

General					
Name	Air Flow (m3/h)	**Connection**	HMI_Liaison_1	**Data type**	Real
Array elements	0	**Length**	4	**Address**	%MD140
Access mode	<absolute access>	**Coding**	IEEE754	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Air Flow (m3/h)

H2 blower

General					
Name	H2 blower	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%Q1.4
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		

Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

H2 blower

Tank level (mm)

General					
Name	Tank level (mm)	**Connection**	HMI_Liaison_1	**Data type**	Real
Array elements	0	**Length**	4	**Address**	%MD128
Access mode	<absolute access>	**Coding**	IEEE754	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Tank level (mm)

Tank level LL

General					
Name	Tank level LL	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M21.1
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Tank level LL

Tank level HH

General					
Name	Tank level HH	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M21.2
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Tank level HH

countinous injection pump 1

General					
Name	countinous injection pump 1	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%Q1.0
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		

Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

countinous injection pump 1

countinous injection pump 2

General					
Name	countinous injection pump 2	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%Q1.1
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

countinous injection pump 2

shocked injection pump 1

General					
Name	shocked injection pump 1	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%Q1.2
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

shocked injection pump 1

shocked injection pump 2

General					
Name	shocked injection pump 2	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%Q1.3
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

shocked injection pump 2

countinous injection pump 1 Fault

General					
Name	countinous injection pump 1 Fault	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M20.5
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

countinous injection pump 1 Fault

countinous injection pump 2 Fault

General					
Name	countinous injection pump 2 Fault	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M20.6
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

countinous injection pump 2 Fault

shocked injection pump 1 Fault

General					
Name	shocked injection pump 1 Fault	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M20.7
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

shocked injection pump 1 Fault

shocked injection pump 2 Fault

General					
Name	shocked injection pump 2 Fault	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M21.0
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		

Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

shocked injection pump 2 Fault

Hypochlorite Flow (m3/h)

General					
Name	Hypochlorite Flow (m3/h)	**Connection**	HMI_Liaison_1	**Data type**	Real
Array elements	0	**Length**	4	**Address**	%MD184
Access mode	<absolute access>	**Coding**	IEEE754	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Hypochlorite Flow (m3/h)

valve to tank Fault

General					
Name	valve to tank Fault	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M20.4
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

valve to tank Fault

HMI_valve to tank opened

General					
Name	HMI_valve to tank opened	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%DB5.DBX1.6
Access mode	<absolute access>	**PLC tag**	HMI."valve to tank opened"	**Coding**	Binary
PLC name	PLC_1				
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

HMI_valve to tank opened manually

General					
Name	HMI_valve to tank opened manually	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%DB5.DBX1.7
Access mode	<absolute access>	**PLC tag**	HMI."valve to tank opened manually"	**Coding**	Binary
PLC name	PLC_1				
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		

Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

air flow is not ok

General					
Name	air flow is not ok	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M22.5
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

air flow is not ok

H2 concentration percent (%)

General					
Name	H2 concentration percent (%)	**Connection**	HMI_Liaison_1	**Data type**	Real
Array elements	0	**Length**	4	**Address**	%MD134
Access mode	<absolute access>	**Coding**	IEEE754	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

H2 concentration percent (%)

Hypochlorite Flow is not ok

General					
Name	Hypochlorite Flow is not ok	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M22.4
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Hypochlorite Flow is not ok

Hypoclorite temperature (C°)

General					
Name	Hypoclorite temperature (C°)	**Connection**	HMI_Liaison_1	**Data type**	Real

Array elements	0	**Length**	4	**Address**	%MD178
Access mode	<absolute access>	**Coding**	IEEE754	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Hypoclorite temperature (C°)

Hypoclorite temperature HH

General					
Name	Hypoclorite temperature HH	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M22.3
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Hypoclorite temperature HH

Bulk Tank Level (mm)

General					
Name	Bulk Tank Level (mm)	**Connection**	HMI_Liaison_1	**Data type**	Real
Array elements	0	**Length**	4	**Address**	%MD152
Access mode	<absolute access>	**Coding**	IEEE754	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Bulk Tank Level (mm)

Acid cleaning Tank(mm)

General					
Name	Acid cleaning Tank(mm)	**Connection**	HMI_Liaison_1	**Data type**	Real
Array elements	0	**Length**	4	**Address**	%MD158
Access mode	<absolute access>	**Coding**	IEEE754	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Acid cleaning Tank(mm)

Acid transfer Pump

General					
Name	Acid transfer Pump	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%Q1.6
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Acid transfer Pump

Acid Tank Drain valve

General					
Name	Acid Tank Drain valve	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%Q2.4
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Acid Tank Drain valve

Acid supply valve

General					
Name	Acid supply valve	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%Q2.1
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Acid supply valve

Acid return Valve

General					
Name	Acid return Valve	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%Q2.2
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		

Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Acid return Valve

Acid cleaning pump 1

General					
Name	Acid cleaning pump 1	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%Q1.7
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Acid cleaning pump 1

Acid cleaning pump 2

General					
Name	Acid cleaning pump 2	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%Q2.0
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Acid cleaning pump 2

Acid Cleaning pump 1 Fault

General					
Name	Acid Cleaning pump 1 Fault	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M22.1
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Acid Cleaning pump 1 Fault

Acid Cleaning pump 2 Fault

General					
Name	Acid Cleaning pump 2 Fault	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M22.2
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		

Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Acid Cleaning pump 2 Fault

Service water valve

General					
Name	Service water valve	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%Q2.5
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Service water valve

HMI_Acid Cleaning pump 1 started

General					
Name	HMI_Acid Cleaning pump 1 started	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%DB5.DBX2.0
Access mode	<absolute access>	**PLC tag**	HMI."Acid Cleaning pump 1 started"	**Coding**	Binary
PLC name	PLC_1				
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

HMI_Acid Cleaning pump 2 started

General					
Name	HMI_Acid Cleaning pump 2 started	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%DB5.DBX2.1
Access mode	<absolute access>	**PLC tag**	HMI."Acid Cleaning pump 2 started"	**Coding**	Binary
PLC name	PLC_1				
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Acid Concentration (%)

General					
Name	Acid Concentration (%)	**Connection**	HMI_Liaison_1	**Data type**	Real

Array elements	0	**Length**	4	**Address**	%MD170
Access mode	<absolute access>	**Coding**	IEEE754	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Acid Concentration (%)

PH analyser (ph)

General					
Name	PH analyser (ph)	**Connection**	HMI_Liaison_1	**Data type**	Real
Array elements	0	**Length**	4	**Address**	%MD164
Access mode	<absolute access>	**Coding**	IEEE754	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

PH analyser (ph)

filter 2

General					
Name	filter 2	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M2.7
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

filter 2

filter 1

General					
Name	filter 1	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M0.6
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

filter 1

electrolizer 1

General					
Name	electrolizer 1	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M0.7
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

electrolizer 1

electrolizer 2

General					
Name	electrolizer 2	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M3.0
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

electrolizer 2

HMI_ELECROLYZER 1 STARTED

General					
Name	HMI_ELECROLYZER 1 STARTED	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%DB5.DBX3.3
Access mode	<absolute access>	**PLC tag**	HMI."ELECROLYZER 1 STARTED"	**Coding**	Binary
PLC name	PLC_1				
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

HMI_ELECROLYZER 2 STARTED

General					
Name	HMI_ELECROLYZER 2 STARTED	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%DB5.DBX3.4
Access mode	<absolute access>	**PLC tag**	HMI."ELECROLYZER 2 STARTED"	**Coding**	Binary
PLC name	PLC_1				
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		

Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Electrolizer 1 Drain valve

General					
Name	Electrolizer 1 Drain valve	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%Q2.3
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Electrolizer 1 Drain valve

Electrolizer 2 Drain valve

General					
Name	Electrolizer 2 Drain valve	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%Q2.7
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Electrolizer 2 Drain valve

FILTER 1 VALVE

General					
Name	FILTER 1 VALVE	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%Q3.0
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

FILTER 1 VALVE

FILTER 2 VALVE

General					
Name	FILTER 2 VALVE	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%Q3.1
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			

Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

FILTER 2 VALVE

Bulk Tank Level LL

General					
Name	Bulk Tank Level LL	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M22.0
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Bulk Tank Level LL

Bulk Acid tank level HH

General					
Name	Bulk Acid tank level HH	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M21.7
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Bulk Acid tank level HH

Acid tank level LL

General					
Name	Acid tank level LL	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M22.7
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Acid tank level LL

Acid tank level HH

General					
Name	Acid tank level HH	**Connection**	HMI_Liaison_1	**Data type**	Bool

Array elements	0	**Length**	1	**Address**	%M21.6
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Acid tank level HH

Seawater supply valve 2

General					
Name	Seawater supply valve 2	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%Q2.6
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Seawater supply valve 2

Seawater supply valve 1

General					
Name	Seawater supply valve 1	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%Q0.2
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

Seawater supply valve 1

valve to Tank

General					
Name	valve to Tank	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%Q0.7
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

valve to Tank

seawater level LL

General					
Name	seawater level LL	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M21.5
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

seawater level LL

seawater level HH

General					
Name	seawater level HH	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M23.0
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

seawater flow is not ok

washing pomps started

General					
Name	washing pomps started	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M2.3
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

washing pomps started

HMI_START

General					
Name	HMI_START	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%DB5.DBX0.0
Access mode	<absolute access>	**PLC tag**	HMI.START	**Coding**	Binary
PLC name	PLC_1				
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0

HMI device value range end value	100	**HMI device value range start value**	0
Values			
ID tag		**Start value**	
Comment			
Comment			
Multiplexing			
Multiplexing	Disabled	**Index tag**	

HMI_STOP

General					
Name	HMI_STOP	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%DB5.DBX0.6
Access mode	<absolute access>	**PLC tag**	HMI.STOP	**Coding**	Binary
PLC name	PLC_1				
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

HMI_AUTO

General					
Name	HMI_AUTO	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%DB5.DBX0.7
Access mode	<absolute access>	**PLC tag**	HMI.AUTO	**Coding**	Binary
PLC name	PLC_1				
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

HMI_valve before filter 1 opened

General					
Name	HMI_valve before filter 1 opened	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%DB5.DBX3.1
Access mode	<absolute access>	**PLC tag**	HMI."valve before filter 1 opened"	**Coding**	Binary
PLC name	PLC_1				
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

HMI_valve before filter 2 opened

General					
Name	HMI_valve before filter 2 opened	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%DB5.DBX3.2
Access mode	<absolute access>	**PLC tag**	HMI."valve before filter 2 opened"	**Coding**	Binary
PLC name	PLC_1				
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			

Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

HMI_countinous injection pump 1 started

General					
Name	HMI_countinous injection pump 1 started	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%DB5.DBX2.2
Access mode	<absolute access>	**PLC tag**	HMI."countinous injection pump 1 started"	**Coding**	Binary
PLC name	PLC_1				
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

HMI_countinous injection pump 2 started

General					
Name	HMI_countinous injection pump 2 started	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%DB5.DBX2.3
Access mode	<absolute access>	**PLC tag**	HMI."countinous injection pump 2 started"	**Coding**	Binary
PLC name	PLC_1				
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

HMI_shocked injection pump 1 started

General					
Name	HMI_shocked injection pump 1 started	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%DB5.DBX2.6
Access mode	<absolute access>	**PLC tag**	HMI."shocked injection pump 1 started"	**Coding**	Binary
PLC name	PLC_1				
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

HMI_shocked injection pump 2 started

General					
Name	HMI_shocked injection pump 2 started	**Connection**	HMI_Liaison_1	**Data type**	Bool

Array elements	0	**Length**	1	**Address**	%DB5.DBX2.7
Access mode	<absolute access>	**PLC tag**	HMI."shocked injection pump 2 started"	**Coding**	Binary
PLC name	PLC_1				
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

HMI_Emergency Stop

General					
Name	HMI_Emergency Stop	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%DB5.DBX3.5
Access mode	<absolute access>	**PLC tag**	HMI."Emergency Stop"	**Coding**	Binary
PLC name	PLC_1				
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

HMI_acid cleaning start

General					
Name	HMI_acid cleaning start	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%DB5.DBX3.6
Access mode	<absolute access>	**PLC tag**	HMI."acid cleaning start"	**Coding**	Binary
PLC name	PLC_1				
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

HMI_HYPOCHLORITE SYSTEM START

General					
Name	HMI_HYPOCHLORITE SYSTEM START	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%DB5.DBX0.1
Access mode	<absolute access>	**PLC tag**	HMI."HYPOCHLORITE SYSTEM START"	**Coding**	Binary
PLC name	PLC_1				
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

HMI_SELECT COUNTINOUS PUMP 1

General					
Name	HMI_SELECT COUNTINOUS PUMP 1	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%DB5.DBX0.2
Access mode	<absolute access>	**PLC tag**	HMI."SELECT COUNTINOUS PUMP 1"	**Coding**	Binary
PLC name	PLC_1				
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

HMI_SELECT COUNTINOUS PUMP2

General					
Name	HMI_SELECT COUNTINOUS PUMP2	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%DB5.DBX0.3
Access mode	<absolute access>	**PLC tag**	HMI."SELECT COUNTINOUS PUMP2"	**Coding**	Binary
PLC name	PLC_1				
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

HMI_SELECT SHOCK PUMP 1

General					
Name	HMI_SELECT SHOCK PUMP 1	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%DB5.DBX0.4
Access mode	<absolute access>	**PLC tag**	HMI."SELECT SHOCK PUMP 1"	**Coding**	Binary
PLC name	PLC_1				
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

HMI_SELECT SHOCK PUMP 2

General					
Name	HMI_SELECT SHOCK PUMP 2	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%DB5.DBX0.5
Access mode	<absolute access>	**PLC tag**	HMI."SELECT SHOCK PUMP 2"	**Coding**	Binary
PLC name	PLC_1				
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					

Multiplexing					
Multiplexing	Disabled	**Index tag**			

ALARM OF SYSTEM 1

General					
Name	ALARM OF SYSTEM 1	**Connection**	HMI_Liaison_1	**Data type**	Int
Array elements	0	**Length**	2	**Address**	%MW20
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic continuous		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

ALARM OF SYSTEM 1

ALARM OF SYSTEM 2

General					
Name	ALARM OF SYSTEM 2	**Connection**	HMI_Liaison_1	**Data type**	Int
Array elements	0	**Length**	2	**Address**	%MW22
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic continuous		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

ALARM OF SYSTEM 2

H2 concentration H

General					
Name	H2 concentration H	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%M21.3
Access mode	<absolute access>	**Coding**	Binary	**PLC name**	PLC_1
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

H2 concentration H

HMI_select booster pump 1

General					
Name	HMI_select booster pump 1	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%DB5.DBX2.5
Access mode	<absolute access>	**PLC tag**	HMI."select booster pump 1"	**Coding**	Binary
PLC name	PLC_1				
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0

HMI device value range end value	100	**HMI device value range start value**	0
Values			
ID tag		**Start value**	
Comment			
Comment			
Multiplexing			
Multiplexing	Disabled	**Index tag**	

HMI_select booster pump 2

General					
Name	HMI_select booster pump 2	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%DB5.DBX2.4
Access mode	<absolute access>	**PLC tag**	HMI."select booster pump 2"	**Coding**	Binary
PLC name	PLC_1				
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

HMI_SELECT SEAWATER INTAKE PUMP 1

General					
Name	HMI_SELECT SEAWATER INTAKE PUMP 1	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%DB5.DBX3.7
Access mode	<absolute access>	**PLC tag**	HMI."SELECT SEAWATER INTAKE PUMP 1"	**Coding**	Binary
PLC name	PLC_1				
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

HMI_SELECT SEAWATER INTAKE PUMP 2

General					
Name	HMI_SELECT SEAWATER INTAKE PUMP 2	**Connection**	HMI_Liaison_1	**Data type**	Bool
Array elements	0	**Length**	1	**Address**	%DB5.DBX4.0
Access mode	<absolute access>	**PLC tag**	HMI."SELECT SEAWATER INTAKE PUMP 2"	**Coding**	Binary
PLC name	PLC_1				
Settings					
Acquisition cycle	1 s	**Acquisition mode**	Cyclic in operation		
Limits					
Upper 2		**Lower 2**			
Linear scaling					
Linear scaling	Disabled	**PLC value range end value**	10	**PLC value range start value**	0
HMI device value range end value	100	**HMI device value range start value**	0		
Values					
ID tag		**Start value**			
Comment					
Comment					
Multiplexing					
Multiplexing	Disabled	**Index tag**			

electro-chlorination station 3_V13_SP1_V18 / HMI_1 [TP1500 Basic color PN]

Connections

HMI_Liaison_1

Name	HMI_Liaison_1	**Communication driver**	SIMATIC S7 300/400	**Comment**	
Online	Enabled	**Station**	Station S7300/ET200M_1	**Partner**	PLC_1
Node	CPU 317-2 PN/DP, PROFINET interface (R0/S2)				

Parameter

HMI device					
Interface	PROFINET (X1)	**Address**	192.168.0.2	**Access point**	S7ONLINE
PLC					
Address	192.168.0.1	**Expansion slot**	2	**Rack**	0
Cyclic operation	On				

electro-chlorination station 3_V13_SP1_V18 / HMI_1 [TP1500 Basic color PN] / HMI alarms

Discrete alarms

Alarme de bit_1

General					
Name	Alarme de bit_1	**ID**	1	**Alarm text**	seawater pump 1 FAULT
Alarm class	Errors	**Alarm group**	<No alarm group>		
Trigger					
Trigger tag	ALARM OF SYSTEM 1	**Address trigger bit**	%M20.0	**Trigger bit number**	8
Info Text					
Info Text					
Acknowledgment					
Acknowledgment tag	<No tag>	**Address of HMI acknowledgment bit**		**Acknowledgment bit number**	0
Tag for acknowledgment by the PLC	<No tag>	**PLC acknowledgment bit address**		**Acknowledgment bit number in the PLC**	0

ALARM OF SYSTEM 1

Alarme de bit_10

General					
Name	Alarme de bit_10	**ID**	10	**Alarm text**	Tank level LL
Alarm class	Errors	**Alarm group**	<No alarm group>		
Trigger					
Trigger tag	ALARM OF SYSTEM 1	**Address trigger bit**	%M21.1	**Trigger bit number**	1
Info Text					
Info Text					
Acknowledgment					
Acknowledgment tag	<No tag>	**Address of HMI acknowledgment bit**		**Acknowledgment bit number**	0
Tag for acknowledgment by the PLC	<No tag>	**PLC acknowledgment bit address**		**Acknowledgment bit number in the PLC**	0

ALARM OF SYSTEM 1

Alarme de bit_11

General					
Name	Alarme de bit_11	**ID**	11	**Alarm text**	Tank level HH
Alarm class	Errors	**Alarm group**	<No alarm group>		
Trigger					
Trigger tag	ALARM OF SYSTEM 1	**Address trigger bit**	%M21.2	**Trigger bit number**	2
Info Text					
Info Text					
Acknowledgment					
Acknowledgment tag	<No tag>	**Address of HMI acknowledgment bit**		**Acknowledgment bit number**	0
Tag for acknowledgment by the PLC	<No tag>	**PLC acknowledgment bit address**		**Acknowledgment bit number in the PLC**	0

ALARM OF SYSTEM 1

Alarme de bit_12

General					
Name	Alarme de bit_12	**ID**	12	**Alarm text**	H2 concentration H
Alarm class	Errors	**Alarm group**	<No alarm group>		
Trigger					
Trigger tag	ALARM OF SYSTEM 1	**Address trigger bit**	%M21.3	**Trigger bit number**	3
Info Text					
Info Text					
Acknowledgment					
Acknowledgment tag	<No tag>	**Address of HMI acknowledgment bit**		**Acknowledgment bit number**	0
Tag for acknowledgment by the PLC	<No tag>	**PLC acknowledgment bit address**		**Acknowledgment bit number in the PLC**	0

ALARM OF SYSTEM 1

Alarme de bit_13

General					
Name	Alarme de bit_13	**ID**	13	**Alarm text**	H2 concentration HH
Alarm class	Errors	**Alarm group**	<No alarm group>		
Trigger					
Trigger tag	ALARM OF SYSTEM 1	**Address trigger bit**	%M21.4	**Trigger bit number**	4
Info Text					
Info Text					
Acknowledgment					
Acknowledgment tag	<No tag>	**Address of HMI acknowledgment bit**		**Acknowledgment bit number**	0
Tag for acknowledgment by the PLC	<No tag>	**PLC acknowledgment bit address**		**Acknowledgment bit number in the PLC**	0

ALARM OF SYSTEM 1

Alarme de bit_14

General					
Name	Alarme de bit_14	**ID**	14	**Alarm text**	seawater level LL
Alarm class	Errors	**Alarm group**	<No alarm group>		
Trigger					
Trigger tag	ALARM OF SYSTEM 1	**Address trigger bit**	%M21.5	**Trigger bit number**	5
Info Text					
Info Text					
Acknowledgment					
Acknowledgment tag	<No tag>	**Address of HMI acknowledgment bit**		**Acknowledgment bit number**	0
Tag for acknowledgment by the PLC	<No tag>	**PLC acknowledgment bit address**		**Acknowledgment bit number in the PLC**	0

ALARM OF SYSTEM 1

Alarme de bit_15

General					
Name	Alarme de bit_15	**ID**	15	**Alarm text**	Acid tank level HH
Alarm class	Errors	**Alarm group**	<No alarm group>		
Trigger					
Trigger tag	ALARM OF SYSTEM 1	**Address trigger bit**	%M21.6	**Trigger bit number**	6
Info Text					
Info Text					
Acknowledgment					
Acknowledgment tag	<No tag>	**Address of HMI acknowledgment bit**		**Acknowledgment bit number**	0
Tag for acknowledgment by the PLC	<No tag>	**PLC acknowledgment bit address**		**Acknowledgment bit number in the PLC**	0

ALARM OF SYSTEM 1

Alarme de bit_16

General					
Name	Alarme de bit_16	**ID**	16	**Alarm text**	Bulk Acid tank level HH
Alarm class	Errors	**Alarm group**	<No alarm group>		
Trigger					
Trigger tag	ALARM OF SYSTEM 1	**Address trigger bit**	%M21.7	**Trigger bit number**	7
Info Text					
Info Text					
Acknowledgment					
Acknowledgment tag	<No tag>	**Address of HMI acknowledgment bit**		**Acknowledgment bit number**	0
Tag for acknowledgment by the PLC	<No tag>	**PLC acknowledgment bit address**		**Acknowledgment bit number in the PLC**	0

ALARM OF SYSTEM 1

Alarme de bit_17

General					
Name	Alarme de bit_17	**ID**	17	**Alarm text**	Bulk Tank Level LL
Alarm class	Errors	**Alarm group**	<No alarm group>		
Trigger					
Trigger tag	ALARM OF SYSTEM 2	**Address trigger bit**	%M22.0	**Trigger bit number**	8
Info Text					
Info Text					
Acknowledgment					
Acknowledgment tag	<No tag>	**Address of HMI acknowledgment bit**		**Acknowledgment bit number**	0
Tag for acknowledgment by the PLC	<No tag>	**PLC acknowledgment bit address**		**Acknowledgment bit number in the PLC**	0

ALARM OF SYSTEM 2

Alarme de bit_18

General					
Name	Alarme de bit_18	**ID**	18	**Alarm text**	Acid Cleaning pump 1 Fault
Alarm class	Errors	**Alarm group**	<No alarm group>		
Trigger					
Trigger tag	ALARM OF SYSTEM 2	**Address trigger bit**	%M22.1	**Trigger bit number**	9
Info Text					
Info Text					
Acknowledgment					
Acknowledgment tag	<No tag>	**Address of HMI acknowledgment bit**		**Acknowledgment bit number**	0
Tag for acknowledgment by the PLC	<No tag>	**PLC acknowledgment bit address**		**Acknowledgment bit number in the PLC**	0

ALARM OF SYSTEM 2

Alarme de bit_19

General					
Name	Alarme de bit_19	**ID**	19	**Alarm text**	Acid Cleaning pump 2 Fault

Alarm class	Errors	**Alarm group**	<No alarm group>		
Trigger					
Trigger tag	ALARM OF SYSTEM 2	**Address trigger bit**	%M22.2	**Trigger bit number**	10
Info Text					
Info Text					
Acknowledgment					
Acknowledgment tag	<No tag>	**Address of HMI acknowledgment bit**		**Acknowledgment bit number**	0
Tag for acknowledgment by the PLC	<No tag>	**PLC acknowledgment bit address**		**Acknowledgment bit number in the PLC**	0

ALARM OF SYSTEM 2

Alarme de bit_2

General					
Name	Alarme de bit_2	**ID**	2	**Alarm text**	seawater pump 2 FAULT
Alarm class	Errors	**Alarm group**	<No alarm group>		
Trigger					
Trigger tag	ALARM OF SYSTEM 1	**Address trigger bit**	%M20.1	**Trigger bit number**	9
Info Text					
Info Text					
Acknowledgment					
Acknowledgment tag	<No tag>	**Address of HMI acknowledgment bit**		**Acknowledgment bit number**	0
Tag for acknowledgment by the PLC	<No tag>	**PLC acknowledgment bit address**		**Acknowledgment bit number in the PLC**	0

ALARM OF SYSTEM 1

Alarme de bit_20

General					
Name	Alarme de bit_20	**ID**	20	**Alarm text**	Hypoclorite temperature HH
Alarm class	Errors	**Alarm group**	<No alarm group>		
Trigger					
Trigger tag	ALARM OF SYSTEM 2	**Address trigger bit**	%M22.3	**Trigger bit number**	11
Info Text					
Info Text					
Acknowledgment					
Acknowledgment tag	<No tag>	**Address of HMI acknowledgment bit**		**Acknowledgment bit number**	0
Tag for acknowledgment by the PLC	<No tag>	**PLC acknowledgment bit address**		**Acknowledgment bit number in the PLC**	0

ALARM OF SYSTEM 2

Alarme de bit_21

General					
Name	Alarme de bit_21	**ID**	21	**Alarm text**	Hypochlorite Flow is not ok
Alarm class	Errors	**Alarm group**	<No alarm group>		
Trigger					
Trigger tag	ALARM OF SYSTEM 2	**Address trigger bit**	%M22.4	**Trigger bit number**	12
Info Text					
Info Text					
Acknowledgment					
Acknowledgment tag	<No tag>	**Address of HMI acknowledgment bit**		**Acknowledgment bit number**	0
Tag for acknowledgment by the PLC	<No tag>	**PLC acknowledgment bit address**		**Acknowledgment bit number in the PLC**	0

ALARM OF SYSTEM 2

Alarme de bit_22

General					
Name	Alarme de bit_22	**ID**	22	**Alarm text**	air flow is not ok
Alarm class	Errors	**Alarm group**	<No alarm group>		
Trigger					
Trigger tag	ALARM OF SYSTEM 2	**Address trigger bit**	%M22.5	**Trigger bit number**	13
Info Text					
Info Text					
Acknowledgment					
Acknowledgment tag	<No tag>	**Address of HMI acknowledgment bit**		**Acknowledgment bit number**	0
Tag for acknowledgment by the PLC	<No tag>	**PLC acknowledgment bit address**		**Acknowledgment bit number in the PLC**	0

ALARM OF SYSTEM 2

Alarme de bit_23

General					
Name	Alarme de bit_23	**ID**	23	**Alarm text**	Bulk Tank Level H
Alarm class	Errors	**Alarm group**	<No alarm group>		
Trigger					
Trigger tag	ALARM OF SYSTEM 2	**Address trigger bit**	%M22.6	**Trigger bit number**	14
Info Text					
Info Text					

Acknowledgment					
Acknowledgment tag	<No tag>	**Address of HMI acknowledgment bit**		**Acknowledgment bit number**	0
Tag for acknowledgment by the PLC	<No tag>	**PLC acknowledgment bit address**		**Acknowledgment bit number in the PLC**	0

ALARM OF SYSTEM 2

Alarme de bit_24

General					
Name	Alarme de bit_24	**ID**	24	**Alarm text**	Acid tank level LL
Alarm class	Errors	**Alarm group**	<No alarm group>		
Trigger					
Trigger tag	ALARM OF SYSTEM 2	**Address trigger bit**	%M22.7	**Trigger bit number**	15
Info Text					
Info Text					
Acknowledgment					
Acknowledgment tag	<No tag>	**Address of HMI acknowledgment bit**		**Acknowledgment bit number**	0
Tag for acknowledgment by the PLC	<No tag>	**PLC acknowledgment bit address**		**Acknowledgment bit number in the PLC**	0

ALARM OF SYSTEM 2

Alarme de bit_25

General					
Name	Alarme de bit_25	**ID**	25	**Alarm text**	seawater level HH
Alarm class	Errors	**Alarm group**	<No alarm group>		
Trigger					
Trigger tag	ALARM OF SYSTEM 2	**Address trigger bit**	%M23.0	**Trigger bit number**	0
Info Text					
Info Text					
Acknowledgment					
Acknowledgment tag	<No tag>	**Address of HMI acknowledgment bit**		**Acknowledgment bit number**	0
Tag for acknowledgment by the PLC	<No tag>	**PLC acknowledgment bit address**		**Acknowledgment bit number in the PLC**	0

ALARM OF SYSTEM 2

Alarme de bit_3

General					
Name	Alarme de bit_3	**ID**	3	**Alarm text**	seawater pump intake 1 Fault
Alarm class	Errors	**Alarm group**	<No alarm group>		
Trigger					
Trigger tag	ALARM OF SYSTEM 1	**Address trigger bit**	%M20.2	**Trigger bit number**	10
Info Text					
Info Text					
Acknowledgment					
Acknowledgment tag	<No tag>	**Address of HMI acknowledgment bit**		**Acknowledgment bit number**	0
Tag for acknowledgment by the PLC	<No tag>	**PLC acknowledgment bit address**		**Acknowledgment bit number in the PLC**	0

ALARM OF SYSTEM 1

Alarme de bit_4

General					
Name	Alarme de bit_4	**ID**	4	**Alarm text**	seawater pump intake 2 Fault
Alarm class	Errors	**Alarm group**	<No alarm group>		
Trigger					
Trigger tag	ALARM OF SYSTEM 1	**Address trigger bit**	%M20.3	**Trigger bit number**	11
Info Text					
Info Text					
Acknowledgment					
Acknowledgment tag	<No tag>	**Address of HMI acknowledgment bit**		**Acknowledgment bit number**	0
Tag for acknowledgment by the PLC	<No tag>	**PLC acknowledgment bit address**		**Acknowledgment bit number in the PLC**	0

ALARM OF SYSTEM 1

Alarme de bit_5

General					
Name	Alarme de bit_5	**ID**	5	**Alarm text**	valve to tank Fault
Alarm class	Errors	**Alarm group**	<No alarm group>		
Trigger					
Trigger tag	ALARM OF SYSTEM 1	**Address trigger bit**	%M20.4	**Trigger bit number**	12
Info Text					
Info Text					
Acknowledgment					
Acknowledgment tag	<No tag>	**Address of HMI acknowledgment bit**		**Acknowledgment bit number**	0
Tag for acknowledgment by the PLC	<No tag>	**PLC acknowledgment bit address**		**Acknowledgment bit number in the PLC**	0

ALARM OF SYSTEM 1

Alarme de bit_6

General					
Name	Alarme de bit_6	**ID**	6	**Alarm text**	countinous injection pump 1 Fault
Alarm class	Errors	**Alarm group**	<No alarm group>		
Trigger					
Trigger tag	ALARM OF SYSTEM 1	**Address trigger bit**	%M20.5	**Trigger bit number**	13
Info Text					
Info Text					
Acknowledgment					
Acknowledgment tag	<No tag>	**Address of HMI acknowledgment bit**		**Acknowledgment bit number**	0
Tag for acknowledgment by the PLC	<No tag>	**PLC acknowledgment bit address**		**Acknowledgment bit number in the PLC**	0

ALARM OF SYSTEM 1

Alarme de bit_7

General					
Name	Alarme de bit_7	**ID**	7	**Alarm text**	countinous injection pump 2 Fault
Alarm class	Errors	**Alarm group**	<No alarm group>		
Trigger					
Trigger tag	ALARM OF SYSTEM 1	**Address trigger bit**	%M20.6	**Trigger bit number**	14
Info Text					
Info Text					
Acknowledgment					
Acknowledgment tag	<No tag>	**Address of HMI acknowledgment bit**		**Acknowledgment bit number**	0
Tag for acknowledgment by the PLC	<No tag>	**PLC acknowledgment bit address**		**Acknowledgment bit number in the PLC**	0

ALARM OF SYSTEM 1

Alarme de bit_8

General					
Name	Alarme de bit_8	**ID**	8	**Alarm text**	shocked injection pump 1 Fault
Alarm class	Errors	**Alarm group**	<No alarm group>		
Trigger					
Trigger tag	ALARM OF SYSTEM 1	**Address trigger bit**	%M20.7	**Trigger bit number**	15
Info Text					
Info Text					
Acknowledgment					
Acknowledgment tag	<No tag>	**Address of HMI acknowledgment bit**		**Acknowledgment bit number**	0
Tag for acknowledgment by the PLC	<No tag>	**PLC acknowledgment bit address**		**Acknowledgment bit number in the PLC**	0

ALARM OF SYSTEM 1

Alarme de bit_9

General					
Name	Alarme de bit_9	**ID**	9	**Alarm text**	shocked injection pump 2 Fault
Alarm class	Errors	**Alarm group**	<No alarm group>		
Trigger					
Trigger tag	ALARM OF SYSTEM 1	**Address trigger bit**	%M21.0	**Trigger bit number**	0
Info Text					
Info Text					
Acknowledgment					
Acknowledgment tag	<No tag>	**Address of HMI acknowledgment bit**		**Acknowledgment bit number**	0
Tag for acknowledgment by the PLC	<No tag>	**PLC acknowledgment bit address**		**Acknowledgment bit number in the PLC**	0

ALARM OF SYSTEM 1

electro-chlorination station 3_V13_SP1_V18 / HMI_1 [TP1500 Basic color PN] / HMI alarms

Analog alarms

This folder is empty.

electro-chlorination station 3_V13_SP1_V18 / HMI_1 [TP1500 Basic color PN] / HMI alarms

Alarm groups

Groupe alarmes_1

Subtype	Alarm group		
General			
Name	Groupe alarmes_1	**ID**	1

Groupe alarmes_10

Subtype	Alarm group		
General			
Name	Groupe alarmes_10	**ID**	10

Groupe alarmes_11

Subtype	Alarm group		
General			
Name	Groupe alarmes_11	**ID**	11

Groupe alarmes_12

Subtype	Alarm group		
General			
Name	Groupe alarmes_12	**ID**	12

Groupe alarmes_13

Subtype	Alarm group		
General			
Name	Groupe alarmes_13	**ID**	13

Groupe alarmes_14

Subtype	Alarm group		
General			
Name	Groupe alarmes_14	**ID**	14

Groupe alarmes_15

Subtype	Alarm group		
General			
Name	Groupe alarmes_15	**ID**	15

Groupe alarmes_16

Subtype	Alarm group		
General			
Name	Groupe alarmes_16	**ID**	16

Groupe alarmes_2

Subtype	Alarm group		
General			
Name	Groupe alarmes_2	**ID**	2

Groupe alarmes_3

Subtype	Alarm group		
General			
Name	Groupe alarmes_3	**ID**	3

Groupe alarmes_4

Subtype	Alarm group		
General			
Name	Groupe alarmes_4	**ID**	4

Groupe alarmes_5

Subtype	Alarm group		
General			
Name	Groupe alarmes_5	**ID**	5

Groupe alarmes_6

Subtype	Alarm group		
General			
Name	Groupe alarmes_6	**ID**	6

Groupe alarmes_7

Subtype	Alarm group		
General			
Name	Groupe alarmes_7	**ID**	7

Groupe alarmes_8

Subtype	Alarm group		
General			
Name	Groupe alarmes_8	**ID**	8

Groupe alarmes_9

Subtype	Alarm group		
General			
Name	Groupe alarmes_9	**ID**	9

electro-chlorination station 3_V13_SP1_V18 / HMI_1 [TP1500 Basic color PN] / HMI alarms

Alarm classes

Acknowledgement

General					
Name	Acknowledgement	**Display name**	A	**ID**	33
Common alarm class	Acknowledgement				
Acknowledgment					
State machine	Alarm with single-mode acknowledgment				
State texts					
Text for "Incoming"	A	**Text for "Outgoing"**	D	**Text for "Acknowledged"**	Q
Colors					
Background "Incoming/Acknowledged"	255, 255, 255	**Background "Incoming"**	255, 0, 31	**Background "Incoming/Outgoing/Acknowledged"**	255, 255, 255
Background "Incoming/Outgoing"	255, 0, 31				

Errors

General					
Name	Errors	**Display name**	!	**ID**	1
Common alarm class	<No alarm class>				
Acknowledgment					
State machine	Alarm with single-mode acknowledgment				
State texts					
Text for "Incoming"	A	**Text for "Outgoing"**	D	**Text for "Acknowledged"**	Q
Colors					
Background "Incoming/Acknowledged"	255, 255, 255	**Background "Incoming"**	255, 0, 31	**Background "Incoming/Outgoing/Acknowledged"**	255, 255, 255
Background "Incoming/Outgoing"	255, 0, 31				

No Acknowledgement

General					
Name	No Acknowledgement	**Display name**	NA	**ID**	34
Common alarm class	No Acknowledgement				
Acknowledgment					
State machine	Alarm without acknowledgment				
State texts					
Text for "Incoming"	A	**Text for "Outgoing"**	D	**Text for "Acknowledged"**	Q
Colors					
Background "Incoming/Acknowledged"	255, 255, 255	**Background "Incoming"**	255, 0, 31	**Background "Incoming/Outgoing/Acknowledged"**	255, 255, 255
Background "Incoming/Outgoing"	255, 0, 31				

System

General					
Name	System	**Display name**	$	**ID**	3
Common alarm class	<No alarm class>				
Acknowledgment					
State machine	Alarm without acknowledgment				
State texts					
Text for "Incoming"	A	**Text for "Outgoing"**	D	**Text for "Acknowledged"**	Q
Colors					
Background "Incoming/Acknowledged"	255, 255, 255	**Background "Incoming"**	255, 255, 255	**Background "Incoming/Outgoing/Acknowledged"**	255, 255, 255
Background "Incoming/Outgoing"	255, 255, 255				

Warnings

General					
Name	Warnings	**Display name**		**ID**	2
Common alarm class	<No alarm class>				
Acknowledgment					
State machine	Alarm without acknowledgment				
State texts					
Text for "Incoming"	A	**Text for "Outgoing"**	D	**Text for "Acknowledged"**	Q
Colors					
Background "Incoming/Acknowledged"	255, 255, 255	**Background "Incoming"**	255, 255, 255	**Background "Incoming/Outgoing/Acknowledged"**	255, 255, 255

Background "Incoming/Outgoing"	255, 255, 255

electro-chlorination station 3_V13_SP1_V18 / HMI_1 [TP1500 Basic color PN] / HMI alarms

System events

This folder is empty.

electro-chlorination station 3_V13_SP1_V18 / HMI_1 [TP1500 Basic color PN]

Recipes

This folder is empty.

electro-chlorination station 3_V13_SP1_V18 / HMI_1 [TP1500 Basic color PN]

Scheduled tasks

This folder is empty.

electro-chlorination station 3_V13_SP1_V18 / HMI_1 [TP1500 Basic color PN] / Text and graphic lists

Text lists

Nom_vue_liste_textes

Name	Nom_vue_liste_textes	**List range**	Value/Range	**Comment**	

Value: 1

Entry type	Single value	**Text**	Vue racine

electro-chlorination station 3_V13_SP1_V18 / HMI_1 [TP1500 Basic color PN] / Text and graphic lists

Graphic lists

This folder is empty.

electro-chlorination station 3_V13_SP1_V18 / HMI_1 [TP1500 Basic color PN] / User administration

User

Administrateur

General			
Name	Administrateur	**Number**	1
Automatic logoff			
Automatic logoff	Enabled	**Logoff time**	5
Comment			
Comment	L'utilisateur 'Administrateur' est affecté au groupe 'Administrateurs'.		
Groups			
Groups	Groupe administrateurs;		

electro-chlorination station 3_V13_SP1_V18 / HMI_1 [TP1500 Basic color PN] / User administration

Groups

Groupe administrateurs

General					
Name	Groupe administrateurs	**Display name**	Groupe administrateurs	**Number**	1
Password aging	Disabled				
Comment					
Comment	Le groupe 'Administrateurs' a tous les droits au début.				
Authorizations					
Authorizations	Gestion des utilisateurs; Contrôle; Commande;				

Utilisateurs

General					
Name	Utilisateurs	**Display name**	Utilisateurs	**Number**	2
Password aging	Disabled				
Comment					
Comment	Le groupe 'Utilisateurs' a le droit 'Commande' au début.				
Authorizations					
Authorizations	Commande;				

electro-chlorination station 3_V13_SP1_V18 / HMI_1 [TP1500 Basic color PN] / User administration

Authorizations

Commande

General					
Name	Commande	**Authorization**	Commande	**Authorization number**	3
Comment					
Comment	Autorisation 'Commande'.				

Contrôle

General					
Name	Contrôle	**Authorization**	Contrôle	**Authorization number**	2
Comment					
Comment	Autorisation 'Contrôle'.				

Gestion des utilisateurs

General					
Name	Gestion des utilisateurs	**Authorization**	Gestion des utilisateurs	**Authorization number**	1
Comment					
Comment	Autorisation 'Gestion utilisateur' pour la gestion des utilisateurs dans la vue des utilisateurs au Runtime.				

electro-chlorination station 3_V13_SP1_V18

Ungrouped devices

This folder is empty.

electro-chlorination station 3_V13_SP1_V18

Security settings

This folder is empty.

electro-chlorination station 3_V13_SP1_V18 / Cross-device functions / Project traces

Measurements

This folder is empty.

electro-chlorination station 3_V13_SP1_V18 / Common data / Alarm classes

Alarm classes

Alarm classes				
Name	**ID**	**Display name**	**Acknowledgment**	**Priority**
Acknowledgement	33	A	True	0
No Acknowledgement	34	NA	False	0

electro-chlorination station 3_V13_SP1_V18 / Common data

Logs

This folder is empty.

electro-chlorination station 3_V13_SP1_V18 / Languages & resources

Project languages

Languages
Reference language
French (France)

Editing language
French (France)

Other project languages
Empty

electro-chlorination station 3_V13_SP1_V18 / Languages & resources / Project texts

Project texts

Project texts		
French (France)	Category	Reference
	Multilingual text category	Subcategory 1\Description
	Multilingual text category	Subcategory 2\Description
	Multilingual text category	Subcategory 3\Description
	Multilingual text category	Subcategory 4\Description
	Multilingual text category	Subcategory 5\Description
	Multilingual text category	Subcategory 6\Description
	Multilingual text category	Subcategory 7\Description
	Multilingual text category	Subcategory 8\Description
	Multilingual text category	Subcategory 9\Description
	Multilingual text category	Subcategory 10\Description
	Multilingual text category	Subcategory 11\Description
	Multilingual text category	Subcategory 12\Description
	Multilingual text category	Subcategory 13\Description
	Multilingual text category	Subcategory 14\Description
	Multilingual text category	Subcategory 15\Description
	Multilingual text category	Subcategory 16\Description
	Multilingual text category	Subcategory 1\Description
	Multilingual text category	Subcategory 2\Description
	Multilingual text category	Subcategory 3\Description
	Multilingual text category	Subcategory 4\Description
	Multilingual text category	Subcategory 5\Description
	Multilingual text category	Subcategory 6\Description
	Multilingual text category	Subcategory 7\Description
	Multilingual text category	Subcategory 8\Description
	Multilingual text category	Subcategory 9\Description
	Multilingual text category	Subcategory 10\Description
	Multilingual text category	Subcategory 11\Description
	Multilingual text category	Subcategory 12\Description
	Multilingual text category	Subcategory 13\Description
	Multilingual text category	Subcategory 14\Description
	Multilingual text category	Subcategory 15\Description
	Multilingual text category	Subcategory 16\Description
	Other text category	electro-chlorination station 3_V13_SP1_V18\Comment
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervisionalarmtexts_1\Range 1\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlockalarmtexts_1\Range 1\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervisionalarmtexts_2\Range 1\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlockalarmtexts_2\Range 1\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervisionalarmtexts_2\Range 9\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlockalarmtexts_2\Range 9\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervisionalarmtexts_2\Range 10\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlockalarmtexts_2\Range 10\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervisionalarmtexts_2\Range 11\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlockalarmtexts_2\Range 11\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervisionalarmtexts_2\Range 12\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlockalarmtexts_2\Range 12\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervisionalarmtexts_2\Range 13\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlockalarmtexts_2\Range 13\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervisionalarmtexts_1\Range 2\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlockalarmtexts_1\Range 2\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervisionalarmtexts_1\Range 3\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlockalarmtexts_1\Range 3\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervisionalarmtexts_1\Range 4\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlockalarmtexts_1\Range 4\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervisionalarmtexts_1\Range 5\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlockalarmtexts_1\Range 5\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervisionalarmtexts_1\Range 6\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlockalarmtexts_1\Range 6\Text

French (France)	Category	Reference
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_1\Range 7\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_1\Range 7\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_1\Range 8\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_1\Range 8\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_1\Range 10\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_1\Range 10\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_1\Range 11\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_1\Range 11\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_1\Range 12\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_1\Range 12\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_1\Range 13\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_1\Range 13\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_1\Range 15\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_1\Range 15\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_1\Range 16\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_1\Range 16\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_1\Range 17\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_1\Range 17\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_1\Range 18\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_1\Range 18\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_1\Range 20\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_1\Range 20\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_1\Range 21\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_1\Range 21\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_1\Range 22\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_1\Range 22\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_1\Range 23\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_1\Range 23\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_1\Range 24\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_1\Range 24\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_2\Range 14\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_2\Range 14\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_2\Range 15\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_2\Range 15\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_2\Range 16\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_2\Range 16\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_3\Range 1\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_3\Range 1\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_3\Range 2\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_3\Range 2\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_3\Range 3\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_3\Range 3\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_1\Range 26\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_1\Range 26\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_1\Range 27\Text

French (France)	Category	Reference
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlock AlarmTexts_1\Range 27\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervision AlarmTexts_3\Range 4\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlock AlarmTexts_3\Range 4\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervision AlarmTexts_3\Range 5\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlock AlarmTexts_3\Range 5\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervision AlarmTexts_3\Range 6\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlock AlarmTexts_3\Range 6\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervision AlarmTexts_3\Range 7\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlock AlarmTexts_3\Range 7\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervision AlarmTexts_3\Range 8\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlock AlarmTexts_3\Range 8\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervision AlarmTexts_3\Range 9\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlock AlarmTexts_3\Range 9\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervision AlarmTexts_3\Range 10\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlock AlarmTexts_3\Range 10\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervision AlarmTexts_3\Range 11\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlock AlarmTexts_3\Range 11\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervision AlarmTexts_3\Range 12\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlock AlarmTexts_3\Range 12\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervision AlarmTexts_3\Range 15\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlock AlarmTexts_3\Range 15\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervision AlarmTexts_3\Range 16\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlock AlarmTexts_3\Range 16\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervision AlarmTexts_3\Range 17\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlock AlarmTexts_3\Range 17\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervision AlarmTexts_3\Range 18\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlock AlarmTexts_3\Range 18\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervision AlarmTexts_3\Range 19\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlock AlarmTexts_3\Range 19\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervision AlarmTexts_3\Range 20\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlock AlarmTexts_3\Range 20\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervision AlarmTexts_1\Range 29\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlock AlarmTexts_1\Range 29\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervision AlarmTexts_1\Range 30\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlock AlarmTexts_1\Range 30\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervision AlarmTexts_2\Range 19\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlock AlarmTexts_2\Range 19\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervision AlarmTexts_2\Range 20\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlock AlarmTexts_2\Range 20\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervision AlarmTexts_2\Range 21\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlock AlarmTexts_2\Range 21\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervision AlarmTexts_1\Range 31\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlock AlarmTexts_1\Range 31\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervision AlarmTexts_1\Range 33\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlock AlarmTexts_1\Range 33\Text

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	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_1\Range 28\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_1\Range 28\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_1\Range 34\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_1\Range 34\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_1\Range 35\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_1\Range 35\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_3\Range 21\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_3\Range 21\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_3\Range 22\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_3\Range 22\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_3\Range 23\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_3\Range 23\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_3\Range 24\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_3\Range 24\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_3\Range 26\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_3\Range 26\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_2\Range 17\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_2\Range 17\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_2\Range 18\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_2\Range 18\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_1\Range 36\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_1\Range 36\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_1\Range 37\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_1\Range 37\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_3\Range 27\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_3\Range 27\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_3\Range 28\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_3\Range 28\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_1\Range 38\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_1\Range 38\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_1\Range 39\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_1\Range 39\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_1\Range 42\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_1\Range 42\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_1\Range 45\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_1\Range 45\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_1\Range 31\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_1\Range 31\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_1\Range 32\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_1\Range 32\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_1\Range 47\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_1\Range 47\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_1\Range 40\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_InterlockAlarmTexts_1\Range 40\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_SupervisionAlarmTexts_1\Range 43\Text

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	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlockalarmtexts_1\Range 43\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervisionalarmtexts_1\Range 44\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlockalarmtexts_1\Range 44\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervisionalarmtexts_1\Range 46\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlockalarmtexts_1\Range 46\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervisionalarmtexts_1\Range 48\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlockalarmtexts_1\Range 48\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervisionalarmtexts_1\Range 19\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlockalarmtexts_1\Range 19\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervisionalarmtexts_2\Range 22\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlockalarmtexts_2\Range 22\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervisionalarmtexts_3\Range 13\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlockalarmtexts_3\Range 13\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervisionalarmtexts_1\Range 25\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlockalarmtexts_1\Range 25\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervisionalarmtexts_1\Range 41\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlockalarmtexts_1\Range 41\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervisionalarmtexts_1\Range 49\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlockalarmtexts_1\Range 49\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervisionalarmtexts_1\Range 50\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlockalarmtexts_1\Range 50\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervisionalarmtexts_2\Range 23\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlockalarmtexts_2\Range 23\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervisionalarmtexts_2\Range 24\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlockalarmtexts_2\Range 24\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervisionalarmtexts_2\Range 25\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlockalarmtexts_2\Range 25\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervisionalarmtexts_2\Range 26\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlockalarmtexts_2\Range 26\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervisionalarmtexts_2\Range 27\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlockalarmtexts_2\Range 27\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Supervisionalarmtexts_2\Range 28\Text
	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Interlockalarmtexts_2\Range 28\Text
	Other text category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Comment
	Other text category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCategories\Comment
	Other text category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCategories1\Comment
	Other text category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCategories2\Comment
	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Warnings\alarmclass name not set_1\AlarmClassData_IDisplayNaming_DisplayName
	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screen management\Templates\Modèle_1\Template_Button_1\Text OFF
	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screen management\Templates\Modèle_1\Template_Button_1\Text ON
	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screen management\Templates\Modèle_1\Template_Button_2\Text OFF
	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screen management\Templates\Modèle_1\Template_Button_2\Text ON
	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screen management\Templates\Modèle_1\Template_Button_3\Text OFF
	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screen management\Templates\Modèle_1\Template_Button_3\Text ON
	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screen management\Templates\Modèle_1\Template_Button_4\Text OFF

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	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screen management\Templates\Modèle_1\Template_Button_4\Text ON
	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screen management\Templates\Modèle_1\Template_Button_5\Text OFF
	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screen management\Templates\Modèle_1\Template_Button_5\Text ON
	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screen management\Templates\Modèle_1\Template_Button_6\Text OFF
	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screen management\Templates\Modèle_1\Template_Button_6\Text ON
	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screen management\Templates\Modèle_1\Template_Button_7\Text OFF
	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screen management\Templates\Modèle_1\Template_Button_7\Text ON
	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screen management\Templates\Modèle_1\Template_Button_8\Text OFF
	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screen management\Templates\Modèle_1\Template_Button_8\Text ON
	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screen management\Templates\Modèle_1\Template_Button_9\Text OFF
	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screen management\Templates\Modèle_1\Template_Button_9\Text ON
	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Acknowledgement\\AlarmClassData_IDisplayNaming_DisplayName
	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\No Acknowledgement\\AlarmClassData_IDisplayNaming_DisplayName
--	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCategories1\Range 0\Text
--	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCategories2\Range 0\Text
Naci Mohamed Aymen	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Button_3\Text OFF
Haraoui Mohamed Amin	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Button_2\Text OFF
Temperature (C°)	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPOCHLORIT SYSTEM\Text field_4\Text
!	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Errors\alarmclass name not set\AlarmClassData_IDisplayNaming_DisplayName
!!	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Safety warnings\alarmclass name not set_4\AlarmClassData_IDisplayNaming_DisplayName
"Main Program Sweep (Cycle)"	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Main [OB1]\Block title
$	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\System\alarmclass name not set_2\AlarmClassData_IDisplayNaming_DisplayName
Automation & Supervision of the Electrochlorination Station	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Button_1\Text OFF
1 (Cold restart scan 1 of OB 1), 3 (Scan 2-n of OB 1)	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Main [OB1]\OB1_SCAN_1
1 (Organization block 1, OB1)	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Main [OB1]\OB1_OB_NUMBR
A	Alarm class text	electro-chlorination station 3_V13_SP1_V18\Acknowledgement\AlarmClassData_IDisplayNaming_DisplayName
A	Alarm class text	electro-chlorination station 3_V13_SP1_V18\Acknowledgement\ShortName
A	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Errors\ComingText
A	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Warnings\ComingText
A	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\System\ComingText
A	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Diagnosis events\ComingText
A	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Acknowledgement\ComingText
A	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\No knowledgement\ComingText
A	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\No Acknowledgement\ComingText
A	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Safety warnings\AcknowledgedText
ACID CLEANING	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Bouton_3\Text ON
Acid Cleaning pump 1 Fault	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Alarme de bit_18\Alarm text
Acid Cleaning pump 2 Fault	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Alarme de bit_19\Alarm text
ACID CLEANING SYSTEM	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Bouton_3\Text OFF
Acid Concentration (%)	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\ACID CLEANING\Text field_1\Text
Acid tank level HH	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Alarme de bit_15\Alarm text
Acid tank level LL	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Alarme de bit_24\Alarm text
Acquitter toutes les erreurs et perturbations	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\ACK_EF
Acquitter toutes les erreurs et perturbations	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\ACK_EF
Acquitter toutes les erreurs et perturbations	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\ACK_EF
Action	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 104\Text
Action	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 204\Text

French (France)	Category	Reference
Action	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 304\Text
Action	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 404\Text
Action	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 504\Text
Action	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 604\Text
Action	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 704\Text
Action	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 804\Text
Activer l'étape figurant dans le paramètre S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\S_ON
Activer l'étape figurant dans le paramètre S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\S_ON
Activer l'étape figurant dans le paramètre S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\S_ON
AD	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Er-rors\ComingGoingText
AD	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Warn-ings\ComingGoingText
AD	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Sys-tem\ComingGoingText
AD	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Diag-nosis events\ComingGoingText
AD	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Ac-knowledgement\ComingGoingText
AD	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\No Acknowledgement\ComingGoingText
Affichage interne du paramètre de sortie S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\S_DISPLAY
Affichage interne du paramètre de sortie S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\S_DISPLAY
Affichage interne du paramètre de sortie S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\S_DISPLAY
Afficher l'étape précédente dans le paramètre S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\S_PREV
Afficher l'étape précédente dans le paramètre S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\S_PREV
Afficher l'étape précédente dans le paramètre S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\S_PREV
Afficher l'étape suivante dans le paramètre S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\S_NEXT
Afficher l'étape suivante dans le paramètre S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\S_NEXT
Afficher l'étape suivante dans le paramètre S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\S_NEXT
Air Flow (m3/h)	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Text field_2\Text
air flow is not ok	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Alarme de bit_22\Alarm text
ALARM	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Bouton_8\Text OFF
ALARM	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Bouton_8\Text ON
ALARM	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\ACID CLEANING\Bouton_4\Text OFF
ALARM	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\ACID CLEANING\Bouton_4\Text ON
ALARM	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\SEAWA-TER INTAKE\Bouton_6\Text OFF
ALARM	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\SEAWA-TER INTAKE\Bouton_6\Text ON
ALARM	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Bouton_7\Text ON
ALARM	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Bouton_7\Text OFF
AlarmDisplay	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screen manage-ment\Templates\Modèle_1\Template_Button_10\Text OFF
AlarmDisplay	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screen manage-ment\Templates\Modèle_1\Template_Button_10\Text ON
Alarmes en attente	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screen manage-ment\Global screen\Fenêtre des alarmes_en attente\Label
Alarmes non acquittées	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screen manage-ment\Global screen\Fenêtre des alarmes_non acquittée\Label
Alarmes système en attente	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screen manage-ment\Global screen\Alarmes système\Label
auto	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Bouton_11\Text OFF
auto	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Bouton_13\Text OFF
AUTO	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Bouton_6\Text OFF
Autorisation 'Commande'.	HMI comment	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\User administra-tion\Commande\Comment
Autorisation 'Contrôle'.	HMI comment	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\User administra-tion\Contrôle\Comment
Autorisation 'Gestion utilisateur' pour la ges-tion des utilisateurs dans la vue des utilisateurs au Runtime.	HMI comment	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\User administra-tion\Gestion des utilisateurs\Comment

French (France)	Category	Reference
Bits 0-3 = 1 (Coming event), Bits 4-7 = 1 (Event class 1)	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Main [OB1]\OB1_EV_CLASS
Bulk Acid tank level HH	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Alarme de bit_16\Alarm text
Bulk Tank Level H	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Alarme de bit_23\Alarm text
Bulk Tank Level LL	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Alarme de bit_17\Alarm text
Category : Supervision type : PLC name : FB name : Step name : Step number : Step specific field	Alarm text	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\M_EVIL\Alarm text
Category : Supervision type : PLC name : FB name : Step name : Step number : Step specific field	Alarm text	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\M_EVSV\Alarm text
Category : Supervision type : PLC name : FB name : Step name : Step number : Step specific field	Alarm text	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\M_EVIL\Alarm text
Category : Supervision type : PLC name : FB name : Step name : Step number : Step specific field	Alarm text	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\M_EVSV\Alarm text
Category : Supervision type : PLC name : FB name : Step name : Step number : Step specific field	Alarm text	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\M_EVIL\Alarm text
Category : Supervision type : PLC name : FB name : Step name : Step number : Step specific field	Alarm text	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\M_EVSV\Alarm text
Category 4	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 401\Text
Category 4	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 402\Text
Category 4	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 403\Text
Category 4	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 404\Text
Category 4	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 405\Text
Category 4	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 406\Text
Category 4	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 407\Text
Category 4	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 408\Text
Category 4	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 409\Text
Category 4	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 410\Text
Category 5	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 501\Text
Category 5	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 502\Text
Category 5	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 503\Text
Category 5	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 504\Text
Category 5	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 505\Text
Category 5	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 506\Text
Category 5	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 507\Text
Category 5	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 508\Text
Category 5	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 509\Text
Category 5	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 510\Text
Category 6	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 601\Text
Category 6	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 602\Text
Category 6	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 603\Text
Category 6	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 604\Text
Category 6	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 605\Text
Category 6	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 606\Text
Category 6	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 607\Text
Category 6	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 608\Text
Category 6	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 609\Text
Category 6	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 610\Text
Category 7	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 701\Text
Category 7	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 702\Text

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Category 7	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCategories\Range 703\Text
Category 7	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCategories\Range 704\Text
Category 7	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCategories\Range 705\Text
Category 7	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCategories\Range 706\Text
Category 7	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCategories\Range 707\Text
Category 7	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCategories\Range 708\Text
Category 7	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCategories\Range 709\Text
Category 7	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCategories\Range 710\Text
Category 8	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCategories\Range 801\Text
Category 8	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCategories\Range 802\Text
Category 8	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCategories\Range 803\Text
Category 8	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCategories\Range 804\Text
Category 8	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCategories\Range 805\Text
Category 8	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCategories\Range 806\Text
Category 8	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCategories\Range 807\Text
Category 8	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCategories\Range 808\Text
Category 8	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCategories\Range 809\Text
Category 8	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCategories\Range 810\Text
Commande	HMI runtime	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\User administration\Commande\ShortName
Contrôle	HMI runtime	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\User administration\Contrôle\ShortName
countinous injection pump 1 Fault	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Alarme de bit_6\Alarm text
countinous injection pump 2 Fault	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Alarme de bit_7\Alarm text
Current(A)	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPOCHLORIT SYSTEM\Text field_3\Text
Cycle time of previous OB1 scan (milliseconds)	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Main [OB1]\OB1_PREV_CYCLE
D	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Errors\GoingText
D	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Warnings\GoingText
D	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\System\GoingText
D	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Diagnosis events\GoingText
D	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Acknowledgement\GoingText
D	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\No Acknowledgement\GoingText
Date and time OB1 started	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Main [OB1]\OB1_DATE_TIME
Débordement : Pas assez de ressources système pour ALARM_S	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\SQ_FLAGS.MSG_OFL
Débordement : Pas assez de ressources système pour ALARM_S	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\SQ_FLAGS.MSG_OFL
Débordement : Pas assez de ressources système pour ALARM_S	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\SQ_FLAGS.MSG_OFL
Débordement : Trop de requêtes ON ou OFF	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\SQ_FLAGS.NF_OFL
Débordement : Trop de requêtes ON ou OFF	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\SQ_FLAGS.NF_OFL
Débordement : Trop de requêtes ON ou OFF	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\SQ_FLAGS.NF_OFL
Débordement : Trop de transitions valides	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\SQ_FLAGS.TV_OFL
Débordement : Trop de transitions valides	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\SQ_FLAGS.TV_OFL
Débordement : Trop de transitions valides	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\SQ_FLAGS.TV_OFL
Débordement : Trop d'étapes activées	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\SQ_FLAGS.SA_OFL
Débordement : Trop d'étapes activées	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\SQ_FLAGS.SA_OFL
Débordement : Trop d'étapes activées	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\SQ_FLAGS.SA_OFL
Demoratice and Pupular Algerian Republic	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HOME\Text field_1\Text
Désactiver le graphe	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\OFF_SQ

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Désactiver le graphe	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\OFF_SQ
Désactiver le graphe	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\OFF_SQ
Désactiver l'étape figurant dans le paramètre S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\S_OFF
Désactiver l'étape figurant dans le paramètre S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\S_OFF
Désactiver l'étape figurant dans le paramètre S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\S_OFF
Différence de temps entre deux cycles	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\TICKS.DELTA
Différence de temps entre deux cycles	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\TICKS.DELTA
Différence de temps entre deux cycles	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\TICKS.DELTA
EMERGENCY STOP	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Bouton_7\Text OFF
Envoyer les messages de WR_USMSG ou les entrer uniquement dans le tampon de diag-nostic	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\SQ_FLAGS.AS_SEND
Envoyer les messages de WR_USMSG ou les entrer uniquement dans le tampon de diag-nostic	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\SQ_FLAGS.AS_SEND
Envoyer les messages de WR_USMSG ou les entrer uniquement dans le tampon de diag-nostic	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\SQ_FLAGS.AS_SEND
Erreur à l'exécution	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\SQ_FLAGS.RT_FAIL
Erreur à l'exécution	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\SQ_FLAGS.RT_FAIL
Erreur à l'exécution	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\SQ_FLAGS.RT_FAIL
Erreur groupée Interlock	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\SQ_FLAGS.ERROR
Erreur groupée Interlock	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\SQ_FLAGS.ERROR
Erreur groupée Interlock	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\SQ_FLAGS.ERROR
Erreur groupée Interlock et Supervision	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\SQ_FLAGS.ERR_FLT
Erreur groupée Interlock et Supervision	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\SQ_FLAGS.ERR_FLT
Erreur groupée Interlock et Supervision	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\SQ_FLAGS.ERR_FLT
Erreur groupée Interlock or Supervision	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\ERR_FLT
Erreur groupée Interlock or Supervision	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\ERR_FLT
Erreur groupée Interlock or Supervision	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\ERR_FLT
Erreur groupée Supervision	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\SQ_FLAGS.FAULT
Erreur groupée Supervision	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\SQ_FLAGS.FAULT
Erreur groupée Supervision	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\SQ_FLAGS.FAULT
Error	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 101\Text
Error	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 102\Text
Error	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 103\Text
Error	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 104\Text
Error	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 105\Text
Error	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 106\Text
Error	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 107\Text
Error	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 108\Text
Error	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 109\Text
Error	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 110\Text
Error : GRAPH-Interlock : $$CpuName$$: Acid Cleaning : @1W%t#SYSTEM_StepNames_3@ : S@1W%03u@ : @1W%t#SYSTEM_InterlockA-larmTexts_3@	Alarm text	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning_DB [DB4]\M_EVIL\Alarm text
Error : GRAPH-Interlock : $$CpuName$$: hypoclorite system : @1W%t#SYSTEM_Step-Names_1@ : S@1W%03u@ : @1W%t#SYS-TEM_InterlockAlarmTexts_1@	Alarm text	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\ELEC-TRO CHLOR STATION_DB [DB2]\M_EVIL\Alarm text
Error : GRAPH-Interlock : $$CpuName$$: hypoclorite system : @1W%t#SYSTEM_Step-Names_1@ : S@1W%03u@ : @1W%t#SYS-TEM_InterlockAlarmTexts_1@	Alarm text	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system_DB [DB6]\M_EVIL\Alarm text

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Error : GRAPH-Interlock : $$CpuName$$: SEAWATER Intake : @1W%t#SYSTEM_Step-Names_2@ : S@1W%03u@ : @1W%t#SYS-TEM_InterlockAlarmTexts_2@	Alarm text	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake_DB [DB1]\M_EVIL\Alarm text
Error : GRAPH-Supervision : $$CpuName$$: Acid Cleaning : @1W%t#SYSTEM_Step-Names_3@ : S@1W%03u@ : @1W%t#SYS-TEM_SupervisionAlarmTexts_3@	Alarm text	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning_DB [DB4]\M_EVSV\Alarm text
Error : GRAPH-Supervision : $$CpuName$$: hypoclorite system : @1W%t#SYSTEM_Step-Names_1@ : S@1W%03u@ : @1W%t#SYS-TEM_SupervisionAlarmTexts_1@	Alarm text	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\ELEC-TRO CHLOR STATION_DB [DB2]\M_EVSV\Alarm text
Error : GRAPH-Supervision : $$CpuName$$: hypoclorite system : @1W%t#SYSTEM_Step-Names_1@ : S@1W%03u@ : @1W%t#SYS-TEM_SupervisionAlarmTexts_1@	Alarm text	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system_DB [DB6]\M_EVSV\Alarm text
Error : GRAPH-Supervision : $$CpuName$$: SEAWATER Intake : @1W%t#SYSTEM_Step-Names_2@ : S@1W%03u@ : @1W%t#SYS-TEM_SupervisionAlarmTexts_2@	Alarm text	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake_DB [DB1]\M_EVSV\Alarm text
Error message	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 106\Text
Error message	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 206\Text
Error message	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 306\Text
Error message	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 406\Text
Error message	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 506\Text
Error message	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 606\Text
Error message	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 706\Text
Error message	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 806\Text
Etat : "Ignorer les étapes" activé	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\MOP.SSKIP
Etat : "Ignorer les étapes" activé	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\MOP.SSKIP
Etat : "Ignorer les étapes" activé	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP.SSKIP
Etat : Acquittement requis	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\MOP.ACKREQ
Etat : Acquittement requis	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\MOP.ACKREQ
Etat : Acquittement requis	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP.ACKREQ
Etat : Afficher toutes les étapes	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\MOP.SALL_DISP
Etat : Afficher toutes les étapes	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\MOP.SALL_DISP
Etat : Afficher toutes les étapes	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP.SALL_DISP
Etat : Afficher uniquement les étapes avec er-reurs et perturbations	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\MOP.SEF_DISP
Etat : Afficher uniquement les étapes avec er-reurs et perturbations	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\MOP.SEF_DISP
Etat : Afficher uniquement les étapes avec er-reurs et perturbations	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP.SEF_DISP
Etat : Le graphe est arrêté	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\MOP.HALT
Etat : Le graphe est arrêté	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\MOP.HALT
Etat : Le graphe est arrêté	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP.HALT
Etat : Mode automatique	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\MOP.AUTO
Etat : Mode automatique	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\MOP.AUTO
Etat : Mode automatique	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP.AUTO
Etat : Mode manuel	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\MOP.MAN
Etat : Mode manuel	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\MOP.MAN
Etat : Mode manuel	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP.MAN
Etat : Mode semi-automatique/Activation de l'étape suivante avec transition	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\MOP.TAP
Etat : Mode semi-automatique/Activation de l'étape suivante avec transition	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\MOP.TAP
Etat : Mode semi-automatique/Activation de l'étape suivante avec transition	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP.TAP
Etat : Mode semi-automatique/Ignorer la tran-sition	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\MOP.TOP
Etat : Mode semi-automatique/Ignorer la tran-sition	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\MOP.TOP
Etat : Mode semi-automatique/Ignorer la tran-sition	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP.TOP

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Etat : N'afficher que les étapes actives	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\MOP.SACT_DISP
Etat : N'afficher que les étapes actives	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\MOP.SACT_DISP
Etat : N'afficher que les étapes actives	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP.SACT_DISP
Etat : Tous les opérandes traités avec N, L, D mis à 0	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\MOP.OPS_ZERO
Etat : Tous les opérandes traités avec N, L, D mis à 0	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\MOP.OPS_ZERO
Etat : Tous les opérandes traités avec N, L, D mis à 0	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP.OPS_ZERO
Etat : Toutes les temporisations internes sont arrêtées	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\MOP.TMS_HALT
Etat : Toutes les temporisations internes sont arrêtées	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\MOP.TMS_HALT
Etat : Toutes les temporisations internes sont arrêtées	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP.TMS_HALT
Etat : Traitement de Supervision activé	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\MOP.SUP
Etat : Traitement de Supervision activé	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\MOP.SUP
Etat : Traitement de Supervision activé	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP.SUP
Etat : Traitement des Interlocks activé	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\MOP.LOCK
Etat : Traitement des Interlocks activé	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\MOP.LOCK
Etat : Traitement des Interlocks activé	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP.LOCK
Etat : Traitement permanent de tous les Inter-locks	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\MOP.IL_PERM
Etat : Traitement permanent de tous les Inter-locks	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\MOP.IL_PERM
Etat : Traitement permanent de tous les Inter-locks	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP.IL_PERM
Etat : Traitement permanent de tous les Inter-locks en mode manuel	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\MOP.ILP_MAN
Etat : Traitement permanent de tous les Inter-locks en mode manuel	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\MOP.ILP_MAN
Etat : Traitement permanent de tous les Inter-locks en mode manuel	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP.ILP_MAN
Etat : Traitement permanent de toutes les transitions	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\MOP.T_PERM
Etat : Traitement permanent de toutes les transitions	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\MOP.T_PERM
Etat : Traitement permanent de toutes les transitions	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP.T_PERM
Exécution cyclique du graphe après l'initialisa-tion	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\SQ_FLAGS.CYC_OP
Exécution cyclique du graphe après l'initialisa-tion	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\SQ_FLAGS.CYC_OP
Exécution cyclique du graphe après l'initialisa-tion	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\SQ_FLAGS.CYC_OP
ExitRuntime	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screen manage-ment\Templates\Modèle_1\Template_Button\Text OFF
ExitRuntime	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screen manage-ment\Templates\Modèle_1\Template_Button\Text ON
Gestion des utilisateurs	HMI runtime	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\User administra-tion\Gestion des utilisateurs\ShortName
GRAPH7_INTERLOCK_ERROR	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\Upstream/downstream permanent instructions\Free comment
GRAPH7_INTERLOCK_ERROR	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\Upstream/downstream permanent instructions\Free comment
GRAPH7_INTERLOCK_ERROR	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\Upstream/downstream permanent instructions\Free comment
GRAPH7_SUPERVISION_FAULT	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\Upstream/downstream permanent instructions\Free comment
GRAPH7_SUPERVISION_FAULT	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\Upstream/downstream permanent instructions\Free comment
GRAPH7_SUPERVISION_FAULT	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\Upstream/downstream permanent instructions\Free comment
GRAPH-Interlock	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 108\Text
GRAPH-Interlock	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 208\Text
GRAPH-Interlock	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 308\Text
GRAPH-Interlock	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 408\Text
GRAPH-Interlock	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 508\Text
GRAPH-Interlock	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 608\Text
GRAPH-Interlock	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 708\Text
GRAPH-Interlock	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 808\Text
GRAPH-Supervision	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 109\Text

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GRAPH-Supervision	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 209\Text
GRAPH-Supervision	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 309\Text
GRAPH-Supervision	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 409\Text
GRAPH-Supervision	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 509\Text
GRAPH-Supervision	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 609\Text
GRAPH-Supervision	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 709\Text
GRAPH-Supervision	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 809\Text
GRAPH-Warning	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 110\Text
GRAPH-Warning	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 210\Text
GRAPH-Warning	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 310\Text
GRAPH-Warning	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 410\Text
GRAPH-Warning	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 510\Text
GRAPH-Warning	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 610\Text
GRAPH-Warning	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 710\Text
GRAPH-Warning	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 810\Text
Groupe administrateurs	HMI runtime	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\User administration\Groupe administrateurs\DisplayName
H2 concentration H	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Alarme de bit_12\Alarm text
H2 concentration HH	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Alarme de bit_13\Alarm text
H2 concentration percent (%)	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPOCHLORIT SYSTEM\Text field_7\Text
HOME	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPOCHLORIT SYSTEM\Bouton_1\Text OFF
HOME	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPOCHLORIT SYSTEM\Bouton_1\Text ON
HOME	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\ACID CLEANING\Bouton_1\Text OFF
HOME	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\ACID CLEANING\Bouton_1\Text ON
HOME	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\SEAWATER INTAKE\Bouton_1\Text OFF
HOME	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\SEAWATER INTAKE\Bouton_1\Text ON
HOME	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\ALARM\Bouton_1\Text OFF
HOME	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\ALARM\Bouton_1\Text ON
HYPOCHLORIT SYSTEM	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Bouton_1\Text ON
HYPOCHLORIT SYSTEM	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\SEAWATER INTAKE\Bouton_5\Text OFF
HYPOCHLORIT SYSTEM	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\SEAWATER INTAKE\Bouton_5\Text ON
Hypochlorite Flow (m3/h)	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPOCHLORIT SYSTEM\Text field_5\Text
Hypochlorite Flow is not ok	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Alarme de bit_21\Alarm text
HYPOCHLORITE SYSTEM	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Bouton_1\Text OFF
Hypoclorite temperature HH	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Alarme de bit_20\Alarm text
I	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Safety warnings\ComingText
Il existe d'autres étapes qui peuvent être affichées dans S_NO.	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\S_MORE
Il existe d'autres étapes qui peuvent être affichées dans S_NO.	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\S_MORE
Il existe d'autres étapes qui peuvent être affichées dans S_NO.	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\S_MORE
Indice de la transition affichée dans T_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\T_DISPIDX
Indice de la transition affichée dans T_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\T_DISPIDX
Indice de la transition affichée dans T_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\T_DISPIDX
Indice de l'étape affichée dans S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\S_DISPIDX
Indice de l'étape affichée dans S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\S_DISPIDX
Indice de l'étape affichée dans S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\S_DISPIDX
Info	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCategories\Range 301\Text

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Info	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCategories\Range 302\Text
Info	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCategories\Range 303\Text
Info	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCategories\Range 304\Text
Info	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCategories\Range 305\Text
Info	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCategories\Range 306\Text
Info	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCategories\Range 307\Text
Info	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCategories\Range 308\Text
Info	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCategories\Range 309\Text
Info	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCategories\Range 310\Text
Institute of Electrical and Electronic Engineering(Ex-INELEC)	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HOME\Text field_3\Text
Interlock	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 102\Text
Interlock	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 202\Text
Interlock	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 302\Text
Interlock	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 402\Text
Interlock	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 502\Text
Interlock	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 602\Text
Interlock	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 702\Text
Interlock	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 802\Text
IO	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Safety warnings\ComingGoingText
Le groupe 'Administrateurs' a tous les droits au début.	HMI comment	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\User administration\Groupe administrateurs\Comment
Le groupe 'Utilisateurs' a le droit 'Commande' au début.	HMI comment	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\User administration\Utilisateurs\Comment
L'étape affichée dans le paramètre S_NO est activée	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\S_ACTIVE
L'étape affichée dans le paramètre S_NO est activée	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\S_ACTIVE
L'étape affichée dans le paramètre S_NO est activée	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\S_ACTIVE
L'utilisateur 'Administrateur' est affecté au groupe 'Administrateurs'.	HMI comment	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\User administration\Administrateur\Comment
man	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPOCHLORIT SYSTEM\Bouton_12\Text OFF
man	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPOCHLORIT SYSTEM\Bouton_14\Text OFF
Maximum cycle time of OB1 (milliseconds)	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Main [OB1]\OB1_MAX_CYCLE
Mémento de front interne pour le traitement séquentiel	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\SQ_FLAGS.SA_BUSY
Mémento de front interne pour le traitement séquentiel	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\SQ_FLAGS.SQ_BUSY
Mémento de front interne pour le traitement séquentiel	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\SQ_FLAGS.SQ_BUSY
Mémento de front interne pour le traitement séquentiel	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\SQ_FLAGS.SA_BUSY
Mémento de front interne pour le traitement séquentiel	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\SQ_FLAGS.SA_BUSY
Mémento de front interne pour le traitement séquentiel	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\SQ_FLAGS.SQ_BUSY
Mémentos de front pour messages de Alarm_S et Alarm_SQ	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\SQ_FLAGS.AS_SIG
Mémentos de front pour messages de Alarm_S et Alarm_SQ	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\SQ_FLAGS.AS_SIG
Mémentos de front pour messages de Alarm_S et Alarm_SQ	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\SQ_FLAGS.AS_SIG
Mémentos de graphe	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\SQ_FLAGS
Mémentos de graphe	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\SQ_FLAGS
Mémentos de graphe	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\SQ_FLAGS
Messages à l'exécution activés ou désactivés par instruction	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\SQ_FLAGS.AS_MSG
Messages à l'exécution activés ou désactivés par instruction	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\SQ_FLAGS.AS_MSG
Messages à l'exécution activés ou désactivés par instruction	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\SQ_FLAGS.AS_MSG
Mettre le graphe à l'état initial	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\INIT_SQ
Mettre le graphe à l'état initial	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\INIT_SQ

French (France)	Category	Reference
Mettre le graphe à l'état initial	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\INIT_SQ
M'hamed Bouguerra University-BOUMERDES	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Text field_4\Text
Minimum cycle time of OB1 (milliseconds)	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Main [OB1]\OB1_MIN_CYCLE
Ministry of higher education and scientific research	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Text field_2\Text
Mode automatique	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\SW_AUTO
Mode automatique	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\SW_AUTO
Mode automatique	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\SW_AUTO
Mode automatique activé	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\AUTO_ON
Mode automatique activé	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\AUTO_ON
Mode automatique activé	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\AUTO_ON
Mode de fonctionnement	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\MOP
Mode de fonctionnement	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\MOP
Mode de fonctionnement	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP
Mode manuel	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\SW_MAN
Mode manuel	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\SW_MAN
Mode manuel	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\SW_MAN
Mode manuel activé	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\MAN_ON
Mode manuel activé	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\MAN_ON
Mode manuel activé	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MAN_ON
Mode semi-automatique/Activation de l'étape suivante avec transition	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\SW_TAP
Mode semi-automatique/Activation de l'étape suivante avec transition	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\SW_TAP
Mode semi-automatique/Activation de l'étape suivante avec transition	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\SW_TAP
Mode semi-automatique/Etape avec transition activé	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\TAP_ON
Mode semi-automatique/Etape avec transition activé	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\TAP_ON
Mode semi-automatique/Etape avec transition activé	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\TAP_ON
Mode semi-automatique/Ignorer la transition	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\SW_TOP
Mode semi-automatique/Ignorer la transition	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\SW_TOP
Mode semi-automatique/Ignorer la transition	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\SW_TOP
Mode semi-automatique/Ignorer la transition activé	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\TOP_ON
Mode semi-automatique/Ignorer la transition activé	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\TOP_ON
Mode semi-automatique/Ignorer la transition activé	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\TOP_ON
Mr Haddouch Rezki	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Button_7\Text OFF
Mr Mohamed Lamine Malki	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Button_6\Text OFF
NA	Alarm class text	electro-chlorination station 3_V13_SP1_V18\No Acknowledgement\AlarmClassData_IDisplayNaming_DisplayName
NA	Alarm class text	electro-chlorination station 3_V13_SP1_V18\No Acknowledgement\ShortName
NavigateHome	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screen management\Templates\Modèle_1\Template_Button_11\Text OFF
NavigateHome	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screen management\Templates\Modèle_1\Template_Button_11\Text ON
Numéro d'étape	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\S_NO
Numéro d'étape	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\S_NO
Numéro d'étape	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\S_NO
Numéro d'étape requis introuvable	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\SQ_FLAGS.NO_SNO
Numéro d'étape requis introuvable	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\SQ_FLAGS.NO_SNO
Numéro d'étape requis introuvable	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\SQ_FLAGS.NO_SNO
O	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Safety warnings\GoingText
OPEN	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPOCHLORIT SYSTEM\Bouton_15\Text OFF
OPEN	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPOCHLORIT SYSTEM\Bouton_16\Text OFF

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Operand	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 101\Text
Operand	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 201\Text
Operand	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 301\Text
Operand	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 401\Text
Operand	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 501\Text
Operand	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 601\Text
Operand	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 701\Text
Operand	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 801\Text
Pas de commutation dans ce cycle	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\SQ_FLAGS.NO_SWI
Pas de commutation dans ce cycle	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\SQ_FLAGS.NO_SWI
Pas de commutation dans ce cycle	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\SQ_FLAGS.NO_SWI
Périodes d'horloge	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\TICKS
Périodes d'horloge	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\TICKS
Périodes d'horloge	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\TICKS
PH analyser (ph)	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\ACID CLEANING\Text field_2\Text
Position	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 105\Text
Position	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 205\Text
Position	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 305\Text
Position	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 405\Text
Position	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 505\Text
Position	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 605\Text
Position	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 705\Text
Position	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 805\Text
Priority of OB Execution	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Main [OB1]\OB1_PRIORITY
PUMP1	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Bouton_20\Text OFF
PUMP1	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Bouton_23\Text OFF
PUMP1	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Bouton_4\Text OFF
PUMP1	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\SEAWA-TER INTAKE\Bouton_4\Text OFF
PUMP1	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\ACID CLEANING\Bouton_20\Text OFF
PUMP2	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Bouton_21\Text OFF
PUMP2	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Bouton_22\Text OFF
PUMP2	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Bouton_24\Text OFF
PUMP2	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\SEAWA-TER INTAKE\Bouton_24\Text OFF
PUMP2	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\ACID CLEANING\Bouton_22\Text OFF
Q	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Er-rors\AcknowledgedText
Q	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Warn-ings\AcknowledgedText
Q	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Sys-tem\AcknowledgedText
Q	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Diag-nosis events\AcknowledgedText
Q	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Ac-knowledgement\AcknowledgedText
Q	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\No Acknowledgement\AcknowledgedText
QGR	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Runtime set-tings\HmiAlarmSettingsData\AcknowledgementGroupText
Reaction	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 103\Text
Reaction	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 203\Text
Reaction	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 303\Text
Reaction	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 403\Text

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Reaction	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 503\Text
Reaction	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 603\Text
Reaction	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 703\Text
Reaction	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 803\Text
Requête : Acquitter l'étape figurant dans le paramètre S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\MOP.ACK_S
Requête : Acquitter l'étape figurant dans le paramètre S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\MOP.ACK_S
Requête : Acquitter l'étape figurant dans le paramètre S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP.ACK_S
Requête : Acquitter toutes les erreurs Interlock et Supervision	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\MOP.ACK
Requête : Acquitter toutes les erreurs Interlock et Supervision	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\MOP.ACK
Requête : Acquitter toutes les erreurs Interlock et Supervision	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP.ACK
Requête : Activer l'étape figurant dans le paramètre S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\MOP.S_ON
Requête : Activer l'étape figurant dans le paramètre S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\MOP.S_ON
Requête : Activer l'étape figurant dans le paramètre S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP.S_ON
Requête : Afficher dans le paramètre S_NO le numéro d'étape figurant dans S_SEL	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\MOP.S_SELOK
Requête : Afficher dans le paramètre S_NO le numéro d'étape figurant dans S_SEL	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\MOP.S_SELOK
Requête : Afficher dans le paramètre S_NO le numéro d'étape figurant dans S_SEL	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP.S_SELOK
Requête : Afficher la transition valide précédente dans le paramètre T_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\MOP.T_PREV
Requête : Afficher la transition valide précédente dans le paramètre T_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\MOP.T_PREV
Requête : Afficher la transition valide précédente dans le paramètre T_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP.T_PREV
Requête : Afficher la transition valide suivante dans le paramètre T_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\MOP.T_NEXT
Requête : Afficher la transition valide suivante dans le paramètre T_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\MOP.T_NEXT
Requête : Afficher la transition valide suivante dans le paramètre T_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP.T_NEXT
Requête : Afficher l'étape précédente dans le paramètre S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\MOP.S_PREV
Requête : Afficher l'étape précédente dans le paramètre S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\MOP.S_PREV
Requête : Afficher l'étape précédente dans le paramètre S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP.S_PREV
Requête : Afficher l'étape suivante dans le paramètre S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\MOP.S_NEXT
Requête : Afficher l'étape suivante dans le paramètre S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\MOP.S_NEXT
Requête : Afficher l'étape suivante dans le paramètre S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP.S_NEXT
Requête : Désactiver l'étape figurant dans le paramètre S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\MOP.S_OFF
Requête : Désactiver l'étape figurant dans le paramètre S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\MOP.S_OFF
Requête : Désactiver l'étape figurant dans le paramètre S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP.S_OFF
Requête : Désactiver toutes les étapes	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\MOP.OFF
Requête : Désactiver toutes les étapes	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\MOP.OFF
Requête : Désactiver toutes les étapes	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP.OFF
Requête : Enregistrer l'étape figurant dans le paramètre S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\MOP.REG_S
Requête : Enregistrer l'étape figurant dans le paramètre S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\MOP.REG_S
Requête : Enregistrer l'étape figurant dans le paramètre S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP.REG_S
Requête : Enregistrer toutes les erreurs Interlock et Supervision	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\MOP.REG
Requête : Enregistrer toutes les erreurs Interlock et Supervision	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\MOP.REG
Requête : Enregistrer toutes les erreurs Interlock et Supervision	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP.REG
Requête : Mettre le graphe à l'état initial	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\MOP.INIT
Requête : Mettre le graphe à l'état initial	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\MOP.INIT
Requête : Mettre le graphe à l'état initial	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP.INIT
Requête : Validation de commutation pour transition	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\MOP.T_PUSH
Requête : Validation de commutation pour transition	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\MOP.T_PUSH

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Requête : Validation de commutation pour transition	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\MOP.T_PUSH
Reserved for system	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Main [OB1]\OB1_RESERVED_1
Reserved for system	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Main [OB1]\OB1_RESERVED_2
S7	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Diagnosis events\alarmclass name not set_3\AlarmClassData_IDisplayNaming_DisplayName
SCALING VALUES	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\System blocks\Program resources\SCALE [FC105]\Block title
Seawater Flow (m3/h)	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPOCHLORIT SYSTEM\Text field_1\Text
SEAWATER INTAKE	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Bouton_2\Text ON
SEAWATER INTAKE SYSTEM	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Bouton_2\Text OFF
seawater level HH	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Alarme de bit_25\Alarm text
seawater level LL	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Alarme de bit_14\Alarm text
seawater pump 1 FAULT	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Alarme de bit_1\Alarm text
seawater pump 2 FAULT	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Alarme de bit_2\Alarm text
seawater pump intake 1 Fault	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Alarme de bit_3\Alarm text
seawater pump intake 2 Fault	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Alarme de bit_4\Alarm text
Sélectionner l'étape à afficher dans S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\S_SEL
Sélectionner l'étape à afficher dans S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\S_SEL
Sélectionner l'étape à afficher dans S_NO	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\S_SEL
shocked injection pump 1 Fault	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Alarme de bit_8\Alarm text
shocked injection pump 2 Fault	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Alarme de bit_9\Alarm text
start	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPOCHLORIT SYSTEM\Bouton_17\Text OFF
start	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPOCHLORIT SYSTEM\Bouton_18\Text OFF
start	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\SEAWATER INTAKE\Bouton_2\Text OFF
start	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\SEAWATER INTAKE\Bouton_3\Text OFF
START	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Bouton_4\Text OFF
START	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\ACID CLEANING\Bouton_3\Text OFF
START	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPOCHLORIT SYSTEM\Bouton_19\Text OFF
started	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPOCHLORIT SYSTEM\Bouton_2\Text OFF
started	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPOCHLORIT SYSTEM\Bouton_3\Text OFF
started	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPOCHLORIT SYSTEM\Bouton_5\Text OFF
started	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPOCHLORIT SYSTEM\Bouton_6\Text OFF
started	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPOCHLORIT SYSTEM\Bouton_8\Text OFF
started	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPOCHLORIT SYSTEM\Bouton_9\Text OFF
started	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\ACID CLEANING\Bouton_5\Text OFF
started	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\ACID CLEANING\Bouton_2\Text OFF
Step structure	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\Step25
Step structure	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\Step41
Step structure	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\Step49
Step structure	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypoclorite system [FB1]\Step50
Step structure	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\Step24
Step structure	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\Step28
Step structure	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\Step27
Step structure	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\Step23
Step structure	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\Step26
Step structure	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEAWATER Intake [FB2]\Step25
Step1	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_StepNames_1\Range 1\Text

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Step1	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_2\Range 1\Text
Step1	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_3\Range 1\Text
Step10	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_2\Range 10\Text
Step10	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 10\Text
Step10	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_3\Range 10\Text
Step11	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_2\Range 11\Text
Step11	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 11\Text
Step11	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_3\Range 11\Text
Step12	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_2\Range 12\Text
Step12	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 12\Text
Step12	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_3\Range 12\Text
Step13	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_2\Range 13\Text
Step13	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 13\Text
Step13	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_3\Range 13\Text
Step14	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_2\Range 14\Text
Step15	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 15\Text
Step15	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_2\Range 15\Text
Step15	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_3\Range 15\Text
Step16	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 16\Text
Step16	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_2\Range 16\Text
Step16	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_3\Range 16\Text
Step17	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 17\Text
Step17	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_3\Range 17\Text
Step17	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_2\Range 17\Text
Step18	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 18\Text
Step18	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_3\Range 18\Text
Step18	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_2\Range 18\Text
Step19	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_3\Range 19\Text
Step19	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_2\Range 19\Text
Step19	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 19\Text
Step2	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 2\Text
Step2	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_3\Range 2\Text
Step20	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 20\Text
Step20	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_3\Range 20\Text
Step20	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_2\Range 20\Text
Step21	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 21\Text
Step21	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_2\Range 21\Text
Step21	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_3\Range 21\Text
Step22	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 22\Text
Step22	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_3\Range 22\Text
Step22	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_2\Range 22\Text
Step23	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 23\Text
Step23	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_3\Range 23\Text
Step23	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_2\Range 23\Text
Step24	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 24\Text

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Step24	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_3\Range 24\Text
Step24	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_2\Range 24\Text
Step25	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 25\Text
Step25	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_2\Range 25\Text
Step26	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 26\Text
Step26	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_3\Range 26\Text
Step26	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_2\Range 26\Text
Step27	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 27\Text
Step27	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_3\Range 27\Text
Step27	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_2\Range 27\Text
Step28	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 28\Text
Step28	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_3\Range 28\Text
Step28	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_2\Range 28\Text
Step29	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 29\Text
Step3	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 3\Text
Step3	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_3\Range 3\Text
Step30	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 30\Text
Step31	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 31\Text
Step32	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 32\Text
Step33	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 33\Text
Step34	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 34\Text
Step35	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 35\Text
Step36	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 36\Text
Step37	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 37\Text
Step38	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 38\Text
Step39	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 39\Text
Step4	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 4\Text
Step4	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_3\Range 4\Text
Step40	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 40\Text
Step41	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 41\Text
Step42	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 42\Text
Step43	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 43\Text
Step44	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 44\Text
Step45	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 45\Text
Step46	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 46\Text
Step47	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 47\Text
Step48	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 48\Text
Step49	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 49\Text
Step5	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 5\Text
Step5	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_3\Range 5\Text
Step50	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 50\Text
Step6	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 6\Text
Step6	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_3\Range 6\Text
Step7	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_2\Range 9\Text
Step7	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 7\Text

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Step7	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_3\Range 7\Text
Step8	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 8\Text
Step8	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_3\Range 8\Text
Step9	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_3\Range 9\Text
Step9	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\PLC_1\SYSTEM_Step-Names_1\Range 31\Text
STOP	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Bouton_5\Text OFF
Students:	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Button_4\Text OFF
Subcategory 1	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries1\Range 1\Text
Subcategory 1	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries2\Range 1\Text
Subcategory 10	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries1\Range 10\Text
Subcategory 10	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries2\Range 10\Text
Subcategory 11	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries1\Range 11\Text
Subcategory 11	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries2\Range 11\Text
Subcategory 12	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries1\Range 12\Text
Subcategory 12	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries2\Range 12\Text
Subcategory 13	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries1\Range 13\Text
Subcategory 13	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries2\Range 13\Text
Subcategory 14	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries1\Range 14\Text
Subcategory 14	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries2\Range 14\Text
Subcategory 15	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries1\Range 15\Text
Subcategory 15	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries2\Range 15\Text
Subcategory 16	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries1\Range 16\Text
Subcategory 16	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries2\Range 16\Text
Subcategory 2	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries1\Range 2\Text
Subcategory 2	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries2\Range 2\Text
Subcategory 3	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries1\Range 3\Text
Subcategory 3	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries2\Range 3\Text
Subcategory 4	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries1\Range 4\Text
Subcategory 4	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries2\Range 4\Text
Subcategory 5	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries1\Range 5\Text
Subcategory 5	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries2\Range 5\Text
Subcategory 6	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries1\Range 6\Text
Subcategory 6	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries2\Range 6\Text
Subcategory 7	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries1\Range 7\Text
Subcategory 7	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries2\Range 7\Text
Subcategory 8	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries1\Range 8\Text
Subcategory 8	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries2\Range 8\Text
Subcategory 9	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries1\Range 9\Text
Subcategory 9	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionSubCatego-ries2\Range 9\Text
Supervisors:	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Button_5\Text OFF
Tank level HH	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Alarme de bit_11\Alarm text
Tank level LL	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Alarme de bit_10\Alarm text
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Bouton_4\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Bouton_5\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Bouton_6\Text ON

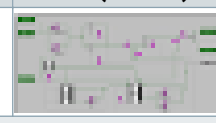
French (France)	Category	Reference
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Bouton_7\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Bouton_2\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Bouton_3\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Bouton_5\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Bouton_6\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Bouton_8\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Bouton_9\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Bouton_11\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Bouton_12\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Bouton_13\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Bouton_14\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\ACID CLEANING\Bouton_5\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\ACID CLEANING\Bouton_2\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Bouton_15\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Bouton_16\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Bouton_17\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Bouton_18\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\SEAWA-TER INTAKE\Bouton_2\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\SEAWA-TER INTAKE\Bouton_3\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\ACID CLEANING\Bouton_3\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Bouton_19\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Bouton_20\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Bouton_21\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Bouton_22\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Bouton_23\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Bouton_24\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\HYPO-CHLORIT SYSTEM\Bouton_4\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\SEAWA-TER INTAKE\Bouton_4\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\SEAWA-TER INTAKE\Bouton_24\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\ACID CLEANING\Bouton_20\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Screens\ACID CLEANING\Bouton_22\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Button_1\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Button_2\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Button_3\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Button_4\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Button_5\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Button_6\Text ON
Text	HMI screen	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN] \Screens\HOME\Button_7\Text ON
Text message	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 107\Text
Text message	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 207\Text
Text message	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 307\Text
Text message	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 407\Text
Text message	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 507\Text
Text message	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 607\Text
Text message	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 707\Text

French (France)	Category	Reference
Text message	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionTypes\Range 807\Text
Transition structure	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\Trans71
Transition structure	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\Trans77
Transition structure	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\Trans81
Transition structure	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\Trans78
Transition structure	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\Trans79
Transition structure	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\Trans68
Transition structure	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\Trans41
Transition structure	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\Trans47
Transition structure	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\Trans48
Transition structure	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\Trans46
Transition structure	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\Trans45
Transition structure	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\Trans42
Transition structure	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\Trans44
Transition structure	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\Trans43
Utilisateurs	HMI runtime	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\User administra-tion\Utilisateurs\DisplayName
Valeur du compteur de périodes 10 ms au der-nier cycle	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\TICKS.OLD
Valeur du compteur de périodes 10 ms au der-nier cycle	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\TICKS.OLD
Valeur du compteur de périodes 10 ms au der-nier cycle	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\TICKS.OLD
Valeur du compteur de périodes 10 ms dans ce cycle	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\TICKS.NEW
Valeur du compteur de périodes 10 ms dans ce cycle	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\TICKS.NEW
Valeur du compteur de périodes 10 ms dans ce cycle	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\TICKS.NEW
Valeur précédente dans S_SEL	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\S_SEL_OLD
Valeur précédente dans S_SEL	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\S_SEL_OLD
Valeur précédente dans S_SEL	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\S_SEL_OLD
Validation de commutation pour transition en mode semi-automatique	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\hypo-clorite system [FB1]\T_PUSH
Validation de commutation pour transition en mode semi-automatique	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\SEA-WATER Intake [FB2]\T_PUSH
Validation de commutation pour transition en mode semi-automatique	Block comment	electro-chlorination station 3_V13_SP1_V18\PLC_1 [CPU 317-2 PN/DP]\Program blocks\Acid Cleaning [FB3]\T_PUSH
valve to tank Fault	Alarm text	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\HMI alarms\Alarme de bit_5\Alarm text
Vue racine	HMI runtime	electro-chlorination station 3_V13_SP1_V18\HMI_1 [TP1500 Basic color PN]\Text and graphic lists\Nom_vue_liste_textes\Entrée_liste_textes_1\Text
Warning	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 201\Text
Warning	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 202\Text
Warning	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 203\Text
Warning	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 204\Text
Warning	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 205\Text
Warning	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 206\Text
Warning	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 207\Text
Warning	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 208\Text
Warning	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 209\Text
Warning	Text List Text Category	electro-chlorination station 3_V13_SP1_V18\SYSTEM_Global_ProDiag_SupervisionCatego-ries\Range 210\Text

electro-chlorination station 3_V13_SP1_V18 / Languages & resources

Project graphics

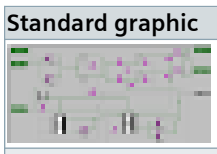
ACID CLEAN

Standard graphic	French (France)
	
Dithering mode	
Same color	Same color
Smoothing	
Disabled	Disabled

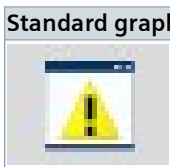
ACID CLEAN_1

Standard graphic	French (France)
Dithering mode	
Same color	Same color
Smoothing	
Disabled	Disabled

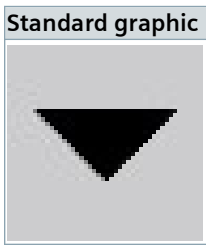
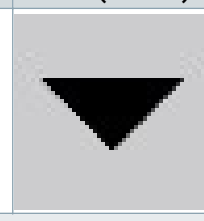
ACID CLEAN_2

Standard graphic	French (France)
	
Dithering mode	
Same color	Same color
Smoothing	
Disabled	Disabled

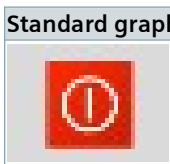
AlarmDisplay_TP1500_Basic_color_PN

Standard graphic	French (France)
	
Dithering mode	
Same color	Same color
Smoothing	
Disabled	Disabled

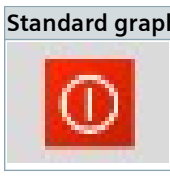
Down_Arrow

Standard graphic	French (France)
	
Dithering mode	
Same color	Same color
Smoothing	
Disabled	Disabled

ExitRuntime_KTP1000_Basic_color_PN

Standard graphic	French (France)
	
Dithering mode	
Same color	Same color
Smoothing	
Disabled	Disabled

ExitRuntime_TP1500_Basic_color_PN

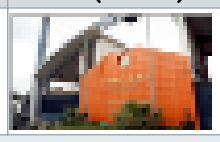
Standard graphic	French (France)
	

Standard graphic	French (France)
Dithering mode	
Same color	Same color
Smoothing	
Disabled	Disabled

Graphique_1

Standard graphic	French (France)
Dithering mode	
Same color	Same color
Smoothing	
Disabled	Disabled

Graphique_2

Standard graphic	French (France)
	
Dithering mode	
Same color	Same color
Smoothing	
Disabled	Disabled

Graphique_3

Standard graphic	French (France)
Dithering mode	
Same color	Same color
Smoothing	
Disabled	Disabled

Graphique_4

Standard graphic	French (France)
Dithering mode	
Same color	Same color
Smoothing	
Disabled	Disabled

Home

Standard graphic	French (France)
Dithering mode	
Same color	Same color
Smoothing	
Disabled	Disabled

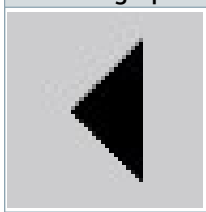
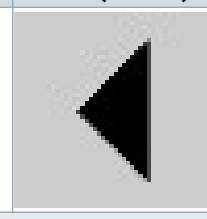
HYPOCHLORIT GENERAL

Standard graphic	French (France)
Dithering mode	
Same color	Same color
Smoothing	
Disabled	Disabled

HYPOCHLORIT GENERAL_1

Standard graphic	French (France)
Dithering mode	
Same color	Same color
Smoothing	
Disabled	Disabled

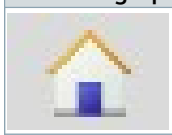
Left_Arrow

Standard graphic	French (France)
	
Dithering mode	
Same color	Same color
Smoothing	
Disabled	Disabled

Logo de HMI_1

Standard graphic	French (France)
SIEMENS SIMATIC HMI	SIEMENS SIMATIC HMI
Dithering mode	
Same color	Same color
Smoothing	
Disabled	Disabled

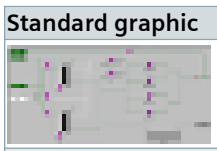
NavigateHome_KTP1000_Basic_color_PN

Standard graphic	French (France)
	
Dithering mode	
Same color	Same color
Smoothing	
Disabled	Disabled

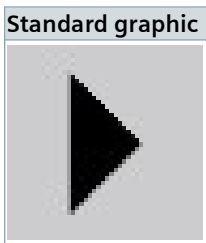
NavigateHome_TP1500_Basic_color_PN

Standard graphic	French (France)
Dithering mode	
Same color	Same color
Smoothing	
Disabled	Disabled

PUMPS

Standard graphic	French (France)
	
Dithering mode	
Same color	Same color
Smoothing	
Disabled	Disabled

Right_Arrow

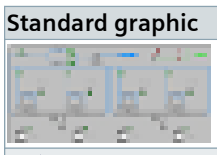
Standard graphic	French (France)
	
Dithering mode	
Same color	Same color

Standard graphic	French (France)
Smoothing	
Disabled	Disabled

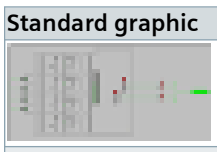
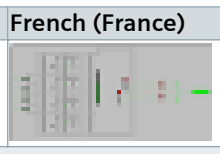
seawater intake

Standard graphic	French (France)
Dithering mode	
Same color	Same color
Smoothing	
Disabled	Disabled

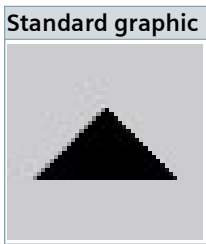
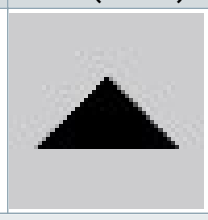
seawater intake_1

Standard graphic	French (France)
	
Dithering mode	
Same color	Same color
Smoothing	
Disabled	Disabled

seawater pumps

Standard graphic	French (France)
	
Dithering mode	
Same color	Same color
Smoothing	
Disabled	Disabled

Up_Arrow

Standard graphic	French (France)
	
Dithering mode	
Same color	Same color
Smoothing	
Disabled	Disabled